Simscape™ Electrical™

User's Guide

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Revision History

September 2013	Online only	New for Version 6.0 (Release 2013b)
March 2014	Online only	Revised for Version 6.1 (Release 2014a)
		(Renamed from *SimPowerSystems™ User's Guide (Third Generation)*)
October 2014	Online only	Revised for Version 6.2 (Release 2014b)
March 2015	Online only	Revised for Version 6.3 (Release 2015a)
September 2015	Online only	Revised for Version 6.4 (Release 2015b)
March 2016	Online only	Revised for Version 6.5 (Release 2016a)
		(Renamed from *SimPowerSystems™ User's Guide (Simscape™ Components)*)
September 2016	Online only	Revised for Version 6.6 (Release 2016b)
March 2017	Online only	Revised for Version 6.7 (Release 2017a)
September 2017	Online only	Revised for Version 6.8 (Release 2017b)
March 2018	Online only	Revised for Version 6.9 (Release 2018a)
September 2018	Online only	Revised for Version 7.0 (Release 2018b)
		(Renamed from *Simscape™ Power Systems™ User's Guide (Simscape™ Components)* and *Simscape™ Electronics™ User's Guide*)
March 2019	Online only	Revised for Version 7.1 (Release 2019a)
		(Renamed from *Simscape™ Electrical™ User's Guide (Electronics, Mechatronics, and Power Systems)*)
September 2019	Online only	Revised for Version 7.2 (Release 2019b)
March 2020	Online only	Revised for Version 7.3 (Release 2020a)
September 2020	Online only	Revised for Version 7.4 (Release 2020b)

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Getting Started

Simscape Electrical Product Description

Model and simulate electronic, mechatronic, and electrical power systems

Simscape Electrical (formerly SimPowerSystems™ and SimElectronics®) provides component libraries for modeling and simulating electronic, mechatronic, and electrical power systems. It includes models of semiconductors, motors, and components for applications such as electromechanical actuation, smart grids, and renewable energy systems. You can use these components to evaluate analog circuit architectures, develop mechatronic systems with electric drives, and analyze the generation, conversion, transmission, and consumption of electrical power at the grid level.

Simscape Electrical helps you develop control systems and test system-level performance. You can parameterize your models using MATLAB® variables and expressions, and design control systems for electrical systems in Simulink®. You can integrate mechanical, hydraulic, thermal, and other physical systems into your model using components from the Simscape family of products. To deploy models to other simulation environments, including hardware-in-the-loop (HIL) systems, Simscape Electrical supports C-code generation.

Simscape Electrical was developed in collaboration with Hydro-Québec of Montreal.

Key Features

- Libraries of electrical components including sensors, actuators, motors, machines, passive devices, and semiconductor devices
- Adjustable model fidelity, including nonlinear effects, operational limits, fault modeling, and temperature-dependent behavior
- SPICE netlist importer for converting SPICE subcircuits of discrete devices to Simscape models
- Application-specific models, including common AC and DC electric drives, smart grids, and renewable energy systems
- Ideal switching, discretization, and phasor simulation for faster model execution
- MATLAB based Simscape language for creating custom component models
- Support for C-code generation (with Simulink Coder™)

Simscape Electrical Block Libraries

Simscape Electrical software includes twelve different top-level libraries. These libraries allow you to model mechatronic systems, analog circuit architectures, and single- and multi-phase electrical power systems. You can also develop control algorithms for these systems within the Simulink environment by using these libraries. All of these libraries, except Specialized Power Systems, contain blocks developed specifically for extending the Simscape Foundation domains and are fully compatible with the Simscape technology. Blocks in the Specialized Power Systems library function in their own domain.

Libraries Compatible with Simscape Technology

All Simscape Electrical libraries, except Specialized Power Systems, contain blocks specifically developed to:

- Extend the Simscape Electrical domain, a single-phase electrical domain.
- Extend the Simscape Three-Phase Electrical domain, a three-phase electrical domain.

These library blocks are written in the Simscape language and are fully compatible with the Simscape technology, including local solvers, data logging, statistics and variable viewers, frequency analysis, and component and library customizations. To configure Simscape Electrical models composed of these library blocks for local-solver simulation, use the Solver Configuration block. Many of the blocks in these libraries also work with other Simscape Foundation domains, such as the Mechanical, Magnetic, and Thermal domains. When working with the Simscape technology compatible library blocks, you can use these capabilities:

- Partitioning Solver
- Simscape HDL Workflow Advisor
- Simscape Results Explorer

These libraries include models of high-fidelity, nonlinear, faultable, electrothermal power electronics. You can use these components to develop mechatronic systems and to build behavioral models for evaluating analog circuit architectures. The libraries also include low-fidelity models that are switched linear and optimized for fast simulation. There are also some models that contain optional ports for thermal analysis.

You can create single-line three-phase diagrams by using the three-phase blocks because the Three-Phase Electrical domain supports signals that contain all three phases as individual elements in a single vector. You can also model each phase individually, for example, to inject a single-line-to-ground fault into your circuit, by expanding the three-phase ports on these blocks into three separate single-phase electrical ports.

The Control library contains Simulink blocks for signal generation, mathematical transformation, and machine control. You can use these components to develop control systems for single- and multi-phase electrical power systems.

Through conserving ports of the same domain, you can directly connect the blocks from these Simscape Electrical libraries to Simscape blocks from:

- Simscape Foundation libraries

- Simscape add-on products, such as Simscape Driveline™, Simscape Multibody™, and Simscape Fluids™

Through physical signal ports, you can connect the physical blocks from these Simscape Electrical libraries to:

- Simulink blocks, including blocks from the Control library, by using converter blocks from the Simscape Utilities library
- Blocks from the Physical Signals library, which is in the Simscape Foundation library.

Specialized Power Systems Library

The Simscape Electrical Specialized Power Systems library contains blocks that use their own, *specialized* electrical domain. The library contains models of typical power equipment such as transformers, electric machines and drives, and power electronics. It also contains control, measurement, and signal generation models that you can use for developing power system control algorithms. The Specialized Power Systems Fundamental Blocks library contains the powergui block, which provides tools for the steady-state analysis of electrical circuits. To configure Specialized Power Systems models for continuous-time, discrete-time, or phasor simulation, and to analyze simulation results, use the powergui block. The powergui block is in the Specialized Power Systems Fundamental Blocks library.

You can connect Specialized Power Systems blocks to Simulink blocks either:

- Directly, through Simulink signal input and output ports.
- Through measurement blocks from the Measurements sublibrary of the Fundamental Blocks library.

Access the Simscape Electrical Block Libraries

You can access the Simscape Electrical libraries from the Simulink Library Browser or from the MATLAB command prompt.

To display the **Electrical** library in the Simulink Library Browser, scroll to the **Simscape** node. Expand the **Simscape** node and then the **Electrical** node. Alternately, at the MATLAB command prompt, enter this command.

```
ee_lib
```
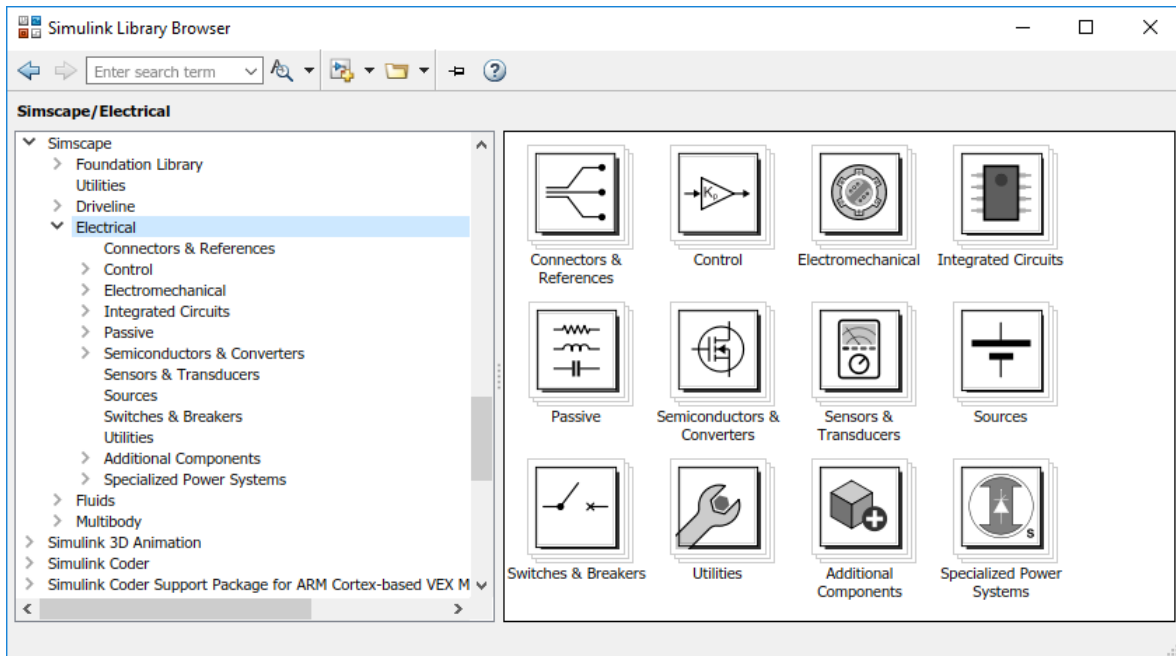
To access the sublibraries in the twelve top-level Simscape Electrical libraries, further expand the nodes. Alternately, use the `open_system` command at the MATLAB command prompt. For example, to access the sublibraries in the **Connectors & References** library, enter the commands:

```
ee_lib;
open_system('ee_lib/Connectors & References')
```

Modeling Analog Circuit Architectures, Mechatronic Systems, and Electrical Power Systems Using Simscape Electrical

In this section...
"Simulink Templates for Modeling with Simscape Electrical" on page 1-6
"Simscape Electrical Blocks and Ports" on page 1-7
"Machine and Transformer Source Code Examples" on page 1-7
"Plotting and Display Options for Asynchronous and Synchronous Machines" on page 1-7
"Choosing the Right Simscape Electrical Technology" on page 1-7
"Assumptions and Limitations" on page 1-8

When you model and analyze mechatronic systems, analog circuit architectures, or electrical power systems using Simscape Electrical, your workflow might include the following tasks:

1   Create a Simulink model that includes components from Simscape Electrical libraries.

In most applications, it is most natural to model the physical system using Simscape Electrical blocks and other Simscape blocks, and then develop the controller or signal processing algorithm in Simulink.

For more information about modeling the physical system, see "Essential Electrical Modeling Techniques" on page 3-2.

2   Define component data by specifying electrical or mechanical properties as defined on a datasheet.

For more information about parametrization, see "Parameterizing Blocks from Datasheets" on page 3-13.

3   Configure the solver options.

For more information about the settings that most affect the solution of a physical system, see "Setting Up Solvers for Physical Models".

4   Run the simulation.

For more information on how to perform time-domain simulation of an electrical system, see "Simulating an Electronic, Mechatronic, or Electrical Power System" on page 3-8.

Simulink Templates for Modeling with Simscape Electrical

On the Simulink start page, the **Simscape** section has model templates that provide you with design patterns for modeling with Simscape Electrical:

•   Electrical

•   Electrical Three-Phase

•   Mechanical Rotational

•   Mechanical Translational

Models you create from these templates have the corresponding reference block, the required Solver Configuration block, and the frequently used Simscape-Simulink interfacing blocks already in the

Simulink canvas. The models also contain links that you can double-click to access other blocks in the corresponding Simscape libraries.

To create a model using one of these **Simscape** templates:

1   Open the Simulink Start page. In the MATLAB Home tab, select the **Simulink** button. Alternatively, at the command line, enter:

    simulink

2   In the **Simscape** section, locate the templates that are preconfigured for modeling with Simscape Electrical. Selecting a template opens a model in the Simulink Editor. To save the model, select **Simulation > Save > Save As**.

Simscape Electrical Blocks and Ports

Simscape Electrical blocks that are written in the Simscape language are fully compatible with Simscape technology, including the local solver, code generation, and data logging.

Simscape Electrical blocks have single-phase, composite three-phase, thermal, magnetic, mechanical translational conserving, and mechanical rotational conserving ports. You can use composite three-phase ports to build models corresponding to single-line diagrams of three-phase electrical systems. Composite three-phase ports connect to other composite three-phase ports. Electrical and mechanical rotational conserving ports connect directly to Simscape Foundation library components and Simscape add-on products such as Simscape Driveline. You can use a Phase Splitter block to split a composite three-phase port into individual electrical conserving ports.

Machine and Transformer Source Code Examples

Simscape Electrical software provides Simscape language source code examples for machines and transformers, which you can view and customize. To access the example blocks, type ThreePhaseExamples_lib at the MATLAB command prompt.

Plotting and Display Options for Asynchronous and Synchronous Machines

For the Machine Inertia block and the asynchronous and synchronous machine blocks in Simscape Electrical software, you can perform some useful plotting and display actions using the **Electrical** menu on the block context menu. For example, to plot torque versus speed (both in SI units) for the Induction Machine Wound Rotor block, right-click the block. From the block context menu, select **Electrical > Plot Torque Speed (SI)**. The software plots the results in a figure window.

Using other options on the **Electrical** menu, you can plot values in per-unit or display base parameter values in the MATLAB Command Window. These options enable you to tune the performance of your three-phase machine quickly.

Choosing the Right Simscape Electrical Technology

Simscape Electrical software includes two different technologies and corresponding libraries. For a comparison of the two technologies, see "Simscape Electrical Block Libraries" on page 1-3. Choose the Simscape Electrical technology most appropriate for your modeling needs and, if possible, build your model using blocks exclusively from that technology. However, if necessary, you can build a

model that uses blocks from both technologies. To do so, use blocks from the **Simscape > Electrical > Specialized Power Systems > Fundamental Blocks > Interface Elements** library to interface between them.

Assumptions and Limitations

The Simscape Electrical blocks let you perform tradeoff analyses to optimize system design, for example, by testing various algorithms with different circuit implementations. The library contains blocks that use either high level or more detailed models to simulate components. Simscape Electrical does not have the capability to:

- Perform either layout (physical design) tasks, or the associated implementation tasks such as layout versus schematic (LVS), design rule checking (DRC), parasitic extraction, and back annotation.
- Model 3-D parasitic effects that are typically important for high-frequency applications.

For these types of requirements, you must use an EDA package specifically designed for the implementation of analog circuits.

See Also
Phase Splitter

More About
- "Simscape Electrical Block Libraries" on page 1-3
- "Essential Electrical Modeling Techniques" on page 3-2
- "Parameterizing Blocks from Datasheets" on page 3-13
- "Setting Up Solvers for Physical Models"
- "Simulating an Electronic, Mechatronic, or Electrical Power System" on page 3-8

Per-Unit System of Units

What Is the Per-Unit System?

The per-unit system is widely used in the power system industry to express values of voltages, currents, powers, and impedances of various power equipment. It is typically used for transformers and AC machines.

For a given quantity (voltage, current, power, impedance, torque, etc.) the per-unit value is the value related to a base quantity.

$$\text{base value in p.u.} = \frac{\text{quantity expressed in SI units}}{\text{base value}}$$

Generally the following two base values are chosen:

- The base power = nominal power of the equipment
- The base voltage = nominal voltage of the equipment

All other base quantities are derived from these two base quantities. Once the base power and the base voltage are chosen, the base current and the base impedance are determined by the natural laws of electrical circuits.

$$\text{base current} = \frac{\text{base power}}{\text{base voltage}}$$

$$\text{base impedance} = \frac{\text{base voltage}}{\text{base current}} = \frac{(\text{base voltage})^2}{\text{base power}}$$

For a transformer with multiple windings, each having a different nominal voltage, the same base power is used for all windings (nominal power of the transformer). However, according to the definitions, there are as many base values as windings for voltages, currents, and impedances.

The saturation characteristic of saturable transformer is given in the form of an instantaneous current versus instantaneous flux-linkage curve: [i1 phi1; i2 phi2; ..., in phin].

When the per-unit system is used to specify the transformer R L parameters, the flux linkage and current in the saturation characteristic must be also specified in pu. The corresponding base values are

$$\text{base instantaneous current} = (\text{base rms current}) \times \sqrt{2}$$

$$\text{base flux linkage} = \frac{(\text{base rms voltage}) \times \sqrt{2}}{2\pi \times (\text{base frequency})}$$

1-9

where current, voltage, and flux linkage are expressed respectively in volts, amperes, and volt-seconds.

For AC machines, the torque and speed can be also expressed in pu. The following base quantities are chosen:

- The base speed = synchronous speed
- The base torque = torque corresponding at base power and synchronous speed

$$\text{base torque} = \frac{\text{base power (3 phases) in VA}}{\text{base speed in radians/second}}$$

Instead of specifying the rotor inertia in kg*m^2, you would generally give the inertia constant H defined as

$$H = \frac{\text{kinetic energy stored in the rotor at synchronous speed in joules}}{\text{machine nominal power in VA}}$$

$$H = \frac{\frac{1}{2} \times J \cdot w^2}{Pnom}$$

The inertia constant is expressed in seconds. For large machines, this constant is around 3–5 seconds. An inertia constant of 3 seconds means that the energy stored in the rotating part could supply the nominal load during 3 seconds. For small machines, H is lower. For example, for a 3-HP motor, it can be 0.5–0.7 seconds.

Example 1: Three-Phase Transformer

Consider, for example, a three-phase two-winding transformer with these manufacturer-provided, typical parameters:

- Nominal power = 300 kVA total for three phases
- Nominal frequency = 60 Hz
- Winding 1: connected in wye, nominal voltage = 25-kV RMS line-to-line

  resistance 0.01 pu, leakage reactance = 0.02 pu
- Winding 2: connected in delta, nominal voltage = 600-V RMS line-to-line

  resistance 0.01 pu, leakage reactance = 0.02 pu
- Magnetizing losses at nominal voltage in % of nominal current:

  Resistive 1%, Inductive 1%

The base values for each single-phase transformer are first calculated:

- For winding 1:

Base power	300 kVA/3 = 100e3 VA/phase
Base voltage	25 kV/sqrt(3) = 14434 V RMS
Base current	100e3/14434 = 6.928 A RMS
Base impedance	14434/6.928 = 2083 Ω

Base resistance	14434/6.928 = 2083 Ω
Base inductance	2083/(2π*60)= 5.525 H

- For winding 2:

Base power	300 kVA/3 = 100e3 VA
Base voltage	600 V RMS
Base current	100e3/600 = 166.7 A RMS
Base impedance	600/166.7 = 3.60 Ω
Base resistance	600/166.7 = 3.60 Ω
Base inductance	3.60/(2π*60) = 0.009549 H

The values of the winding resistances and leakage inductances expressed in SI units are therefore

- For winding 1: R1= 0.01 * 2083 = 20.83 Ω; L1= 0.02*5.525 = 0.1105 H
- For winding 2: R2= 0.01 * 3.60 = 0.0360 Ω; L2= 0.02*0.009549 = 0.191 mH

For the magnetizing branch, magnetizing losses of 1% resistive and 1% inductive mean a magnetizing resistance Rm of 100 pu and a magnetizing inductance Lm of 100 pu. Therefore, the values expressed in SI units referred to winding 1 are

- Rm = 100*2083 = 208.3 kΩ
- Lm = 100*5.525 = 552.5 H

Example 2: Asynchronous Machine

Now consider a three-phase, four-pole Asynchronous Machine block in SI units. It is rated 3 HP, 220 V RMS line-to-line, 60 Hz.

The stator and rotor resistance and inductance referred to stator are

- Rs = 0.435 Ω; Ls = 2 mH
- Rr = 0.816 Ω; Lr = 2 mH

The mutual inductance is Lm = 69.31 mH. The rotor inertia is J = 0.089 kg.m^2.

The base quantities for one phase are calculated as follows:

Base power	3 HP*746VA/3 = 746 VA/phase
Base voltage	220 V/sqrt(3) = 127.0 V RMS
Base current	746/127.0 = 5.874 A RMS
Base impedance	127.0/5.874 = 21.62 Ω
Base resistance	127.0/5.874 = 21.62 Ω
Base inductance	21.62/(2π*60)= 0.05735 H = 57.35 mH
Base speed	1800 rpm = 1800*(2π)/60 = 188.5 radians/second
Base torque (three-phase)	746*3/188.5 = 11.87 newton-meters

Using the base values, you can compute the values in per-units.

Rs= 0.435 / 21.62 = 0.0201 pu Ls= 2 / 57.35 = 0.0349 pu

Rr= 0.816 / 21.62 = 0.0377 pu Lr= 2 / 57.35 = 0.0349 pu

Lm = 69.31/57.35 = 1.208 pu

The inertia is calculated from inertia J, synchronous speed, and nominal power.

$$H = \frac{\frac{1}{2} \times J \cdot w^2}{Pnom} = \frac{\frac{1}{2} \times 0.089 \times (188.5)^2}{3 \times 746} = 0.7065 \text{ seconds}$$

If you open the dialog box of the Asynchronous Machine block in pu units provided in the Machines library of the Simscape Electrical Specialized Power Systems Fundamental Blocks library, you find that the parameters in pu are the ones calculated.

Base Values for Instantaneous Voltage and Current Waveforms

When displaying instantaneous voltage and current waveforms on graphs or oscilloscopes, you normally consider the peak value of the nominal sinusoidal voltage as 1 pu. In other words, the base values used for voltage and currents are the RMS values given multiplied by $\sqrt{2}$.

Why Use the Per-Unit System Instead of the Standard SI Units?

Here are the main reasons for using the per-unit system:

* When values are expressed in pu, the comparison of electrical quantities with their "normal" values is straightforward.

  For example, a transient voltage reaching a maximum of 1.42 pu indicates immediately that this voltage exceeds the nominal value by 42%.
* The values of impedances expressed in pu stay fairly constant whatever the power and voltage ratings.

  For example, for all transformers in the 3–300 kVA power range, the leakage reactance varies approximately 0.01–0.03 pu, whereas the winding resistances vary between 0.01 pu and 0.005 pu, whatever the nominal voltage. For transformers in the 300 kVA to 300 MVA range, the leakage reactance varies approximately 0.03–0.12 pu, whereas the winding resistances vary between 0.005–0.002 pu.

  Similarly, for salient pole synchronous machines, the synchronous reactance X_d is generally 0.60–1.50 pu, whereas the subtransient reactance X'_d is generally 0.20–0.50 pu.

  It means that if you do not know the parameters for a 10-kVA transformer, you are not making a major error by assuming an average value of 0.02 pu for leakage reactances and 0.0075 pu for winding resistances.

The calculations using the per-unit system are simplified. When all impedances in a multivoltage power system are expressed on a common power base and on the nominal voltages of the different subnetworks, the total impedance in pu seen at one bus is obtained by simply adding all impedances in pu, without considering the transformer ratios.

2

Tutorials

Build and Simulate Composite and Expanded Three-Phase Models

In this section...
"Select System Component Blocks and Build a Resistive Three-Phase Model" on page 2-2
"Specify Simulation Parameters" on page 2-4
"Load Impedance Parameters" on page 2-4
"Specify Display Parameters" on page 2-5
"Simulate and Analyze the Resistive Three-Phase Model" on page 2-5
"Simulate and Analyze a Reactive Three-Phase Model" on page 2-6
"Create an Expanded Balanced Three-Phase Model" on page 2-6
"Create an Expanded Unbalanced Three-Phase Model" on page 2-7
"Simulate the Expanded Balanced and Unbalanced Models and Analyze the Results" on page 2-7

In this example, you build and analyze a simple Simscape Electrical model that simulates the behavior of a three-phase AC voltage source driving a purely resistive three-phase load. You then modify the load in this model to change it to:

- A reactive three-phase load
- A resistive three-phase load expanded into individual phases
- An expanded three-phase load that does not have equal resistance in each phase

For the completed initial model, see Simple Three-Phase Model.

Select System Component Blocks and Build a Resistive Three-Phase Model

1   Open the Simulink Start page. In the MATLAB Home tab, select the **Simulink** button. Alternatively, at the command line, enter:

    ```
    simulink
    ```

2   In the **Simscape** section, find the templates that are preconfigured for modeling with Simscape Electrical. Select the Electrical Three-Phase template. A model that contains these blocks opens in the Simulink canvas.

Block	Purpose	Library
Scope	Display phase voltages and currents for the three-phase system.	**Simulink > Sinks**
Electrical Reference	Provide the ground connection for electrical conserving ports.	**Simscape > Foundation Library > Electrical > Electrical Elements**
PS-Simulink Converter	Convert the physical signals to Simulink signals.	**Simscape > Utilities**
Simulink-PS Converter	Convert Simulink signals to physical signals.	**Simscape > Utilities**

Block	Purpose	Library
Solver Configuration	Define solver settings that apply to all physical modeling blocks.	**Simscape > Utilities**
Grounded Neutral (Three-Phase)	Provide an electrical ground connection for each phase of the three-phase system.	**Simscape > Electrical > Connectors & References**
Line Voltage Sensor (Three-Phase)	Measure the line-line voltages of a three-phase system and output a three-element physical signal vector.	**Simscape > Electrical > Sensors & Transducers**

The model also contains two links that you can double-click to access blocks from Simscape and Simscape Electrical libraries. For more information on using templates for modeling with Simscape Electrical, see "Modeling Analog Circuit Architectures, Mechatronic Systems, and Electrical Power Systems Using Simscape Electrical" on page 1-6.

3 Delete the Simulink-PS Converter and Line Voltage Sensor (Three-Phase) blocks.

4 Add these blocks to the model.

Block	Purpose	Library
RLC (Three-Phase)	Model the resistive, inductive, and capacitive properties of the three-phase load.	**Simscape > Electrical > Passive > RLC Assemblies**
Current Sensor (Three-Phase)	Convert the electrical current flowing in each phase of the three-phase load into a physical signal proportional to that current.	**Simscape > Electrical > Sensors & Transducers**
Phase Voltage Sensor (Three-Phase)	Convert the voltage across each phase of the three-phase system into a physical signal proportional to that voltage.	**Simscape > Electrical > Sensors & Transducers**
Voltage Source (Three-Phase)	Provide an ideal three-phase voltage source that maintains a sinusoidal voltage across its output terminals, regardless of the current flowing in the source.	**Simscape > Electrical > Sources**

5 Copy the PS-Simulink Converter and Grounded-Neutral (Three-Phase) blocks by right-clicking them and dragging them to new locations on canvas.

6 Add a second input port to the Scope block.

   a Right-click the Scope block.

   b From the context menu, select **Signals & Ports > Number of Input Ports > 2**

7 Connect the blocks as shown.
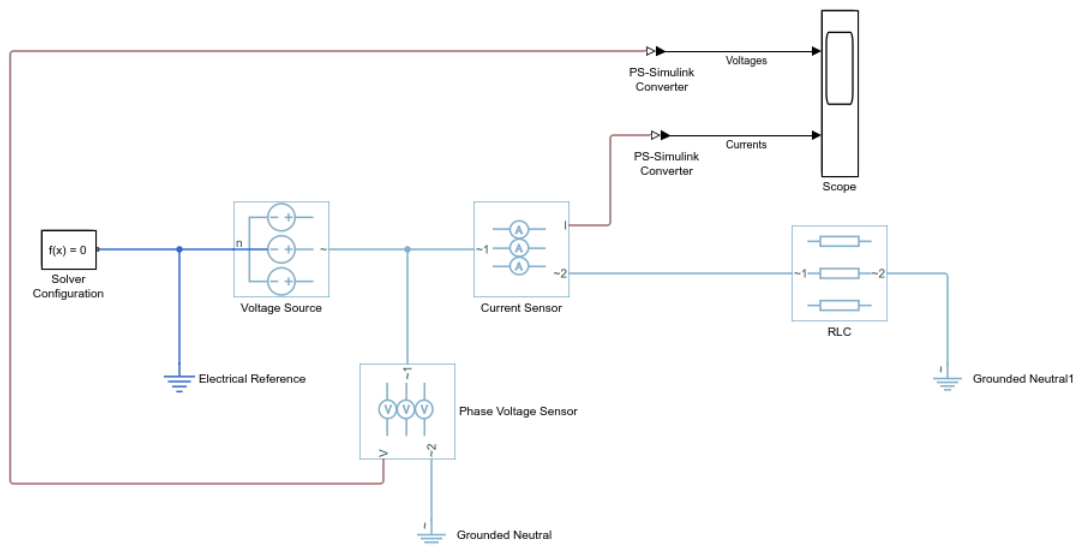
8   Remove the on-canvas annotations titled Open Simscape Library and Open Simscape Electrical Library. Save the model using the name `simplethreephasemodel`.

The blocks in this model use composite three-phase ports. For more information, see "Three-Phase Ports" on page 3-5.

Specify Simulation Parameters

As with Simscape models, you must include a Solver Configuration block in each topologically distinct physical network. This model has a single physical network, so use one Solver Configuration block.

1   In the Solver Configuration block, select **Use local solver** and set **Sample time** to `0.0001`.

In Simscape-based models, the local solver is a sample-based solver that represents physical network states as discrete states. For most Simscape Electrical models, the local solver is an appropriate first choice. The solver updates block states once per simulation time step, as determined by **Sample time**. For simulation of a 60-Hz AC system, an appropriate sample time is a value in the order of `1e-4`. For more information on solver options, see Solver Configuration.

If you prefer to use a continuous solver instead of a discrete solver, clear the **Use local solver** check box in the Solver Configuration block. The simulation then uses the Simulink solver specified in the model configuration parameters (**Modeling** > **Model Settings**). For Simscape Electrical models, an appropriate solver choice is the moderately stiff solver ode23t. For a 60 Hz AC system, specify a value for **Max step size** in the order of `1e-4`. For more information, see "Variable-Step Continuous Explicit Solvers".

2   In the Simulink Editor, set the simulation **Stop time** to `0.1`.

Load Impedance Parameters

The RLC block models resistive, inductive, and capacitive characteristics of the three-phase load. Using the **Component structure** parameter, you can specify a series or parallel combination of resistance, inductance, and capacitance.

In the RLC block, the defaults are:

- **Component structure** — R.
- **Resistance** — 1 Ω.

Using the default **Component structure** value, R, models a three-phase load that is purely resistive in nature. The resistance in *each* phase is 1 Ω.

Specify Display Parameters

Sensor blocks in the model convert the current and voltage in each phase of the three-phase system to proportional physical signals. PS-Simulink Converter blocks convert the physical signals into Simulink signals for the Scope block to display.

1 Of these three types of blocks, only the converter blocks have parameters. For this example:

- Set **Output signal unit** of the **PS-Simulink Converter1** block to V. This setting ensures that the block outputs a signal with the same magnitude as the voltage signal that enters it.
- Set **Output signal unit** of the **PS-Simulink Converter2** block to A. This setting ensures that the block outputs a signal with the same magnitude as the current signal that enters it.

2 Label the input signals to the Scope block. Double-click each line, and type the appropriate label, `Voltages` or `Currents`, as shown in the model graphic.

You are ready to simulate the model and analyze the results.

Simulate and Analyze the Resistive Three-Phase Model

1 Save the model.
2 Simulate the model.
3 View the phase currents and voltages. Double-click the Scope block.
4 From the scope menu, select **View > Configuration Properties**. Set **Layout** to 1-by-2 display.
5 To scale the scope axes to the data, click the **Autoscale** button ⤢.
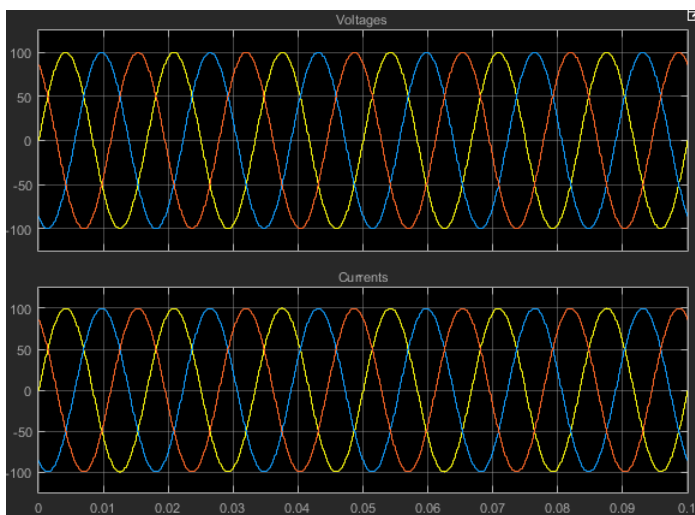
In this simulation, the **Component structure** parameter of the RLC (Three-Phase) block specifies that the electrical characteristics of the three-phase load are purely resistive. Therefore, for each

phase of the three-phase system, the voltage and current remain in phase with each other. Because the resistance in each phase is *1* Ω, the magnitude of the phase voltage is equal to the magnitude of the phase current.

Simulate and Analyze a Reactive Three-Phase Model

You can modify the model to create a reactive load. A reactive load has inductive or capacitive characteristics.

1 Save this version of the model using the name `simplethreephasemodel_reactive`.

2 In the RLC (Three-Phase) block, set:

- **Component structure** to `Series RL`
- **Inductance** to `0.002`

3 Simulate the model.

4 View the simulation results. Autoscale the scope axes.

5 Examine the results in closer detail. For example, click the Zoom button and drag a box over the first third of one of the plots.
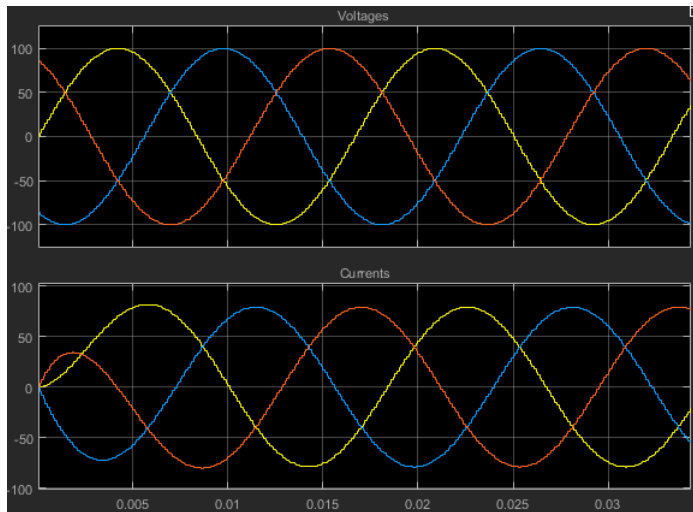
The electrical characteristics of three-phase load are no longer purely resistive. Because the load has an inductive characteristic, the current flowing in each phase lags the voltage.

Create an Expanded Balanced Three-Phase Model

1 Open the resistive three-phase model `simplethreephasemodel` that you initially created.

2 Delete the RLC (Three-Phase) block.

3 Drag two copies of the Phase Splitter block into the model from the **Simscape > Electrical > Connections & References** library.

4 Flip one of the Phase Splitter blocks horizontally. Right-click the block and select **Rotate & Flip > Flip Block > Left-Right**.

5 Drag a Resistor element into the model from the **Simscape > Foundation Library > Electrical > Electrical Elements** library.

6   To create space for more components, hide the Resistor element label. Right-click the resistor and select **Format > Show Block Name** to clear this option.

7   Make two more copies of the Resistor element.

8   Connect the components as shown.
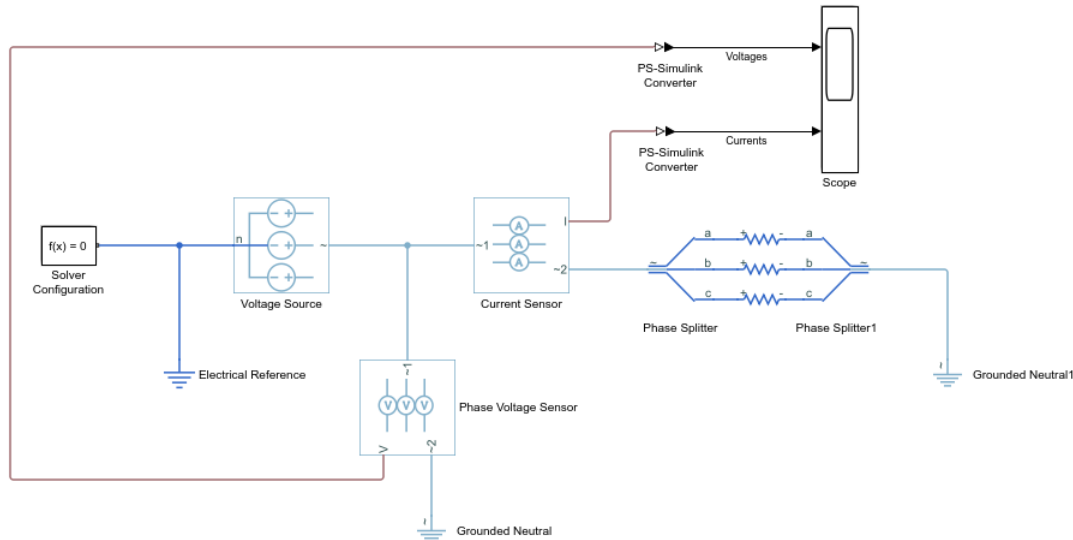
9   Save this version of the modified model using the name `simplethreephasemodel_expanded_balanced`.

This model name reflects that the load previously modeled by the RLC block is now expanded into individual phases. The load is still balanced, that is, there is equal resistance in each phase.

Create an Expanded Unbalanced Three-Phase Model

1   Unbalance the load in `simplethreephasemodel_expanded_balanced` by changing the resistance in one phase. Double-click the phase-c resistor element. Change **Resistance** to 2.

2   Save this version of the modified model using the name `simplethreephasemodel_expanded_unbalanced`.

This model name reflects that the three-phase load previously modeled by the RLC block is expanded into individual phases. The load is unbalanced, that is, the resistance in one of the phases is higher than in the other two.

Simulate the Expanded Balanced and Unbalanced Models and Analyze the Results

1   Simulate the `simplethreephasemodel_expanded_balanced` model. In the menu bar of the Simulink Explorer, click the **Run** button.

2   View the simulation results. Double-click the Scope block.

3   To scale the scope axes to the data, click the **Autoscale** button .
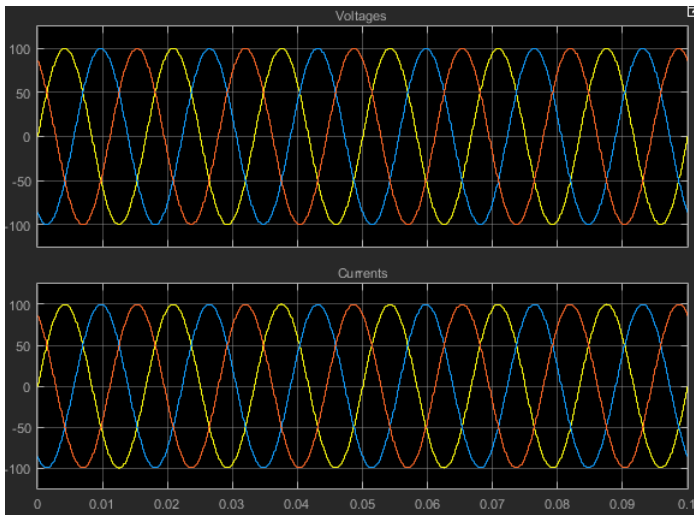
In the `simplethreephasemodel`, the **Component structure** parameter of the RLC (Three-Phase) block specifies that the three-phase load is purely resistive. In this version of the model, the load is expanded into an individual resistive element for each phase, but the resistance in each phase is unchanged. For each phase of the three-phase system, the voltage and current remain in phase with each other. Because the resistance in each phase is 1 Ω, the magnitude of the phase voltage is equal to the magnitude of the phase current.

Comparing these results with the results for the three-phase resistive model shows that a block with composite three-phase ports, the RLC (Three-Phase) block in the original model, produces results with the same fidelity as that of expanded phases.

4 Open the `simplethreephasemodel_expanded_unbalanced` model.

5 Simulate the model. Autoscale the scope axes.
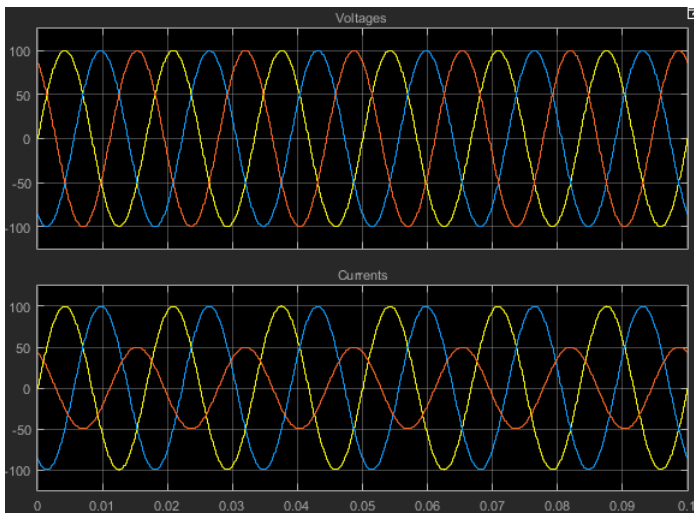
In this version of the model, the c-phase of the three-phase load has twice the resistance of the other two. Therefore, half as much current flows in that phase, as the second plot shows. However, because the load remains purely resistive, the voltage and current remain in phase with each other.

See Also

Related Examples

- "Modeling Analog Circuit Architectures, Mechatronic Systems, and Electrical Power Systems Using Simscape Electrical" on page 1-6
- "Essential Electrical Modeling Techniques" on page 3-2

DC Motor Model

In this section...
"Select Blocks to Represent System Components" on page 2-10
"Build the Model" on page 2-10
"Specify Model Parameters" on page 2-12
"Configure the Solver Parameters" on page 2-14
"Run the Simulation and Analyze the Results" on page 2-14

In this example, you model a DC motor driven by a constant input signal that approximates a pulse-width modulated signal and look at the current and rotational motion at the motor output.

To see the completed model, open the "PWM-Controlled DC Motor" on page 10-127 example.

Select Blocks to Represent System Components

Select the blocks to represent the input signal, the DC motor, and the motor output displays.

The following table describes the role of the blocks that represent the system components.

Block	Description
Solver Configuration	Defines solver settings that apply to all physical modeling blocks
PS-Simulink Converter	Converts the input physical signal to a Simulink signal
Controlled PWM Voltage	Generates the signal that approximates a pulse-width modulated motor input signal
H-Bridge	Drives the DC motor
DC Motor	Converts input electrical energy into mechanical motion
Current Sensor	Converts the electrical current that drives the motor into a measurable physical signal proportional to the current
DC Voltage Source	Generates a DC voltage
Electrical Reference	Provides the electrical ground
Mechanical Rotational Reference	Provides the mechanical ground
Ideal Rotational Motion Sensor	Converts the rotational motion of the motor into a measurable physical signal proportional to the motion
Scope	Displays motor current and rotational motion

Build the Model

1 Create a new model.

2 Add to the model the blocks listed in the following table. The Library column of the table specifies the hierarchical path to each block.

Block	Library	Quantity
Solver Configuration	Simscape > Utilities	1
PS-Simulink Converter	Simscape > Utilities	2
Controlled PWM Voltage	Simscape > Electrical > Integrated Circuits	1
H-Bridge	Simscape > Electrical > Semiconductors & Converters > Converters	1
DC Motor	Simscape > Electrical > Electromechanical > Brushed Motors	1
Current Sensor	Simscape > Foundation Library > Electrical > Electrical Sensors	1
DC Voltage Source	Simscape > Foundation Library > Electrical > Electrical Sources	1
Electrical Reference	Simscape > Foundation Library > Electrical > Electrical Elements	1
Mechanical Rotational Reference	Simscape > Foundation Library > Mechanical > Rotational Elements	1
Ideal Rotational Motion Sensor	Simscape > Foundation Library > Mechanical > Mechanical Sensors	1
Scope	Simulink > Commonly Used Blocks	2

Note You can use the Simscape function `ssc_new` with domain type `electrical` to create a Simscape model that contains these blocks:

- **Simulink-PS Converter**
- **PS-Simulink Converter**
- **Scope**
- **Solver Configuration**
- **Electrical Reference**

3 Rename and connect the blocks as shown in the diagram.
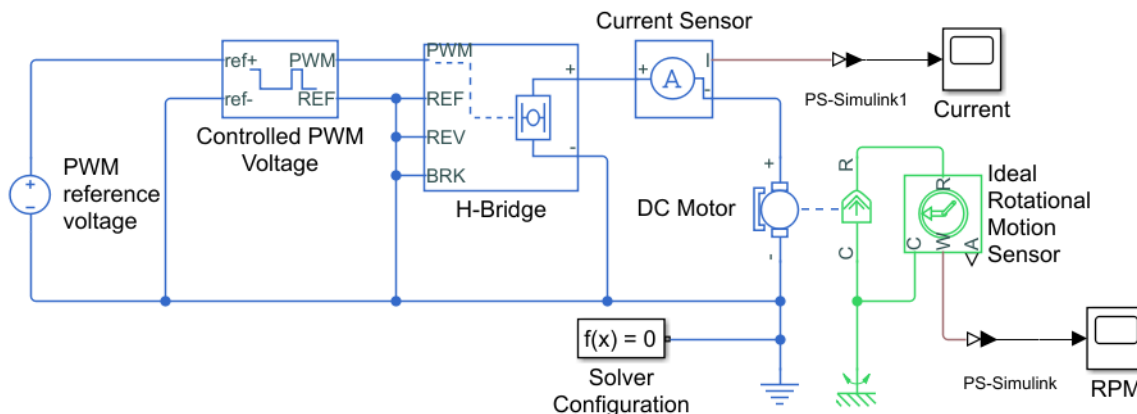
Now you are ready to specify block parameters.

Specify Model Parameters

Specify the following parameters to represent the behavior of the system components:

Model Setup Parameters

The following blocks specify model information that is not specific to a particular block:

- Solver Configuration
- Electrical Reference
- Mechanical Rotational Reference

As with Simscape models, you must include a Solver Configuration block in each topologically distinct physical network. This example has a single physical network, so use one Solver Configuration block with the default parameter values.

You must include an Electrical Reference block in each Simscape Electrical network. You must include a Mechanical Rotational Reference block in each network that includes electromechanical blocks. These blocks do not have any parameters.

For more information about using reference blocks, see "Grounding Rules".

Motor Input Signal Parameters

You generate the motor input signal using these blocks:

- The DC Voltage Source block (PWM reference voltage) generates a constant signal.
- The Controlled PWM Voltage block generates a pulse-width modulated signal.
- The H-Bridge block drives the motor.

In this example, all input ports of the H-Bridge block except the PWM port are connected to ground. As a result, the H-Bridge block behaves as follows:

- When the motor is on, the H-Bridge block connects the motor terminals to the power supply.
- When the motor is off, the H-Bridge block acts as a freewheeling diode to maintain the motor current.

In this example, you simulate the motor with a constant current whose value is the average value of the PWM signal. By using this type of signal, you set up a fast simulation that estimates the motor behavior.

1 Set the DC Voltage Source block parameters as follows:

- **Constant voltage** to 2.5

2 Set the Controlled PWM Voltage block parameters as follows:

- **PWM frequency** to 4000
- **Simulation mode** to Averaged

  This value tells the block to generate an output signal whose value is the average value of the PWM signal. Simulating the motor with an averaged signal estimates the motor behavior in the presence of a PWM signal. To validate this approximation, use value of PWM for this parameter.

3 Set the H-Bridge block parameters as follows:

- **Simulation mode** to Averaged

  This value tells the block to generate an output signal whose value is the average value of the PWM signal. Simulating the motor with an averaged signal estimates the motor behavior in the presence of a PWM signal. To validate this approximation, use value of PWM for this parameter.

Note The simulation mode for both the Controlled PWM Voltage and H-Bridge blocks must be the same.

Motor Parameters

Configure the block that models the motor.

Set the DC Motor block parameters as follows, leaving the unit settings at their default values where applicable:

- **Electrical Torque** tab:

  - **Model parameterization** to By rated power, rated speed & no-load speed
  - **Armature inductance** to 0.01
  - **No-load speed** to 4000
  - **Rated speed (at rated load)** to 2500
  - **Rated load (mechanical power)** to 10
  - **Rated DC supply voltage** to 12
- **Mechanical** tab:

  - **Rotor inertia** to 2000
  - **Rotor damping** to 1e-06

Current Display Parameters

Specify the parameters of the blocks that create the motor current display:

- Current Sensor block
- **PS-Simulink Converter1** block
- **Current** scope

Of the three blocks, only the PS-Simulink Converter1 block has parameters. Set the PS-Simulink Converter1 block **Output signal unit** parameter to A to indicate that the block input signal has units of amperes.

Torque Display Parameters

Specify the parameters of the blocks that create the motor torque display:

- Ideal Rotational Motion Sensor block
- PS-Simulink Converter block
- **RPM** scope

Of the three blocks, only the PS-Simulink Converter block has parameters you need to configure for this example. Set the PS-Simulink Converter block **Output signal unit** parameter to rpm to indicate that the block input signal has units of revolutions per minute.

Note You must type this parameter value. It is not available in the drop-down list.

Configure the Solver Parameters

Configure the solver parameters to use a continuous-time solver because Simscape Electrical models only run with a continuous-time solver. Increase the maximum step size the solver can take so the simulation runs faster.

1   In the model window, select **Modeling** > **Model Settings** to open the Configuration Parameters dialog box.
2   Select ode15s (Stiff/NDF) from the **Solver** list.
3   Expand **Additional options** and enter 1 for the **Max step size** parameter value.
4   Click **OK**.

For more information about configuring solver parameters, see "Simulating an Electronic, Mechatronic, or Electrical Power System" on page 3-8.

Run the Simulation and Analyze the Results

In this part of the example, you run the simulation and plot the results.

In the model window, select **Simulation** > **Run** to run the simulation.

To view the motor current and torque in the Scope windows, double-click the Scope blocks. You can do this before or after you run the simulation.

Note By default, the scope displays appear stacked on top of each other on the screen, so you can only see one of them. Click and drag the windows to reposition them.

The following plot shows the motor current.
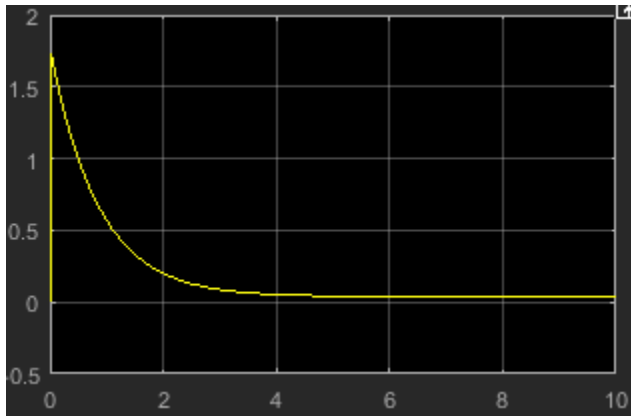
Motor Current

The next plot shows the motor rpm.
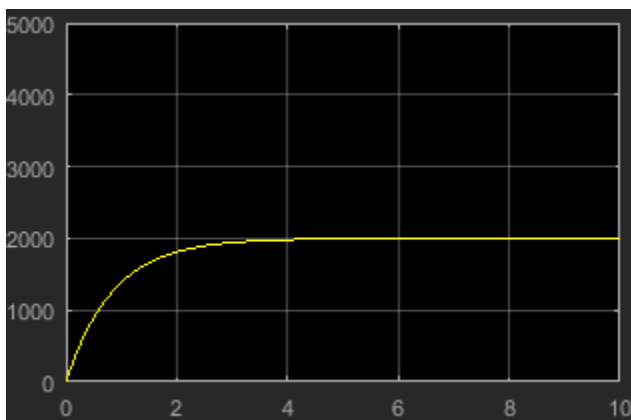
Motor RPM

As expected, the motor runs at about 2000 rpm when the applied DC voltage is 2.5 V.

Triangle Wave Generator Model

In this section...

In this example, you model a triangle wave generator using Simscape Electrical blocks and custom Simscape Electrical blocks, and then look at the voltage at the wave generator output.

You use a classic circuit configuration consisting of an integrator and a noninverting amplifier to generate the triangle wave, and use datasheets to specify block parameters. For more information, see "Parameterizing Blocks from Datasheets" on page 3-13.

To see the completed model, open the "Triangle Wave Generator" on page 10-302 example.

Select Blocks to Represent System Components

First, you select the blocks to represent the input signal, the triangle wave generator, and the output signal display.

You model the triangle wave generator with a set of physical blocks. The wave generator consists of:

- Two operational amplifier blocks
- Resistors and a capacitor that work with the operational amplifiers to create the integrator and noninverting amplifier
- Simulink-PS Converter and PS-Simulink Converter blocks whose function is to bridge the physical part of the model, which uses physical signals, and the rest of the model, which uses Simulink signals.

You have a manufacturer datasheet for the two operational amplifiers you want to model. Later in the example, you use the datasheet to parameterize the Simscape Electrical Band-Limited Op-Amp block.

The following table describes the role of the blocks that represent the system components.

Block	Description
Sine Wave	Generates a sinusoidal signal that controls the resistance of the Variable Resistor block
Scope	Displays the triangular output wave
Simulink-PS Converter	Converts the sinusoidal Simulink signal to a physical signal
Solver Configuration	Defines solver settings that apply to all physical modeling blocks
PS-Simulink Converter	Converts the output physical signal to a Simulink signal
Capacitor	Works with an operational amplifier and resistor block to create the integrator

Block	Description
Resistor	Works with the operational amplifier and capacitor blocks to create the integrator and noninverting amplifier
Variable Resistor	Supplies a time-varying resistance that adjusts the gain of the integrator, which in turn varies the frequency and amplitude of the generated triangular wave
DC Voltage Source	Generates a DC reference signal for the operational amplifier block of the noninverting amplifier
Voltage Sensor	Converts the electrical voltage at the output of the integrator into a physical signal proportional to the current
Electrical Reference	Provides the electrical ground
Band-Limited Op-Amp	Works with the capacitor and resistor to create an integrator and a noninverting amplifier
Diode	Limits the output of the Band-Limited Op-Amp block, to make the output waveform independent of supply voltage

Build the Model

Create a Simulink model, add blocks to the model, and connect the blocks.

1 Create a new model.

2 Add to the model the blocks listed in this table. The Library Path column of the table specifies the hierarchical path to each block.

Block	Library Path	Quantity
Sine Wave	**Simulink > Sources**	1
Scope	**Simulink > Commonly Used Blocks**	1
Simulink-PS Converter	**Simscape > Utilities**	1
Solver Configuration	**Simscape > Utilities**	1
PS-Simulink Converter	**Simscape > Utilities**	1
Capacitor	**Simscape > Foundation Library > Electrical > Electrical Elements**	1
Resistor	**Simscape > Foundation Library > Electrical > Electrical Elements**	3
Variable Resistor	**Simscape > Foundation Library > Electrical > Electrical Elements**	1
Electrical Reference	**Simscape > Foundation Library > Electrical > Electrical Elements**	2
DC Voltage Source	**Simscape > Foundation Library > Electrical > Electrical Sources**	1
Voltage Sensor	**Simscape > Foundation Library > Electrical > Electrical Sensors**	1

Block	Library Path	Quantity
Band-Limited Op-Amp	Simscape > Electrical > Integrated Circuits	2
Diode	Simscape > Electrical > Semiconductor & Converters	2

Note You can use the Simscape function `ssc_new` with a domain type of `electrical` to create a Simscape model that contains these blocks:

- **Simulink-PS Converter**
- **PS-Simulink Converter**
- **Scope**
- **Solver Configuration**
- **Electrical Reference**

3 Rename and connect the blocks as shown in the diagram. The blocks in the triangle wave generator circuit are organized in two stages. The Comparator Stage contains a comparator constructed from a Band-Limited Op-Amp block and two Resistor blocks. The Integrator Stage contains an integrator constructed from another Band-Limited Op-Amp block, a Resistor, a Capacitor, and Electrical Reference.
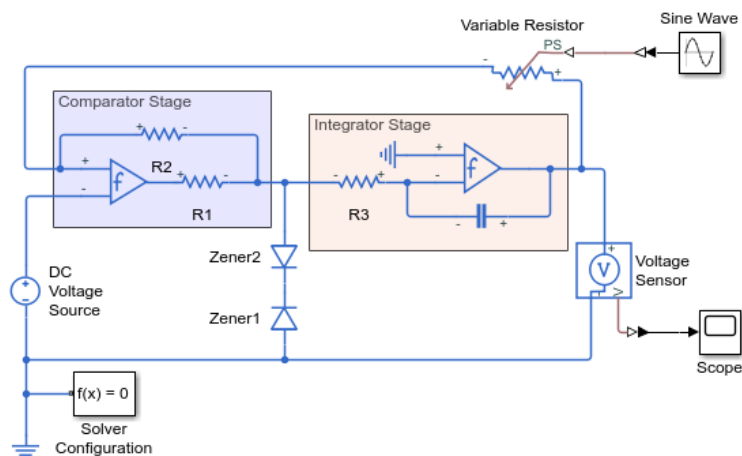
Specify Model Parameters

Specify these parameters to represent the behavior of the system components:

Model Setup Parameters

These blocks specify model information that is not specific to a particular block:

- Solver Configuration
- Electrical Reference

As with Simscape models, you must include a Solver Configuration block in each topologically distinct physical network. This example has a single physical network, so use one Solver Configuration block with the default parameter values.

You must include an Electrical Reference block in each Simscape Electrical network. This block does not have any parameters.

Input Signal Parameters

Generate the sinusoidal control signal using the Sine Wave block.

Set the Sine Wave block parameters as follows:

- **Amplitude** — `0.5e4`
- **Bias** — `1e4`
- **Frequency** — `pi/5e-4`

Triangle Wave Generator Parameters

Configure the blocks modeling the physical system that generates the triangle wave:

- Integrator stage — Band-Limited Op-Amp, Capacitor, and Resistor block R3
- Comparator stage — Band-Limited Op-Amp1, Resistor blocks R1 and R2
- Variable Resistor
- Diode and Diode1
- Simulink-PS Converter and PS-Simulink Converter blocks that bridge the physical part of the model and the Simulink part of the model.

1   Accept the default parameters for the Simulink-PS Converter block. These parameters establish the units of the physical signal at the block output such that they match the expected default units of the Variable Resistor block input.

2   Set the two Band-Limited Op-Amp block parameters for the LM7301 device with a +–20V power supply:

- The datasheet gives the gain as 97 dB, which is equivalent to 10 ^ (97 / 20) = 7.1e4. Set the **Gain, A** parameter to `7.1e4`.
- The datasheet gives input resistance as 39 Mohms. Set **Input resistance, Rin** to `39e6`.
- Set **Output resistance, Rout** to `0` ohms. The datasheet does not quote a value for Rout, but the term is insignificant compared to the output resistor that it drives.
- Set minimum and maximum output voltages to –20 V and +20 V, respectively.
- The datasheet gives the maximum slew rate as 1.25 V/μs. Set the **Maximum slew rate, Vdot** parameter to `1.25e6` V/s.
- Set the bandwidth to `4e6`.

3   Set the two Diode block parameters for a 4.3 V zener diode. To model a BZX384-B4V3, set block parameters as follows:

- On the **Main** tab, set **Diode model** to `Piecewise Linear`. This selects a simplified Zener diode model that is more than adequate to test the correct operation of this circuit.

- Leave the **Forward voltage** as 0.6 V — this is a typical value for most diodes.

- The datasheet gives the forward current as 250 mA when the forward voltage is 1V. So that the Diode block matches this, set the **On resistance** to (1 V – 0.6 V) / 250 mA = `1.6` ohms.

- The datasheet gives the reverse leakage current as 3 µA at a reverse voltage of 1 V. Therefore, set the **Off conductance** to 3 µA / 1 V = `3e-6` S.

- The datasheet gives the reverse voltage as 4.3 V. On the Breakdown tab, set the **Reverse breakdown voltage Vz** to `4.3` V.

- Set the **Zener resistance Rz** to a suitably small number. The datasheet quotes the Zener voltage for a reverse current of 5 mA. For the Diode block to be representative of the real device, the simulated reverse voltage should be close to 4.3V at 5mA. As Rz tends to zero, the reverse breakdown voltage tends to Vz regardless of current, as the voltage-current gradient becomes infinite. However, for good numerical properties, Rz must not be made too small. If, say, you allow a 0.01 V error on the Zener voltage at 5 mA, then Rz is 0.01 V / 5 mA = 2 ohms. Set the **Zener resistance** parameter to this value.

4 The Voltage Sensor block does not have any parameters.

5 Accept the default parameters for the Variable Resistor block. These parameters establish the units of the physical signal at the block output such that they match the expected default units of the Variable Resistor block input.

6 Set the Capacitor block parameters as follows:

- **Capacitance** — `2.5e-9`
- **Capacitor voltage** — `0.08`

   This value starts the oscillation in the feedback loop. It is found in the **Variables** tab.

- **Series resistance** — `0`

7 Set the DC Voltage Source block **Constant voltage**parameter to `0`.

8 Set the Resistor R3 block **Resistance** parameter to `10000`.

9 Set the Resistor R1 block **Resistance** parameter to `1000`.

10 Set the Resistor R2 block **Resistance** parameter to `10000`.

11 Accept the default parameters for the PS-Simulink Converter block. These parameters establish the units of the physical signal at the block output such that they match the expected default units of the Scope block input.

Signal Display Parameters

Specify the parameters of the Scope block to display the triangular output signal.

Double-click the Scope block and then click the **View > Configuration Properties** to open the Scope Configuration Properties dialog box. On the **Logging** tab, clear the **Limit data points to last** check box.

Configure the Solver Parameters

Configure the solver parameters to use a continuous-time solver.Simscape Electrical models only run with a continuous-time solver when the Simscape Solver Configuration block has its **Local Solver**

parameter cleared. You also change the simulation end time, tighten the relative tolerance for a more accurate simulation, and remove the limit on the number of simulation data points Simulink saves.

1   In the model window, select **Modeling > Model Settings** to open the Configuration Parameters dialog box.

2   In the **Solver** category in the tree on the left side of the dialog box:

   - Enter `2000e-6` for the **Stop time** parameter value.
   - Select `ode23t (Mod. stiff/Trapezoidal)` from the **Solver** list.
   - Enter `4e-5` for the **Max step size** parameter value.
   - Enter `1e-6` for the **Relative tolerance** parameter value.

3   In the **Data Import/Export** category in the **Select** tree, clear the **Limit data points to last** check box.

4   Click **OK**.

For more information about configuring solver parameters, see "Simulating an Electronic, Mechatronic, or Electrical Power System" on page 3-8.

Simulate Model and Analyze Results

Run the simulation and plot the results.

In the model window, select **Simulation > Run** to run the simulation.

To view the triangle wave in the Scope window, double-click the Scope block. You can do this before or after you run the simulation.

The following plot shows the voltage waveform. As the resistance of the Variable Resistor block increases, the amplitude of the output waveform increases and the frequency decreases.
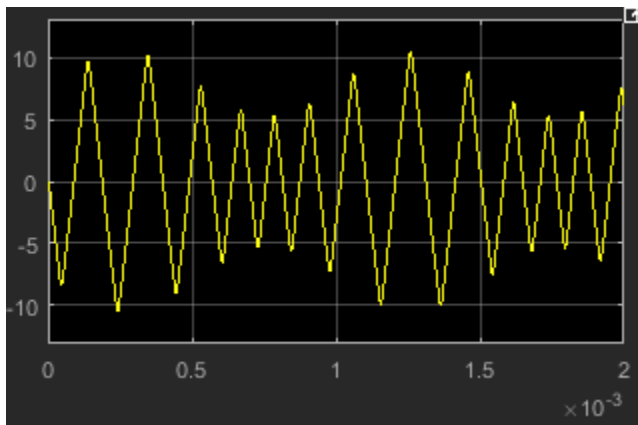
Triangle Waveform Voltage

Modeling and Simulation Basics

Essential Electrical Modeling Techniques

In this section...
"Overview of Modeling Rules" on page 3-2
"Required Blocks" on page 3-3
"Creating a New Model" on page 3-3
"Modeling Instantaneous Events" on page 3-3
"Using Simulink Blocks to Model Physical Components" on page 3-4

Overview of Modeling Rules

Simscape Electrical models are essentially Simscape block diagrams refined for modeling single- and multi-phase electronic, mechatronic, and electrical power systems. Simscape Electrical blocks feature these port types:

- Three-phase ports, which connect the phases of a three-phase electrical system between Simscape Electrical blocks.

  There are two three-phase port types in Simscape Electrical blocks, composite and expanded. You can connect a composite three-phase port only to another composite three-phase port. You can connect the individual electrical conserving ports of an expanded three-phase port only to other electrical conserving ports. For more information, see "Three-Phase Ports" on page 3-5.

- Electrical and mechanical rotational conserving ports ▣, which connect directly to Simscape foundation blocks.

  Each port type has specific Across and Through variables associated with it. To learn about the rules to follow when building an electromechanical model, see "Basic Principles of Modeling Physical Networks".

- Physical signal ports ▷, which connect to Simulink blocks through Simulink-PS Converter and PS-Simulink Converter blocks from the Simscape Utilities library. These blocks convert physical signals to and from Simulink mathematical signals.

Keep these rules in mind when using each port type in Simscape Electrical blocks.

- You can connect physical conserving ports only to other conserving ports of the same type. Electrical conserving ports in Simscape Electrical blocks can connect directly to Simscape electrical components. Mechanical rotational conserving ports in Simscape Electrical blocks can connect directly to Simscape mechanical rotational components.

- The physical connection lines that connect conserving ports are nondirectional lines that carry physical variables (Across and Though variables) rather than signals. You cannot connect physical conserving ports to Simulink ports or to physical signal ports.

- You can branch physical connection lines. When you do so, directly connected components have the same Across variables. The value of any Through variable (e.g., current or torque) transferred along the physical connection line is divided among the multiple components connected by the branches.

  For each Through variable, the sum of the values flowing into a branch point equals the sum of the values flowing out.

- You can connect physical signal ports to other physical signal ports using regular connection lines, similar to Simulink signal connections. These connection lines carry physical signals between Simscape Electrical blocks.

- You can connect physical signal ports to Simulink ports through converter blocks. Use the Simulink-PS Converter block to connect Simulink outports to physical signal inports. Use the PS-Simulink Converter block to connect physical signal outports to Simulink inports.

- Unlike Simulink signals, physical signals can have units. In Simscape Electrical block dialog boxes, you can specify the units along with the parameter values, where appropriate. Use the converter blocks to associate units with an input signal and to specify the desired output signal units.

For an example of these rules applied to an electromechanical model, see "Three-Phase Asynchronous Machine Starting" on page 10-430.

Required Blocks

Each topologically distinct physical network in a diagram requires exactly one Solver Configuration block from the Simscape Utilities library. The Solver Configuration block specifies global environment information for simulation and provides parameters for the solver that your model needs for simulation.

Each electrical network requires an Electrical Reference block. This block establishes the electrical ground for the circuit. Networks with electromechanical blocks also require a Mechanical Rotational Reference block. For more information about using reference blocks, see "Grounding Rules".

Creating a New Model

An easy way to start a new Simscape Electrical model, prepopulated with the required blocks, is to use the Simscape function `ssc_new`. For more information, see "Creating a New Simscape Model".

Another way to start a new model is to use a Simscape template from the Simulink start page. The start page includes model templates that provide you with design patterns for modeling electrical, three-phase electrical, mechanical rotational, and mechanical translational networks using Simscape Electrical. For more information, see "Modeling Analog Circuit Architectures, Mechatronic Systems, and Electrical Power Systems Using Simscape Electrical" on page 1-6.

You can also use the "Creating A New Circuit" example as a template for a new electronic circuit model. This example opens a simple electrical model, prepopulated with some useful blocks, and also opens an Electrical Starter Palette, which contains links to the most often used electrical components. Open the example by typing `ssc_new_elec` in the MATLAB Command Window and use **File > Save As** to save the example model under the desired name. Then delete the unwanted blocks and add new ones from the Electrical Starter Palette and from the block libraries.

Modeling Instantaneous Events

When working with Simscape Electrical, your model may include Simulink blocks that are associated with events or discrete sampling. Such blocks can create instantaneous changes to the physical system inputs through the Simulink-PS Converter block that connects them. When you build this type of model, make sure that the corresponding zero crossings are generated.

Many blocks in the Simulink library generate these zero crossings by default. For example, the Pulse Generator block produces a discrete-time output by default, and generates the corresponding zero

crossings. To generate zero crossings for all Simulink blocks that model instantaneous events, in the Solver Configuration Parameters for the model, expand **Solver details** and in the **Zero crossing options**, for the **Zero crossing control** option, select `Use local settings` or `Enable all`. For more information about zero crossing control, see "Zero-crossing control".

Using Simulink Blocks to Model Physical Components

To run a fast simulation that approximates the behavior of the physical components in a system, you may want to use Simulink blocks to model one or more physical components.

The "Modeling an Integrated Circuit" on page 10-310 example uses Simulink to model a physical component. The 2-Input NOR (Behavioral Model) masked subsystem is a behavioral model, built using Simscape Foundation Library blocks.
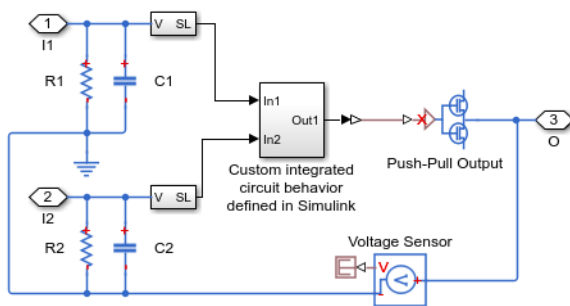
This behavioral model contains a subsystem comprised of Simulink blocks, which implements the custom integrated-circuit behavior.
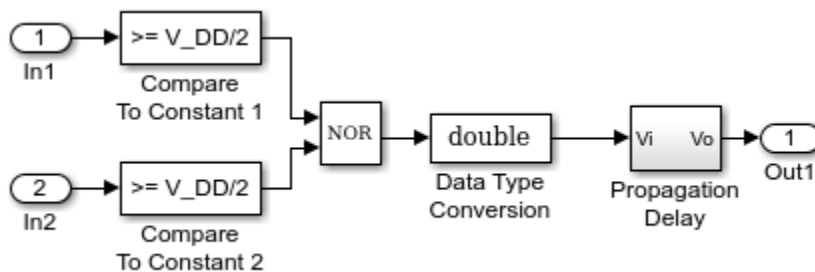
The Simulink Logical Operator block implements the behavioral model of the two-input NOR gate. Using Simulink in this manner introduces algebraic loops, unless you place a lag somewhere between the physical signal inputs and outputs. In this case, a first-order lag is included in the Propagation Delay subsystem to represent the delay due to gate capacitances. For applications where no lag is required, use blocks from the Physical Signals sublibrary in the Simscape Foundation Library to implement the desired functionality.

Three-Phase Ports

In this section...
"About Three-Phase Ports" on page 3-5
"Expand and Collapse Three-Phase Ports on a Block" on page 3-6

About Three-Phase Ports

In Simscape Electrical software, you can connect the phases of a three-phase system between blocks using two types of ports.

- Composite three-phase port
- Expanded three-phase port

Composite three-phase ports represent three individual electrical conserving ports with a single block port. You can use composite three-phase ports to build models that correspond to single-line diagrams of three-phase electrical systems. Instead of explicitly connecting each phase of the three-phase system between blocks, you connect all three phases using a single port. You can connect composite three-phase ports only to other composite three-phase ports.

Expanded three-phase ports represent the individual phases of a three-phase system using three separate electrical conserving ports. You individually connect each phase of the three-phase system between blocks. Electrical conserving ports can connect directly to electrical components from the Simscape and Simscape Electrical libraries.

Composite three-phase ports produce results with the same fidelity as expanded three-phase ports. Both connection methods consider instantaneous phase voltages and currents and are suitable for modeling balanced and unbalanced three-phase electrical power systems. Each electrical conserving port in an expanded three-phase port has a Through variable of scalar current and an Across variable of scalar voltage. For a composite three-phase port, the Through variable is a three-element current, and the Across variable is a three-element voltage.

You can use the Phase Splitter block to expand a composite three-phase port into separate electrical conserving ports. The separate electrical ports can then connect to Simscape Electrical electrical components.

The figure shows two simple circuits that contrast the composite and expanded connection methods. The two circuits produce the same results.
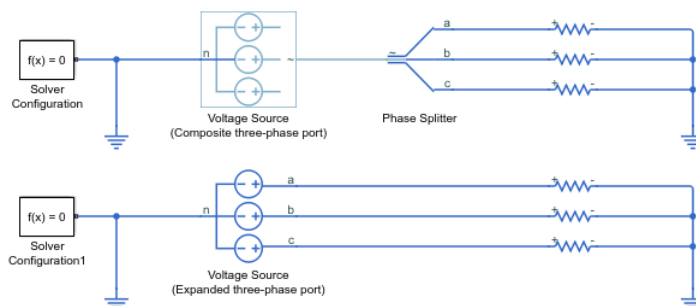
The top circuit uses a Voltage Source block with a composite three-phase port ~. The bottom circuit uses a Voltage Source block with expanded electrical conserving ports **a**, **b**, and **c**. In each circuit, the instantaneous phase voltages and currents are the same.

Expand and Collapse Three-Phase Ports on a Block

Simscape Electrical blocks that have composite three-phase ports have an option to switch between composite and expanded ports.

- Right-click the block. On the **Simscape block choices** context menu, select **Expanded three-phase ports** or **Composite three-phase ports**.

  For blocks with a single composite port **~**, the expanded electrical ports are labeled **a**, **b**, and **c**. For blocks with more than one composite port **~1** and **~2**, the expanded electrical ports are labeled **a1**, **b1**, **c1** and **a2**, **b2**, **c2**.

Switch Between Physical Signal and Electrical Ports

Some Simscape Electrical blocks have an option to switch certain ports between physical signal and electrical conserving ports. An electrical conserving port is a Simscape physical conserving port that has a Through variable of current and an Across variable of voltage. For a comparison of Simscape physical signal and physical conserving ports, see "Connector Ports and Connection Lines".

To switch a block connection port between an electrical conserving port ◘ and a physical signal port ▷:

1 Right-click the block.
2 On the **Simscape > Block choices** context menu, select a variant that includes the term **Electrical**, for an electrical conserving port, or **PS** for a physical signal port.

Simulating an Electronic, Mechatronic, or Electrical Power System

In this section...
"Selecting a Solver" on page 3-8
"Specifying Simulation Accuracy/Speed Tradeoff" on page 3-8
"Avoiding Simulation Issues" on page 3-9
"Running a Time-Domain Simulation" on page 3-9
"Running a Small-Signal Frequency-Domain Analysis" on page 3-9

Selecting a Solver

Simscape Electrical software supports all of the continuous-time solvers that Simscape supports. For more information, see "Setting Up Solvers for Physical Models".

You can select any of the supported solvers for running a simulation of an electronic model. The variable-step solvers, `ode23t` and `ode15s`, are recommended for most applications because they run faster and work better for systems with a range of both fast and slow dynamics. The `ode23t` solver is closest to the solver that SPICE traditionally uses.

To use Simulink Coder software to generate standalone C or C++ code from your model, you must use the `ode14x` or `ode1be` solvers. For more information about code generation, see "Code Generation".

Specifying Simulation Accuracy/Speed Tradeoff

To trade off accuracy and simulation time, adjust one or more of the following parameters:

• **Relative tolerance** in the Simulink Configuration Parameters dialog box

• **Absolute tolerance** in the Simulink Configuration Parameters dialog box

• **Max step size** in the Simulink Configuration Parameters dialog box

• **Consistency Tolerance** in the Solver Configuration block dialog box

In most cases, the default tolerance values produce accurate results without sacrificing unnecessary simulation time. The parameter value that is most likely to be inappropriate for your simulation is **Max step size**, because the default value, `auto`, depends on the simulation start and stop times rather than on the amount by which the signals are changing during the simulation. If you are concerned about the solver missing significant behavior, change the parameter to prevent the solver from taking too large a step.

The Simulink documentation describes the following parameters in more detail and provides tips on how to adjust them:

• "Relative tolerance"

• "Absolute tolerance"

• "Max step size"

The Solver Configuration block reference page in the Simscape documentation explains when to adjust the **Consistency Tolerance** parameter value.

Avoiding Simulation Issues

If you experience a simulation issue, first read "Troubleshooting Simulation Errors" to learn about general troubleshooting techniques.

There are a few techniques you can apply to any Simscape Electrical model to overcome simulation issues:

- Add parasitic capacitors and/or resistors (specifically, junction capacitance and ohmic resistance) to the circuit to avoid numerical issues. The "Astable Oscillator" on page 10-304 example uses these devices.
- Adjust the current and voltage sources so they start at zero and ramp up to their final values rather than starting at nonzero values.

To learn about avoiding simulation errors in the presence of specific Simscape Electrical model configurations, see "Modeling Instantaneous Events" on page 3-3 and "Using Simulink Blocks to Model Physical Components" on page 3-4.

Running a Time-Domain Simulation

When you run a time-domain simulation, Simscape Electrical software uses the Simscape solver to analyze the physical system in the Simulink environment. For more information, see "How Simscape Simulation Works".

Running a Small-Signal Frequency-Domain Analysis

You can perform small-signal analysis for Simscape and Simscape Electrical models using linearization capabilities of Simulink software. For more information, see "Linearize an Electronic Circuit".

See Also
Solver Configuration

More About
- "How Simscape Simulation Works"
- "Trimming and Linearization"
- "Troubleshooting"

Selecting the Output Model for Logic Blocks

In this section...
"Available Output Models" on page 3-10
"Quadratic Model Output and Parameters" on page 3-11

Available Output Models

The blocks in the Logic sublibrary of the Integrated Circuits library provide a choice of two output models:

- Linear — Models the gate output as a voltage source driving a series resistor and capacitor connected to ground. This is suitable for logic circuit operation under normal conditions and when the logic gate drives other high-impedance CMOS gates. The block sets the value of the gate output capacitor such that the resistor-capacitor time constant equals the **Propagation delay** parameter value. The linear output model is shown in the following illustration.
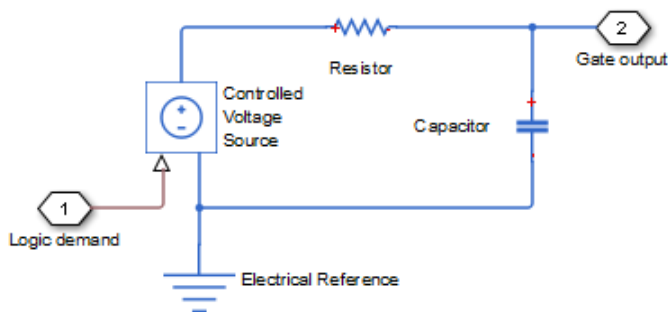
- Quadratic — Models the gate output in terms of a complementary N-channel and P-channel MOSFET pair. This adds more fidelity, which becomes relevant if drawing higher currents from the gate output, or if exercising the gate under fault conditions. In addition, the gate input demand is lagged to approximate the **Propagation delay** parameter value. Default parameters are representative of the 74HC logic gate family. The quadratic output model is shown in the next illustration.
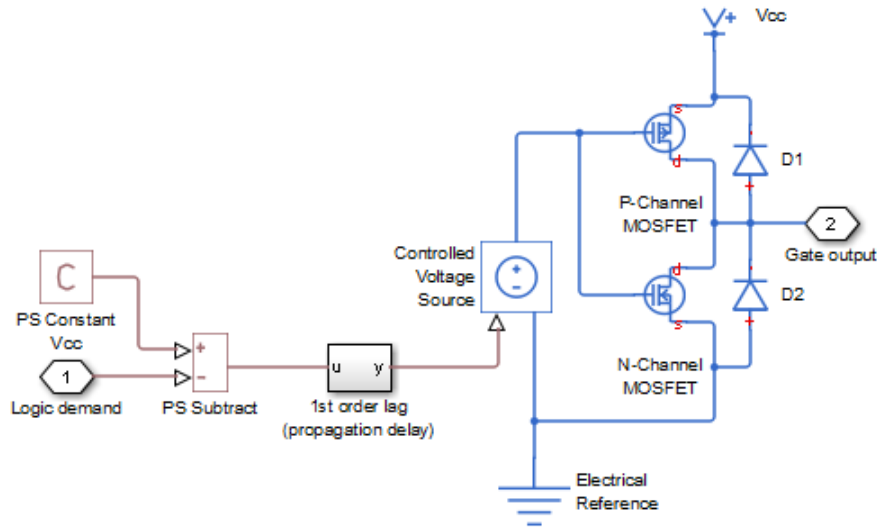
Use the **Output current-voltage relationship** parameter on the **Outputs** tab of the block dialog box to specify the output model.

For most system models, MathWorks recommends selecting the linear option because it supports faster simulation. If necessary, you can use the more detailed output model to validate simulation results obtained from the simpler model.

Quadratic Model Output and Parameters

If you select the quadratic model, use the following parameters to control the block output:

- **Supply voltage** — Supply voltage value (Vcc) applied to the gate in your circuit. The default value is 5 V.

- **Measurement voltage** — The gate supply voltage for which mask data output resistances and currents are defined. The default value is 5 V.

- **Logic HIGH output resistance at zero current and at I_OH** — A row vector [R_OH1 R_OH2] of two resistance values. The first value R_OH1 is the gradient of the output voltage-current relationship when the gate is logic HIGH and there is no output current. The second value R_OH2 is the gradient of the output voltage-current relationship when the gate is logic HIGH and the output current is I_OH. The default value is [25 250] Ω.

- **Logic HIGH output current I_OH when shorted to ground** — The resulting current when the gate is in the logic HIGH state, but the load forces the output voltage to zero. The default value is 63 mA.

- **Logic LOW output resistance at zero current and at I_OL** — A row vector [R_OL1 R_OL2] of two resistance values. The first value R_OL1 is the gradient of the output voltage-current relationship when the gate is logic LOW and there is no output current. The second value R_OL2 is the gradient of the output voltage-current relationship when the gate is logic LOW and the output current is I_OL. The default value is [30 800] Ω.

- **Logic LOW output current I_OL when shorted to Vcc** — The resulting current when the gate is in the logic LOW state, but the load forces the output voltage to the supply voltage Vcc. The default value is -45 mA.

- **Propagation delay** — Time it takes for the output to swing from LOW to HIGH or HIGH to LOW after the input logic levels change. For quadratic output, it is implemented by the lagged gate input demand. The default value is 25 ns.

- **Protection diode on resistance** — The gradient of the voltage-current relationship for the protection diodes when forward biased. The default value is 5 Ω.

- **Protection diode forward voltage** — The voltage above which the protection diode is turned on. The default value is 0.6 V.

The following graphic illustrates the quadratic output model parameterization, using the default parameter output characteristics for a +5V supply.
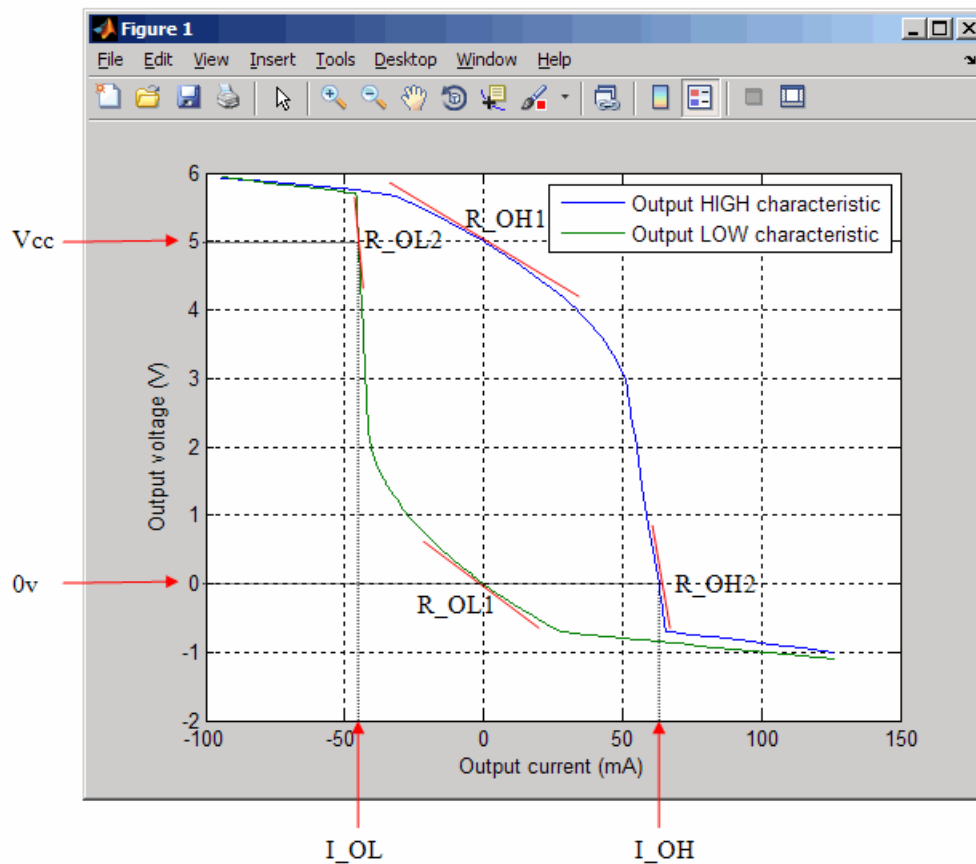
Parameterizing Blocks from Datasheets

Simscape Electrical is a system-level simulation tool that provides blocks with a commensurate level of fidelity. Block parameters are designed, where possible, to match the data found on manufacturer datasheets. For example, the bipolar transistor blocks support parameterization in terms of the small-signal quantities quoted on a datasheet, and the underlying model is simpler than models typically used by specialist EDA simulation tools. The smaller number of parameters and simpler underlying models can support MATLAB system performance analysis better, and thus support design choices. Following system design, you can perform validation in hardware or more detailed modeling and validation using an EDA simulation tool.

The following parameterization examples illustrate various block parameterization techniques:

- Example 1: "Parameterize a Piecewise Linear Diode Model from a Datasheet" on page 3-14
- Example 2: "Parameterize an Exponential Diode from a Datasheet" on page 3-17
- Example 3: "Parameterize an Exponential Diode from SPICE Netlist" on page 3-21
- Example 4: "Parameterize an Op-Amp from a Datasheet" on page 3-25

Most of the time, datasheets should be a sufficient source of parameters for Simscape Electrical blocks (see Examples 1 on page 3-14, 2 on page 3-17, and 4 on page 3-25). Sometimes, there is need for more information than is available on the datasheet, and data can be augmented from a manufacturer SPICE netlist. For example, circuit performance may depend on one or two critical components, and increased accuracy is needed either for parameter values or the underlying model. Simscape Electrical libraries contain a SPICE-compatible sublibrary to support this case, as is illustrated by Example 3 on page 3-21. If you have many components that need to be modeled to a high level of accuracy, then Simulink cosimulation with a specialist circuit simulator may be a better option.

You can also use the SPICE conversion assistant to convert SPICE components into Simscape equivalents. For more information, see "Converting a SPICE Netlist to Simscape Blocks" on page 3-55

In mechatronic applications in particular, you may need to model input-output behavior of integrated circuits, such as PWM waveform generators and H-bridges. For these two examples, Simscape Electrical libraries contain abstracted-behavior equivalent blocks that you can use. Where you need to model other devices, possible options include creating your own abstracted model using the Simscape language, or using Simulink blocks. For an example of using Simulink blocks, see the "Modeling an Integrated Circuit" on page 10-310 example.

When looking for a datasheet, make sure that you have the originating manufacturer datasheet because some resellers abbreviate them.

For additional ways to parameterize and validate your model, see "Additional Parameterization Workflows" on page 3-27.

Parameterize a Piecewise Linear Diode Model from a Datasheet

The "Triangle Wave Generator" on page 10-302 example model, also described in "Triangle Wave Generator Model" on page 2-16, contains two zener diodes that regulate the maximum output voltage from an op-amp amplifier circuit. Each of these diodes is implemented with the Simscape Electrical Diode block, parameterized using the `Piecewise Linear` option. This simple model is sufficient to check correct operation of the circuit, and requires fewer parameters than the `Exponential` option of the Diode block. However, when specifying the parameters, you need to take into account the bias condition that will be used in the circuit. This example explains how to do this.

The Phillips Semiconductors datasheet for a BZX384–B4V3 gives the following data:

Working voltage, V_Z(V) at I_{Ztest} = 5 mA	4.3
Diode capacitance, C_d(pF)	450
Reverse current, I_R(µA) at V_R = 1 V	3
Forward voltage, V_F(V) at I_F = 5 mA	0.7

In the datasheet, the tabulated values for V_F are for higher forward currents. This value of 0.7V at 5mA is extracted from the datasheet current-voltage curve, and is chosen as it matches the zener current used when quoting the working voltage of 4.3V.

To match the datasheet values, the example sets the piecewise linear diode block parameters as follows:

- **Forward voltage**. Leave as default value of 0.6V. This is a typical value for most diodes, and the exact value is not critical. However, it is important that the value set is taken into account when calculating the **On resistance** parameter.

- **On resistance**. This is set using the datasheet information that the forward voltage is 0.7V when the current is 5mA. The voltage to be dropped by the **On resistance** parameter is 0.7V minus the **Forward voltage** parameter, that is 0.1V. Hence the **On resistance** is 0.1V / 5mA = 20 Ω.

- **Off conductance**. This is set using the datasheet information on reverse current. The reverse current is 3µA for a reverse voltage of 1V. Hence the **Off conductance** should be set to 3µA / 1V = 3e-6 S.

- **Reverse breakdown voltage**. This parameter should be set to the datasheet working voltage parameter, 4.3V.

- **Zener resistance**. This needs to be set to a suitable small number. Too small, and the voltage-current relationship becomes very steep, and simulation convergence may not be as efficient. Too large, and the zener voltage will be incorrect. For the Diode block to be representative of the real device, the simulated reverse voltage should be close to 4.3V at 5mA (the reverse bias current provided by the circuit). Allowing a 0.01 V error on the zener voltage at 5mA, the zener resistance R_Z will be 0.01V / 5mA = 2 Ω.

- **Junction capacitance**. This parameter is set to the datasheet diode capacitance value, 450 pF.

Block Parameters: Diode ✕

Diode

This block represents a diode. Use the Diode model parameter to select one of the following model types:

[1] Piecewise Linear Diode. This option invokes the diode model from the Simscape Foundation Library.

[2] Exponential Diode. Uses the standard exponential diode equation I = Is*(exp(V/(N*Vt))-1) where Is is the Saturation current, Vt is the thermal voltage, and N is the emission coefficient (>=1). Vt is given by Vt = k*T/e where k is Boltzmann's constant, T is the absolute Temperature of the p-n junction, and e is the magnitude of charge on an electron.

For finite Reverse breakdown voltage, the diode will exhibit reverse breakdown characteristics.

Right-click on the block and select Simscape > Block choices to access variant implementations of the block.

Source code

Settings

| Main | Breakdown | Capacitance | Variables |

Capacitance: Fixed or zero junction capacitance   ▼

Junction capacitance: 450   pF   ⌄

Charge dynamics: Do not model charge dynamics   ▼

OK   Cancel   Help   Apply

Parameterize an Exponential Diode from a Datasheet

Example 1 on page 3-14 uses a piecewise linear approximation to the diode's exponential current-voltage relationship. This results in more efficient simulation, but requires some thought to go into the setting of block parameter values. An alternative is to use a more complex model that is valid for a wider range of voltage and current values. This example uses the `Exponential` parameterization option of the Diode block.

This model either requires two data points from the diode current-voltage relationship, or values for the underlying equation coefficients, namely the saturation current IS and the emission coefficient N. The BZX384-B4V3 datasheet only provides values for the former case. Some datasheets do not give the necessary data for either case, and you must follow the processes in Example 1 on page 3-14 or Example 3 on page 3-21 instead.

The two data points in the table below are from the BZX384-B4V3 datasheet current-voltage curve:

Diode forward voltage, V_F	0.7V	1V
Diode forward current, I_F	5mA	250mA

Set the exponential diode block parameters as follows:

- **Currents [I1 I2]**. Set to [5 250] mA.
- **Voltages [V1 V2]**. Set to [0.7 1.0] V.
- **Reverse breakdown voltage**. Set to the datasheet working voltage value, 4.3V.
- **Ohmic resistance, RS**. Set to 0.01 Ω. This is an example of a parameter that cannot be determined from the datasheet. However, setting its value to zero is not necessarily a good idea, because a small value can help simulation convergence for some circuit topologies. Physically, this term will not be zero because of the connection resistances.
- **Junction capacitance**. Set to the datasheet diode capacitance value, 450 pF.

  A more complex capacitance model is also available for the Diode component with the exponential equation option. However, the datasheet does not provide the necessary data. Moreover, the operation of this circuit is not sufficiently sensitive to voltage-dependent capacitance effects to warrant the extra detail.

Block Parameters: Diode ✕

Diode

This block represents a diode. Use the Diode model parameter to select one of the following model types:

[1] Piecewise Linear Diode. This option invokes the diode model from the Simscape Foundation Library.

[2] Exponential Diode. Uses the standard exponential diode equation $I = Is*(exp(V/(N*Vt))-1)$ where Is is the Saturation current, Vt is the thermal voltage, and N is the emission coefficient (>=1). Vt is given by $Vt = k*T/e$ where k is Boltzmann's constant, T is the absolute Temperature of the p-n junction, and e is the magnitude of charge on an electron.

For finite Reverse breakdown voltage, the diode will exhibit reverse breakdown characteristics.

Right-click on the block and select Simscape > Block choices to access variant implementations of the block.

Source code

Settings

| Main | Breakdown | Capacitance | Temperature Dependence | Variables |

Diode model:	Exponential	▼
Parameterization:	Use two I-V curve data points	▼
Currents [I1 I2]:	[5 250]	mA ⌄
Voltages [V1 V2]:	[0.7 1.0]	V ⌄
Ohmic resistance, RS:	0.01	Ohm ⌄
Measurement temperature:	25	degC ⌄
Number of series diodes:	1	
Number of parallel diodes:	1	

OK   Cancel   Help   Apply

Block Parameters: Diode

Diode

This block represents a diode. Use the Diode model parameter to select one of the following model types:

[1] Piecewise Linear Diode. This option invokes the diode model from the Simscape Foundation Library.

[2] Exponential Diode. Uses the standard exponential diode equation I = Is*(exp(V/(N*Vt))-1) where Is is the Saturation current, Vt is the thermal voltage, and N is the emission coefficient (>=1). Vt is given by Vt = k*T/e where k is Boltzmann's constant, T is the absolute Temperature of the p-n junction, and e is the magnitude of charge on an electron.

For finite Reverse breakdown voltage, the diode will exhibit reverse breakdown characteristics.

Right-click on the block and select Simscape > Block choices to access variant implementations of the block.

Source code

Settings

| Main | Breakdown | Capacitance | Temperature Dependence | Variables |

Reverse breakdown voltage: 4.3   V

OK   Cancel   Help   Apply

Parameterize an Exponential Diode from SPICE Netlist

If a datasheet does not provide all of the data required by the component model, another source is a SPICE netlist for the component. Components are defined by a particular type of SPICE netlist called a subcircuit. The subcircuit defines the coefficients for the defining equations. Most component manufacturers make subcircuits available on their websites. The format is ASCII, and you can directly read off the parameters. The BZX384-B4V3 subcircuit can be obtained from Philips Semiconductors.

The subcircuit data can be used to parameterize the Simscape Electrical Diode block either in conjunction with the datasheet, or on its own. For example, the Ohmic resistance is defined in the subcircuit as RS = 0.387, thus providing the missing piece of information in Example 2 on page 3-17.

An alternative workflow is to use the Simscape Electrical Additional Components/SPICE Semiconductors sublibrary. The SPICE Diode block in this sublibrary can be directly parameterized from the subcircuit by setting:

- **Saturation current, IS** to `1.033e-15`
- **Ohmic resistance, RS** to `0.387`
- **Emission coefficient, N** to `1.001`
- **Zero-bias junction capacitance, CJO** to `2.715e-10`
- **Junction potential, VJ** to `0.7721`
- **Grading coefficient, M** to `0.3557`
- **Capacitance coefficient, FC** to `0.5`
- **Reverse breakdown current, IBV** to `0.005`
- **Reverse breakdown voltage, BV** to `4.3`

Note that where there is a one-to-one correspondence between subcircuit parameters and datasheet values, the numbers often differ. One reason for this is that datasheet values are sometimes given for maximum values, whereas subcircuit values are normally for nominal values. In this example, the CJO value of 271.5 pF differs from the datasheet capacitance of 450 pF at zero bias for this reason.
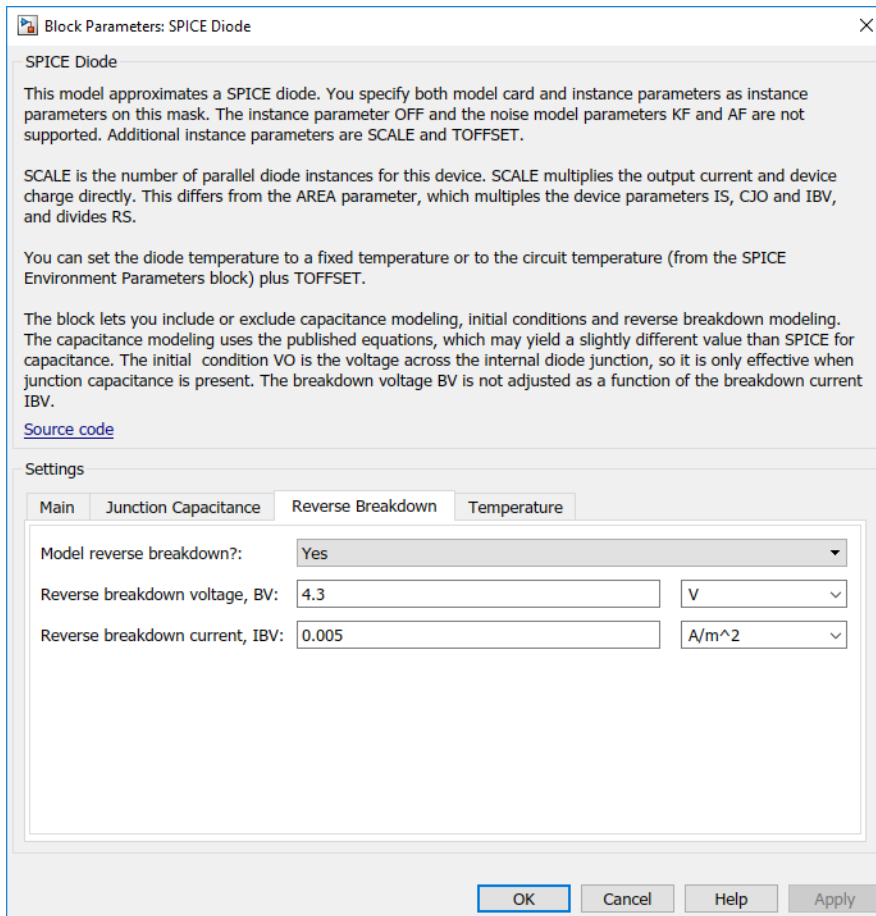
Parameterize an Op-Amp from a Datasheet

The "Triangle Wave Generator" on page 10-302 example model, also described in "Triangle Wave Generator Model" on page 2-16, contains two op-amps, parameterized based on a datasheet for an LM7301. The National Semiconductor datasheet gives the following data for this device:

Gain	97dB = 7.1e4
Input resistance	39MΩ
Slew rate	1.25V/µs
Bandwidth	4MHz

The Band-Limited Op-Amp and Finite-Gain Op-Amp blocks have been designed to work from manufacturer datasheets. Implementing detailed op-amp device models, derived from manufacturer SPICE netlist models, is not recommended, because it provides more accuracy than is typically warranted and slows down simulations. The simple parameterization of the Simscape Electrical op-amp blocks allows you to determine the sensitivity of your circuit to abstracted performance values, such as maximum slew rate and bandwidth. Because of this behavior-based parameterization, you can determine which specification of op-amp is required for a given application. A circuit designer can later match these behavioral parameters, determined from the model, against specific op-amp devices.

Based on the datasheet values above, set the Band-Limited Op-Amp block parameters as follows:

- **Gain** set to `7.1e4`
- **Input resistance, Rin** set to 39e6Ω
- **Output resistance, Rout** set to zero. The value is not defined, but will be small compared to the 1000Ω load seen by the op-amp.
- **Minimum output, Vmin** set to the negative supply voltage, `-20`V in this model
- **Maximum output, Vmax** set to the positive supply voltage, `20`V in this model
- **Maximum slew rate, Vdot** set to `1.25/1e-6` V/s
- **Bandwidth, f** set to `4e6` Hz

Note that these parameters correspond to the values for +-5 volt operation. The datasheet also gives values for +-2.2V and +-30V operation. It is usually better to pick values for a supply voltage below what your circuit uses, because performance is worse at lower voltages; for example, the gain is less, and the input impedance is less. You can use the variation in op-amp parameters with supply voltage to suggest a typical range of parameter values for which you should check the operation of your circuit.

Block Parameters: Band-Limited Op-Amp

Band-Limited Op-Amp

This block models a band-limited op-amp. If the voltages at the positive and negative pins are denoted Vp and Vm, then the output voltage is given by

Vout = A*(Vp-Vm)*1/(s/(2*pi*f)+1)-Iout*Rout

where A is the gain, Rout is the output resistance, Iout is the output current, s is the Laplace operator, and f is the 3dB bandwidth. The input current is given by (Vp-Vm)/Rin where Rin is the input resistance. The no-load output voltage is limited the range Vmin to Vmax, and the slew rate is limited to +-Vdot.

The Initial output voltage, V0, sets the initial op-amp output voltage. Note that this does not take account of any voltage drop across Rout. The initial condition is not used if you select the Start simulation from steady state option in the Solver Configuration block.

Settings

Parameters

Gain, A:	7.1e4	
Input resistance, Rin:	39e6	Ohm
Output resistance, Rout:	0	Ohm
Minimum output, Vmin:	-20	V
Maximum output, Vmax:	20	V
Maximum slew rate, Vdot:	1.25/1e-6	V/s
Bandwidth, f:	4e6	Hz
Initial output voltage, V0:	0	V

OK   Cancel   Help   Apply

Additional Parameterization Workflows

In this section...
"Validation Using Data from SPICE Tool" on page 3-27
"Parameter Tuning Against External Data" on page 3-27
"Building an Equivalent Model of a SPICE Netlist" on page 3-27

Validation Using Data from SPICE Tool

You can validate a parameterized Simscape Electrical component by comparing its behavior to the data from a specialist circuit simulation tool that uses a manufacturer SPICE netlist. Make sure to create a test harness for the component that validates the data across relevant operating points and frequencies.

Parameter Tuning Against External Data

If you have lab measurements of the device, or data from another simulation environment, you can use this to tune the parameters of the equivalent Simscape Electrical component. For an example of parameter tuning, see "Solar Cell Parameter Extraction From Data" on page 10-316.

Building an Equivalent Model of a SPICE Netlist

In "Parameterize an Exponential Diode from SPICE Netlist" on page 3-21, parameterization from a SPICE netlist is relatively straightforward because the netlist defines a single device (the diode) plus the corresponding model card (the parameters). Conversely, a netlist for an op-amp may have more than ten devices, plus supporting model cards. In principle, it is possible to build your own equivalent model of a more complex device by using the SPICE-compatible blocks in the **Simscape** > **Electrical** > **Additional Components** library. Connect the components together using the information in the netlist. Before embarking on this, make sure that the Additional Components sublibraries contain all the SPICE-compatible component models you need.

If the device models you wish to model are complex (hundreds of components), then cosimulation with an external circuit simulator may be a better approach.

See Also
SPICE Diode

Simulating Thermal Effects in Rotational and Translational Actuators

In this section...
"Using the Thermal Ports" on page 3-28
"Thermal Model for Actuator Blocks" on page 3-29

Using the Thermal Ports

All blocks that represent rotational and translational actuators with electrical windings can optionally show a thermal port for each electrical winding. So, for example:

- A DC Motor block can optionally show a single thermal port corresponding to the armature
- A Shunt Motor block can optionally show two thermal ports, one for the stator winding and one for the field winding

The thermal port represents copper resistance losses which convert electrical power to heat. These losses are sometimes referred to as i^2R losses. The thermal ports do not represent iron losses due to, for example, Eddy currents and hysteresis.

The thermal ports are hidden by default. To expose the thermal port on a particular block instance in your block diagram:

1 Right-clicking the block where you want to show the thermal port.

2 Selecting **Simscape** > **Block choices** > **Show thermal port**.
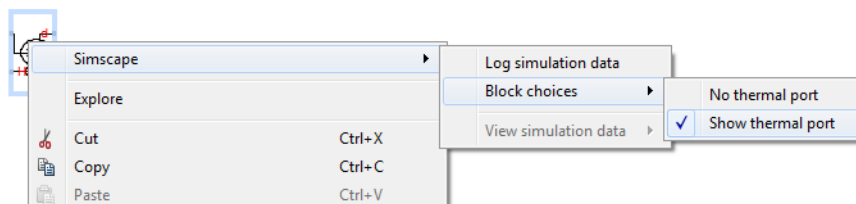
When the thermal port is exposed, the block dialog box contains two additional tabs, **Temperature Dependence** and **Thermal Port**. For actuator blocks with single winding, these tabs always contain the same set of parameters.
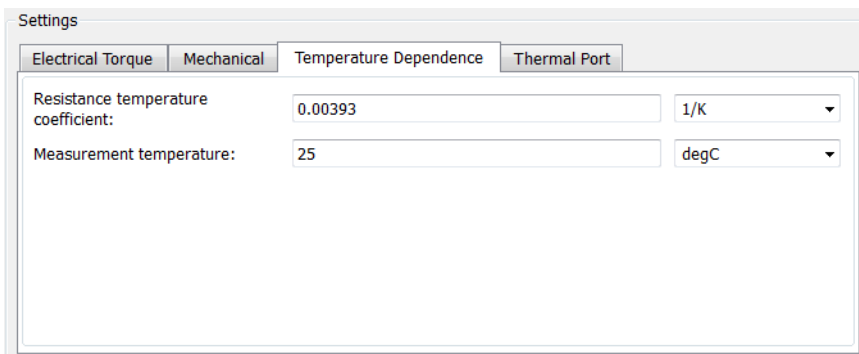
- **Resistance temperature coefficient** — Parameter α in the equation defining resistance as a function of temperature, as described in "Thermal Model for Actuator Blocks" on page 3-29. The default value is for copper, and is `0.00393` 1/K.

- **Measurement temperature** — The temperature for which motor parameters are defined. The default value is 25 °C.

Settings

| Electrical Torque | Mechanical | Temperature Dependence | **Thermal Port** |

| Thermal mass: | 100 | J/K |
| Initial temperature: | 25 | degC |
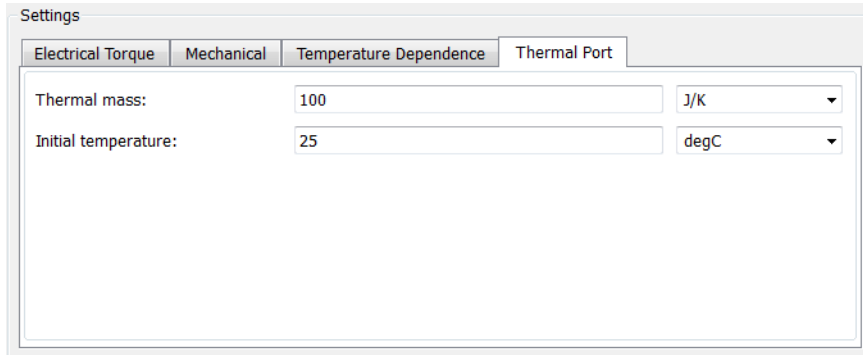
- **Thermal mass** — Thermal mass of the electrical winding, defined as the energy required to raise the temperature by one degree. The default value is `100` J/K.

- **Initial temperature** — The temperature of the thermal port at the start of simulation. The default value is 25 °C.

For more information on selecting the parameter values, see "Thermal Model for Actuator Blocks" on page 3-29.

Parameters for actuator blocks with two windings differ, and are described on the respective block reference pages.

Thermal Model for Actuator Blocks

The following illustration shows the thermal port model used by the actuator blocks. The heat generated by the copper windings is provided as an input to the S physical signal input port of the Ideal Heat Flow Source. The thermal mass represents the lumped thermal mass of the copper winding where thermal mass is defined as the energy required to raise its temperature by one degree. If the mass is denoted M and the specific heat capacity is c_p, then thermal mass is $M \cdot c_p$.
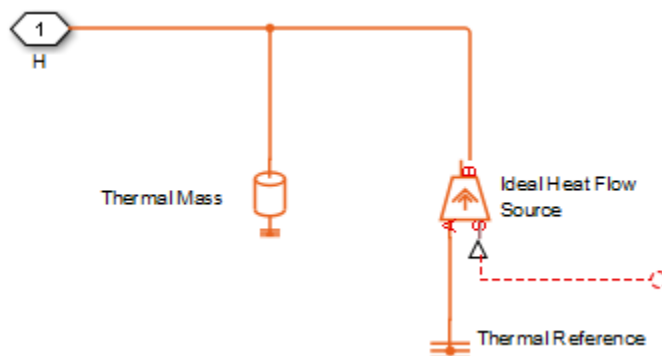
Winding resistance is assumed linearly dependent on temperature, and is given by:

$R = R_0 \left(1 + \alpha \left(T - T_0\right)\right)$

where:

- R is the resistance at temperature T.
- R_0 is the resistance at the measurement (or reference) temperature T_0.
- α is the resistance temperature coefficient. A typical value for copper is 0.00393/K.

Simulating Thermal Effects in Semiconductors

In this section...
"Using the Thermal Ports" on page 3-31
"Cauer Thermal Model" on page 3-32
"Foster Thermal Model" on page 3-33
"External Thermal Model" on page 3-34
"Thermal Mass Parameterization" on page 3-34
"Electrical Behavior Depending on Temperature" on page 3-35
"Improving Numerical Performance" on page 3-35
"Model Thermal Losses for a Rectifier" on page 3-36

Thermal modeling provides data that helps you to estimate cooling requirements for your system by using the thermal ports. Some of the blocks in the Simscape Electrical Semiconductors & Converters library have thermal variants that allow you to determine device temperatures by simulating heat generation. For example, the IGBT (Ideal, Switching) block, which models a three-terminal semiconductor device, has thermal variants that can simulate the heat generated by switching events and conduction losses.

For more information on selecting the parameter values, see "Improving Numerical Performance" on page 3-35. For explanation of the relationship between the **Thermal Port** and **Temperature Dependence** tabs in a block dialog box, see "Electrical Behavior Depending on Temperature" on page 3-35.

Using the Thermal Ports

Certain Simscape Electrical blocks, such as the blocks in the Semiconductors & Converters library, contain an optional thermal port that is hidden by default. If you want to simulate the generated heat and device temperature, expose the thermal port by:

1 Right-clicking the block where you want to show the thermal port.

2 Selecting **Simscape** > **Block choices** > **Show thermal port**.
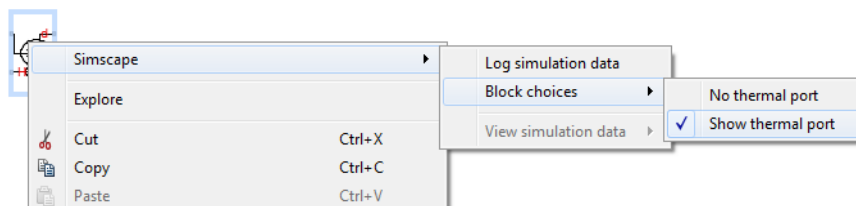
When the thermal port is exposed, the Block Parameters window for that block contains an additional tab, **Thermal Port**. Which parameters are visible depend on the value you set for the **Thermal network** parameter:

• "Cauer Thermal Model" on page 3-32

• "Foster Thermal Model" on page 3-33

- "External Thermal Model" on page 3-34

All blocks with optional thermal ports include an optional internal thermal model to keep your diagram uncluttered.

Cauer Thermal Model

This figure shows an equivalent model of the internal Cauer thermal model for semiconductor devices.
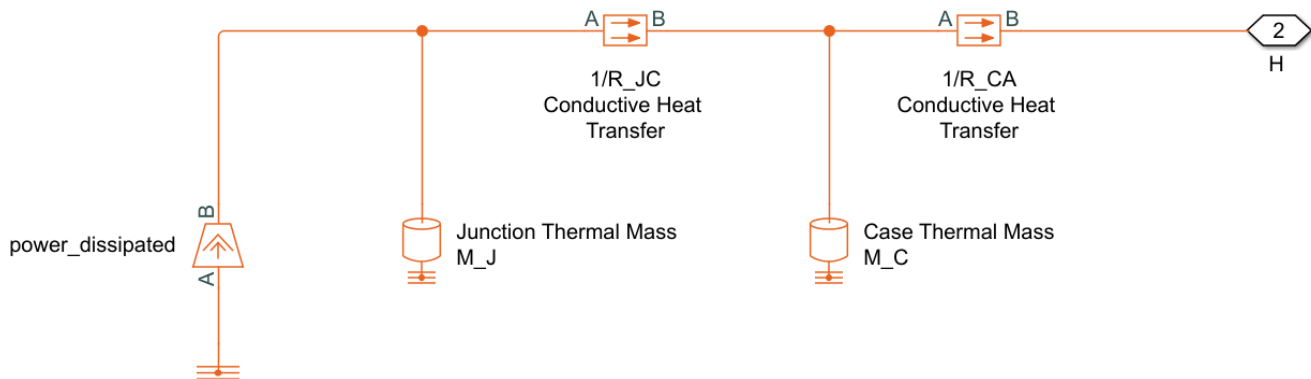
Port H corresponds to thermal port **H** of the block. The two Thermal Mass blocks represent the thermal mass of the device case and the thermal mass of the semiconductor junction, respectively. The Heat Flow Rate Source block (called power_dissipated in the diagram) inputs heat to the model with a value equal to the electrically generated heat from the device.

The two Conductive Heat Transfer blocks model the thermal resistances. Resistance R_JC (conductance 1/R_JC) represents the thermal resistance between junction and case. Because of this resistance, the junction will be hotter than the case under normal conditions. Resistance R_CA represents the thermal resistance between port **H** and the device case. If the device has no heat sink, then you should connect port **H** to a Temperature Source block with its temperature set to ambient conditions. If your device does have an external heat sink, then you must model the heat sink externally to the device and connect the heat sink thermal mass directly to port **H**.

If you choose to simulate the internal thermal network of the block through Cauer model, the following parameters will be visible:

- **Junction case and case-ambient (or case-heatsink) thermal resistances, [R_JC R_CA]** — A row vector [$R\_JC \; R\_CA$] of two thermal resistance values, represented by the two Conductive Heat Transfer blocks. The first value, R_JC, is the thermal resistance between the junction and the case. The second value, R_CA, is the thermal resistance between port **H** and the device case. The default value is [0 10] K/W.

- **Thermal mass parameterization** — Select whether you want to parameterize the thermal masses in terms of thermal time constants (By thermal time constants), or specify the thermal mass values directly (By thermal mass). For more information, see "Thermal Mass Parameterization" on page 3-34. The default is By thermal time constants.

- **Junction and case thermal time constants, [t_J t_C]** — A row vector [$t\_J \; t\_C$] of two thermal time constant values. The first value, t_J, is the junction time constant. The second value, t_C, is the case time constant. To enable this parameter, set the **Thermal mass parameterization** to By thermal time constants. The default value is [0 10] s.

- **Junction and case thermal masses, [M_J M_C]** — A row vector [M_J M_C] of two thermal mass values. The first value, M_J, is the junction thermal mass. The second value, M_C, is the case thermal mass. To enable this parameter, set the **Thermal mass parameterization** to By thermal mass. The default value is [0 1] J/K.

- **Junction and case initial temperatures, [T_J T_C]** — A row vector [T_J T_C] of two temperature values. The first value, T_J, is the junction initial temperature. The second value, T_C, is the case initial temperature. The default value is [25 25] °C.

The following rules apply:

- Case thermal mass must be greater than zero.

- Junction thermal mass can only be set to zero if the junction-case resistance is also set to zero.

- If both the case and junction thermal masses are defined, but the junction-case resistance is zero, then the initial temperatures assigned to the junction and case must be identical.

Foster Thermal Model

This figure shows an equivalent model of the internal Foster thermal model for semiconductor devices.
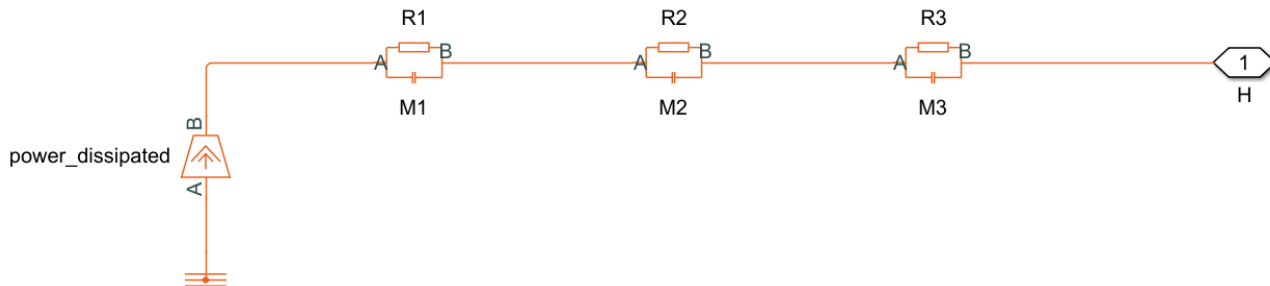
Port **H** corresponds to thermal port **H** of the block. The Heat Flow Rate Source block (called power_dissipated in the diagram) inputs heat to the model with a value equal to the electrically generated heat from the device. Because this option uses Foster Thermal Model blocks to model the thermal network, you need to connect a thermal source to the **H** port either directly or through some additional thermal components so that the power flow has a well-defined path. This is not needed in the Cauer thermal model because the thermal masses already provide a path to a thermal reference.

If you choose to simulate the internal thermal network of the block through Foster model, the following parameters will be visible:

- **Thermal resistances, [R1 R2 ... Rn]** — A row of n thermal resistance values, represented by the Foster elements used in the thermal network. These values must all be greater than zero. The default value is [4 6] K/W.

- **Thermal mass parameterization** — Select whether you want to parameterize the thermal masses in terms of thermal time constants (By thermal time constants), or specify the thermal mass values directly (By thermal mass). For more information, see "Thermal Mass Parameterization" on page 3-34. The default is By thermal time constants.

- **Thermal time constants, [t1 t2 ... tn]** — A row vector of n thermal time constant values, where n is the number of Foster elements used in the thermal network. The length of this vector must match the length of **Thermal resistances, [R1 R2 ... Rn]**. These values must all be greater than

zero. With this parameterization, the thermal masses are computed as $M_i = t_i/R_i$, where M_i, t_i and R_i are the thermal mass, thermal time, and thermal resistance for the ith Foster element. To enable this parameter, set **Thermal mass parameterization** to By thermal time constants. The default value is [6 18] s.

- **Thermal masses, [M1 M2 … Mn]** — A row vector of *n* thermal mass values, where n is the number of Foster elements used in the thermal network. These values must all be greater than zero. To enable this parameter, set **Thermal mass parameterization** to By thermal mass. The default value is [1.5 3] J/K.

For the internal Foster thermal model, the thermal resistances, thermal time constants, and thermal masses must all be greater than zero.

External Thermal Model

If you want to model the thermal network of a semiconductor block externally to the block itself, set the **Thermal network** parameter to External. This figure shows the equivalent model of the internal thermal model for semiconductor devices.
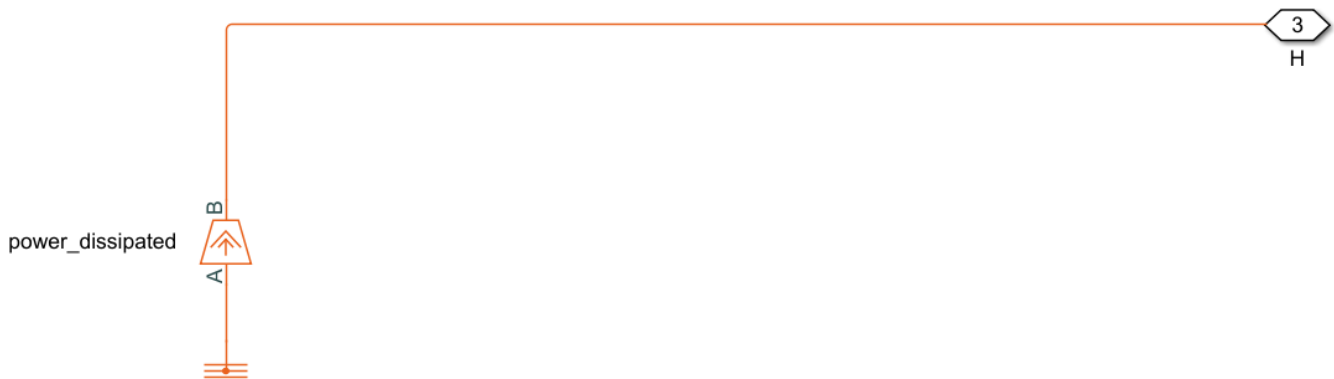
Port **H** corresponds to thermal port **H** of the block. The Heat Flow Rate Source block (called power_dissipated in the diagram) represents the total dissipated power in the block. The dissipated power is output as heat flow to the H node. Similar to the Foster thermal model, you need to connect a thermal source or additional thermal components to the H node so that the heat has somewhere to flow.

If you choose to simulate the internal thermal network of the block externally, there are no additional parameters.

Thermal Mass Parameterization

If you need to estimate thermal masses, there are two parameterization options:

- By thermal time constants — Parameterize the thermal masses in terms of thermal time constants. This is the default.
- By thermal mass — Specify the thermal mass values directly.

For the Cauer model (junction and case), the thermal time constants t_J and t_C are defined as follows:

$$t\_J = M\_J \cdot R\_JC$$

$t\_C = M\_C \cdot R\_CA$

where M_J and M_C are the junction and case thermal masses, respectively, R_JC is the thermal resistance between junction and case, and R_CA is the thermal resistance between port **H** and the device case.

For the `Foster model`, the thermal time constant, t_i, is defined as follows for the ith Foster element:

$t_i = M_i \cdot R_i,$

where M_i and R_i are the thermal mass and the thermal resistance of the ith Foster element, respectively.

You can determine the case-time constant by experimental measurement. If data is not available for the junction-time constant, you can either omit the constant and set the junction-case resistance to zero, or you can set the junction-time constant to a typical value of one-tenth of the case-time constant. Alternatively, you can estimate thermal masses based on the device dimensions and averaged material-specific heats.

Electrical Behavior Depending on Temperature

For blocks with optional thermal ports, there are two simulation options:

- Simulate the generated heat, device temperature, and the effect of temperature on the electrical equations.
- Simulate the generated heat and device temperature, but do not include effect of temperature on the electrical equations. Use this option when the impact of temperature on the electrical equations is small for the temperature range that you are simulating, or where the primary task of the simulation is to capture the heat generated to support system-level design.

The thermal port and the **Thermal Port** tab of the Block Parameters window let you simulate the generated heat and device temperature. For blocks with a **Temperature Dependence** tab, it is possible to simulate the impact of the junction temperature on the electrical characteristics. The **Thermal Dependence** tab lets you model the effect that the temperature of the semiconductor junction has on the electrical equations. Therefore:

- To simulate all of the temperature effects, show the block's thermal port and, if the block has a **Temperature Dependence** tab, set the **Parameterization** parameter to one of the provided options, for example, `Use an I-V data point at second measurement temperature`.
- To simulate only the generated heat and device temperature, show the block's thermal port and, on the **Temperature Dependence** tab, set **Parameterization** to `None — Simulate at parameter measurement temperature`.

Improving Numerical Performance

Set realistic values for thermal masses and resistances. Otherwise, junction temperatures can become extreme, and out of range for valid results, which can manifest as numerical difficulties during simulation. You can test if numerical difficulties are a result of unrealistic thermal values by turning off the temperature dependence for the electrical equations, by opening the Block Parameters window, clicking the **Thermal Dependence** tab, and setting **Parameterization** to `None — Simulate at parameter measurement temperature`.

The thermal time constants are generally much slower than electrical time constants, so the thermal aspects of your model are unlikely to dictate the maximum fixed time step you can simulate at (for example, for hardware-in-the-loop simulations). However, if you need to remove detail (for example, to speed up simulation), the junction-thermal mass time constant is typically an order of magnitude faster than the case-thermal mass time constant. Yo can remove the effect of the junction-thermal mass by setting the junction-thermal mass and junction-case thermal resistance to zero.

Model Thermal Losses for a Rectifier

Model Heat Transfer for a Single Rectifier Diode

To model and measure heat transfer as a function of the thermal characteristics of a semiconductor, connect a Foster model-based thermal network and a temperature sensor to a block with a thermal port.

1   Open the model. At the MATLAB command prompt, enter:

    ee_rectifier_diodes

    The model contains a three-phase rectifier that includes six Diode blocks.

2   Select a thermal variant for the Diode1 block by right-clicking the block and, from the context menu, selecting **Simscape > Block choices**. Select **Show thermal port**.

3   Open the Diode1 block. In the **Thermal Port** settings, set **Thermal network** to External.

4   Add a Simscape Electrical block that represents the heat flow between the diode and the environment. Open the Simulink Library browser, click **Simscape > Electrical > Passive > Thermal**, and add a Foster Thermal Model block to the model.

5   Open the Foster Thermal Model block and modify these parameters:

    a   **Thermal resistance data** — Specify [0.00311 0.008493 0.00252 0.00288] K/W.

    b   **Thermal time constant data** — Specify [0.0068 0.0642 0.3209 2.0212] s.

6   Add these blocks to represent the ambient temperature as a constant by using an ideal temperature source.

    a   From the Simulink Library browser, open the **Simscape > Foundation Library > Thermal > Thermal Sources** library and add a Controlled Temperature Source block.

    b   From the **Simscape > Foundation Library > Thermal > Thermal Elements** library, add a Thermal Reference block.

    c   From the **Simscape > Foundation Library > Physical Signals > Sources** library, add a PS Constant block. For the **Constant** parameter, specify a value of 300.

7   Add these blocks to measure and display the temperature of Diode1:

    a   From the Simulink Library browser, open the **Simscape > Foundation Library > Thermal > Thermal Sensors** library and add a Temperature Sensor block.

    b   From the **Simscape > Utilities** library, add a PS-Simulink Converter block. For the **Output signal unit** parameter, select K.

    c   From the **Simulink > Sinks** library, and add a Scope block.

8   Arrange and connect the blocks as shown in the figure.
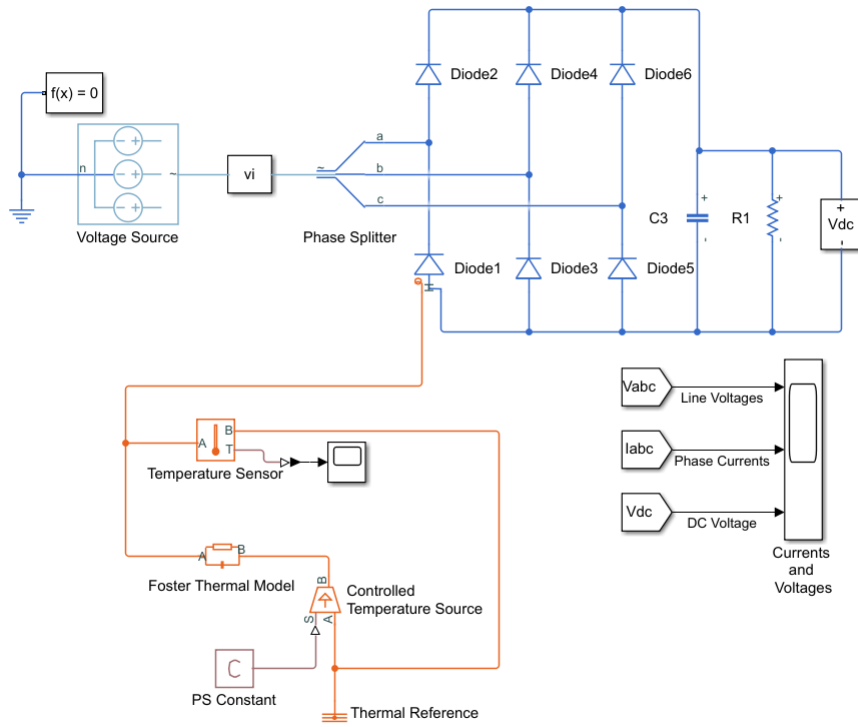
9 Label the signal from the PS-Simulink Converter block to the Scope block by double-clicking the line between the blocks and entering `Temp (K)`.

10 Simulate the model.

11 To see the temperature data, open the Scope block.
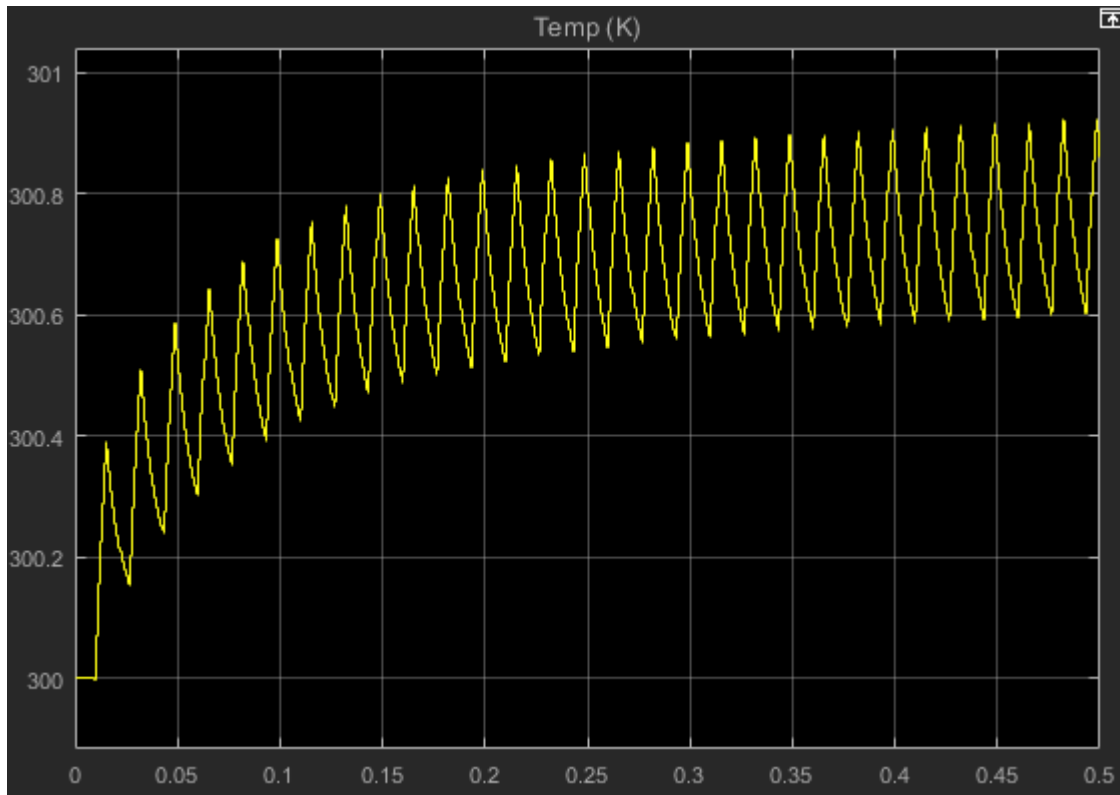
The temperature of Diode1 fluctuates over a temperature range of 0.3 K as it increases from the initial value of 300 K to a settling point of 300.6–300.9 K toward the end of the simulation.

Model Heat Transfer for All Rectifier Diodes

To see the total heat generated by all of the semiconductors in the rectifier, use data logging and the Simscape Results Explorer.

1   To enable the thermal ports on all the rectifier diodes, select thermal variants for the Diode2, Diode3, Diode4, Diode5, and Diode6 blocks by right-clicking the blocks and selecting **Simscape > Block choices > Show thermal port**.

2   Open the Diode2, Diode3, Diode4, Diode5, and Diode6 blocks and, in the **Thermal port** settings, set **Thermal network** to External.

3   Add blocks to measure the heat transfer for each diode by creating a Foster thermal model subsystem.

   a   Make a copy of this group of blocks:

   •   Foster Thermal Model

   •   Controlled Temperature Source

   •   PS Constant

   •   Thermal Reference

   b   Arrange and connect the copied blocks as shown in the figure.
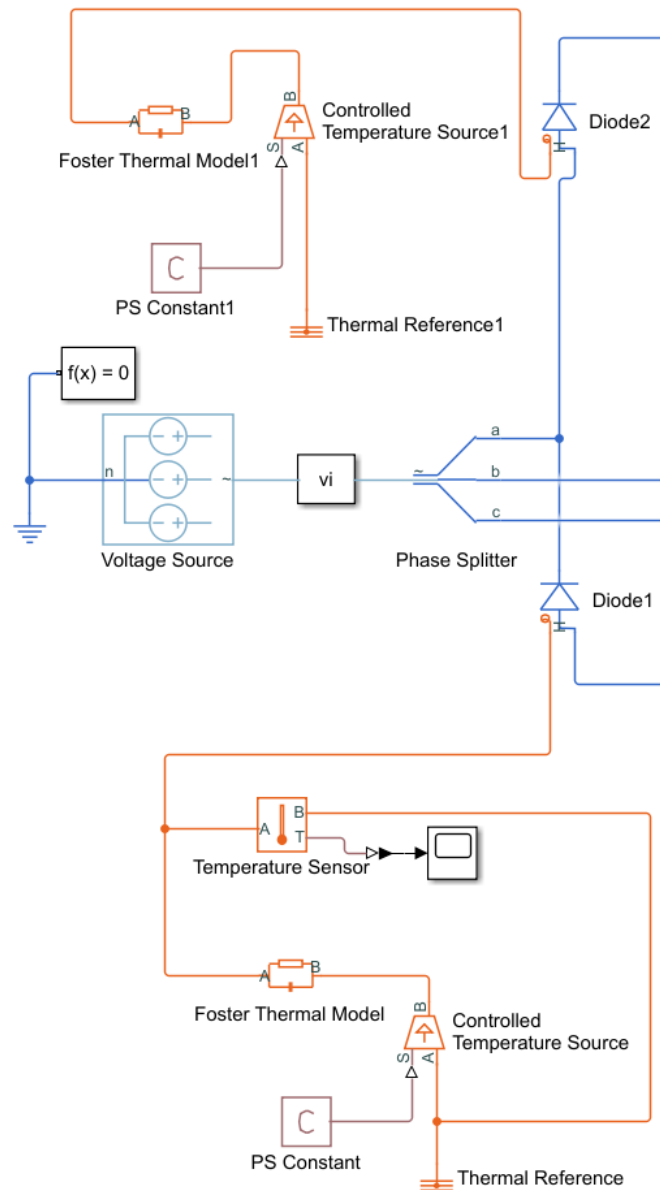
c   Create a subsystem from the copied blocks and rename the subsystem as Foster_D2. For more information, see "Create Subsystems".

d   Open the Foster_D2 subsystem. Open the Conn1 block, and for the **Port location on the parent subsystem** parameter, select `Right`.
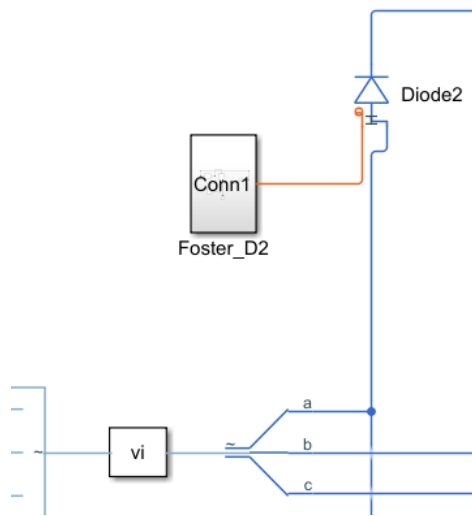
e Make four copies of the Foster_D2 subsystem. Attach one subsystem to each of the remaining Diode blocks and rename the subsystems as Foster_D3 through Foster_D6 to match the Diode3 through Diode6 block names.

4 Simulate the model.

5 View the results using the Simscape Results Explorer.

    a In the model window, in the text under **Three-Phase Rectifier**, click **Explore simulation results**.

    b To display the temperature data for **Diode1**, in the Simscape Results Explorer window, expand the **Diode1 > H** node and click **T**.

    c To display the DC voltage in a separate plot, expand the **Sensing Vdc > Voltage Sensor** node, press **CTRL**, and click **V**.

    d To display the temperature data for all the diodes, expand the **Diode2 > H** node, press **CTRL**, and click **T**. Repeat the process for Diode3 through Diode6.

    e To overlay the temperature data in single plot, in the Simscape Results Explorer window,

    above the tree-node window, click the options ⊞ button. In the Options dialog box, for **Plot signals**, select Overlay. To accept the change, click **OK**. Click and drag the legend down to see the temperature data clearly.
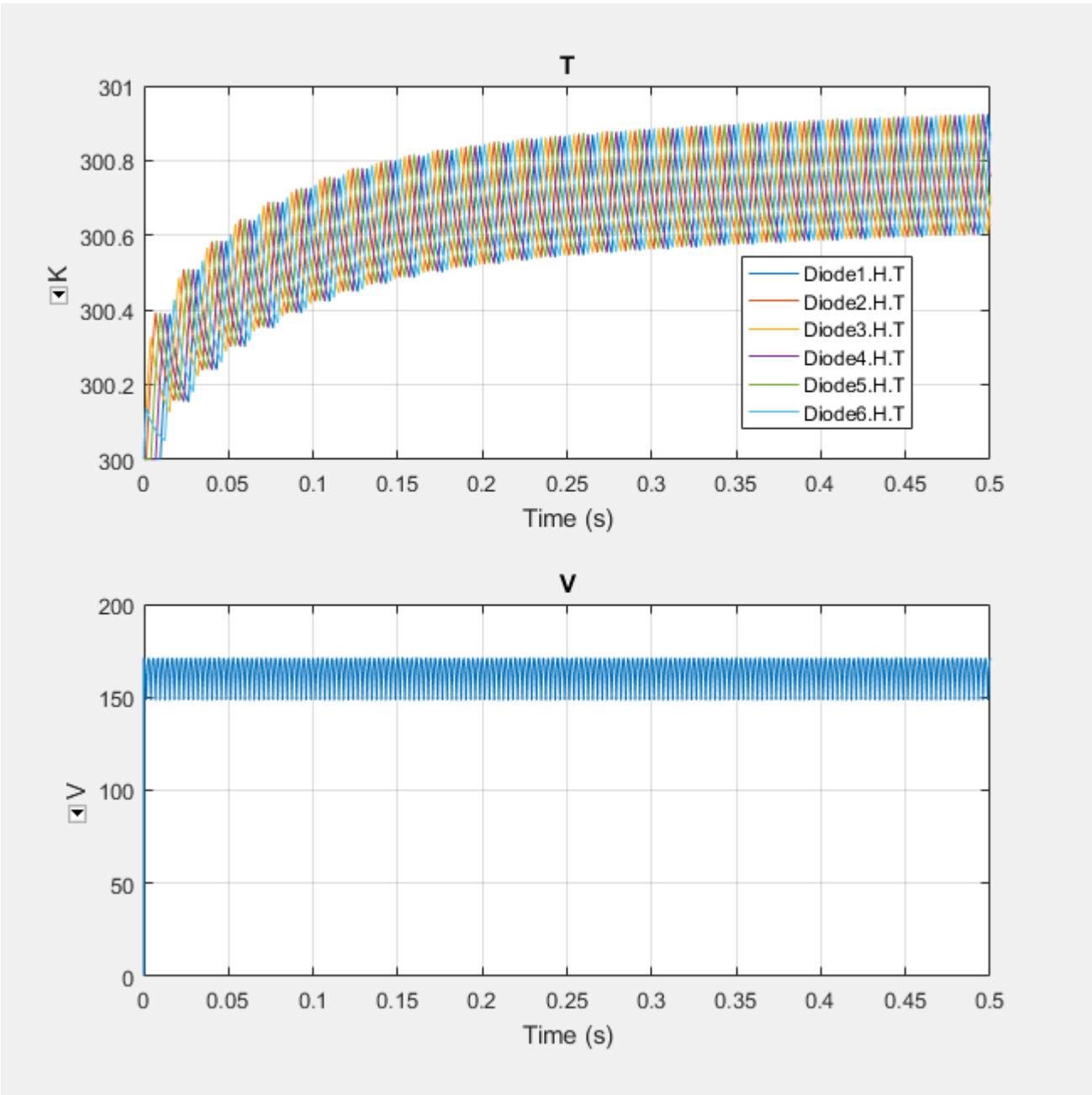
The temperature profile for each diode lags, in succession, behind the temperature profile of Diode1. For each diode, the temperature also rises and settles along the same values as the temperature profile for Diode1. The data indicate that, because of the lagging behavior of the individual diode temperatures, the temperature of the rectifier rises and settles along the same temperature profile as the diodes, but with less fluctuation.

Plot Basic Characteristics for Battery Blocks

A quick plot feature lets you visualize the voltage-charge characteristic for battery blocks, based on the current block parameter values.

This feature is implemented for Battery and Battery (Table-Based) blocks.

To plot the battery voltage-charge characteristics:

1   Right-click a battery block in your model and, from the context menu, select **Electrical > Basic characteristics**. The software automatically computes a set of bias conditions, based on the block parameter values, and opens a figure window containing a plot of no-load voltage versus the state-of-charge (SOC) for the block.

For example, the following plot corresponds to the default parameter values of a Battery block with infinite charge.
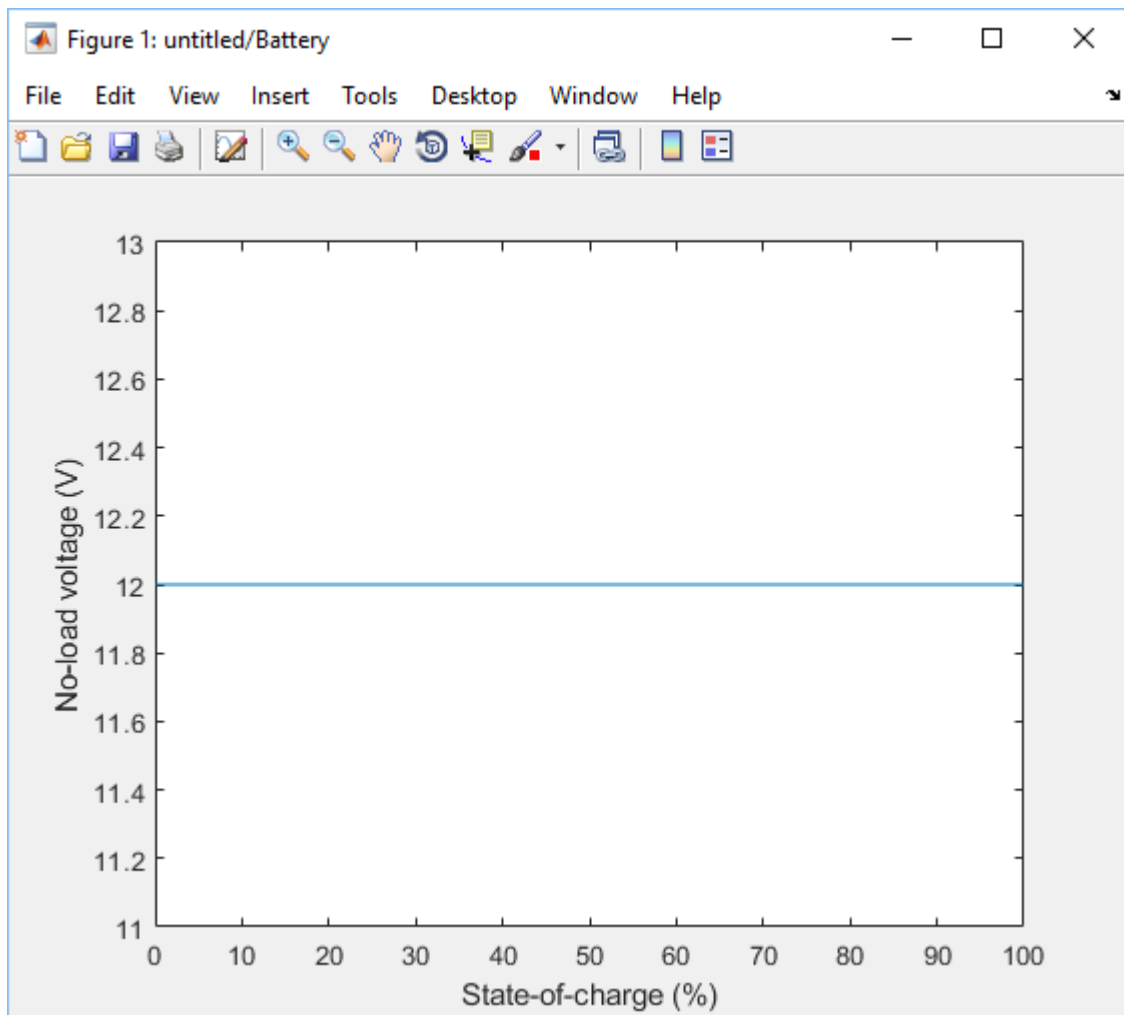
2   If you change the block parameter values and plot the characteristics again, the plot opens in a new window. This way, you can compare the plots side-by-side and see how the parameter values affect the resulting voltage-charge characteristics for the block.

For example, if you change the **Battery charge capacity** parameter value to `Finite` and **Self-discharge** to `Enabled`, the new plot looks like this.
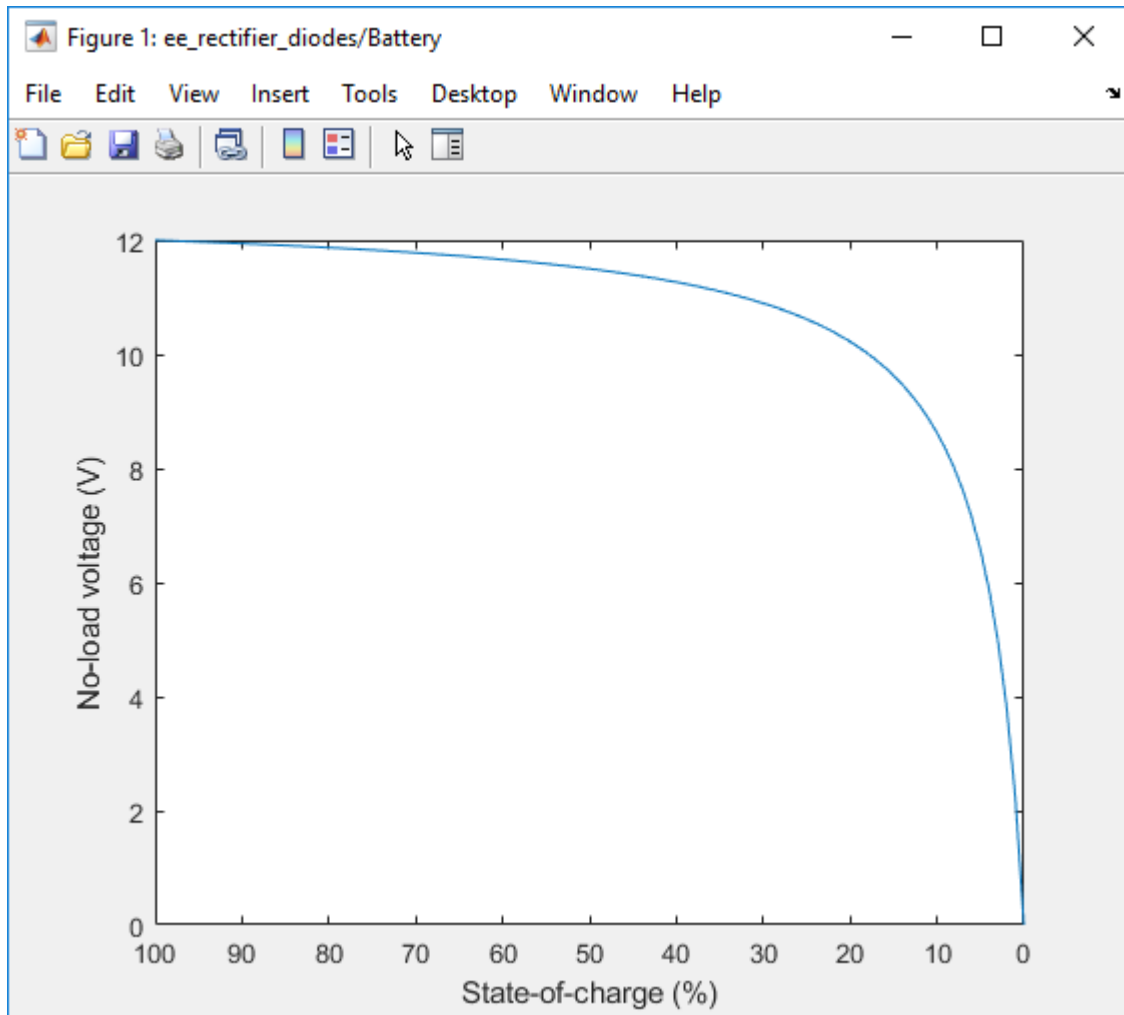
See Also
Battery | Battery (Table-Based)

Plot Basic Characteristics for Semiconductor Blocks

A quick plot feature lets you visualize the basic I-V characteristics for semiconductor switching devices, based on the current block parameter values.

This feature is implemented for nonthermal variants of the following blocks in the Semiconductors library:

- N-Channel IGBT
- N-Channel MOSFET (both threshold-based and surface-potential-based variants)
- P-Channel MOSFET (both threshold-based and surface-potential-based variants)
- N-Channel LDMOS FET
- P-Channel LDMOS FET
- N-Channel JFET
- P-Channel JFET
- NPN Bipolar Transistor
- PNP Bipolar Transistor

To plot the characteristics, right-click an appropriate semiconductor block in your model and, from the context menu, select **Electrical > Basic characteristics**.

Note For surface-potential-based N-Channel MOSFET and P-Channel MOSFET blocks, the **Electrical > Explore characteristics** option is also available. This option opens the Characteristics Viewer tool, which lets you perform an in-depth study of block characteristics and match the block behavior to a set of target characteristics. For more information, see "MOSFET Characteristics Viewer" on page 3-47.

To plot the basic characteristics:

1   Right-click a semiconductor block in your model and, from the context menu, select **Electrical > Basic characteristics**. The software automatically computes a set of bias conditions, based on the block parameter values, and opens a figure window containing a plot of the DC I-V characteristics for the block.

    For example, the following plot corresponds to the default parameter values of a threshold-based N-Channel MOSFET block.
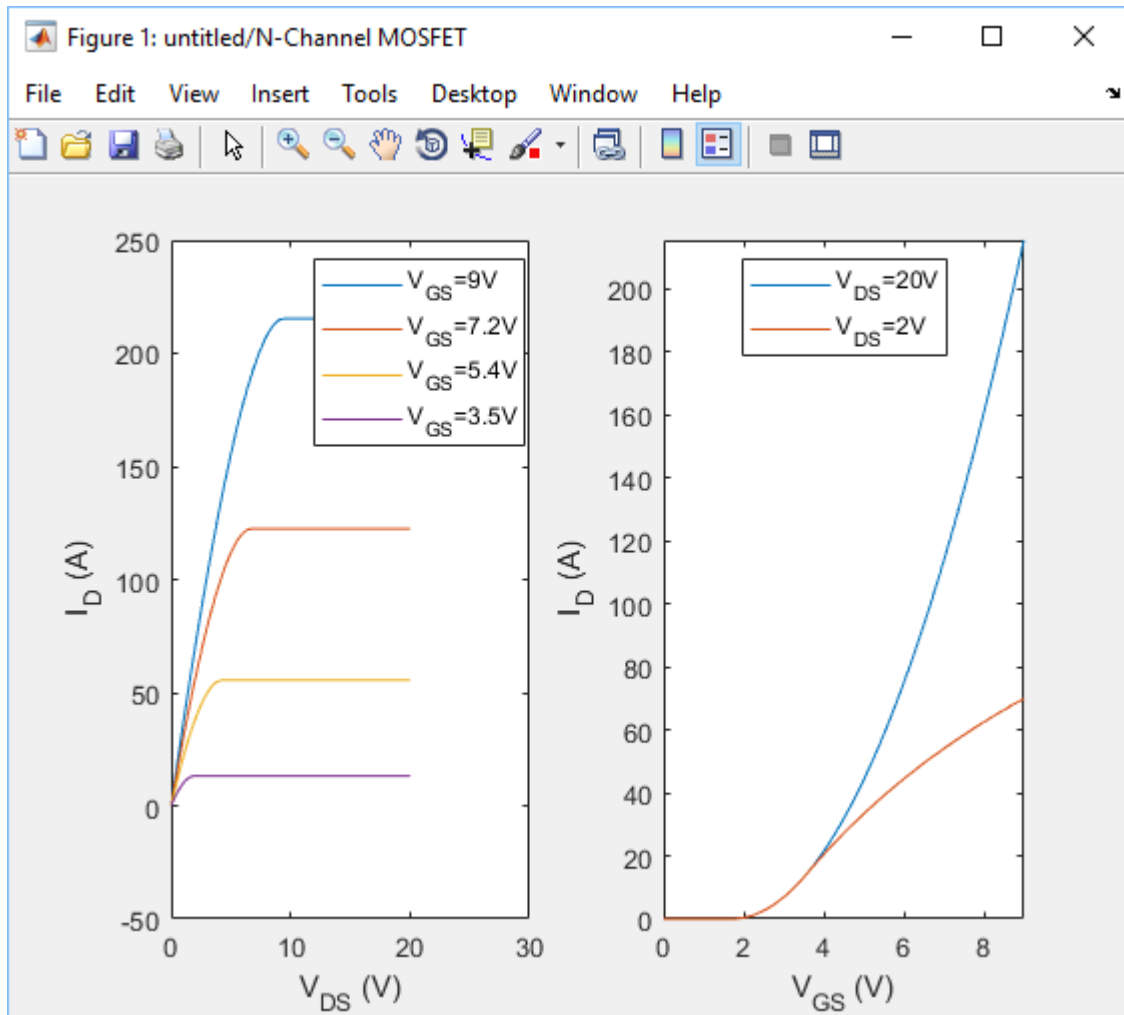
2 If you change the block parameter values and plot the characteristics again, the plot opens in a new window. This way, you can compare the plots side-by-side and see how the parameter values affect the resulting DC I-V characteristics for the block.

For example, if you change the **Gate-source voltage, Vgs, for R_DS(on)** parameter value to 20 V, the new plot looks like this.
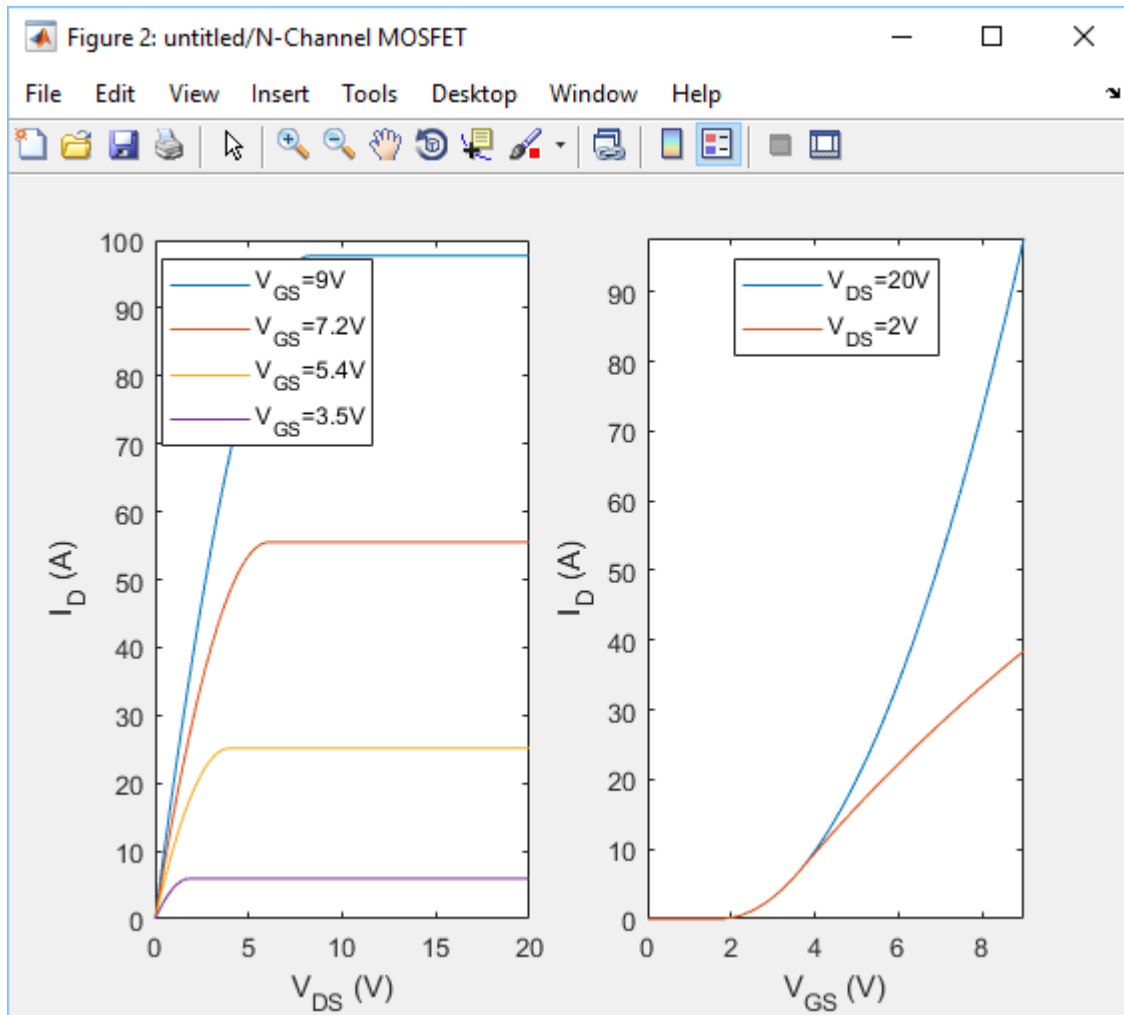
See Also

More About

- "MOSFET Characteristics Viewer" on page 3-47

MOSFET Characteristics Viewer

In this section...
"Suggested Workflow" on page 3-47
"Add and Manage Characteristics" on page 3-49
"Choose Parameters and Generate Plots" on page 3-51
"Save the Results" on page 3-53

The Characteristics Viewer tool lets you study characteristics of a particular parameterization of a surface-potential-based MOSFET block and match the block behavior to a set of target characteristics. The tool allows you to:

- Plot simulated data, using the current block parameters.
- Overlay simulated data plots over tabulated target data.
- Modify block parameters.
- When satisfied with the results of the parameters tuning in the Characteristics Viewer, update the block parameters in the model.
- Save generated parameter sets for future reuse in a different model.

Suggested Workflow

The Characteristics Viewer tool is available for surface-potential-based N-Channel MOSFET or P-Channel MOSFET blocks only. To switch to a surface-potential-based variant when you add an N-Channel MOSFET or P-Channel MOSFET block from the library, right-click the block in your model and, from the context menu, select **Simscape > Block choices > Surface-potential-based**. Then, when you right-click the block again, the context menu will contain the **Electrical** option, necessary to start the parameterization tool.

To use the MOSFET parameterization tool:

1 Right-click a surface-potential-based MOSFET block in your model and, from the context menu, select **Electrical > Explore characteristics**. A `charactericViewer` window opens.

2 Double-click **Add characteristics**. Specify the characteristics type (target, simulated, or both), and the desired values. Click **Add to plot**.

Continue adding more characteristics, as needed. The **Replace plot** button lets you replace previously added plots. You can also use the **List/Delete characteristics** block, iteratively with **Add characteristics**, to configure your characteristics set.

3 Double-click **Choose parameters** and select the parameters of interest.

4 Double-click **Generate plots**.

5 Iterate between the previous two steps to tune the parameters by matching the simulation results to the target curves.

6 When satisfied with the results of the parameters tuning, double-click **Update starting block parameters** to update the block parameters in your model. Until you perform this step, the block in the original model is not affected.

7 You can double-click **Save data** to save the generated characteristics as a MAT-file, for future reuse in a different model.

Add and Manage Characteristics

You start the MOSFET parameters tuning process by specifying the desired set of target characteristics:

1 In the `charactericViewer` window, double-click **Add characteristics**.

The Characteristics window opens.

2   Enter **Plot number**. This number defines the number of the figure that the characteristic will be plotted on. It allows you to add multiple characteristics to the same figure, for overlaying characteristics on top of each other. However, the figure will comprise one *xy*-axis only.

3   Specify the **Characteristic type**:

- `Target only` — The plot will contain data that you specified, in terms of both input and output values. No simulation will be performed in this case. The data will simply be added to the appropriate plot.

- `Simulated only` — The plot will contain data that is a result of a simulation over the input bias conditions that you specify.

- `Target and simulated` — The plot will contain both types of data. This option is useful if you are trying to adjust parameters for the model to fit data that you have extracted from a datasheet.

4   Select **Sweep type**, which defines the *x*-axis variable for the resultant plot:

- `V_GS` — Sweep over the gate-source voltage.

- `V_DS` — Sweep over the drain-source voltage.

- `I_D` — Sweep over the drain current. Normally, the drain current is not a typical input for a characteristic sweep.

5   If the **Characteristic type** is `Simulated only`, specify **Sweep range**. This is a vector of values indicating the range for the swept variable. Only the minimum and maximum values of this vector are utilized by the tool, since the exact sample points for the output data are determined by the variable-step simulation.

6   If the **Characteristic type** is `Target only` or `Target and simulated`, specify **Sweep values**. This is a vector of values for the swept variable at which the output is sampled for the target data. As an example, for an I_D-V_DS characteristic extracted from a datasheet, the vector would contain the V_DS values corresponding to the sampled I_D values in the target curve.

7   Select **Step type** to define the second independent input bias condition. The choices are the same as for **Sweep type**. For example, if an I_D-V_DS curve is defined as being at a constant V_GS, choose `V_GS` for **Step type**.

8   Use **Step values** to specify the values for the stepped variable. For example, if an I_D-V_DS curve is desired for V_GS values of 0 and 10V, set **Step type** to `V_GS` and **Step values** to `[0 10]`.

9   Select **Output type**, which defines the output measurement for the characteristic. This is the *y*-axis variable for the resultant plot. The available values are: `V_GS`, `V_DS`, `I_D`, `C_GG`, `C_GD`, `C_DG`, and `C_DD`. The capacitances C_GG, C_GD, C_DG, and C_DD are defined according to their terminals. To relate these quantities to the datasheet parameters of Ciss, Crss and Coss, note that C_GG = Ciss, C_DD = Coss, and C_GD = Crss at V_GS = 0.

      V_GS is not a good choice as an output for the surface-potential-based MOSFET model. This value is provided in anticipation of using this tool for other device types.

10 If the **Characteristic type** is `Target only` or `Target and simulated`, specify **Output values**. This is the target data that you want to plot in the figures. Provide this data as an *m*-by-*n* matrix, where *m* is the size of **Step values** and *n* is the size of **Sweep values**.

11 Click **Add to plot** to add the characteristic specification to the appropriate **Plot number**.

12 Continue adding more characteristics, as needed.

The **Replace plot** button lets you replace previously added plots. You can also use the **List/ Delete characteristics** block, iteratively with **Add characteristics**, to configure your characteristics set.

Choose Parameters and Generate Plots

After you have specified the desired set of target characteristics, the next step is to define the parameters for the MOSFET block:

1 In the `charactericViewer` window, double-click **Choose parameters**.
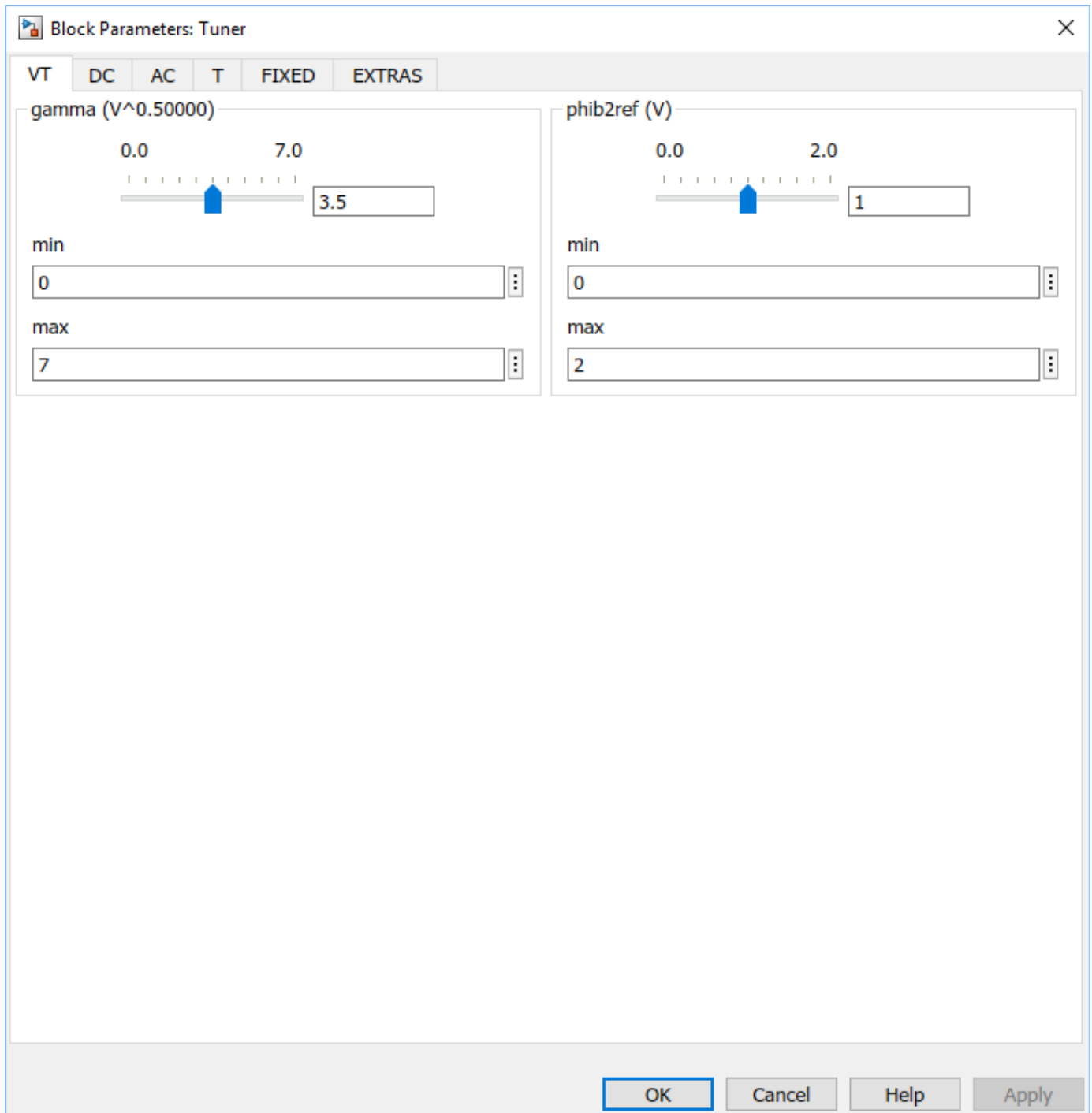
The Tuner window opens. It contains a series of sliders on different tabs, according to which feature of the MOSFET characteristics is most impacted by the specific parameter:

- The **VT** tab displays parameters that primarily impact the threshold voltage (**gamma** and **phib2ref**).
- The parameters on the **DC** tab primarily affect the DC characteristics.

- The parameters on the **AC** tab primarily affect the MOSFET dynamics.

- The parameters on the **T** tab affect temperature scaling.

- The parameters on the **FIXED** tab are generally fixed at some particular value that is not easy to derive from the displayed characteristics, such as the simulation temperature and the gate resistance (which is often indicated directly on datasheets).

- The **EXTRAS** tab contains other parameters, which impact the characteristics in ways similar to parameters that already appear on other tabs. For example, **Rsref** (the series resistance associated with the source) operates similarly to **betaref** from the **DC** tab. As a result, it is not always possible to disentangle these two effects.

2   Use the sliders on the appropriate tabs of the Tuner dialog.

You can modify the `min` and `max` values, as needed, because they simply define the range over which the various sliders work. These values have no meaning for the underlying model parameters. Changing a `min` or `max` value automatically updates the slider range, without needing to click **OK** or **Apply**.

3   After adjusting the sliders, generate the plots to see how close the simulation data is to the target data. In the `charactericViewer` window, double-click **Generate plots**.

4   Iterate between tuning the parameters and generating plots until the simulation results match the target curves.

Save the Results

Once you are satisfied with the results of the parameters tuning:

- Double-click **Update starting block parameters** to update the block parameters in your model. Until you perform this step, the block in the original model is not affected.

Note For this step to work, the original model must stay open while you are tuning the parameters.

- You can also double-click **Save data** to save the generated characteristics as a MAT-file, for future reuse in a different model. Specify the file name for saving the data. Inside the file, all the data is saved in an object named `parameterHelper`.

To apply the parameters stored in this object to another MOSFET block, select the MOSFET block in a model and, in the MATLAB Command Window, type:

```
parameterHelper.parameters.updateBlockParameters(gcbh)
```

This command applies the parameter values to the block defined by the handle `gcbh`.

You can also use a string instead of the block handle, for example:

```
parameterHelper.parameters.updateBlockParameters(gcb)
```

To inspect the parameters directly, type `parameterHelper.parameters.values` for the values (stored as character vectors) or `parameterHelper.parameters.names` for the names.

See Also

More About

Converting a SPICE Netlist to Simscape Blocks

You can convert SPICE components into Simscape equivalents using the SPICE conversion assistant. Often this conversion is automatic. However, because SPICE is a rich language, it is not always possible to perform a full conversion without some manual intervention.

To convert SPICE subcircuits into equivalent Simscape components, follow these steps.

1 Use the `subcircuit2ssc` function to generate Simscape language component files from a SPICE netlist file. You can use the optional `subcircuit1,…,subcircuitN` input arguments to specify which subcircuits to convert.

2 Make any necessary manual conversions to the generated Simscape component files. To identify the required manual conversions, check the comments at the beginning of the generated Simscape component files. You can use the optional `unsupportedCommands` output argument to generate a `struct` array that lists unsupported SPICE commands for each subcircuit.

3 Build the library using `ssc_build` or add individual components to your model using Simscape Component blocks.

There are many different SPICE simulators with variations in syntax and syntax interpretation. The conversion assistant uses the same syntax as Cadence® PSpice and, where such differences exist, complies with PSpice.

Commands

The SPICE conversion assistant supports these commands:

- `.FUNC` — Reusable function
- `.PARAM` — Definable parameter
- `.MODEL` — Set of reusable component parameters
- `.SUBCKT` — Subcircuit
- `.LIB` — Directive to include models from an external netlist
- `.INC` — Directive to include contents of external netlist

The conversion assistant implements `.FUNC` SPICE commands using Simscape functions. These functions are placed inside a package sublibrary named `+subcircuit_name_simscape_functions`, where *subcircuit_name* is the name of the subcircuit being converted.

Specify the `.MODEL` syntax for resistors, capacitors, and inductors, as

```
.MODEL <model name> res(r=<value>)
.MODEL <model name> cap(c=<value>)
.MODEL <model name> ind(l=<value>)
```

where the `r`, `c`, and `l` values are scaling factors for the value specified on the component declaration. This behavior complies with PSpice, but is not consistent across all simulators.

The conversion assistant does not automatically convert initial conditions specified using the `.IC` statement. However, you can specify initial conditions for capacitors and inductors using the syntax `IC=<value>`. Also, you can manually convert any `.IC` statements from the generated Simscape component files.

Because the purpose of the conversion assistant is to help convert SPICE subcircuits into Simscape blocks, simulation commands, such as `.TRAN`, are ignored.

Numeric Suffixes

The conversion assistant supports these numeric SPICE suffixes:

Suffix	Name	Scale
T	Tera	1e12
G	Giga	1e9
MEG	Mega	1e6
K	Kilo	1e3
M	Milli	1e-3
MIL	--	25.4e-6
U	Micro	1e-6
N	Nano	1e-9
P	Pico	1e-12
F	Femto	1e-15

Mathematical Functions

The conversion assistant supports these basic mathematical functions used in SPICE and MATLAB.

Elementary Math

Name	SPICE Function	MATLAB Function
Absolute value	abs	abs
Smallest element	min	min
Largest element	max	max
Sign function	sgn	sign

Trigonometry

Name	SPICE Function	MATLAB Function
Sine	`sin`	`sin`
Inverse sine	`asin`	`asin`
Hyperbolic sine	`sinh`	`sinh`
Cosine	`cos`	`cos`
Inverse cosine	`acos`	`acos`
Hyperbolic cosine	`cosh`	`cosh`
Tangent	`tan`	`tan`
Inverse tangent	`atan`	`atan`
Four-quadrant inverse tangent	`atan2`	`atan2`
Hyperbolic tangent	`tanh`	`tanh`

Exponents and Logarithms

Name	SPICE Function	MATLAB Function
Power	`**` or `pwr`	`^` or `power`
Exponential	`exp`	`exp`
Natural logarithm	`ln` or `log`	`log`
Base-10 logarithm	`log10`	`log10`
Square root	`sqrt`	`sqrt`

The conversion assistant interprets `log()` as the natural logarithm rather than the base-10 logarithm. Not all SPICE simulators are consistent in this regard, so ensure that this interpretation is congruent with your SPICE model.

Other

In addition, the conversion assistant supports these SPICE functions:

Name	SPICE Function
If condition	`if`
Saturation	`limit`
Current through device	`i`
Voltage across device	`v`
Step function	`stp`
Derivative (see Limitations on page 3-61)	`ddt`
Table	`table`

Symbols

The conversion assistant recognizes these SPICE symbols:

- + at the start of a line indicates line continuation from the previous line
- * at the start of a line indicates that the entire line is a comment
- ; within a line indicates the beginning of an inline comment

Components

The notation for SPICE commands in this section follows these rules:

- `<argument>` refers to a required item in a command line
- `<argument>*` refers to a required item in a command line that occur one or more times
- `[argument]` refers to an optional item in a command line
- `[argument]*` refers to an optional item in a command line that occur zero or more times

This list shows the full set of supported SPICE components, and their supported SPICE netlist notations. You can specify only the `.MODEL` parameters that differ from SPICE default values.

Sources

- Independent voltage source

  ```
  V<name> <+ node> <- node> [DC] <value>
  V<name> <+ node> <- node> exp(<v1> <v2> <td1> <tc1> <td2> <tc2>)
  V<name> <+ node> <- node> pulse(<v1> <v2> <td> <tr> <tf> <pw> <per>)
  V<name> <+ node> <- node> pwl(<<tj> <vj>>*)
  V<name> <+ node> <- node> sffm(<voff> <vampl> <fc> <mod> <fm>)
  V<name> <+ node> <- node> sin(<voff> <vampl> <freq> <td> <df>)
  ```

- Independent current source

  ```
  I<name> <+ node> <- node> [DC] <value>
  I<name> <+ node> <- node> exp(<i1> <i2> <td1> <tc1> <td2> <tc2>)
  I<name> <+ node> <- node> pulse(<i1> <i2> <td> <tr> <tf> <pw> <per>)
  I<name> <+ node> <- node> pwl(<<tj> <ij>>*)
  I<name> <+ node> <- node> sffm(<ioff> <iampl> <fc> <mod> <fm>)
  I<name> <+ node> <- node> sin(<ioff> <iampl> <freq> <td> <df>)
  ```

- Current-controlled voltage source

  ```
  H<name> <+ node> <- node> <voltage source name> <gain>
  H<name> <+ node> <- node> VALUE={<expression>}
  H<name> <+ node> <- node> POLY(<value>) <voltage source name>* <coefficient>*
  H<name> <+ node> <- node> TABLE {<expression>}=< <input value>, <output value> >*
  H<name> <+ node> <- node> <voltage source name> TABLE=< <input value>, <output value> >*
  ```

- Voltage-controlled voltage source

  ```
  E<name> <+ node> <- node> <+ control node> <- control node> <gain>
  E<name> <+ node> <- node> VALUE={<expression>}
  E<name> <+ node> <- node> POLY(<value>) <<+ control node> <- control node>>* <coefficient>*
  E<name> <+ node> <- node> TABLE {<expression>}=< <input value>, <output value> >*
  E<name> <+ node> <- node> <+ control node> <- control node> TABLE=< <input value>, <output value> >*
  ```

- Current-controlled current source

  ```
  F<name> <+ node> <- node> <voltage source name> <gain>
  F<name> <+ node> <- node> VALUE={<expression>}
  F<name> <+ node> <- node> POLY(<value>) <voltage source name>* <coefficient>*
  F<name> <+ node> <- node> TABLE {<expression>}=< <input value>, <output value> >*
  F<name> <+ node> <- node> <voltage source name> TABLE=< <input value>, <output value> >*
  ```

- Voltage-controlled current source

```
G<name> <+ node> <- node> <+ control node> <- control node> <gain>
G<name> <+ node> <- node> VALUE={<expression>}
G<name> <+ node> <- node> POLY(<value>) <<+ control node> <- control node>>* <coefficient>*
G<name> <+ node> <- node> TABLE {<expression>}=< <input value>, <output value> >*
G<name> <+ node> <- node> <+ control node> <- control node> TABLE=< <input value>, <output value> >*
```

- Behavioral source (The <expression> does not need to appear in braces {})

```
B<name> <+ node> <- node> V=<expression>
B<name> <+ node> <- node> I=<expression>
```

Passive Devices

- Resistor

```
R<name> <+ node> <- node> [model name] <value>
.MODEL <model name> res(r=<value>)
```

- Capacitor

```
C<name> <+ node> <- node> [model name] <value> [IC=<value>]
.MODEL <model name> cap(c=<value>)
```

- Inductor

```
L<name> <+ node> <- node> [model name] <value> [IC=<value>]
.MODEL <model name> ind(l=<value>)
```

- Inductor coupling

```
K<name> <inductor name> <inductor name>* <value>
```

Switches

- Voltage-controlled switch

```
S<name> <+ node> <- node> <+ control node> <- control node> <model name>
.MODEL <model name> sw(ron=<value>, roff=<value>, vt=<value>, vh=<value>)
```

- Current-controlled switch

```
W<name> <+ node> <- node> <voltage source name> <model name>
.MODEL <model name> csw(ron=<value>, roff=<value>, it=<value>, ih=<value>)
```

Semiconductor Devices

- Diode

```
D<name> <+ node> <- node> <model name> [area]
.MODEL <model name> d(is=<value>, rs=<value>, n=<value>, cjo=<value>, vj=<value>,
    +m=<value>, fc=<value>, tt=<value>, revbrk=<value>, bv=<value>, ibv=<value>,
    +xti=<value>, eg=<value>)
```

- Bipolar junction transistor (BJT)

NPN

```
Q<name> <collector node> <base node> <emitter node> [substrate node] <model name> <area>
.MODEL <model name> npn(bf=<value>, br=<value>, cjc=<value>, cje=<value>, cjs=<value>,
    +eg=<value>, fc=<value>, ikf=<value>, ikr=<value>, irb=<value>, is=<value>, isc=<value>,
    +ise=<value>, itf=<value>, mjc=<value>, mje=<value>, mjs=<value>, nc=<value>, ne=<value>,
    +nf=<value>, nr=<value>, rb=<value>, rbm=<value>, rc=<value>, re=<value>, tf=<value>,
    +tr=<value>, vaf=<value>, var=<value>, vjc=<value>, vje=<value>, vjs=<value>, vtf=<value>,
    +xcjc=<value>, xtb=<value>, xtf=<value>, xti=<value>)
```

PNP

```
Q<name> <collector node> <base node> <emitter node> [substrate node] <model name> <area>
.MODEL <model name> pnp(bf=<value>, br=<value>, cjc=<value>, cje=<value>, cjs=<value>,
    +eg=<value>, fc=<value>, ikf=<value>, ikr=<value>, irb=<value>, is=<value>, isc=<value>,
    +ise=<value>, itf=<value>, mjc=<value>, mje=<value>, mjs=<value>, nc=<value>, ne=<value>,
    +nf=<value>, nr=<value>, rb=<value>, rbm=<value>, rc=<value>, re=<value>, tf=<value>,
```

```
      +tr=<value>, vaf=<value>, var=<value>, vjc=<value>, vje=<value>, vjs=<value>, vtf=<value>,
      +xcjc=<value>, xtb=<value>, xtf=<value>, xti=<value>)
```

- Junction field-effect transistor (JFET)

  N-Channel

```
J<name> <drain node> <gate node> <source node> <model name> [area]
.MODEL <model name> njf(beta=<value>, cgd=<value>, cgs=<value>, fc=<value>, is=<value>,
   +lambda=<value>, m=<value>, n=<value>, rd=<value>, rs=<value>, vto=<value>, xti=<value>)
```

  P-Channel

```
J<name> <drain node> <gate node> <source node> <model name> [area]
.MODEL <model name> pjf(beta=<value>, cgd=<value>, cgs=<value>, fc=<value>, is=<value>,
   +lambda=<value>, m=<value>, n=<value>, rd=<value>, rs=<value>, vto=<value>, xti=<value>)
```

- Metal-oxide-semiconductor field-effect transistor (MOSFET)

  N-Channel (only level-1 and level-3 are supported)

```
M<name> <drain node> <gate node> <source node> <bulk node> <model name>
   +[L=<value>] [W=<value>] [AD=<value>] [AS=<value>] [PD=<value>] [PS=<value>] [NRD=<value>]
   +[NRS=<value>] [M=<value>]
.MODEL <model name> nmos(cbd=<value>, cbs=<value>, cgbo=<value>, cgdo=<value>,
   +cgso=<value>, cj=<value>, cjsw=<value>, delta=<value>, eta=<value>, fc=<value>,
   +gamma=<value>, is=<value>, js=<value>, kappa=<value>, kp=<value>, lambda=<value>,
   +ld=<value>, level=<value>, mj=<value>, mjsw=<value>, n=<value>, neff=<value>, nfs=<value>,
   +nss=<value>, nsub=<value>, nrd=<value>, nrs=<value>, pb=<value>, phi=<value>, rd=<value>,
   +rs=<value>, rsh=<value>, theta=<value>, tox=<value>, tpg=<value>, ucrit=<value>,
   +uexp=<value>, uo=<value>, vmax=<value>, vto=<value>, xj=<value>)
```

  P-Channel (only level-1 and level-3 are supported)

```
M<name> <drain node> <gate node> <source node> <bulk node> <model name>
   +[L=<value>] [W=<value>] [AD=<value>] [AS=<value>] [PD=<value>] [PS=<value>] [NRD=<value>]
   +[NRS=<value>] [M=<value>]
.MODEL <model name> pmos(cbd=<value>, cbs=<value>, cgbo=<value>, cgdo=<value>,
   +cgso=<value>, cj=<value>, cjsw=<value>, delta=<value>, eta=<value>, fc=<value>,
   +gamma=<value>, is=<value>, js=<value>, kappa=<value>, kp=<value>, lambda=<value>,
   +ld=<value>, level=<value>, mj=<value>, mjsw=<value>, n=<value>, neff=<value>, nfs=<value>,
   +nss=<value>, nsub=<value>, nrd=<value>, nrs=<value>, pb=<value>, phi=<value>, rd=<value>,
   +rs=<value>, rsh=<value>, theta=<value>, tox=<value>, tpg=<value>, ucrit=<value>,
   +uexp=<value>, uo=<value>, vmax=<value>, vto=<value>, xj=<value>)
```

Subsystems

- Subcircuit

```
X<name> [node]* <subcircuit name> [PARAMS: < <name>=<value> >*]
```

Performing Manual Conversions

After you generate the Simscape component files, inspect each file header for messages regarding unsupported SPICE commands. For example, the conversion assistant does not support the implementation of temperature coefficients for resistors:

```
R1 p n 1k TC=0.01,-0.002
```

The generated Simscape component file contains all the supported conversions, and this header, which identifies the temperature coefficients of the resistor for manual conversion:

```
component test
% test
% Component automatically generated from a SPICE netlist (11-Dec-2018 09:34:57).
% Users should manually implement the following SPICE commands in order to
% achieve a complete implementation:
%    R1: tc 0.01 -0.002
```

To complete the conversion, modify the Simscape component file to implement the missing components. For more information about performing manual conversions and this particular scenario, see `subcircuit2ssc`.

Limitations

- The netlist must be written in PSpice format and be syntactically correct. The conversion assistant does not check for proper PSpice syntax.
- Only a subset of the PSpice netlist language is supported. However, unsupported PSpice commands are identified at the top of the corresponding Simscape component file to facilitate manual conversion.
- To build generated Simscape components into Simscape blocks, parameter values must conform to Simscape constraints. For example, capacitance of a fundamental capacitor and inductance of a fundamental inductor must be nonzero.
- The conversion assistant does not support the use of the derivative SPICE function, `ddt`, inside a function call.

See Also
`ssc_build` | `subcircuit2ssc`

More About
- "Building Custom Block Libraries"
- "Composite Components"

Photovoltaic Thermal (PV/T) Hybrid Solar Panel

This example shows how to model the cogeneration of electrical power and heat using a hybrid PV/T solar panel. The generated heat is transferred to water for household consumption.

It uses blocks from the Simscape™ Foundation™, Simscape Electrical™, and Simscape Fluids™ libraries. The electrical portion of the network contains a Solar Cell block, which models a set of photovoltaic (PV) cells, and a Load subsystem, which models a resistive load. The thermal network models the heat exchange that occurs between the physical components of the PV panel (glass cover, heat exchanger, back cover) and the environment. Heat is exchanged through conduction, convection, and radiation. The thermal-liquid network contains a pipe, a tank, and pumps. The pumps control the flows of the liquids through the system.

To model the reflection, absorption and transmission of light in the glass cover, an optical model is embedded in a MATLAB® Function block.

Model overview

Open the model to view its structure:

```
open_system('sscv_hybrid_solar_panel');
```
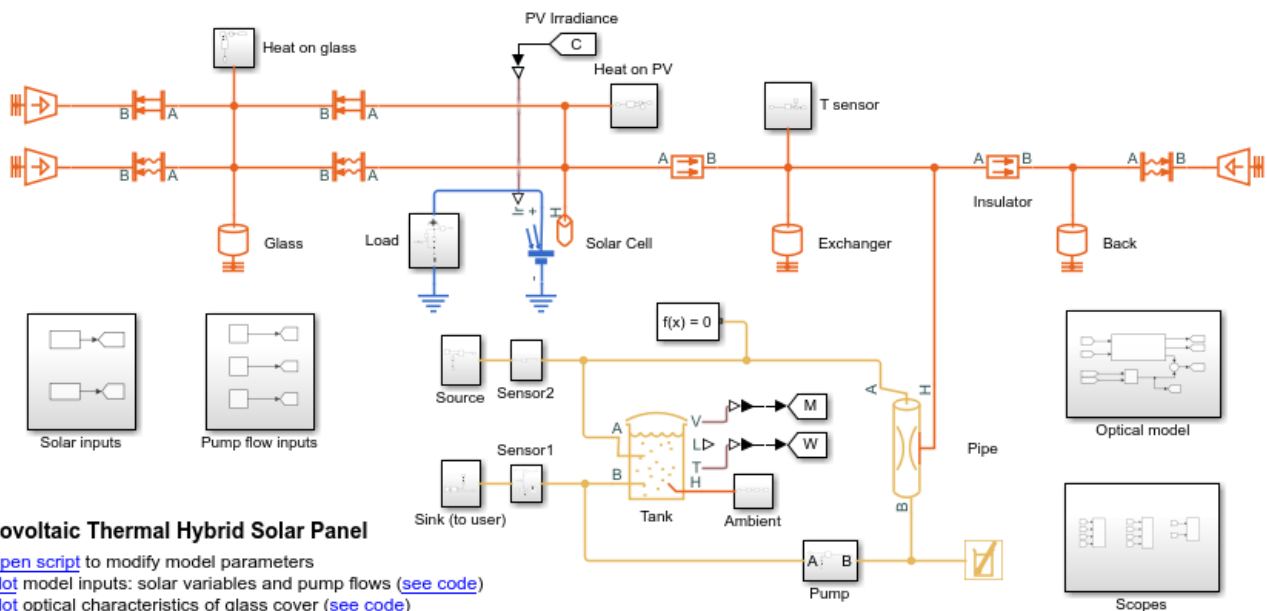

The thermal network is in red, the electrical network in blue and the thermal liquid network in yellow. There are subsystems for the solar and pump inputs. There is also a subsystem that contains scopes for visualizing the simulation results. Another subsystem contains the function for the optical model.

Parameters

You can use the hybrid_solar_panel_data.m script to change the parameter values that this example uses for components such as the load, solar cell, pipe, and tank.

```
edit sscv_hybrid_solar_panel_data;
```

Inputs

The inputs of the model are the pump flows and the solar variables for irradiance and incidence angle. A repeating sequence block is used to define the inputs because they follow a 24-hour periodic cycle.

```
open_system('sscv_hybrid_solar_panel/Solar inputs');
```



```
open_system('sscv_hybrid_solar_panel/Pump flow inputs');
```
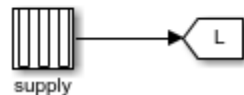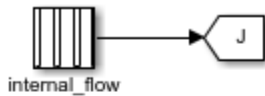

The sun rises at 6:00 and sets at 19:00. The irradiance follows a bell curve that peaks at 12:30. The incidence angle changes from pi/3 to 0.

There are three pumps. One pump models user demand, another models source supply, and a third models internal flow that forces convection in the pipe. The demand is constant and only non-zero from 10:00 to 22:00. The supply is constant and only non-zero from 18:00 to 6:00. The internal flow is also constant and only non-zero from 6:00 to 22:00. This model is used for the internal flow because it is not efficient to force heat exchange during the night when the ambient temperature is low.

You can use the hybrid_solar_panel_plot_inputs.m script to plot the inputs:

```
sscv_hybrid_solar_panel_plot_inputs;
```
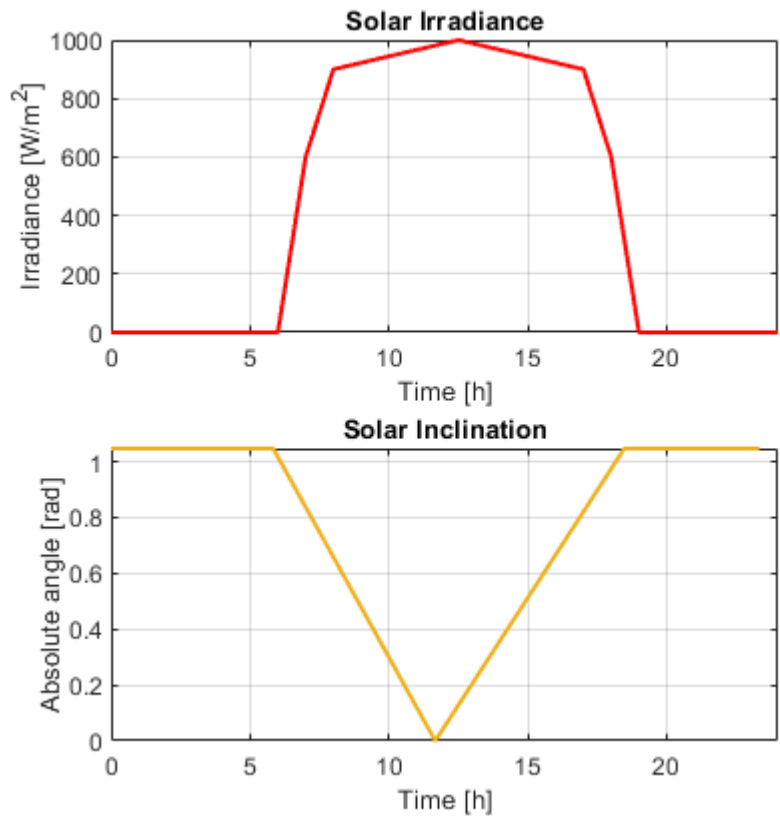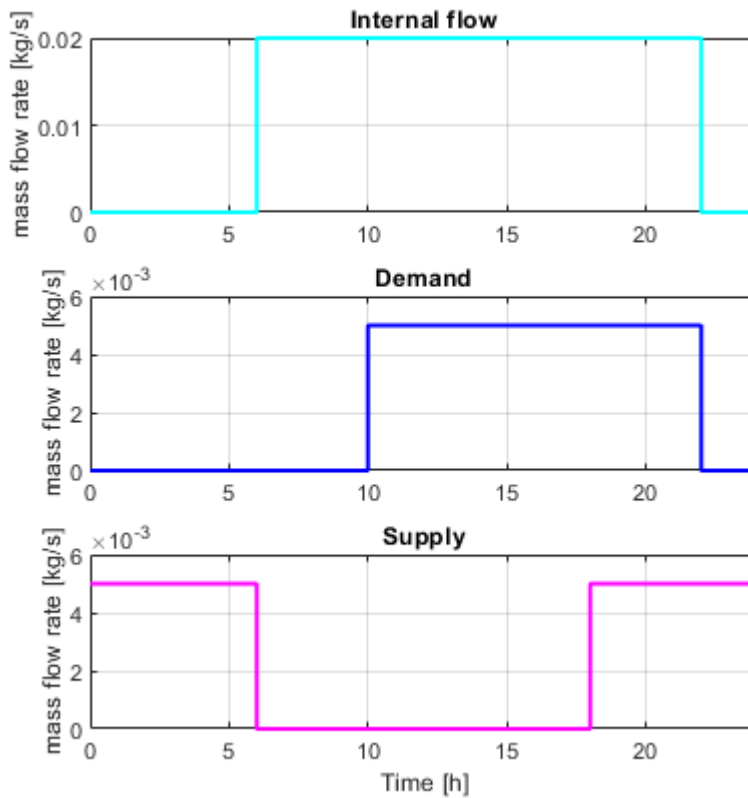
Optical model for the glass cover

The optical model is inside a subsystem:

```
open_system('sscv_hybrid_solar_panel/Optical model');
```
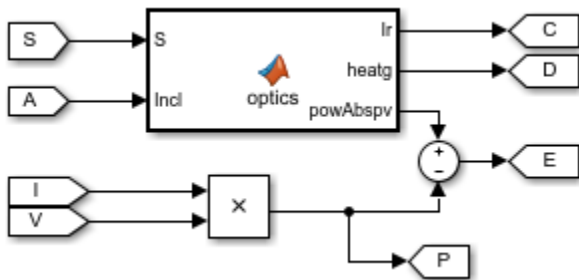

It consists of a MATLAB® Function block, with the 2 solar inputs, and 3 outputs: the transmitted irradiance on the PV cells, the heat absorbed by the glass, and the radiative power absorbed by the PV cells. Part of it will be transformed into electrical power (V*I) and the rest will be heat absorbed by the PV cells.

From an optical point of view, the glass consists of 2 parallel boundaries (air-glass, glass-air), each one of those reflects and transmits light. The reflection coefficient in a boundary is obtained from the

Fresnel equations. r_p is for P-polarization and r_s for S-polarization. The total reflection is the average of both, and the transmittance is $1 - r$ as there is no absorption so far:

$$r_p = \left(\frac{n_{rel}^2 \cos(\theta_i) - \sqrt{n_{rel}^2 - \sin(\theta_i)^2}}{n_{rel}^2 \cos(\theta_i) + \sqrt{n_{rel}^2 - \sin(\theta_i)^2}} \right)^2$$

$$r_s = \left(\frac{\cos(\theta_i) - \sqrt{n_{rel}^2 - \sin(\theta_i)^2}}{\cos(\theta_i) + \sqrt{n_{rel}^2 - \sin(\theta_i)^2}} \right)^2$$

$$r = \frac{1}{2}(r_p + r_s)$$

$$t = 1 - r$$

This is an example of the optical coefficients rp, rs, r and t in function of incidence angle:

```
nrel = 1.52; %Optical index from air to glass
theta = linspace(0, pi/2, 100);
rp = ( nrel^2*cos(theta) - sqrt(nrel^2 - sin(theta).^2) ).^2./...
    ( nrel^2*cos(theta) + sqrt( nrel^2 - sin(theta).^2 ) ).^2 ;
rs = ( cos(theta) - sqrt(nrel^2 - sin(theta).^2) ).^2./...
    ( cos(theta) + sqrt( nrel^2 - sin(theta).^2 ) ).^2 ;
r = 0.5*(rp + rs);
t = 1 - r;

figure();
plot(theta*180/pi, rp, 'Color', [0 1 1], 'LineWidth', 1.5);
hold on
plot(theta*180/pi, rs, 'Color', [0 0.5 1], 'LineWidth', 1.5);
plot(theta*180/pi, r, 'Color', [0 0 1], 'LineWidth', 1.5);
plot(theta*180/pi, t, 'Color', 'm', 'LineWidth', 1.5);
legend('rp','rs','r','t');
xlabel('Incidence angle (deg)');
grid on
box on
```
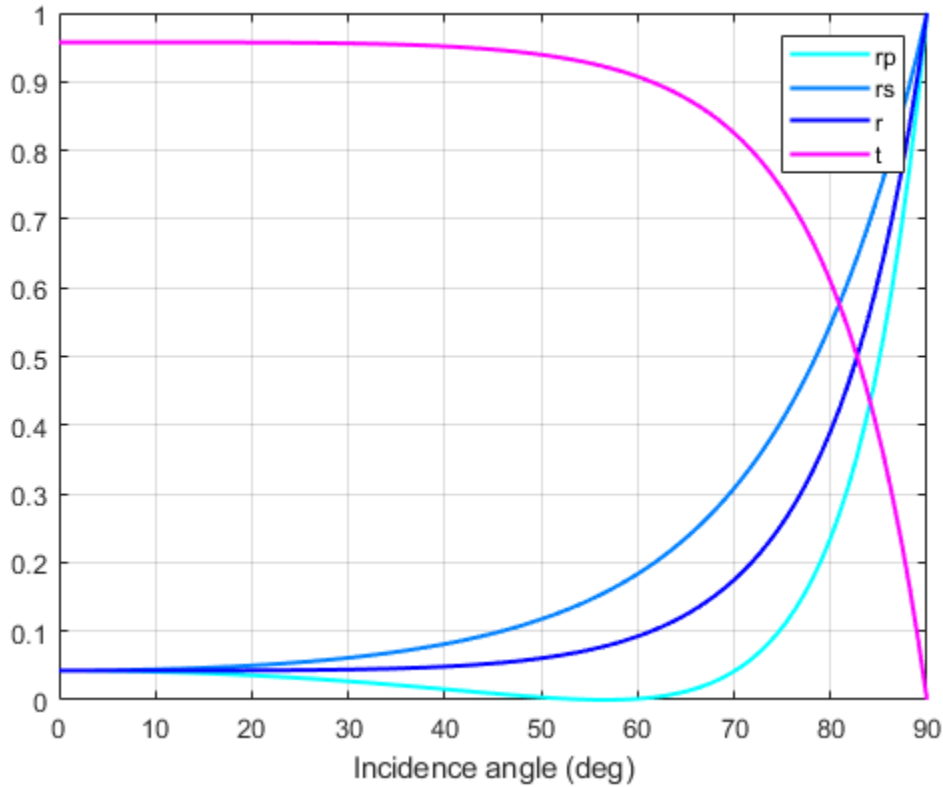
This is what happens in one boundary, but the glass has 2 parallel boundaries separated by d_g. The angle after the 1st boundary is the incidence angle on the 2nd boundary and is calculated from **Snell's Law**:

$$n_1 \sin(\theta_1) = n_2 \sin(\theta_2)$$

When the light enters the glass, it absorbs part of it with a constant probability per unit length (alpha_g), resulting in an exponential decay from distance travelled for the transmittance coefficient in the glass:

$$\tau_g = \exp\left(\frac{-\alpha_g d_g}{\cos(\theta_2)}\right)$$

Then, when it arrives at the 2nd boundary, it reflects and transmits again with Fresnel equations. The reflected light is trapped inside the glass, reflecting infinite times between the 2 boundaries until completely absorbed. The total reflection and transmission coefficients of the system are then the sum of an infinite geometrical series, for which the result is:

$$T_g = \frac{t_1 \tau_g t_2}{1 - r_1 r_2 \tau_g^2}$$

$$R_g = r_1 + \frac{t_1^2 \tau_g^2 r_2}{1 - r_1 r_2 \tau_g^2}$$

$$A_g = 1 - T_g - R_g$$

Finally, the total optical coefficients for the glass are:

```
sscv_hybrid_solar_panel_plot_optics;
```
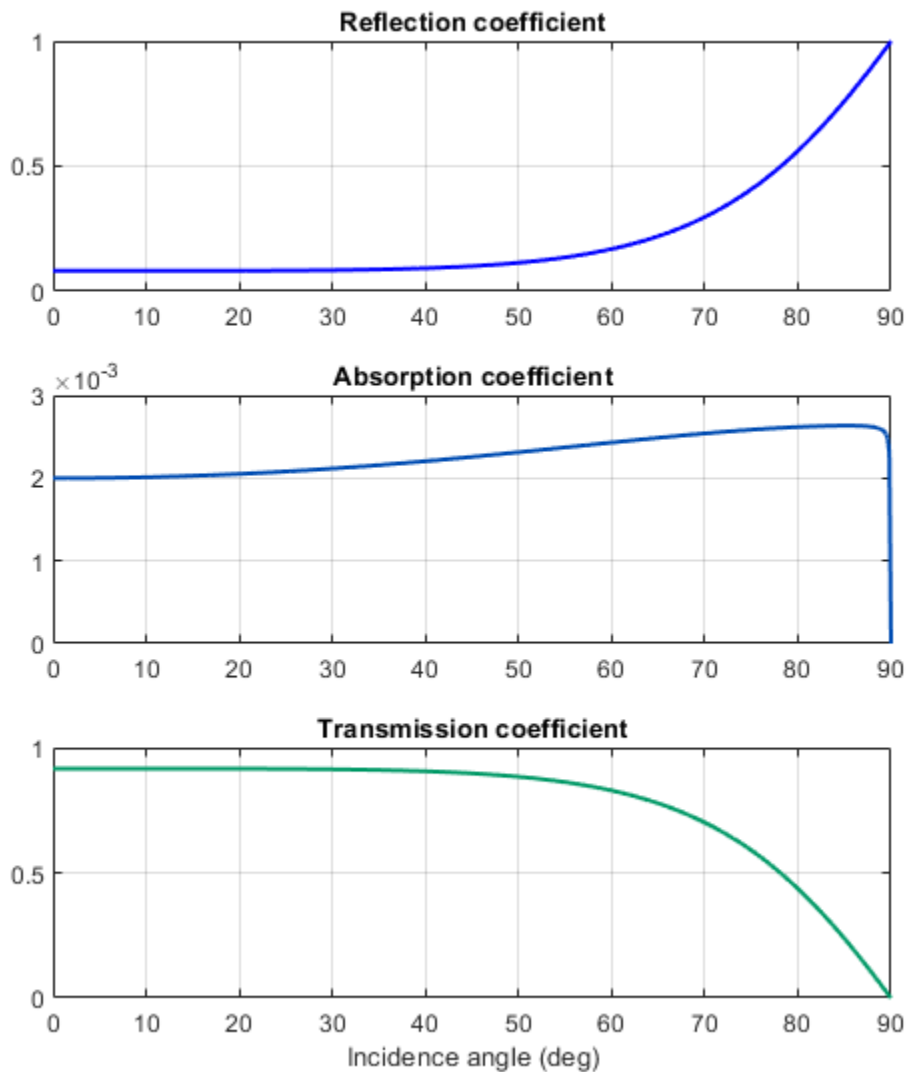

Outputs

The outputs of the model are the temperatures of all components of the panel, the electrical and thermal power, and the volume in the tank.

You can use the script hybrid_solar_panel_plot_outputs to plot the solution:

```
sscv_hybrid_solar_panel_plot_outputs;
```
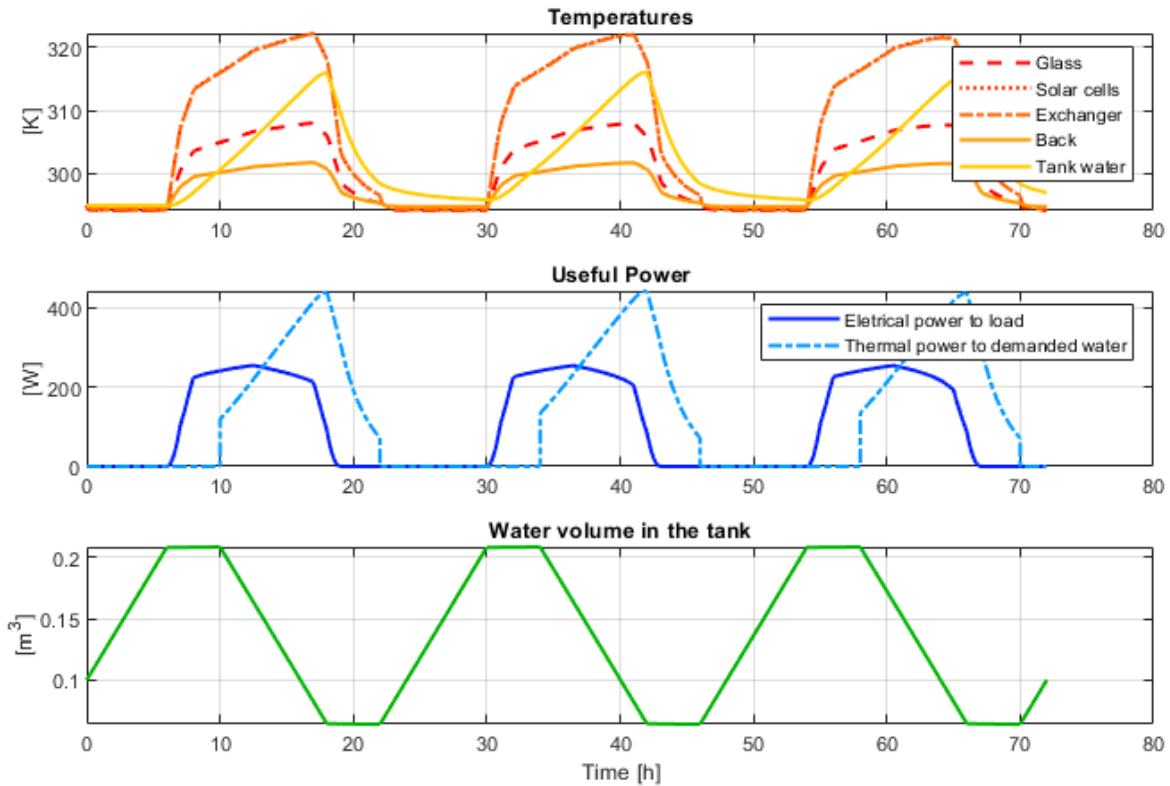
Efficiency calculation

From the outputs it is possible to calculate the electrical, thermal, and total efficiency of the panel:

```
sscv_hybrid_solar_panel_efficiency;
```

```
****** Efficiency Calculation *********

Total input energy from the sun in the period: 43.7869 kWh
Average input energy from the sun per day: 14.5956 kWh/day

Total electrical energy supplied to the load: 7.5161 kWh
Average electrical energy supplied per day: 2.5054 kWh/day

Total absolute thermal energy in the water supplied to the user: 26.1105 kWh
Total absolute thermal energy in the water extracted from the source: 16.5053 kWh

Total used thermal energy (sink - source): 9.6052 kWh
Average used thermal energy per day (sink - source): 3.2017 kWh/day

Electrical efficiency: 0.17165
Thermal efficiency: 0.21936
Total efficiency: 0.39101

**************************************
```

The electrical efficiency is on the order of standard PV cells, but adding the thermal efficiency the production of energy is significantly better, with a system efficiency on the order of a cogeneration plant.

A further analysis could use Simulink® Design Optimization™ or other optimization tools to find optimal values for certain parameters eligible for control, maximizing total efficiency.

Another improvement would be the addition of controllers to the pumps and the electrical load, in order to drive the system to different operating points and optimize the performance.

See Also

Solar Cell

Modeling Machines

- "Machine Parameterization" on page 4-2
- "Per-Unit Conversion for Machine Parameters" on page 4-3
- "Machine Plotting and Display Options" on page 4-4
- "Initialize Synchronous Machines and Controllers" on page 4-6

Machine Parameterization

In Simscape Electrical software, induction machines are parameterized using fundamental parameters. Each synchronous machine is parameterized using standard or fundamental parameters.

Machine fundamental parameters include the values of inductances and resistances of the stator and rotor *d*- and *q*-axis equivalent circuits. These parameters fully specify the electrical characteristics of the machine, but you cannot determine them directly from machine test responses. Hence, it is more common to parameterize a synchronous machine using a standard parameter set. You can obtain the standard parameters by observing responses at the machine terminals with suitable tests scenarios.

You can tell the parameter set a block uses because the block name includes the parameter set name, e.g. Induction Machine Squirrel Cage. The parameters you can set in the block dialog box correspond to the parameterization type.

If a machine block has standard and fundamental variants, base your block choice on the parameters you are most familiar with or you have available. Standard block variants use classical equations to convert standard parameter values that you enter to fundamental parameter values for use at run time.

If a machine block has an SI and a per-unit variant, base your block choice on the parameters you have available. For machine blocks that are SI variants, you enter the number of pole pairs and the SI values for the nominal voltage, power, and frequency on the main tab of the dialog box. You also enter SI values for the resistance and reactance parameters on the impedance tab, and for the magnetic flux linkage parameters on the initial condition tab. The block uses classical equations to calculate per-unit base values from the parameters on the main tab. It expresses the resistance, inductance, and magnetic flux linkage parameters as per-unit ratios of the SI values (resistance, reactance, and magnetic flux linkage) and the base values for use at run time.

The field circuit and rotational ports of machine blocks use SI units. However, the pu measurement port of machine blocks outputs a vector of physical signals in per-unit.

See Also

More About
- "Per-Unit System of Units" on page 1-9
- "Per-Unit Conversion for Machine Parameters" on page 4-3

Per-Unit Conversion for Machine Parameters

Impedance Conversion Equations

For machine impedance parameters (resistance, inductance, and reactance), the relationships between SI and per-unit values are defined by these equations:

$$R = \frac{R_{(SI)}}{R_{base}}$$

$$L = X = \frac{X_{(SI)}}{X_{base}}$$

where:

- $R_{(SI)}$ is the resistance, expressed in Ω.
- R_{base} is the per-unit base resistance, expressed in Ω.
- R is the per-unit resistance.
- $X_{(SI)}$ is the reactance, expressed in Ω.
- X_{base} is the per-unit base reactance, expressed in Ω.
- X is the per-unit reactance.
- L is the per-unit inductance.

Magnetic Flux Linkage Conversion Equations

For machine magnetic flux linkage parameters, the relationship between SI and per-unit values is defined by

$$\psi = \frac{\psi_{(SI)}}{\psi_{base}}$$

where:

- $\psi_{(SI)}$ is the magnetic flux linkage, expressed in Wb.
- ψ_{base} is the per-unit base magnetic flux linkage, expressed in Wb.
- ψ is the per-unit magnetic flux linkage.

See Also

More About

- "Per-Unit System of Units" on page 1-9

Machine Plotting and Display Options

Use the **Electrical** menu on the block context menu to perform plotting and display actions for certain blocks in the Simscape ElectricalElectromechanical sublibrary. For example, you can plot torque versus speed for the Induction Machine Wound Rotor block, either in SI or per-unit units.

Using other options on the **Electrical** menu, you can display values in per-unit or display base parameter values in the MATLAB Command Window. These options enable you to initialize and tune your three-phase machine quickly.

Asynchronous Machine Options

The context menus of certain asynchronous machine blocks contain some or all of these options:

- **Display Base Values** — Displays the machine per-unit base values in the MATLAB Command Window.
- **Plot Torque Speed (SI)** — Plots torque versus speed, both measured in SI units, in a MATLAB figure window using the present machine parameters.
- **Plot Torque Speed (pu)** — Plots torque versus speed, both measured in per-unit, in a MATLAB figure window using the present machine parameters.
- **Plot Open-Circuit Saturation** — Plots terminal voltage versus no-load stator current, both in per-unit, or, for SI blocks, in V and A, respectively, in a MATLAB figure window. The plot contains three traces:
  - Unsaturated
  - Saturated
  - Derived
- **Plot Saturation Factor** — Plots saturation factor applied to magnetic inductance versus magnetic flux linkage in per-unit, or for SI blocks, in Wb, in a MATLAB figure window.
- **Plot Saturated Inductance** — Plots magnetizing inductance versus per-unit magnetic flux linkage, both in per-unit, or, for SI blocks, in H and Wb, respectively, in a MATLAB figure window.

Induction Machine Options

The context menus of certain induction machine blocks contain some or all of these options for displaying the associated values in the MATLAB Command Window:

- **Display Base Values** — Displays the machine per-unit base values in the MATLAB Command Window
- **Display Associated Base Values** — Displays the associated per-unit base values in the MATLAB Command Window.
- **Display Associated Initial Conditions** — Displays the associated intitial condition values in the MATLAB Command Window.
- **Plot Open-Circuit Saturation (pu)** — Plots air-gap voltage, V_{ag}, versus field current, i_{fd}, both measured in per-unit, in a MATLAB figure window. The plot contains three traces:
  - Unsaturated — **Stator d-axis mutual inductance (unsaturated), Ladu** you specify
  - Saturated — **Per-unit open-circuit lookup table (Vag versus ifd)** you specify

- Derived — Open-circuit lookup table (per-unit) derived from the **Per-unit open-circuit lookup table (Vag versus ifd)** you specify. This data is used to calculate the saturation factor, K_s, versus magnetic flux linkage, ψ_{at}, characteristic.

- **Plot Saturation Factor (pu)** — Plots saturation factor, K_s, versus magnetic flux linkage, ψ_{at}, both measured in per-unit, in a MATLAB figure window using the present machine parameters. This value is derived from parameters you specify:

  - **Stator d-axis mutual inductance (unsaturated), Ladu**
  - **Per-unit field current saturation data, ifd**
  - **Per-unit air-gap voltage saturation data, Vag**

Machine Inertia Block Options

For the Machine Inertia block, you can display the inertia parameters and base values using the **Electrical** menu on the block context menu. The block displays parameter values in the MATLAB Command Window.

Initialize Synchronous Machines and Controllers

In Simscape Electrical software, you can specify steady-state power and voltage values for a synchronous machine. Based on the values you specify, the machine block calculates the initial field circuit and rotational input values required to achieve this steady state. Starting a machine at steady state prevents undesired transient effects in your simulation.

1 Calculate the required power and voltage characteristics of your load circuit.

2 In the **Initial Conditions** tab of the dialog box, set **Specify initialization by** to `Electrical power and voltage output`.

3 Enter the required power and voltage values and click **OK**.

4 Right-click the machine block and select **Electrical > Display Associated Initial Conditions**.

   Simscape Electrical Power Systems calculates the field circuit and rotational port values required to start the machine in steady state and displays them in the MATLAB Command Window.

5 Use these values to input parameters to the blocks connected to the field circuit and rotational ports of the synchronous machine.

Note If you set **Specify initialization by** to `Mechanical and magnetic states`, Simulink does not calculate the associated initial conditions fro the machine.

Customization

Build Custom Blocks Using the Three-Phase Electrical Domain

Simscape Foundation domains include a three-phase electrical domain. You can use this domain to develop your own custom three-phase blocks using Simscape language. To refer to this domain in your custom component declarations, use the following syntax:

```
foundation.electrical.three_phase
```

Additionally, the ThreePhaseExamples library, included in the Simscape Electrical product examples, contains a Fundamental library, a Transforms library, and the following custom three-phase components:

- Permanent Magnet Synchronous Motor
- Synchronous Machine
- Synchronous Machine (simplified)
- Zigzag Transformer

You can use these simplified example models to write your own custom component files.

To open the custom library, at the MATLAB command prompt, type `ThreePhaseExamples_lib`. Double-click any block in the library to open its dialog box, and then click the **Source code** link in the block dialog box to open the Simscape source file for this block in the MATLAB Editor.

To customize the block for your application, edit the source file and save it under another name.

For example, you can create a folder called `+MyMachines` and save the source files for your customized machines in this folder. Create this folder in your working directory, or in another directory that is on the MATLAB path. Running the `ssc_build` command on this package generates the `MyMachines_lib` library model. This library contains all your custom machine blocks and is located in the same directory where you have created the `+MyMachines` folder. Open the `MyMachines_lib` library by double-clicking it or by typing its name at the MATLAB command prompt.

For more information on packaging and deploying Simscape component files, see "Building Custom Block Libraries".

Things to keep in mind when writing component files:

- If you create a custom component by modifying an existing one, do not forget to change the name of the component and the name of the resulting block.
- The component name must be the same as the name of the Simscape file. For example, if you plan to save your component in a file called `MyComponent.ssc`, change the declaration line in the file:

  ```
  component MyComponent
  ```
- The comment line immediately following the component declaration (that is, the first line beginning with the % character) defines the name of the block, as it appears in the custom library next to the block icon and at the top of the block dialog box. If you do not specify this comment, then the component name serves as the block name. The block name must be unique within the subpackage (sublibrary) where it resides.
- Additional comments, below the line specifying the block name, are interpreted as the block description. You do not have to modify them when copying an existing file, but if you change the way the component works, it makes sense to reflect the change in the block description. The block description is for informational purposes only.

- When modifying component equations, if you introduce additional terms, make sure to add the appropriate variables or parameters to the component declaration section. For example, if you add zero-sequence dynamics to the component equations, declare an additional parameter for stator zero-sequence inductance, L0, and an additional variable for the initial stator zero-sequence magnetic flux linkage.

The "Custom Synchronous Machine" on page 5-4 tutorial shows how you can modify the Synchronous Machine component file and customize it for use in your applications. For more information on writing customized component files, see "Custom Components".

See Also

More About
- "Custom Synchronous Machine" on page 5-4
- "Custom Components"
- "Foundation Domains"

Custom Synchronous Machine

The ThreePhaseExamples library, included in the product examples, contains simplified example models that you can use to write your own machine and transformer component files. The Synchronous Machine component in the ThreePhaseExamples library is similar to the Synchronous Machine Round Rotor block, but its equations have been simplified to omit zero-sequence dynamics. The Synchronous Machine block is therefore suitable for balanced operation only.

This example shows how you can further simplify the component file and make a custom machine block that does not account for the stator rate of change of flux.

1 In your working directory, create a folder called +MyMachines. This folder will contain the source files for your customized machines.

2 To open the library of simplified component examples, at the MATLAB command prompt, type:

```
ThreePhaseExamples_lib
```
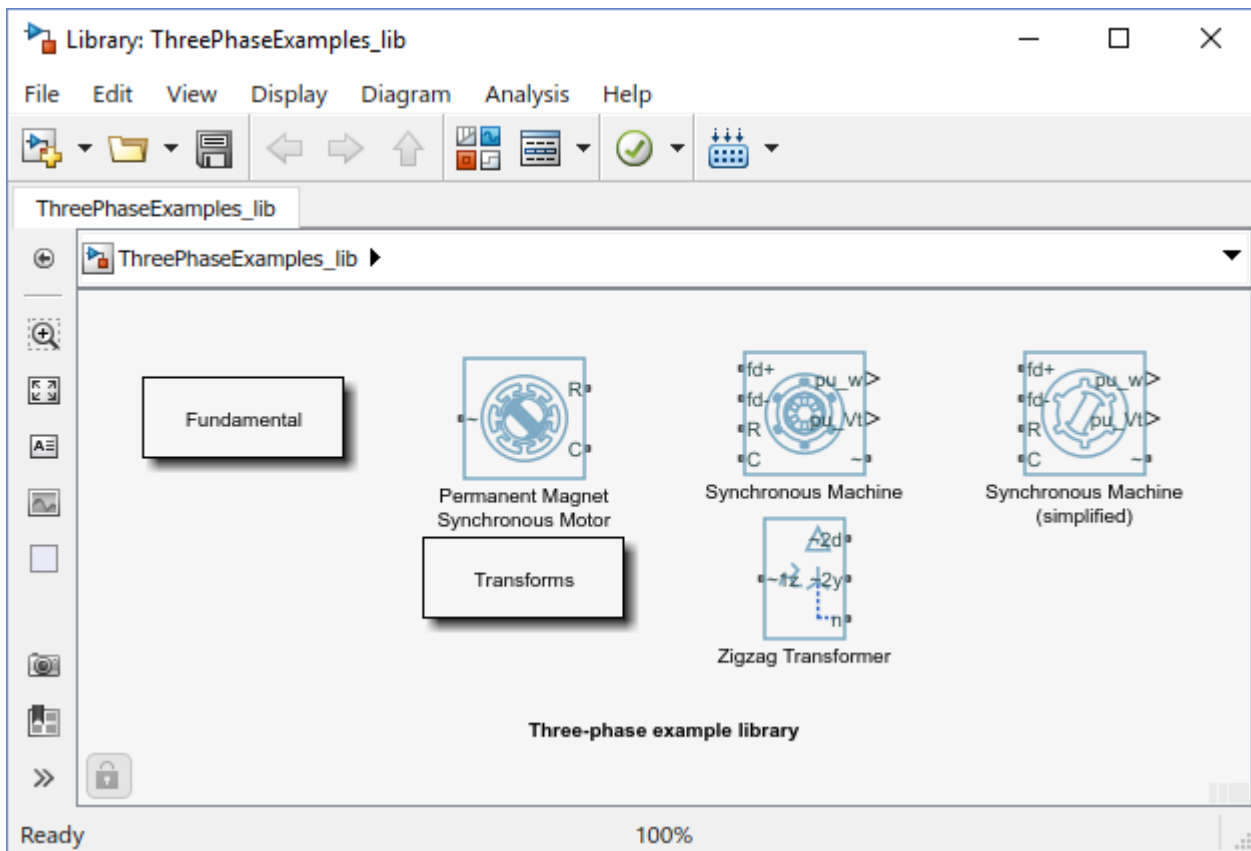

3 Double-click the Synchronous Machine block.

4 In the block dialog box, click the **Source code** link.

The Simscape source file for this block opens in the MATLAB Editor.

5 Change the name of the component, the name of the block, and the block description by replacing these lines of the file:

```
component sm
% Synchronous Machine :1.5
```

```
% Synchronous machine (SM) with a round rotor parameterized
% using fundamental per-unit parameters. The defining equations are
% simplified by omitting the zero-sequence dynamics: the model is suitable
% for balanced operation.
% The model contains effect of rate of change of magnetic flux linkages
% on stator voltages, effect of speed variation on stator voltages, one
% damper winding on the d-axis and two damper windings on the q-axis.

% Copyright 2012-2018 The MathWorks, Inc.
```

with:

```
component sm1
% Simplified Synchronous Machine
% This synchronous machine does not include the stator d.psi/dt terms.
```

6 To remove the stator rate of change of flux terms, scroll down to the `equations` section and modify the stator voltage equations from:

```
% Per unit stator voltage equations
pu_ed == oneOverOmega*pu_psid.der - pu_psiq*pu_velocity - Ra*pu_id;
pu_eq == oneOverOmega*pu_psiq.der + pu_psid*pu_velocity - Ra*pu_iq;
```

to:

```
% Per unit stator voltage equations
pu_ed == -pu_psiq*pu_velocity - Ra*pu_id;
pu_eq ==  pu_psid*pu_velocity - Ra*pu_iq;
```

7 Save the file in the **+MyMachines** folder as `sm1.ssc`. The name of the Simscape file must match the component name.

8 To generate the custom library containing the new block, at the MATLAB command prompt, type:

```
ssc_build(MyMachines)
```

This command generates the `MyMachines_lib` library model in your working directory.

9 To open the custom library, at the MATLAB command prompt, type:

```
MyMachines_lib
```

The library contains the Simplified Synchronous Machine block, which you can now use in your models.
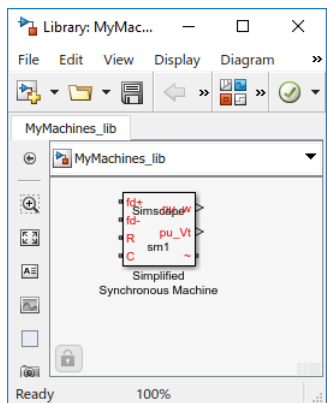
See Also

`ssc_build`

More About

- "Build Custom Blocks Using the Three-Phase Electrical Domain" on page 5-2
- "Custom Components"
- "Customizing the Block Name and Appearance"
- "Component Equations"

Control

Tune an Electric Drive

In this section...
"Cascade Control Structure" on page 6-2
"Equations for PI Tuning Using the Pole Placement Method" on page 6-2
"Equations for DC Motor Controller Tuning" on page 6-4
"Tune the Electric Drive in the Example Model" on page 6-6

This example shows how to tune an electric drive using a cascade control structure.

Cascade Control Structure

The figure shows a feedback control loop that uses a cascade control structure. The outer speed-control loop is slower acting than the inner current-control loop.
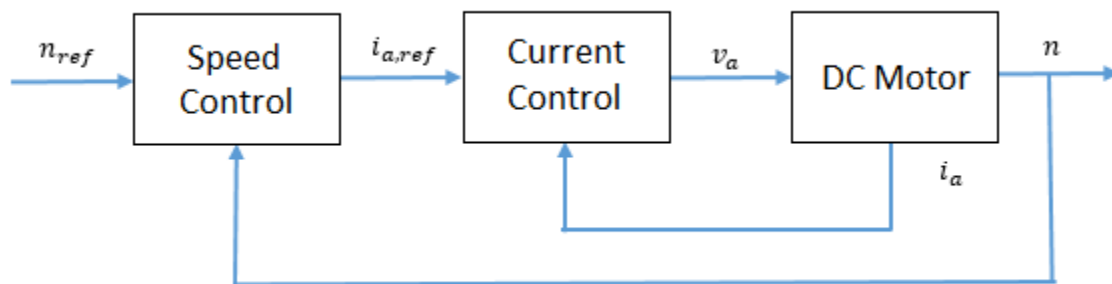
Equations for PI Tuning Using the Pole Placement Method

To satisfy the required control performance for a simple discrete plant model, $G_f(z^{-1})$, use a closed loop PI control system $G_{PI}(z^{-1})$. The transient performance can be expressed in terms of the overshoot. The overshoot decreases relative to the damping factor:

$$\sigma = e^{\frac{-\pi\xi}{\sqrt{1-\xi^2}}}$$

where,

- σ is overshoot.
- ξ is the damping factor.

The response time, t_r, depends on the damping and the natural frequency, ω_n, such that:

- If $\xi < 0.7$,

$$t_r \cong \frac{4}{\omega_n\xi}.$$

- If $\xi \geq 0.7$,

$$t_r \cong \frac{6\xi}{\omega_n}.$$

The general workflow for designing a PI controller for a first-order system is:

1 Discretize the plant model using the zero-order hold (ZOH) discretization method. That is, given that the first-order equation representing the plant is

$$G(s) = \frac{K_m}{T_m s + 1},$$

where,

- K_m is the first-order gain.
- T_m is time constant of the first-order system.

Setting

$$s = \frac{1 - z^{-1}}{z^{-1} T_s},$$

yields the discrete plant model,

$$G(z^{-1}) = \frac{K_m \left(\frac{T_s}{T_m}\right) z^{-1}}{1 + \left(\frac{T_s - T_m}{T_m}\right) z^{-1}} = \frac{b_1 z^{-1}}{1 + a_1 z^{-1}},$$

where T_s is sample time for the discrete-time controller.

2 Write a discrete-time representation for the PI controller using the same transform. For

$$G_{PI}(s) = K_P + K_I \left(\frac{1}{s}\right),$$

setting

$$s = \frac{1 - z^{-1}}{z^{-1} T_s},$$

yields the discrete controller model,

$$G_{PI}(z^{-1}) = \frac{K_P + (K_I T_s - K_P) z^{-1}}{1 - z^{-1}} = \frac{q_0 + q_1 z^{-1}}{1 - z^{-1}}.$$

Combining the discrete equations for the plant and the controller yields the closed loop transfer function for the system,

$$G_0(z^{-1}) = \frac{q_0 b_1 z^{-1} + q_1 b_1 z^{-2}}{1 + (a_1 - 1 + q_0 b_1) z^{-1} + (-a_1 + q_1 b_1) z^{-2}},$$

The denominator of the transfer function is the characteristic polynomial. That is,

$$P_{c0}(z^{-1}) = 1 + (a_1 - 1 + q_0 b_1) z^{-1} + (-a_1 + q_1 b_1) z^{-2}.$$

3 The characteristic polynomial for achieving the required performance is defined as

$$P_{cd}(z^{-1}) = 1 + \alpha_1 z^{-1} + \alpha_2 z^{-2},$$

where,

- $\alpha_1 = -2e^{-\xi \omega_n T_s} \cos\left(\omega_n T_s \sqrt{1 - \xi^2}\right).$
- $\alpha_2 = e^{-2\xi \omega_n T_s}.$

4 To determine the controller parameters, set the characteristic polynomial for the system equal to the characteristic polynomial for the required performance. If

$$P_{c0}(z^{-1}) = P_{cd}(z^{-1}),$$

then

$$\alpha_1 = a_1 - 1 + q_0 b_1$$

and

$$\alpha_2 = -a_1 + q_1 b_1.$$

Solving for q_0 and q_1 yields

$$q_0 = \frac{\alpha_1 - a_1 + 1}{b_1}$$

and

$$q_1 = \frac{\alpha_2 + a_1}{b_1}.$$

Therefore, the general equations for the proportional and integral control parameters for the first-order system are

$$K_P = q_0$$

and

$$K_I = \frac{q_1 + K_p}{T_s}.$$

Equations for DC Motor Controller Tuning

Assuming that, for the system in the example model, $K_b = K_t$, the simplified mathematical equations for voltage and torque of the DC motor are

$$v_a = L_a \frac{di_a}{dt} + R_a i_a + K_b \omega$$

and

$$T_e = J_m \frac{d\omega}{dt} + B_m \omega + T_{load} = K_b i_a,$$

where:

- v_a is the armature voltage.
- i_a is the armature current.
- L_a is the armature inductance.
- R_a is the armature resistance.
- ω is the rotor angular velocity
- T_e is the motor torque.
- T_{load} is the load torque.
- J_m is the rotor moment of inertia.
- B_m is the viscous friction coefficient.
- K_b is a constant of proportionality.

To tune the current controller, assume that the model is linear, that is, that the back electromotive force, as represented by $K_b\omega$, is negligible. This assumption allows for an approximation of the plant model using this first-order Laplace equation:

$$G_i(s) = \frac{\frac{1}{R_a}}{\left(\frac{L_a}{R_a}\right)s + 1}.$$

Given the system requirements, you can now solve for K_P and K_I. The requirements for the current controller in the example model are:

- Sample time, T_s = 1 ms.
- Overshoot, σ = 5%.
- Response time, t_r = 0.11 s.

Therefore, the proportional and integral parameters for the current controller are:

- K_P = 7.7099.
- K_I = 455.1491.

To tune the speed controller, approximate the plant model with a simple model. First assume that the inner loop is much faster than the outer loop. Also assume that there is no steady-state error. These assumptions allow for the use a first-order system by considering a transfer function of 1 for the inner current loop.

To output rotational velocity in revolutions per minute, the transfer function is multiplied by a factor of 30/π. To take as control input the armature current instead of the motor torque, the transfer function is multiplied by the proportionality constant, K_b. The resulting approximation for the outer-loop plant model is

$$G_n(s) = \frac{\frac{30K_b}{\pi B_m}}{\left(\frac{J_m}{B_m}\right)s + 1}.$$

The speed controller has the same sample time and overshoot requirements as the current controller, but the response time is slower, such that:

- Sample time T_s = 1 ms.
- Overshoot σ = 5%.
- Response time t_r = 0.50 s.

Therefore, the proportional and integral parameters for the speed controller are:

- K_P = 0.0045
- K_I = 0.0405

Tune the Electric Drive in the Example Model

1 Explore the components of the DC motor and the cascaded controller.

a Open the model. At the MATLAB command prompt, enter

```
model = 'ee_dc_motor_control'
open_system(model)
```
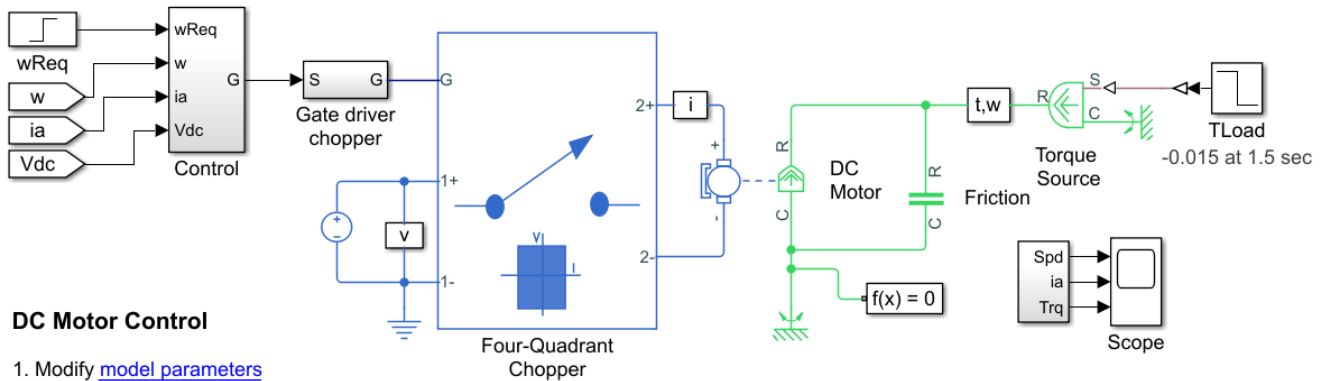

DC Motor Control

1. Modify model parameters
2. Explore simulation results using sscexplore
3. Learn more about this example

b The **Control** subsystem contains the model of the cascaded control system built using blocks from the Simulink library.
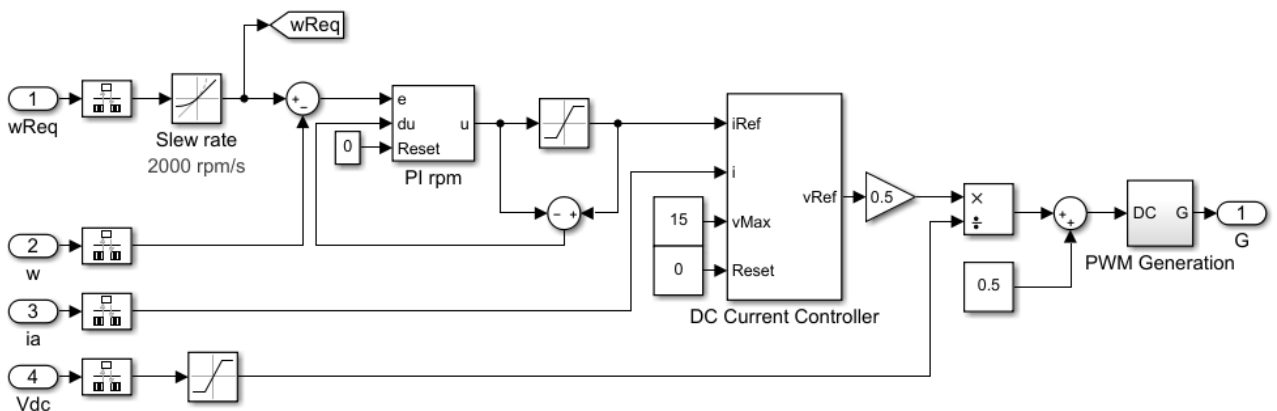
c The Four Quadrant Chopper block represents a four-quadrant DC-DC chopper that contains two bridge arms, each of which has two IGBT (Ideal, Switching) blocks. When the input

voltage exceeds the threshold of `0.5` V, the IGBT (Ideal, Switching) blocks behave like linear diodes with a forward-voltage of `0.8  V` and a resistance of `1e-4` ohm. When the threshold voltage is not exceeded, the IGBT (Ideal, Switching) blocks act like linear resistors with an off-state conductance of `1e-5` 1/ohm.

2   Simulate the model.

```
sim(model)
```

3   View the results. Open the **Scope** block.
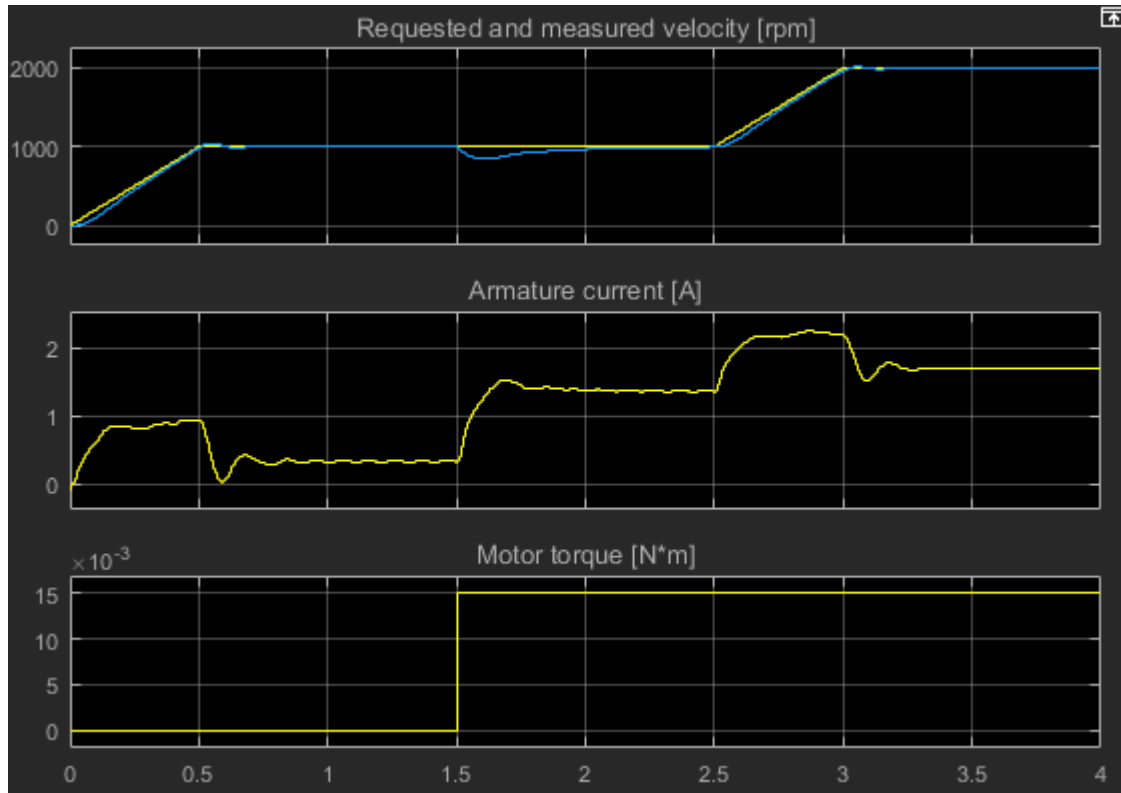
At 1.5 seconds, there is a load torque that results in a steady-state error.

4   Tune the DC motor controller. The `ee_getDCMotorFirstOrderPIParams` function calculates the proportional gain, K_P, and the integral gain, K_I, for the first-order system in this example.

The function syntax is `[Kp, Ki] = getParamPI(Km,Tm,Ts,sigma,tr)`.

The input arguments for the function are the system parameters and the requirements for the controller:

- `Km` is the first-order gain.
- `Tm` is the time constant of the first-order system.
- T_s is the sample time for the discrete-time controller.
- `sigma` is the desired maximum overshoot, σ.
- t_r is the desired response time.

a   To examine the equations in the function, enter

```
edit ee_getDCMotorFirstOrderPIParams
```

b To calculate the controller parameters using the function, save these system parameters to the workspace:

```
Ra=4.67;              % [Ohm]
La=170e-3;              % [H]
Bm=47.3e-6;           % [N*m/(rad/s)]
Jm=42.6e-6;           % [Kg*m^2]
Kb=14.7e-3;           % [V/(rad/s)]
Tsc=1e-3;              % [s]
```

c Calculate the parameters for tuning the current controller as a function of the parameters and requirements for the inner controller:

- Km = 1/Ra.
- Tm = La/Ra.
- Ts = Tsc.
- sigma = 0.05.
- Tr = 0.11.

```
[Kp_i, Ki_i] = ee_getDCMotorFirstOrderPIParams(1/Ra,La/Ra,Tsc,0.05,0.11)

Kp_i =

    7.7099


Ki_i =

  455.1491
```

The gain parameters for the current controller are saved to the workspace.

d Calculate the parameters for tuning the speed controller based on the parameters and requirements for the outer controller:

- Km = Kb*(30/pi).
- Tm = Jm/Ra.
- Ts = Tsc.
- sigma = 0.05.
- Tr = 0.5.

```
[Kp_n, Ki_n] = ee_getDCMotorFirstOrderPIParams((Kb*(30/pi))/Bm,Jm/Bm,Tsc,0.05,0.5)

Kp_n =

    0.0045


Ki_n =

    0.0405
```

The gain parameters for the speed controller are saved to the workspace.

5   Simulate the model using the saved gain parameters for the speed and controllers.

    `sim(model)`

6   View the results. Open the **Scope** block.
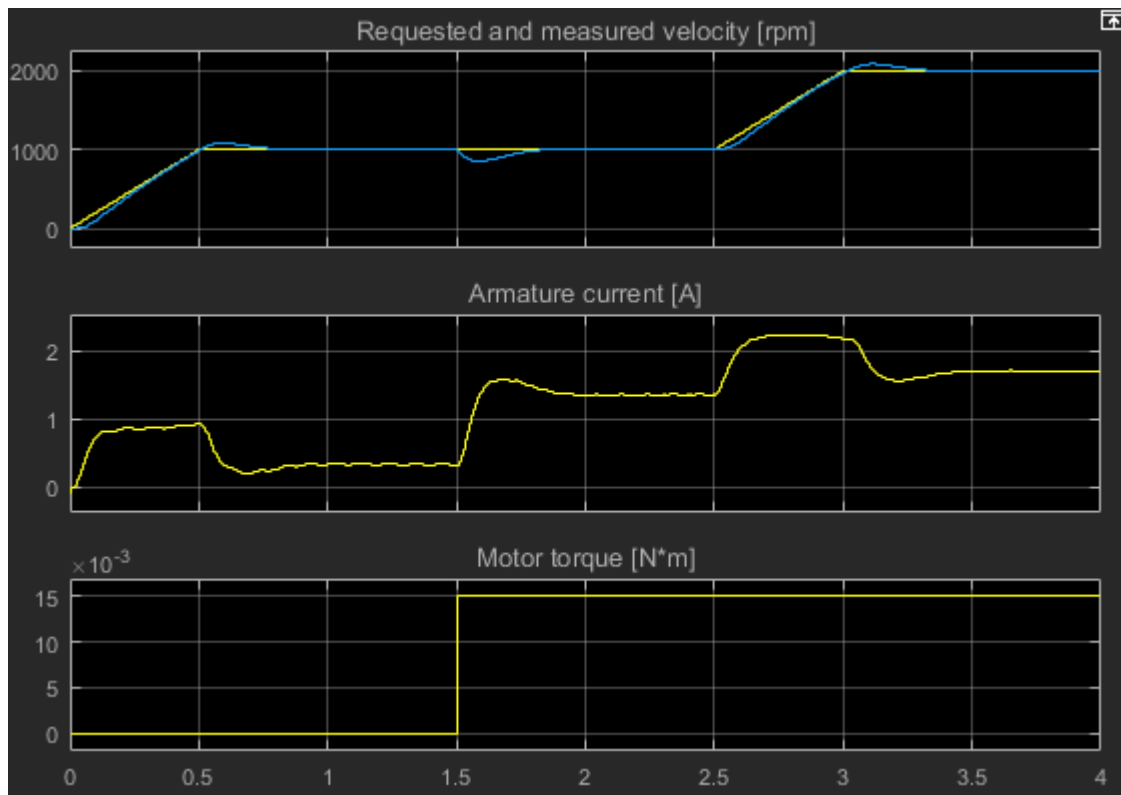
There is slightly more overshoot, however, the controller responds much faster to the load torque change.

See Also

Inertia | Rotational Electromechanical Converter | Rotational Friction

Related Examples

- "DC Motor Control" on page 10-476

Simulation and Analysis of Power Engineering Systems

Optimize Block Settings for Simulating with the Partitioning Solver

In this section...
"Update Solver and Zero-Sequence Settings Using the ee_solverUpdate Function" on page 7-2
"Limitations of the ee_updateSolver Function" on page 7-9

The Partitioning solver is a Simscape fixed-step local solver that improves performance for certain models. However, not all networks can simulate with the Partitioning solver. Some models that use the Partitioning solver can produce errors and fail to initialize due to numerical difficulties. To resolve numerical difficulties preventing initialization with asynchronous, synchronous, and permanent magnet rotor machine blocks, you can exclude zero-sequence terms. Excluding parasitic conductance resolves numerical difficulties with the Floating Neutral (Three-Phase) and Neutral Connection block, which include such conductance by default.

To determine the best solver choice for your model, use the `ee_updateSolver` helper function, which is useful for iterating with various solvers. The function updates certain parameter values for every instance of these blocks in your model:

- Solver Configuration blocks
- Machine blocks that have a **Zero sequence** parameter
- Connection blocks that have a **Parasitic conductance to ground** parameter

The function syntax is `ee_updateSolver(solver,system)`. Specify both input arguments using character vectors. The table shows how the function updates the values, depending on the solver that you specify.

Input Argument	Solver Configuration Block (*Solver type*)	Solver Configuration Block (*Use local solver* and *Use fixed-cost runtime consistency iterations*)	Asynchronous, Synchronous, and Permanent Magnet Rotor Machine Blocks (*Zero sequence*)	Floating Neutral (Three-Phase) Block and Neutral Connection Block (*Parasitic conductance to ground*)
'Partitioning'	Partitioning	Selected	Exclude	0
'Backward Euler' or 'BackwardEuler'	Backward Euler	Selected	Include	1e-12
'Trapezoidal'	Trapezoidal	Selected	Include	1e-12
'Global' or 'Nonlocal'	No change	Cleared	Include	1e-12

Update Solver and Zero-Sequence Settings Using the ee_solverUpdate Function

This example shows how to use the `ee_solverUpdate` function to configure the Solver Configuration and PMSM blocks in a model for simulation with the Partitioning solver and the Backward Euler solver. It also shows how to compare the simulation duration times and the results.

1   Open the model. At the MATLAB command prompt, enter this code.

See Code

```
model = 'ee_pmsm_drive';
open_system(model)
```


Three-Phase PMSM Drive

1. Explore simulation results using sscexplore
2. Learn more about this example

Two blocks that the `ee_solverUpdate` function can update are the Solver Configuration block and PMSM block.

2   Save the parameter settings for the two blocks.

See Code

```
% Define the Solver Configuration block and the path
%    to it as variables
solvConfig = 'Solver Configuration';
solvConfigPath = [model,'/',solvConfig];

% Define the machine block and the path
%    to it as variables
machine = 'Permanent Magnet Synchronous Motor';
machinePath = [model,'/',machine];

% Create a cell array that contains configuration data
configBaseline = {'Block','Parameter','Value';
        'Solver Configuration','Use local solver',...
        get_param(solvConfigPath,'UseLocalSolver');
        'Solver Configuration','Solver type',...
```

```
        get_param(solvConfigPath,'LocalSolverChoice');
        'Solver Configuration','Sample time',...
        get_param(solvConfigPath,'LocalSolverSampleTime');
        'Solver Configuration',...
        'Use fixed-cost runtime consistency iterations',...
        get_param(solvConfigPath,'DoFixedCost');
        'Machine','Zero sequence',...
        get_param(machinePath,'zero_sequence')}];
```

The settings are saved to `configBaseline` array in the MATLAB workspace.

configBaseline		
{} 6x3 cell		
1	2	3
1 Block	Parameter	Value
2 Solver Configuration	Use local solver	off
3 Solver Configuration	Solver type	NE_BACKWARD_EULER_ADVANCER
4 Solver Configuration	Sample time	Ts
5 Solver Configuration	Use fixed-cost runtime consistency iterations	off
6 Machine	Zero sequence	pe.enum.park.zerosequence.include

The settings of interest for the Solver Configuration block are:

- **Use local solver** — The option to use a local Simscape solver is cleared.
- **Solver type** — Backward Euler, a Simscape local fixed- cost solver, is specified. However, if you open the block dialog box, you can see that it is not enabled because the option to use a local solver is cleared.
- **Use fixed-cost runtime consistency iterations** — The option to use fixed-cost is cleared. This option is also disabled when the option to use a local solver is cleared.

For the machine, the **Zero sequence** parameter is set to `Include`. Zero-sequence equations can cause numerical difficulty when you simulate with the Partitioning solver.

3   To return all simulation outputs within a single `Simulink.SimulationOutput` object so that you can later compare simulation times, enable the single-output format of the `sim` command.

```
% Enable single-output format
set_param(model,'ReturnWorkspaceOutputs', 'on')
```

4   Mark the rotor torque signal, which connects the **trqMotor** From block to a Mux block, for Simulation Data Logging and viewing with the Simulation Data Inspector.

See Code

```
% Define the trqMotor From block and the path
%    to it as variables
torqueSensor = 'From6';
signalSubsystem = 'Signals';
torqueSensorPath = [model,'/',signalSubsystem,'/',torqueSensor];

% Mark the output signal from the trqMotor From block
%   for Simulink(R) data logging
```

```
phTorqueSensor = get_param(torqueSensorPath,'PortHandles');
set_param(phTorqueSensor.Outport(1),'DataLogging','on')
```

The logging badge 📶 marks the signal in the model.

5 Determine the results and how long it takes to simulate with the baseline settings.

See Code

```
% Run a timed simulation using the Baseline solver configuration
out = sim(model);
tBaseline  = out.SimulationMetadata.TimingInfo.ExecutionElapsedWallTime;
```

6 Use `ee_updateSolver` function to change to the Backward Euler solver configuration. Save the configuration settings, and compare the settings to the baseline settings.

See Code
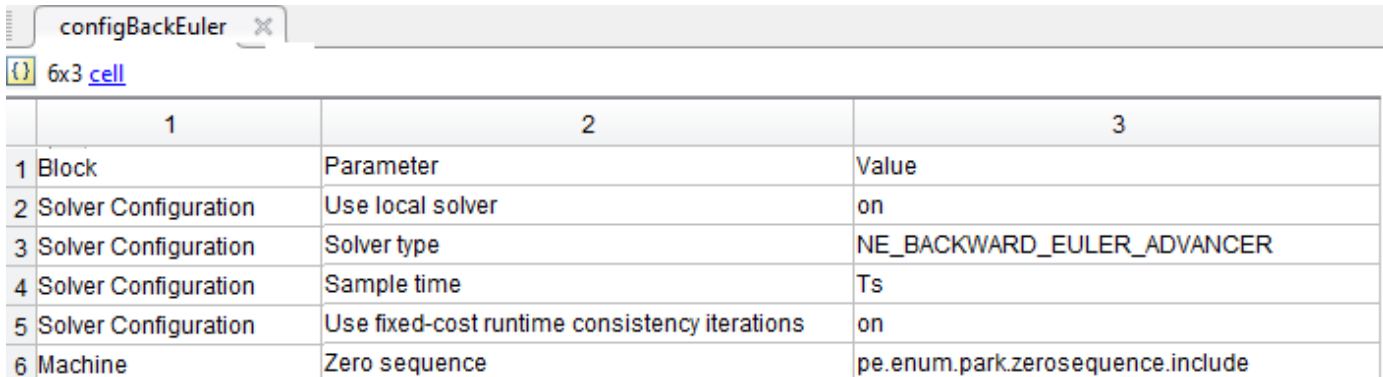
```
% Configure for Backward Euler solver simulation
ee_updateSolver('Backward Euler',model)

% Save the new parameter settings and compare them to the baseline
%   configuration.

% Create a cell array that contains configuration data
configBackEuler = {'Block','Parameter','Value';
        'Solver Configuration','Use local solver',...
        get_param(solvConfigPath,'UseLocalSolver');
        'Solver Configuration','Solver type',...
        get_param(solvConfigPath,'LocalSolverChoice');
        'Solver Configuration','Sample time',...
        get_param(solvConfigPath,'LocalSolverSampleTime');
        'Solver Configuration',...
        'Use fixed-cost runtime consistency iterations',...
        get_param(solvConfigPath,'DoFixedCost');
        'Machine','Zero sequence',...
        get_param(machinePath,'zero_sequence')};

% Compare the Partitioning solver block settings to the Baseline settings
configDiff = setdiff(configBackEuler,configBaseline)

configDiff =

  1×1 cell array

        {'on'}
```

	1	2	3
	Block	Parameter	Value
1	Block	Parameter	Value
2	Solver Configuration	Use local solver	on
3	Solver Configuration	Solver type	NE_BACKWARD_EULER_ADVANCER
4	Solver Configuration	Sample time	Ts
5	Solver Configuration	Use fixed-cost runtime consistency iterations	on
6	Machine	Zero sequence	pe.enum.park.zerosequence.include

configBackEuler ✕

{} 6x3 cell

The option to use the local solver, which is set to Backward Euler by default, and the option to use fixed-cost runtime consistency iterations are now both selected.

7 Run a timed simulation using the Backward Euler solver.

See Code

```
out = sim(model);
tBackEuler = out.SimulationMetadata.TimingInfo.ExecutionElapsedWallTime;
```

8   If you change the local solver to the Partitioning solver and simulate the model now, an error occurs because of the zero-sequence terms. Use the `ee_updateSolver` function to configure the model for simulating with the Partitioning solver without generating an error. Save the configuration settings, compare the settings to baseline settings, and run a timed simulation.

See Code

```
% Configure for Partioning solver simulation
ee_updateSolver('Partitioning', model)

% Create a cell array that contains configuration data
configPartitioning = {'Block','Parameter','Value';
         'Solver Configuration','Use local solver',...
         get_param(solvConfigPath,'UseLocalSolver');
         'Solver Configuration','Solver type',...
         get_param(solvConfigPath,'LocalSolverChoice');
         'Solver Configuration','Sample time',...
         get_param(solvConfigPath,'LocalSolverSampleTime');
         'Solver Configuration',...
         'Use fixed-cost runtime consistency iterations',...
         get_param(solvConfigPath,'DoFixedCost');
         'Machine','Zero sequence',...
         get_param(machinePath,'zero_sequence')};

% Compare the Partitioning solver block settings to the Baseline settings
configDiff = setdiff(configPartitioning,configBaseline)

%  Run a timed simulation using the Partitioning solver
out = sim(model);
tPartitioning = out.SimulationMetadata.TimingInfo.ExecutionElapsedWallTime;

configDiff =

  3×1 cell array

    {'NE_PARTITIONING_ADVANCER'        }
    {'ee.enum.park.zerosequence.exclude'}
    {'on'                              }

Warning: Initial conditions for nondifferential variables
not supported. The following states may deviate from
requested initial conditions:
    ['<a
    href="matlab:open_and_hilite_system('ee_pmsm_drive/Battery')"...
        >ee_pmsm_drive/Battery</a>']
    Battery.num_cycles
      o In ee.sources.battery_base
    ['<a
    href="matlab:open_and_hilite_system('ee_pmsm_drive/Permanent
    Magnet Synchronous Motor')">ee_pmsm_drive/Permanent
    Magnet Synchronous Motor</a>']
    Permanent_Magnet_Synchronous_Motor.angular_position
```

	1	2	3
	configPartitioning ✕		
	{} 6x3 cell		
	1	2	3
1	Block	Parameter	Value
2	Solver Configuration	Use local solver	on
3	Solver Configuration	Solver type	NE_PARTITIONING_ADVANCER
4	Solver Configuration	Sample time	Ts
5	Solver Configuration	Use fixed-cost runtime consistency iterations	on
6	Machine	Zero sequence	pe.enum.park.zerosequence.exclude

The solver type is now set to the Partitioning solver and the machine is configured to exclude zero-sequence terms.

The simulation runs without generating an error. It does generate a warning because initial conditions for nondifferential variables are not supported for the Partitioning solver.

9 Print tables that show:

- Simulation time for each solver
- Percent differences in speed for the local solvers versus the baseline global solver.

See Code

```
% Display the simulation times
compTimeDiffTable = table({'Baseline';...
    'Backward Euler';...
    'Partitioning'},...
    {tBaseline;tBackEuler;tPartitioning},...
'VariableNames', {'Solver','Sim_Duration'});

display(compTimeDiffTable);

% Compute and display the percent difference for the simulation times
spdBackEulerVsBaseline = 100*(tBaseline - tBackEuler)/tBaseline;
spdPartitionVsBaseline = 100*(tBaseline - tPartitioning)/tBaseline;

compPctDiffTable = table({'Backward Euler versus Baseline';...
    'Partitioning versus Baseline'},...
    {spdBackEulerVsBaseline;...
    spdPartitionVsBaseline},...
'VariableNames', {'Comparison','Percent_Difference'});

display(compPctDiffTable);


    3×2 table

        Solver          Sim_Duration
    _____    _____

    'Baseline'          [38.0255]
    'Backward Euler'    [23.4011]
    'Partitioning'      [ 9.2042]
```

```
compPctDiffTable =

  2×2 table

              Comparison                  Percent_Difference
    _____   _____

    'Backward Euler versus Baseline'           [38.4594]
    'Partitioning versus Baseline'             [75.7946]
```

Simulation time on your machine may differ because simulation speed depends on machine processing power and the computational cost of concurrent processes. The local fixed-step Partitioning and Backward Euler solvers are faster than the baseline solver, which is a global, variable-step solver. The Partitioning solver is faster than the Backward Euler solver.

10 To compare the results, open the Simulation Data Inspector.

See Code

```
% Get Simulation Data Inspector run IDs for
%    the last three runs
runIDs = Simulink.sdi.getAllRunIDs;
runBackEuler = runIDs(end - 1);
runPartition = runIDs(end);

% Open the Simulation Data Inspector
Simulink.sdi.view

compBaselinePartition = Simulink.sdi.compareRuns(runBackEuler,...
    runPartition);
```

To see the comparison, click **Compare** and then click **From6**.
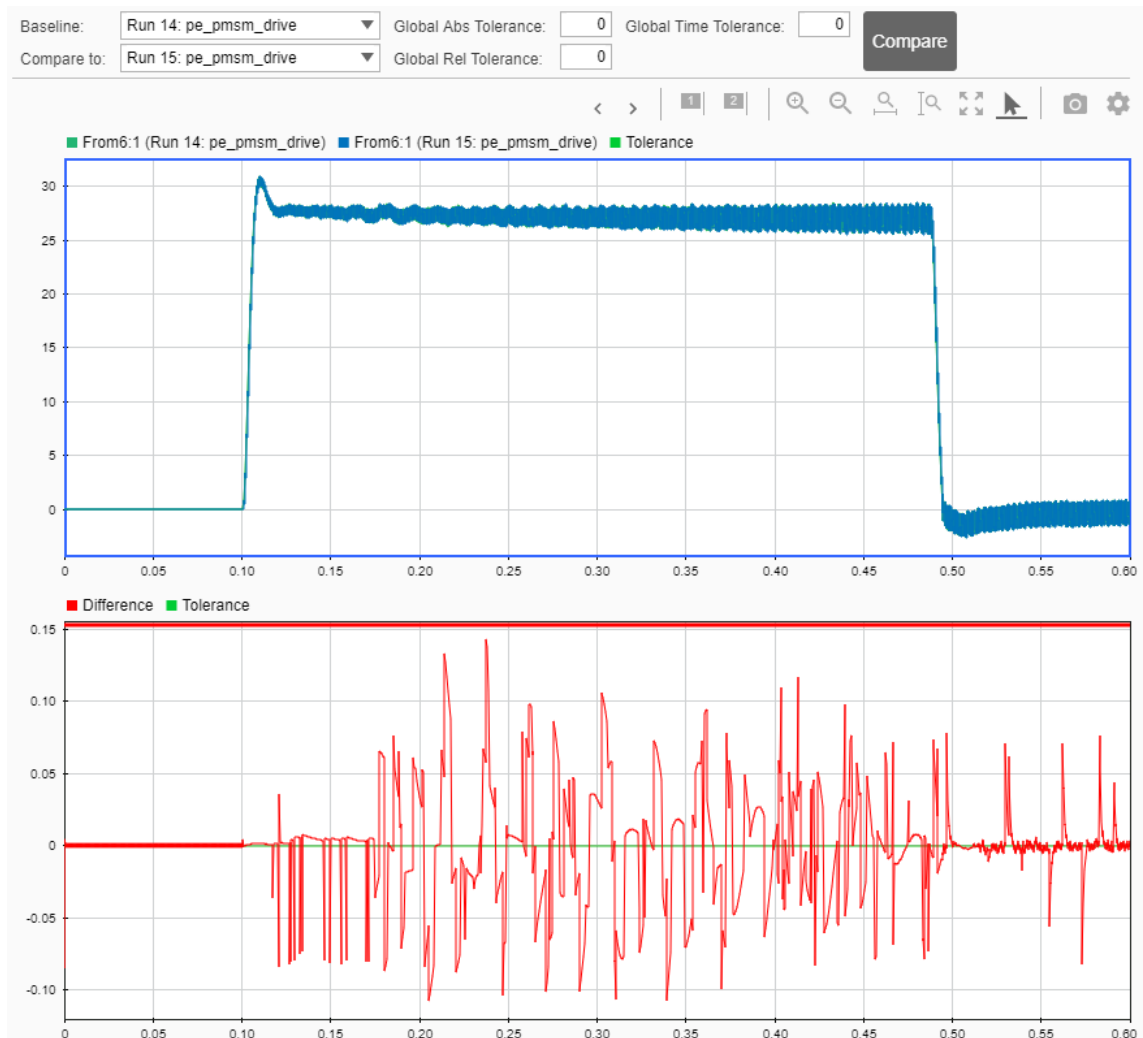
The first plot shows the overlay of the Backward Euler and Partitioning solver simulation results. The second plot shows how they differ. The default tolerance for differences is 0. To determine if the accuracy of the results meets your requirements, you can adjust the relative, absolute, and time tolerances. For more information, see "Compare Simulation Data".

You can also use the `ee_updateSolver` function to reset the model for simulation with a global solver.

See Code

```
% Configure for Global/Nonlocal solver simulation
ee_updateSolver('Global',model)
```

Limitations of the ee_updateSolver Function

Using the `ee_updateSolver` function does not guarantee that a simulation does not generate an error or that a simulation produces accurate results. To ensure that simulation accuracy meets your requirements, it is a recommended practice to compare simulation results to baseline results whenever you change model or block settings.

See Also
PMSM | Solver Configuration

Related Examples
- "Increase Simulation Speed Using the Partitioning Solver"

Phasor-Mode Simulation Using Simscape Components

You can run your model in phasor mode to speed up simulation. In Simscape, phasor mode is known as frequency-time equation formulation. In general, this formulation leads to accurate simulation of AC models using larger time steps than the traditional time formulation.

Use frequency-time equation forumulation to speed up your simulation when:

- Your simulation contains periodic AC signals with a common fundamental frequency
- You are interested in the slow-moving AC-related quantities, such as amplitude or phase, and the DC output signals

Set up the model

To measure the time required to run a simulation, open the model *ee_sm_control* and create a model callback.

```
mdl = load_system('ee_sm_control');
open_system(mdl);
set_param(mdl,'StartFcn','tic;');
set_param(mdl,'StopFcn','tsim=toc;');
```
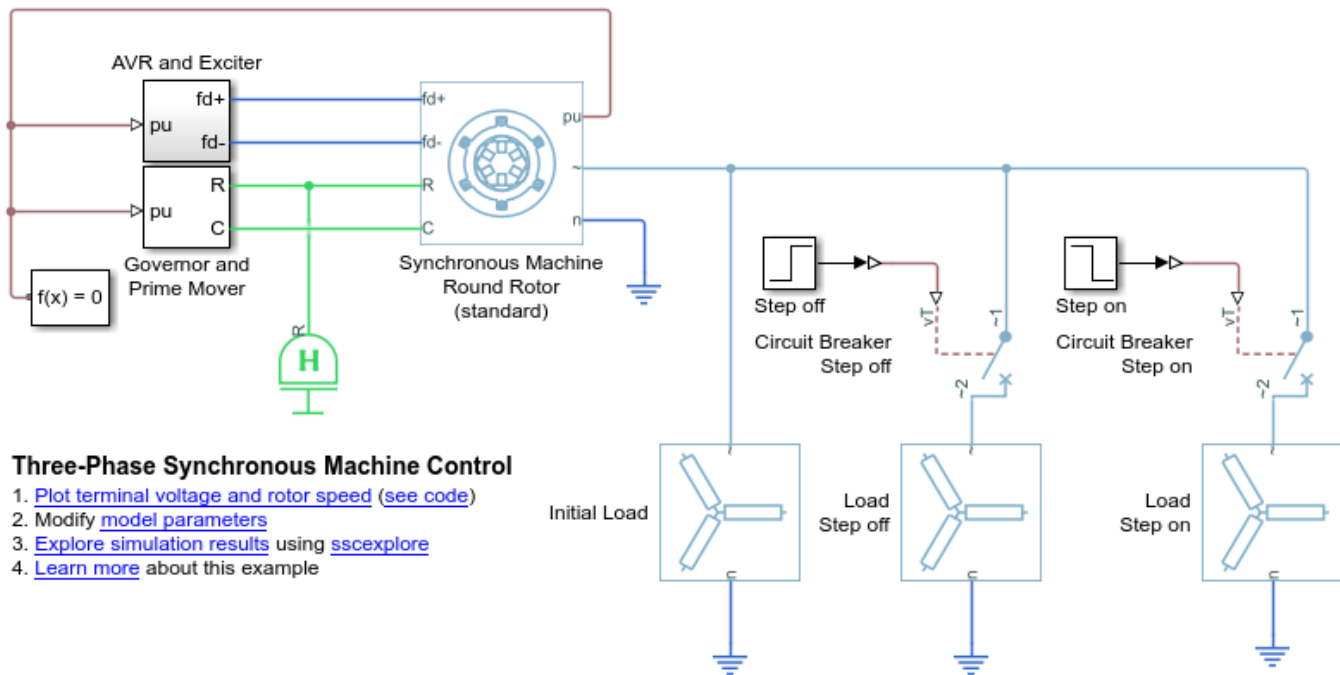

Run a time-based simulation

Double-click the Solver Configuration block and apply the following configuration:

- Enable the local solver by checking the **Use local solver** check box
- Set the `Sample time` parameter to `1e-3`
- Set the `Equation formulation` parameter to `Time`

You can also run this code to configure the block.

```
blk = find_system(mdl,'MaskType','Solver Configuration');
set_param(blk,'UseLocalSolver','on');
set_param(blk,'LocalSolverSampleTime','1e-3');
set_param(blk,'EquationFormulation','NE_TIME_EF');
```

Simulate the model and save the run time and logging variable.

```
sim(get_param(mdl,'Name'));
tsim_time = round(tsim,2);
simlog_ee_sm_control_time = simlog_ee_sm_control;
```

Run a phasor-mode simulation

Double-click the Solver Configuration block and apply the following configuration:

- Enable the local solver by checking the `Use local solver` check box
- Set the `Sample time` parameter to `1e-2`
- Set the `Equation formulation` parameter to `Frequency and time`

You can also run this code to configure the block.

```
blk = find_system(mdl,'name','Solver Configuration');
set_param(blk,'UseLocalSolver','on');
set_param(blk,'LocalSolverSampleTime','1e-2');
set_param(blk,'EquationFormulation','NE_FREQUENCY_TIME_EF');
```

Simulate the model and save the run time and logging variable.

```
sim(get_param(mdl,'Name'));
tsim_phasor = round(tsim,2);
simlog_ee_sm_control_phasor = simlog_ee_sm_control;
```

Compare DC results

Plot the field voltage and rotor speed for both the time and frequency-time simulations. For each simulation mode, display markers at every 50 data points.

```
[hTime,hPhasor]=setup_figure(simlog_ee_sm_control_time,simlog_ee_sm_control_phasor,'dc');
legend([hTime,hPhasor],{['Time (t=',num2str(tsim_time),'s)'],['Phasor (t=',num2str(tsim_phasor),
```
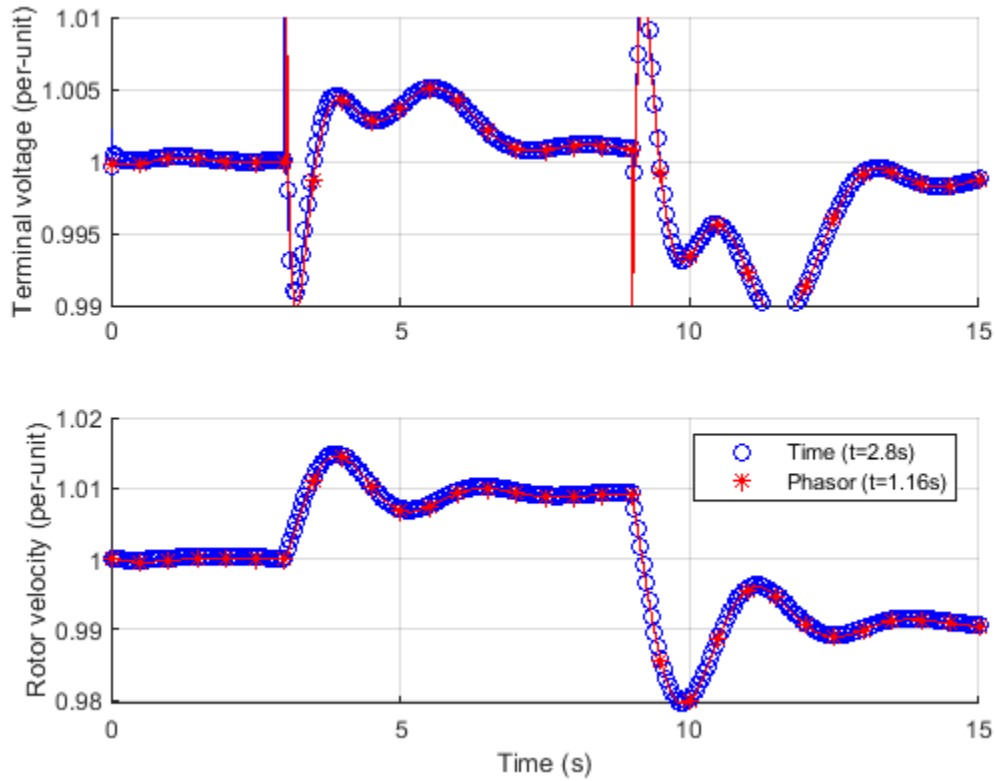
The phasor simulation reproduces near-identical results as the time-based simulation, despite using a time step that is 10 times larger. The measured simulation time is also shown for each of the simulation modes in the plot legend. This performance indicator is different on different machines, but the frequency-time simulation should be about two times faster than the time simulation. Note that the actual time required per step is higher in the frequency-time case, but the overal time is reduced.

Compare AC results

Plot the *a*-phase voltage of the synchronous machine over the time period `1s` to `1.1s`. Because of the larger time steps in the frequency-time formulation, the resolution of the AC quantity is too small to make out the sine wave. The points that are available are undersampled, but still accurate.

```
[hTime,hPhasor]=setup_figure(simlog_ee_sm_control_time,simlog_ee_sm_control_phasor,'ac');
legend([hTime,hPhasor],{['Time (t=',num2str(tsim_time),'s)'],['Phasor (t=',num2str(tsim_phasor),
```
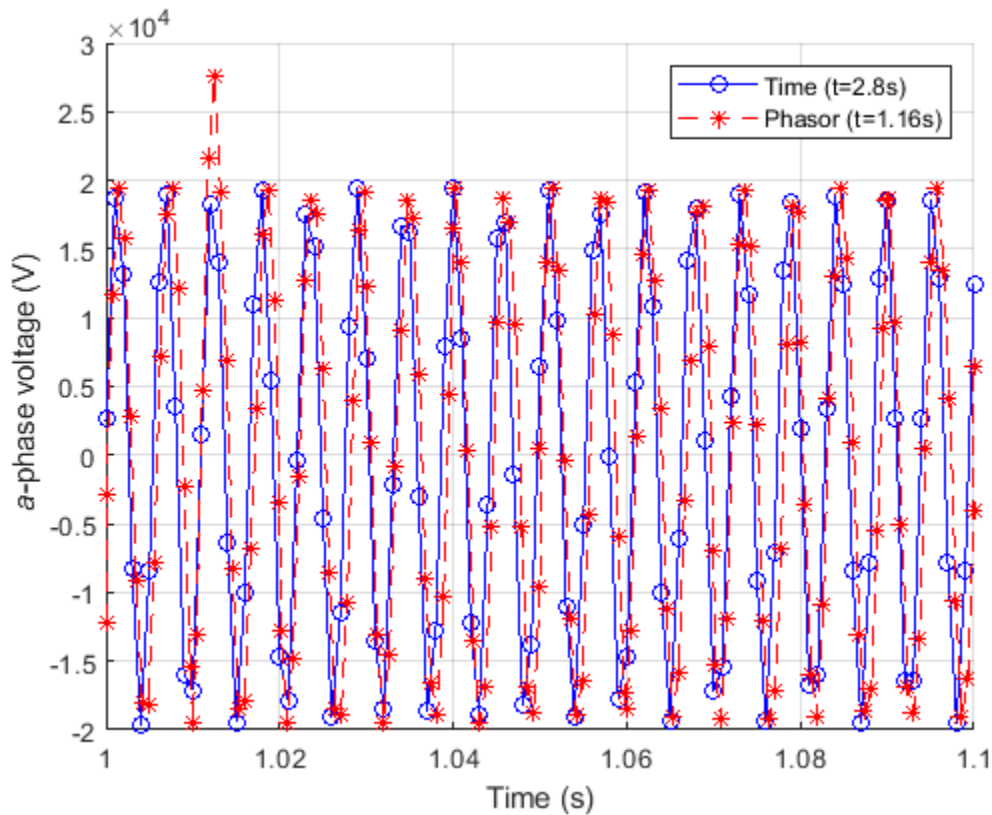
In general, use frequency-time formulation to speed up simulations where the outputs of interest are DC or slow-moving AC quantities. You can use periodic sensors to measure slow-moving properties of AC signals such as amplitude and phase in both time and frequency time formulations. For more information, see the PS Harmonic Estimator (Amplitude, Phase) block.

Sometimes there are small phase offsets between time- and frequency-time-generated AC signals. This difference is caused by the accumulated integration error of a slightly different signal frequency over time.

See Also
Solver Configuration

More About
- "Frequency and Time Simulation Mode"

Examine the Simulation Data Logging Configuration of a Model

Many analyses that you can perform using Simscape Electrical require a simulation log variable in your MATLAB workspace. The model in this example is configured to log Simscape data for the whole model for the entire simulation time. To examine the data logging configuration of a model:

1. Open the model. At the MATLAB command prompt, enter

   ```
   model = 'ee_rectifier_power_dissipated';
   open(model)
   ```

2. Open the model configuration parameters and then, in the left pane, select **Simscape**. Relevant parameters are:

   - **Log simulation data** — Data logging is enabled for the whole model because this parameter is set to All so you can calculate the power dissipated by any of the semiconductors in the model.

   - **Workspace variable name** — This parameter, which is also referred to as the name of the simulation log variable, is specified as simlog_ee_rectifier_power_dissipated.

   - **Limit data points** — You can calculate the power dissipated for the entire simulation time because the option is not selected.

Alternatively, you can determine the Simscape data logging configuration without opening the model configuration parameters, by using the get_param function. For example, for the ee_rectifier_power_dissipated model, to determine:

- If all, some, or no data is logged, at the MATLAB command prompt, enter

  ```
  get_param(model,'SimscapeLogType')

  ans =

      'all'
  ```

- The name of the Simscape logging variable

  ```
  get_param(model,'SimscapeLogName')

  ans =

      'simlog_ee_rectifier_power_dissipated'
  ```

- If the option to limit data-points is on or off

  ```
  get_param(model,'SimscapeLogLimitData')

  ans =

      'off'
  ```

See Also

Functions
get_param

Related Examples

- "Data Logging"

Perform a Power-Loss Analysis

In this section...
"Prerequisite" on page 7-17
"Calculate Average Power Losses for the Simulation" on page 7-17
"Analyze Power Dissipation Differences Using Instantaneous Power Dissipation" on page 7-18
"Mitigate Transient Effects in Simulation Data" on page 7-21

This example shows how to analyze power loss and how to mitigate transient power dissipation behavior. Analyzing power loss, with and without transients, is useful for determining if components are operating within safety and efficiency guidelines.

Prerequisite

This example requires a simulation log variable in your MATLAB workspace. The model in this example is configured to log Simscape data for the whole model for the entire simulation time.

To learn how to determine if a model is configured to log simulation data, see "Examine the Simulation Data Logging Configuration of a Model" on page 7-15.

Calculate Average Power Losses for the Simulation

1. Open the model. At the MATLAB command prompt, enter

```
model = 'ee_rectifier_power_dissipated';
open(model)
```


Power-Loss Analysis of a Three-Phase Rectifier

1. Calculate power dissipated by all of the diodes in the model for the entire simulation time (see command window for output).
2. Calculate power dissipated by diode D6 for the entire simulation time (see command window for output).
3. Explore simulation results using sscexplore.
4. Calculate the maximum power dissipated by diode D6. (see command window for output).
5. Calculate power dissipated by diode D6 between simulation time, t = 1e-3 to 0.5 s (see command window for output).
6. Plot power dissipated by the diodes (see code).
7. Calculate power dissipated by all of the diodes between simulation time, t = 0.4 to 0.5 s (see command window for output).
8. Learn more about the ee_getPowerLossSummary function.

2 Simulate the model.

```
sim(model)
```

The simulation log variable, which is named `simlog_ee_rectifier_power_dissipated`, appears in the workspace.

3 Calculate the average losses for the entire simulation for each of the diodes in the model.

```
rectifierLosses = ee_getPowerLossSummary(simlog_ee_rectifier_power_dissipated.Rectifier)

rectifierLosses =

  6×2 table
```

LoggingNode	Power
'ee_rectifier_power_dissipated.Rectifier.D6'	52.222
'ee_rectifier_power_dissipated.Rectifier.D3'	52.222
'ee_rectifier_power_dissipated.Rectifier.D4'	52.194
'ee_rectifier_power_dissipated.Rectifier.D5'	52.194
'ee_rectifier_power_dissipated.Rectifier.D1'	52.194
'ee_rectifier_power_dissipated.Rectifier.D2'	52.194

On average, diodes D3 and D6 dissipate more power than the other diodes in the rectifier.

Analyze Power Dissipation Differences Using Instantaneous Power Dissipation

The Diode blocks each have a *power_dissipated* variable, which measures instantaneous power dissipation. To investigate the differences in the average power dissipated by the diodes, view the simulation data using the Simscape Results Explorer.

1 Open the simulation data using the Results Explorer.

```
sscexplore(simlog_ee_rectifier_power_dissipated)
```

2 View the instantaneous power dissipated by the diodes.

    a Expand the **Rectifier** node

    b Expand the **D1** through **D6** nodes

    c Click the `power_dissipated` nodes for diode **D1**, and then **Ctrl+click** the `power_dissipated` nodes for the other five diodes.

    d In the Results Explorer window, click the plot options ![icon] button and set **Plot signals** to `Separate`.
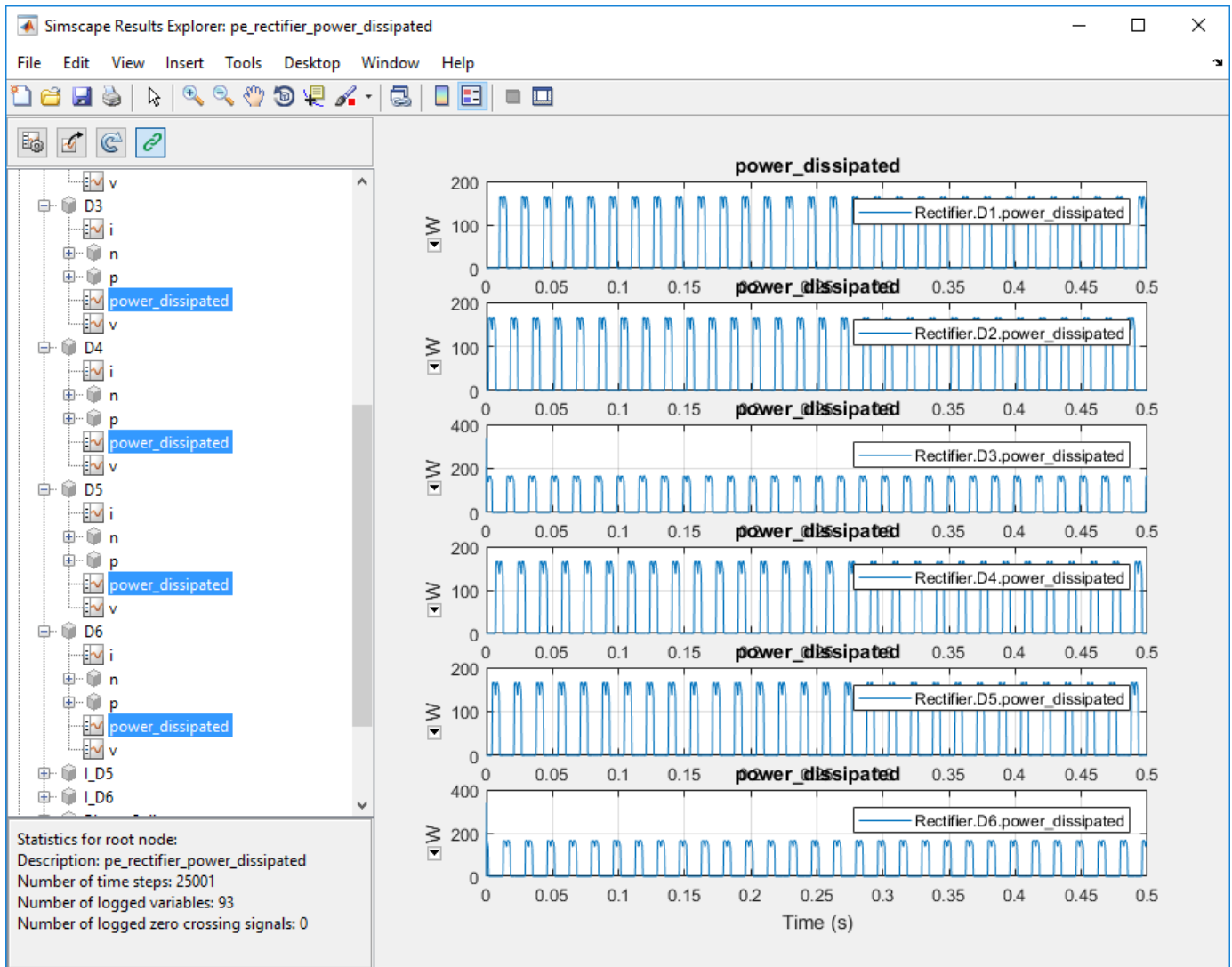
At the beginning of the simulation, there is a difference in the power dissipation for each diode.

3 Take a closer look at the differences. Overlay the plots and zoom to the beginning of the simulation.

a In the Results Explorer window, click the plot options button.

b Enable the **Limit time axis** option.

c For **Stop time**, specify `0.02`.

d Set **Plot signals** to `Overlay`.

e Click **OK**.
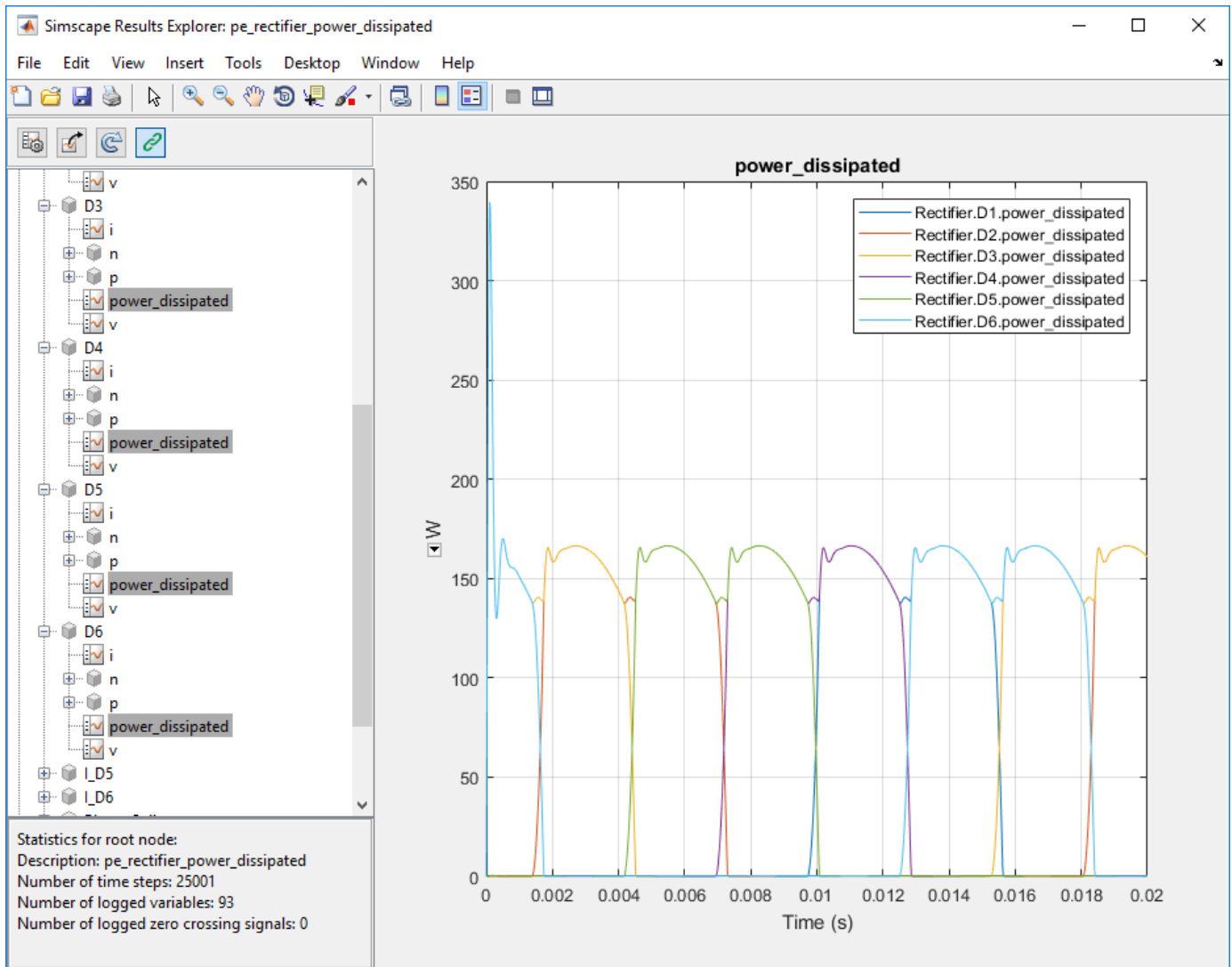
The variation in power dissipation is due to transient behavior at the beginning of the simulation. The model reaches steady state at simulation time, $t \simeq 0.001$ seconds.

4   Determine the average power dissipation for only the diodes during the interval that contains transient behavior.

```
rectifierLosses = ee_getPowerLossSummary(simlog_ee_rectifier_power_dissipated.Rectifier,0,1e-3)

rectifierLosses =

  6×2 table

    LoggingNode        Power
    _____    _____

    'Rectifier.D3'      174.88
    'Rectifier.D6'      174.88
    'Rectifier.D4'     0.27539
    'Rectifier.D5'     0.27539
```

```
'Rectifier.D1'     0.12482
'Rectifier.D2'     0.032017
```

The average power dissipated by diodes D3 and D6 exceeds the average for the other diodes.

5   Output a table of the maximum power dissipation for each diode, for the entire simulation time.

```
pd_D1_max = max(simlog_ee_rectifier_power_dissipated.Rectifier.D1.power_dissipated.series.values);
pd_D2_max = max(simlog_ee_rectifier_power_dissipated.Rectifier.D2.power_dissipated.series.values);
pd_D3_max = max(simlog_ee_rectifier_power_dissipated.Rectifier.D3.power_dissipated.series.values);
pd_D4_max = max(simlog_ee_rectifier_power_dissipated.Rectifier.D4.power_dissipated.series.values);
pd_D5_max = max(simlog_ee_rectifier_power_dissipated.Rectifier.D5.power_dissipated.series.values);
pd_D6_max = max(simlog_ee_rectifier_power_dissipated.Rectifier.D6.power_dissipated.series.values);

diodes = {'D1';'D2';'D3';'D4';'D5';'D6'};
PowerMax = [pd_D1_max;pd_D2_max;pd_D3_max;pd_D4_max;pd_D5_max;pd_D6_max];

T = table(PowerMax,'RowNames', diodes)

T =

  6×1 table

         PowerMax
         _____

    D1    166.45
    D2    166.45
    D3    339.54
    D4    166.45
    D5    166.45
    D6    339.54
```

The maximum instantaneous power dissipation for diodes D3 and D6 is almost double the maximum instantaneous power dissipation for the other diodes.

Mitigate Transient Effects in Simulation Data

To mitigate the transient power dissipation at the beginning of the simulation, use the final simulation state to initialize a new simulation at steady-state conditions.

1   Configure the model to save the final state.

   a   Open the model configuration parameters.

   b   In the **Solver** pane, change the **Stop time** from `0.5` to 1e-3.

   c   In the **Data Import/Export** pane, select these options:

   - **Final States**
   - **Save final operating point**

   d   Click **Apply.**

2   Run the simulation.

   The final state is saved as the variable *xFinal* in the MATLAB workspace.

3   Configure the model to initialize using *xFinal*, in the model configuration parameters.

   a   In the Data Import/Export pane:

- Select the **Initial state** option.
- Change the **Initial state** parameter value from `xInitial` to `xFinal`.
- Clear the **Final states** option.

  b In the **Solver** pane, change the **Stop time** to `0.5`.

  c Click **OK**.

4 Run the simulation.

5 View the data from the new simulation.

  a Click the **Reload logged data** ⟳ button in the Simscape Results Explorer.

  b Click **OK** to confirm that `simlog_ee_rectifier_power_dissipated` is the variable name that contains the logged data.

  c To see the data more clearly, click and drag the legend away from the peak amplitudes.
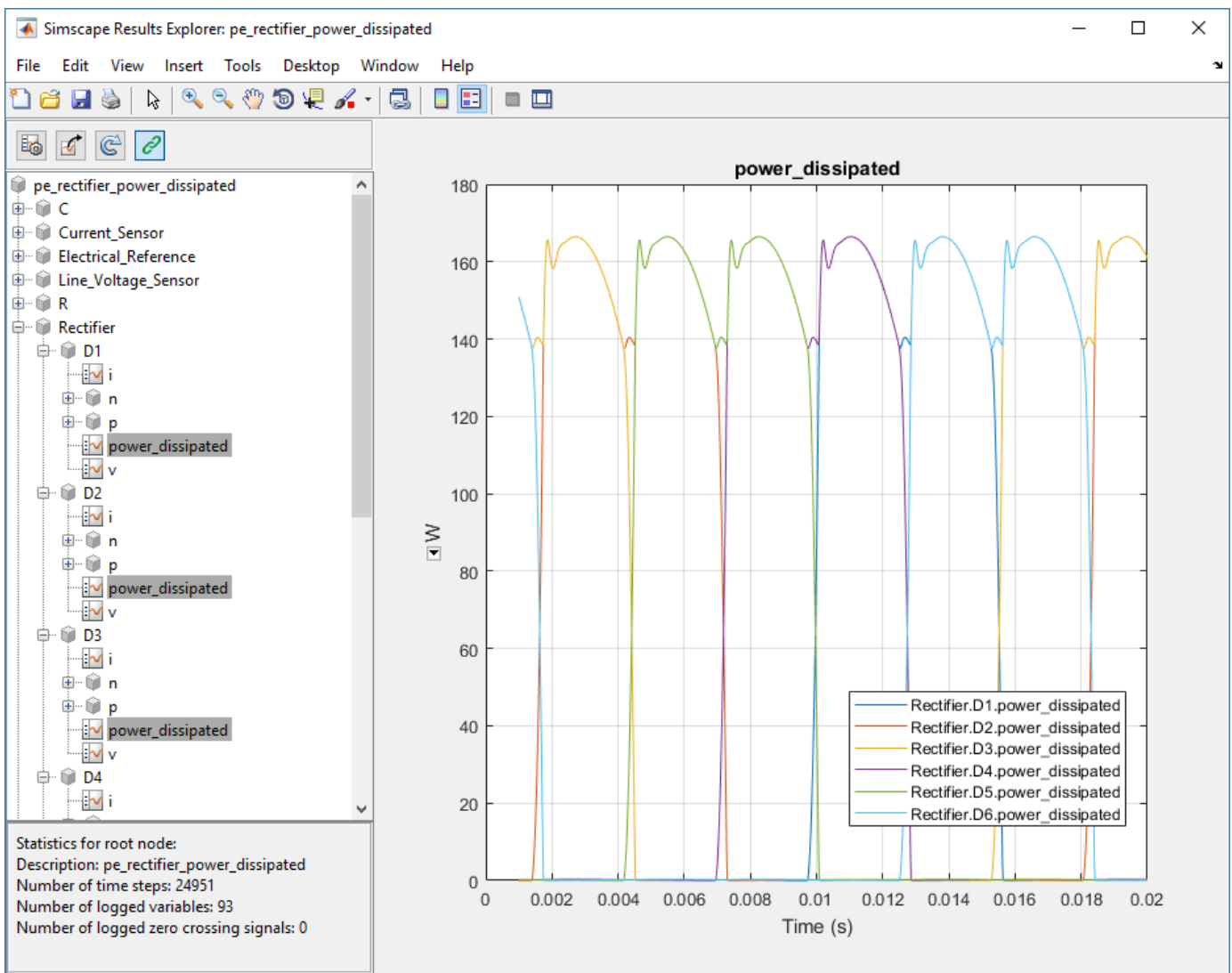
The plot shows that the simulation no longer contains the transient.

6 Output a table of the maximum power dissipation for each diode, for the modified simulation.

```
pd_D1_max = max(simlog_ee_rectifier_power_dissipated.Rectifier.D1.power_dissipated.series.values);
pd_D2_max = max(simlog_ee_rectifier_power_dissipated.Rectifier.D2.power_dissipated.series.values);
pd_D3_max = max(simlog_ee_rectifier_power_dissipated.Rectifier.D3.power_dissipated.series.values);
pd_D4_max = max(simlog_ee_rectifier_power_dissipated.Rectifier.D4.power_dissipated.series.values);
pd_D5_max = max(simlog_ee_rectifier_power_dissipated.Rectifier.D5.power_dissipated.series.values);
pd_D6_max = max(simlog_ee_rectifier_power_dissipated.Rectifier.D6.power_dissipated.series.values);

diodes = {'D1';'D2';'D3';'D4';'D5';'D6'};
PowerMax = [pd_D1_max;pd_D2_max;pd_D3_max;pd_D4_max;pd_D5_max;pd_D6_max];

T = table(PowerMax,'RowNames', diodes)

T =

  6×1 table

         PowerMax
         _____

    D1    166.45
    D2    166.45
    D3    166.45
    D4    166.45
    D5    166.45
    D6    166.45
```

The maximum instantaneous power dissipation for diodes D3 and D6 is the same as the maximum instantaneous power dissipation for the other diodes.

See Also

Functions
ee_getEfficiency | ee_getPowerLossSummary | ee_getPowerLossTimeSeries

Related Examples

- "Power-Loss Analysis of a Three-Phase Rectifier" on page 10-566
- "Examine the Simulation Data Logging Configuration of a Model" on page 7-15
- "Data Logging"
- "About the Simscape Results Explorer"

Choose a Simscape Electrical Function for an Offline Harmonic Analysis

In this section...

Harmonic Distortion

Nonlinear loads create power distortion in the form of harmonics, that is, voltages and currents that are multiples of the fundamental frequency. Harmonic waveforms can result in energy losses though heat dissipation and in reduced power quality. They can also cause equipment to malfunction or to become damaged. Standards development organizations such as the Institute of Electrical and Electronics Engineers (IEEE) and the International Electrotechnical Commission (IEC) define the recommended limits for harmonic content in electric power systems.

Harmonic Analysis Functions

You can use the simulation and analysis functions in Simscape Electrical to perform an offline, that is post-simulation, analysis to examine harmonic distortion in your model. The `ee_plotHarmonics` function generates a bar chart. The `ee_getHarmonics` and `ee_calculateThdPercent` functions provide harmonic data in numerical form.

To decide which functions and workflows to use for your harmonic analysis, consider your goals. The table cross-references the harmonic functions with common harmonic analysis according to the data the function outputs and the task requires.

Goal	`ee_plotHarmonics`	`ee_getHarmonics`	`ee_calculateThdPercent`
Evaluate the relative overall harmonic distortion	• Bar chart of the percentage of fundamental magnitude • Fundamental peak value • Total harmonic distortion (THD) percentage		

Goal	ee_plotHarmonics	ee_getHarmonics	ee_calculateThdPercent
Compare the harmonic distortion to standard limits		• Fundamental frequency • Harmonic orders • Harmonic magnitudes	Total harmonic distortion (THD) percentage
Determine the parameters for filtering harmonic distortion		• Fundamental frequency • Harmonic orders • Harmonic magnitudes	

Evaluate Relative Overall Harmonic Distortion

Use this workflow for a high-level understanding of the waveform distortion in your power system.

1 Enable Simscape data logging.
2 Save the logged voltage or current data to a variable.
3 Use the `ee_plotHarmonics` function to generate a bar chart of harmonic percentages with the peak fundamental magnitude and the total harmonic distortion (THD) percentage displayed in the plot title.

Compare Harmonic Distortion to Standard Limits

Use this workflow to obtain values for evaluating the IEEE or IEC suitability of your power system.

1 Enable Simscape data logging.
2 Save the logged voltage or current data to a variable.
3 Use the `ee_getHarmonics` function to obtain the harmonic orders, the magnitude for each order, and the fundamental frequency.
4 Save the fundamental peak to a new variable.
5 Calculate the RMS voltage or current for each order.
6 Calculate the harmonic distortion percentage for individual harmonics.
7 Use the `ee_calculateThdPercent` function to obtain the total harmonic distortion (THD).
8 Compare the percentage data for each order and the THD percentage to the standard limits.

Minimize Harmonic Distortion with Passive Filters

Use this workflow to determine the parameters for filtering the distorted waveforms with passive filters. Use individual, series-tuned filters for specific harmonic orders. Use a single high-pass filter to filter higher orders.

1 Enable Simscape data logging.
2 Save the logged voltage or current data in a variable.
3 Use the `ee_getHarmonics` function to obtain the harmonic orders, the magnitude for each order, and the fundamental frequency.

4   Identify the harmonic orders that you want to filter.

5   For each filter:

       a   Specify the filter size, in terms of reactive power compensation, and specify the filter quality.

       b   Calculate the capacitor reactance at the tuned harmonic order.

       c   Calculate the filter capacitance.

       d   Calculate the inductor reactance at the tuned harmonic order.

       e   Calculate the filter inductance.

       f   Calculate the filter resistance.

Verify the Results of an Online Harmonic Analysis

You can examine harmonic distortion in your model online, that is during simulation, using the Simscape Spectrum Analyzer block. To verify the results from the Spectrum Analyzer block:

1   To determine the THD in your model, perform an online analysis. For information, see "Perform an Online Harmonic Analysis Using the Simscape Spectrum Analyzer Block" on page 7-27.

2   Use the `ee_getHarmonics` and `ee_calculateThdPercent` functions to determine the THD in your model.

3   Compare the THD values for the online and offline analyses. If the results differ, reconfigure the Spectrum Analyzer block.

See Also

Blocks
Spectrum Analyzer

Functions
`ee_calculateThdPercent` | `ee_getHarmonics` | `ee_plotHarmonics`

Related Examples

- "Harmonic Analysis of a Three-Phase Rectifier" on page 10-492
- "Perform an Online Harmonic Analysis Using the Simscape Spectrum Analyzer Block" on page 7-27
- "Data Logging"

Perform an Online Harmonic Analysis Using the Simscape Spectrum Analyzer Block

In this section...
"Harmonic Distortion" on page 7-27
"Prerequisite" on page 7-27
"Perform an Offline Harmonic Analysis" on page 7-27
"Perform an Online Harmonic Analysis" on page 7-30

Harmonic Distortion

Nonlinear loads create power distortion in the form of harmonics, that is, voltages and currents that are multiples of the fundamental frequency. Harmonic waveforms can result in energy losses through heat dissipation and in reduced power quality. They can also cause equipment to malfunction or to become damaged. Standards development organizations such as the Institute of Electrical and Electronics Engineers (IEEE) and the International Electrotechnical Commission (IEC) define the recommended limits for harmonic content in electric power systems.

This example shows how to examine harmonic distortion in your model using offline, that is after simulation, and online, that is during simulation, analyses. The offline analysis uses the Simscape Electrical harmonic analysis functions and helps you to determine configuration settings for, and verify the results of, the online analysis. The online analysis uses the Simscape Spectrum Analyzer block.

Prerequisite

This example requires a simulation log variable in your MATLAB workspace. The model in this example is configured to log Simscape data for the whole model for the entire simulation time.

To learn how to determine if a model is configured to log simulation data, see "Examine the Simulation Data Logging Configuration of a Model" on page 7-15.

Perform an Offline Harmonic Analysis

1   Open the model. At the MATLAB command prompt, enter:

```
model = 'ee_composite_rectifier';
open_system(model)
```
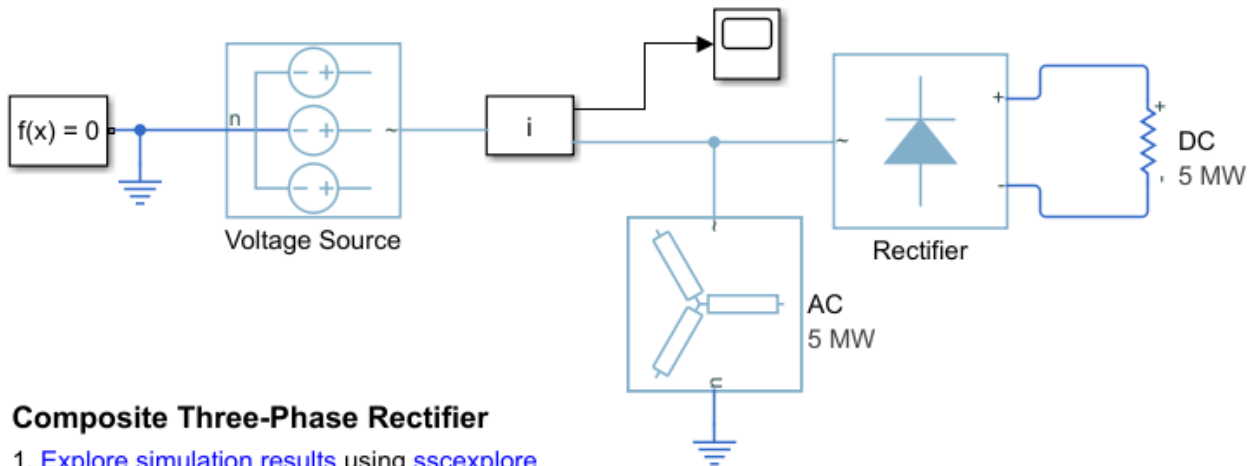
Composite Three-Phase Rectifier

1. Explore simulation results using sscexplore
2. Learn more about this example

The example model contains a three-phase rectifier. The model also contains a Selector block that outputs only the *a*-phase from three-phase current signal that it receives from the PS-Simulink Converter block.

2   Simulate the model.

```
sim(model)
```

3   View the time-domain results. Open the Scope block.
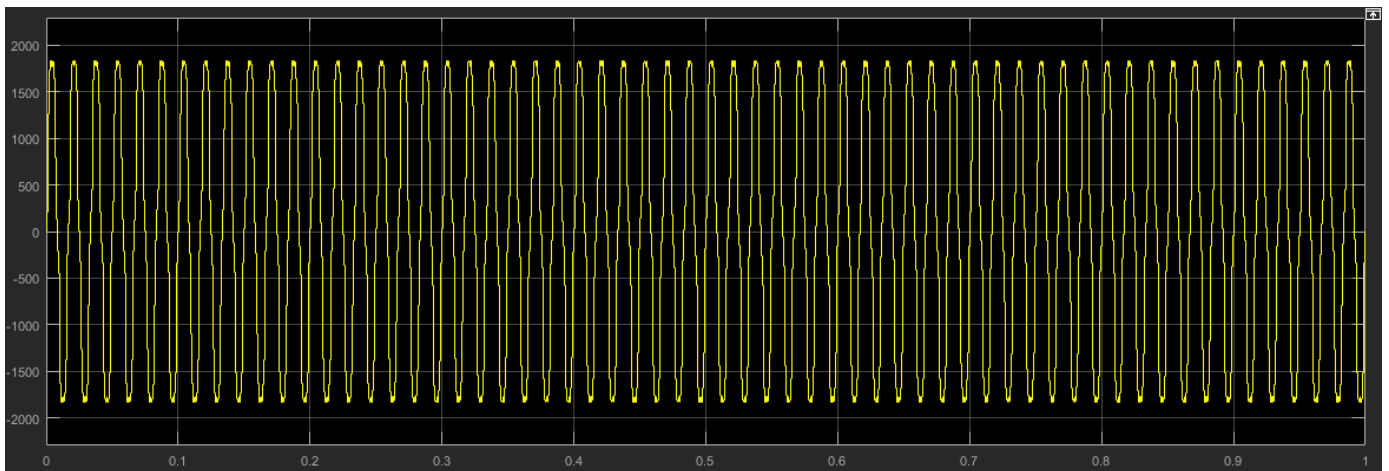
The time domain analysis shows that the rectifier is converting the voltage, but it does not include any information about the frequencies in the signal.

4   Determine configuration settings and calculate the expected results for an online harmonic analysis. Perform an offline harmonic analysis.

a   The Simscape Electrical harmonic analysis functions require that you use a fixed-step solver. Determine the solver type and sample time for the model. To turn on sample-time highlighting, in the Simulink editor menu bar, select **Debug** > **Information Overlays** > **Sample Time** > **Colors**.
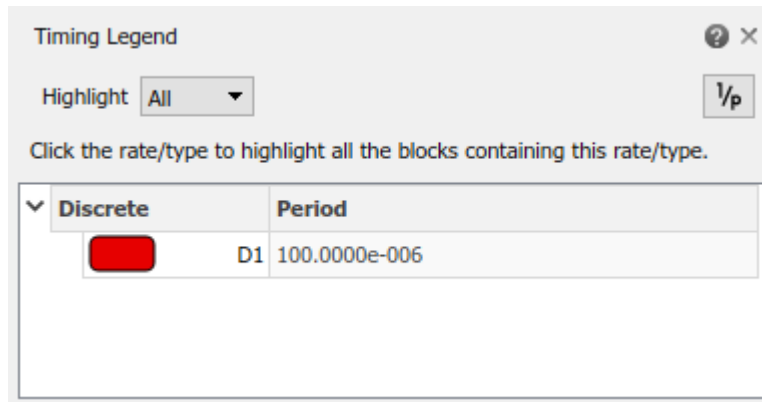
The model is running at a discrete rate, therefore it is using a fixed-step solver, with a sample time of 1e-4 s.

b Use the `ee_getHarmonics` function to calculate the harmonic order, the harmonic magnitude, and the fundamental frequency based on the voltage source currents.

```
[harmonicOrder,harmonicMagnitude,fundamentalFrequency] = ...
ee_getHarmonics(simlog_ee_composite_rectifier.Voltage_Source.I);
```

c Performing an online harmonic analysis using the Spectrum Analyzer block requires that you specify a value for maximum harmonic order and the resolution bandwidth (RBW). The RBW depends on the fundamental frequency.

Extract and display the maximum harmonic order and the fundamental frequency:

```
disp(['Maximum Harmonic Order = ', num2str(max(harmonicOrder))])
disp(['Fundamental Frequency  = ', num2str(fundamentalFrequency)])
```

```
Maximum Harmonic Order = 30
Fundamental Frequency  = 60
```

d Determine the peak value of the fundamental frequency. This value is useful for filtering out negligible harmonics and for verifying the results of the offline analyses.

```
fundamentalPeak = harmonicMagnitude(harmonicOrder==1);
disp(['Peak value of fundamental = ', num2str(fundamentalPeak),' A']);
```

```
Peak value of fundamental = 1945.806 A
```

e Filter out small harmonics by identifying and keeping harmonics that are greater than one thousandth of the fundamental peak frequency.

```
threshold = fundamentalPeak ./ 1e3;
aboveThresold = harmonicMagnitude > threshold;
harmonicOrder = harmonicOrder(aboveThresold)';
harmonicMagnitude = harmonicMagnitude(aboveThresold)';
```

f Display the harmonic data in a MATLAB table.

```
harmonicRms = harmonicMagnitude./sqrt(2);
harmonicPct = 100.*harmonicMagnitude./harmonicMagnitude(harmonicOrder == 1);
harmonicTable = table(harmonicOrder,...
    harmonicMagnitude,...
    harmonicRms,...
    harmonicPct,...
    'VariableNames',{'Order','Magnitude','RMS','Percentage'});
display(harmonicTable);
```

```
harmonicTable =

  10×4 table

    Order    Magnitude     RMS      Percentage
    _____    _____    _____    _____

      1       1945.8      1375.9       100
      5       218.86      154.75      11.248
      7       105.83      74.835       5.439
     11       85.135        60.2      4.3753
     13       57.599      40.729      2.9602
     17       50.417       35.65      2.5911
     19       37.612      26.596       1.933
     23       33.859      23.942      1.7401
     25       26.507      18.743      1.3622
     29       23.979      16.955      1.2323
```

g   Calculate the total harmonic distortion (THD) percentage using the `ee_calculate_ThdPercent` function.

```
thdPercent = ee_calculateThdPercent(harmonicOrder,harmonicMagnitude);
disp(['Total Harmonic Distortion Percentage = ' num2str(thdPercent),' %']);
```

```
Total Harmonic Distortion percentage = 14.1721 %
```

Perform an Online Harmonic Analysis

1   In the Simulink editor that contains the `ee_composite_rectifier` model, replace the Scope block with a Spectrum Analyzer block from the Simscape Utilities Library:

   a   Delete the Scope block.

   b   Left-click within the block diagram.

   c   After the search icon appears, type `spec`, and then from the list, select the `Spectrum Analyzer` from the `Utilities` library.

   d   Connect the Spectrum Analyzer block to the output signal from the Subsystem i.
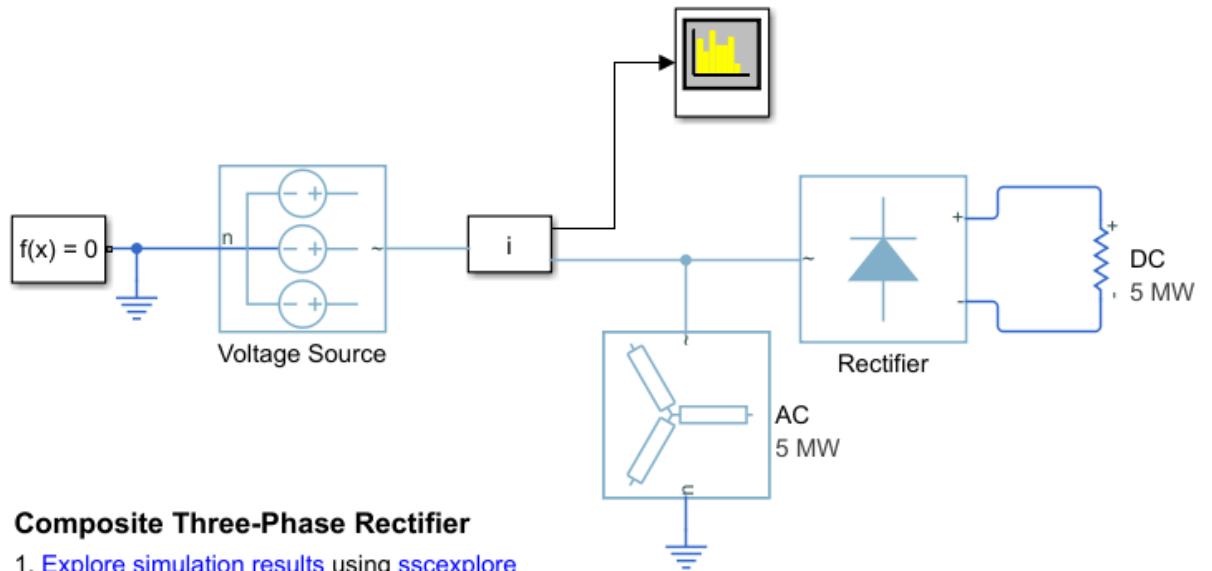
Composite Three-Phase Rectifier

1. Explore simulation results using sscexplore
2. Learn more about this example

2   Configure the Spectrum Analyzer block using the Spectrum Settings panel.

a   Open the Spectrum Analyzer.

b   Open the Spectrum Settings panel. On the Spectrum Analyzer toolbar, click the **Spectrum Settings** button.

c   Configure the parameters on the **Main Options** pane.

    i   Configure the block to display the root mean square (RMS) of the frequency. From the **Type** dropdown menu, select RMS.

    ii   Determine the value to specify for the resolution bandwidth (RBW) using this equation:

$$RBW = \frac{NENBW*f}{N},$$

where,

- *NENBW* is the normalized effective noise bandwidth, a factor of the windowing method used. The Hanning (Hann) window has an *NENBW* value of approximately 1.5.
- *f* is the fundamental frequency.
- *N* is the number of periods.
- *RBW* is the resolution bandwidth in Hz.

For a fundamental frequency of 60 Hz over 10 periods, using a Hann window,

$$RBW = \frac{1.5*60Hz}{10} = 9Hz$$

For **RBW (Hz)**, specify 9.

d   Expand the **Windows Options** pane and specify an **Overlap (%)** of 90.

    e   Specify the maximum number of peaks for the analyzer to display. In the menu bar, select **Tools > Measurements > Peak Finder**. Alternatively, in the Spectrum Analyzer toolbar,

          select the Peak Finder ⌛ button. In the **Peakfinder** pane, in the **Settings** section, for **Max Num of Peaks**, enter 30. This value is based on the maximum harmonic order as indicated by the offline analysis.

    f   Set the number of harmonics to use for measuring harmonic distortion. Specify a number that captures the largest harmonic order that the offline analysis captures. In the menu bar, select **Tools > Measurements > Distortion Measurements**. Alternatively, in the Scope

          toolbar, click the Distortion Measurements 📈 button. Scroll as required to see the **Distortion Measurements** pane.

          In the **Distortion Measurements** pane, for **Num Harmonics**, again enter 30.

3   Simulate the model.

```
sim(model)
```
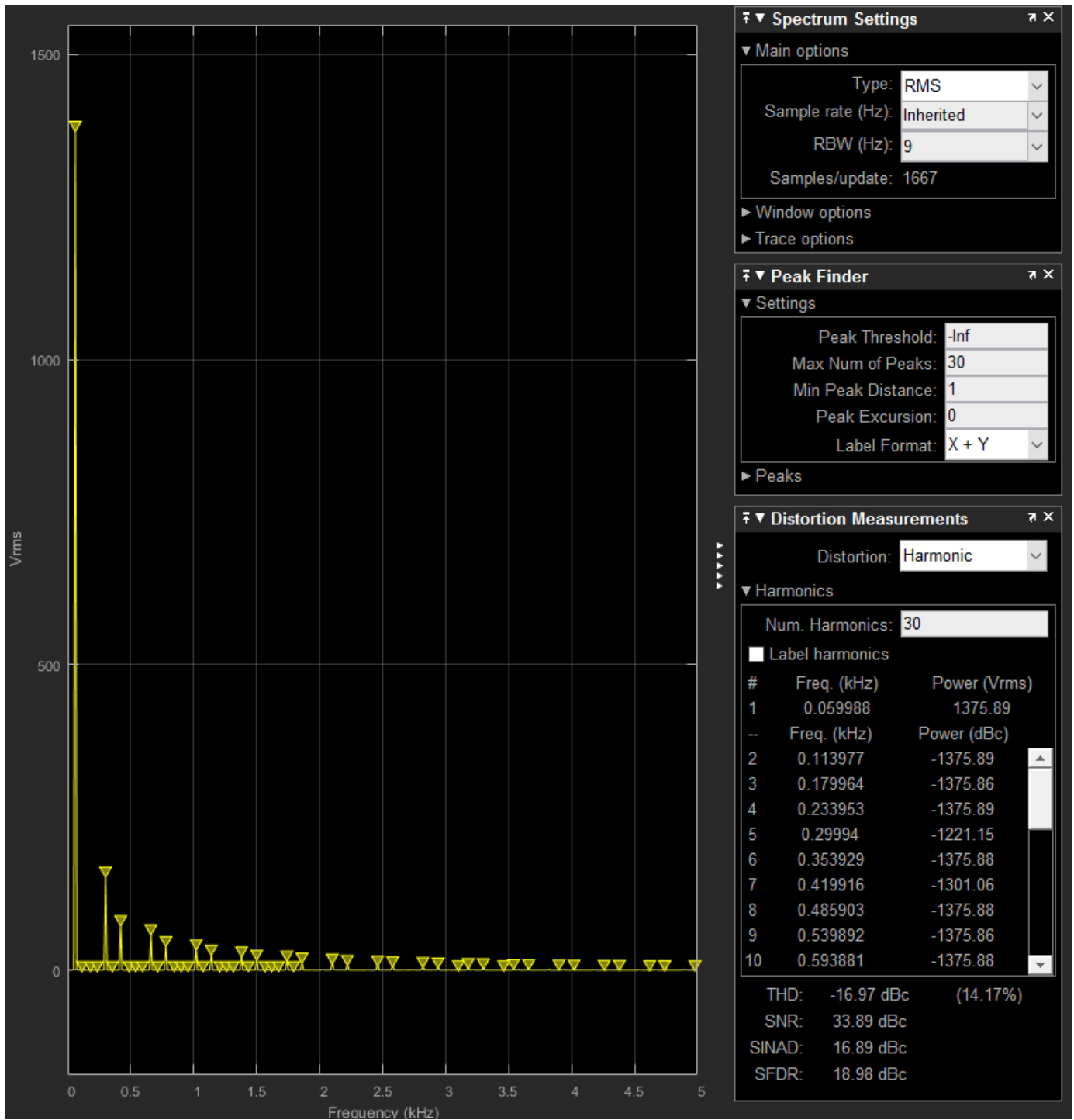
The THD percentage is 14.17% and the fundamental peak power is 1375.89 Vrms at 0.06 kHz (60 Hz). These results agree with the results from the offline harmonic analysis.

See Also

Blocks
PS-Simulink Converter | Selector | Spectrum Analyzer

Functions
ee_calculateThdPercent | ee_getHarmonics | ee_plotHarmonics

Related Examples

- "Harmonic Analysis of a Three-Phase Rectifier" on page 10-492
- "Choose a Simscape Electrical Function for an Offline Harmonic Analysis" on page 7-24
- "Data Logging"

Perform a Load-Flow Analysis Using Simscape Electrical

In this section...

Simscape Electrical can perform a power-flow, or load-flow, analysis for an AC electrical power transmission system modeled using the Simscape three-phase electrical domain. A load-flow analysis allows you to determine the voltage magnitudes, voltage phase angles, active power, and reactive power of the electrical system in steady-state operation.

For a given steady-state operating point, the load-flow data reveals the:

- Voltage magnitude and voltage phase angle at each bus
- Active and reactive power generation for each generator that supplies the grid
- Active and reactive power that flows to each load that places demand on the grid

You can use the data to determine ideal operating conditions or estimate the response of your system to hypothetical situations. For example, if you know the active and reactive power in each transmission line, you can determine if the remaining lines can handle the extra load that occurs when one or more transmission lines go offline.

You can also use the data to calculate transmission line or system losses and examine the overall voltage profile of the network. Investigating these attributes can help you determine if the system needs reactive power compensation to overcome low voltage levels.

Network Requirements for a Simscape Electrical Load-Flow Analysis

To determine the steady-state load-flow solution for a three-phase network using Simscape Electrical, your model must be:

- Compatible with and configured for the Simscape frequency and time simulation mode. For more information, see "Frequency and Time Simulation Mode" and Solver Configuration.
- Load balanced. The level of approximation of the load-flow analysis depends on how balanced the system is and the level of harmonics that are present.
- Enabled for Simscape data logging. For complex models or long simulation runs, you can improve simulation performance by enabling data logging for selected blocks by using local solver settings. For a load-flow analysis, data logging is required only for Busbar blocks. For more information, see "Enable Data Logging for the Whole Model" and "Log Data for Selected Blocks Only".

Essential Blocks for a Load-Flow Analysis

Bus Bar Connectors

In an electrical transmission system, a bus bar connector, or *bus*, is a vertical line that connects power components such as generators, loads, and transformers. To represent buses, the **Simscape > Electrical > Connectors & References** library provides the Busbar block.

Three-Phase Voltage Sources

You need to select the right three-phase voltage source for your model to conduct a load-flow analysis. Which source you choose depends on whether you want to prioritize simulation accuracy or performance. The balance between simulation accuracy and performance depends, in part, on the blocks that you use to represent the voltage sources in your analysis model. Simulation accuracy is a measure of model fidelity, that is, how closely the simulation results agree with mathematical and empirical models. As model fidelity increases, so does the computational cost of simulation. As computational cost increases, simulation speed, decreases. Conversely, as model fidelity decreases, simulation speed increases.

Prioritize Model Fidelity by Using Machine Blocks

To prioritize model fidelity over simulation speed, represent voltage sources by using induction or synchronous machine blocks. For modeling induction machines, the **Simscape > Electrical > Electromechanical > Asynchronous Machines** library provides both the Induction Machine Squirrel Cage and Induction Machine Wound Rotor blocks. For modeling synchronous machines, the **Simscape > Electrical > Electromechanical > Synchronous Machines** library provides the Synchronous Machine Model 2.1, Synchronous Machine Round Rotor, and Synchronous Machine Salient Pole blocks.

Prioritize Simulation Speed by Using Load Flow Source Blocks

For a faster simulating, but lower fidelity model, represent the voltage sources in your analysis model by using a Load Flow Source block from the **Simscape > Electrical > Sources** library. The Load Flow Source block supplies either an idealized or a current-dependent voltage source. The voltage can contain series impedance or can act as a source for a swing, PV, or PQ bus.

Performing a Load-Flow Analysis

To examine load-flow data for a three-phase Simscape Electrical transmission system model that is compatible with frequency-time simulation mode:

1 Enable Simscape data logging.

2 Parameterize the voltage sources.

   At the beginning of a load-flow analysis, the equation variables for transmission line losses are unknown. While the unknown variables are being solved, the buses balance the losses by providing or absorbing active and reactive power. For each bus there are four variables:

   • P — Active power
   • Q — Reactive power
   • V — Voltage
   • θ — Phase angle

Two of the variables are known and two are unknown. Which variables are known and which are unknown depends on the actively controlled three-phase sources and loads that are connected to the bus bar. The voltage source block configurations determine which bus types are used in load-flow analysis. You can include more than one bus type in your model. Bus type options are:

- Swing bus — A swing, slack, or reference, bus balances the active and reactive power in a system. The slack bus serves as an angular reference for other buses in the system. The phase angle of a swing bus is 0° and the voltage magnitude is specified. A typical value is `1 pu`. At the beginning of the load-flow analysis, P and Q are the unknown variables for this bus.
- PV bus — A PV (or generator) bus balances the active and reactive power in a system by supplying a constant, active power and voltage. At the beginning of the load-flow analysis, θ and Q are the unknown variables for this bus.
- PQ bus — A PQ (or load) bus determines the amount of active and reactive power that is consumed. At the beginning of the load-flow analysis, V and θ are the unknown variables for this bus.

If your model contains one or more:

- Load Flow Source blocks — For each block, for the **Source type** parameter, set the bus type to one of these options:

  - `Swing bus`
  - `PV bus`
  - `PQ bus`

  Specify the related parameters, which differ depending on which bus type you choose.

  To avoid a simulation issue due to a nonoptimal minimum for PV or PQ buses, in the **Expected Ranges** settings, specify minimum and maximum values for the **Internal source phase search range** parameter.
- Induction machine blocks — For each block, specify the priority and beginning values for the block using the **Variables** settings. In the **Main** settings, set the **Initialization option** parameter to `Set targets for load flow variables`. In the **Variables** settings, select a **Priority** and specify a **Beginning Value** for:

  - **Slip**
  - **Real power generated**
  - **Mechanical power consumed**

  For more on information on setting initial target values by using the **Variables** settings, see "Set Priority and Initial Target for Block Variables".

  To fully specify the initial condition, you must include an initialization constraint in the form of a high-priority target value. For example, if your induction machine is connected to an Inertia block, the initial condition for the induction machine is completely specified if, in the **Variables** settings of the Inertia block, the **Priority** for **Rotational velocity** is set to `High`. Alternatively, you could set the **Priority** to `None` for the Inertia block **Rotational velocity**, and instead set the **Priority** for the induction machine block **Slip**, **Real power generated**, or **Mechanical power consumed** to `High`.
- Synchronous machine blocks — For each block, specify the bus type and beginning values using the **Initial Conditions** settings. The available parameter targets depend on whether the block is configured for a swing, PV, or PQ bus. In the **Initial Conditions** settings:

    a   Set the **Initialization option** parameter to `Set targets for load flow variables`.

    b   Select a bus for the **Source type** parameter.

    c   Specify values for the related bus parameter.

    d   To avoid a simulation issue due to a nonoptimal minimum, in the **Expected ranges** settings, specify minimum and maximum values for the **Internal source phase search range** parameter.

3   Configure each Busbar block:

    a   Set the **Number of connections** to 2, 3, or 4.

    b   Specify the voltage and frequency to match the specified values of the connected voltage source block.

    c   To view the load-flow data using a Scope block, expose the optional measurement ports on the Busbar block:

       •   To expose ports **Vt** and **ph**, set **Measurement ports** to `Yes`.

       •   To expose ports **P** and **Q**, set **Measurement ports** to `Yes`.

    Connect the Busbar and Scope blocks.

4   Configure the Solver Configuration block. Set **Equation formulation** to `Frequency and time`.

5   Simulate the model.

After simulating, you can view the load-flow results in the Busbar block annotation and in the Simscape logging data that the model outputs to the MATLAB workspace.

For examples that show how to perform a load-flow analysis, see:

•   2-Bus Loadflow on page 10-85

•   IEEE 9-Bus Loadflow on page 10-83

Machine Parameterization and Variable Initialization

You can use the data from your load-flow analysis to correctly initialize three-phase induction and synchronous machine blocks. For examples, see:

•   Induction Motor Initialization with Loadflow on page 10-87

•   Synchronous Machine Initialization with Loadflow on page 10-89

Load-Flow Analyzer App

If your model is configured for a power-flow analysis, you can also use the **Load-Flow Analyzer** to perform a power-flow, or load-flow, analysis for a three-phase AC electrical power transmission system. The tool generates two tables. The app generates two tables. One of the tables contains data for the network nodes, as represented by Busbar, Load Flow Source, synchronous machine, induction machine, and three-phase load blocks. The other table contains data for the network connections, as represented by transmission lines, cable, and transformer blocks. When you open the tool, the tables are preloaded with the specified parameter values for the relevant blocks in the current or specified model. After you run the power-flow analysis, the tables also display the steady-state voltage magnitudes, voltage phase angles, active power, and reactive power for the node and connection blocks.

The **Load-Flow Analyzer** app allows you to:

- Run a load-flow analysis.
- Highlight and update load-flow input block parameter values for busbar, load flow source, synchronous machine, induction machine, and three-phase load blocks.
- Change the bus type of load flow source, synchronous machine, and induction machine blocks.
- Select and highlight node and connection blocks in the model.
- Sort columns in the tables by increasing or decreasing values.
- Export the data to a spreadsheet, a MAT-file, or comma-separated variable (CSV) files.

Troubleshooting Load-Flow Analysis and Initialization Issues

If you encounter issues when simulating a load-flow model, apply these troubleshooting measures. Testing your load-flow model incrementally can help you avoid specifying nonphysical load-flow requirements.

Internal Load-Flow Source Impedance

Including internal source impedance for a Load Flow Source block when the **Source type** parameter of the block is set to `Swing bus`, `PV bus`, or `PQ bus` can prevent initialization convergence. To resolve any convergence issues, use one of these methods:

- Limit the solution range by specifying a value for the **Internal source phase search range** parameter.
- Neglect source impedance.
- Model the impedance externally from the Load Flow Source block.

Field-Circuit Transient or Initial Rotor Acceleration

If you initialize a synchronous machine block for a load-flow analysis, the block solves all Park-transformed flux variables and mechanical variables for steady state. However, incorrect initialization of an automatic voltage regulator (AVR) or governor can result in a field-circuit transient or an initial rotor acceleration. To resolve these issues:

1 Determine the initialization values for the torque and field voltage.

   a Run the load-flow analysis by using approximated values for the AVR and governor and settings.

   b Make a note of these values in the load-flow results reported by the adjacent Busbar block:

   - Voltage magnitude
   - Phase angle
   - Generated real power
   - Generated reactive power

   c For the synchronous machine block, in the **Initial Conditions** settings, set the **Initialization option** parameter to `Set real power, reactive power, terminal voltage, and terminal phase`.

   d Specify these parameters using the values from the load flow results:

- **Terminal voltage magnitude**
- **Terminal voltage angle**
- **Terminal active power**
- **Terminal reactive power**

e   Print the required initial conditions for the AVR and governor to the MATLAB workspace. Right-click the machine block and, from the context menu, select **Electrical > Display Associated Initial Conditions**. The relevant data are the field circuit voltage, *si_efd0*, and the mechanical torque, *si_torque0*.

2   Specify the AVR and governor initial conditions using the calculated initial condition values.

For example, the table shows the annotated data for the Busbar block that is next to the Synchronous Machine Salient Pole block in `ee_loadflow_sm_initialization`, the model for the "Synchronous Machine Initialization with Loadflow" on page 10-89 example. If you open the Synchronous Machine Salient Pole block, click the **Initial Conditions** settings, and set the **Initialization option** parameter to `Set real power, reactive power, terminal voltage, and terminal phase`, you can observe that the specified parameter values are equal to the load-flow simulation values.

Note The specified parameter values have already been entered to match the load flow results for this model.

Physical Quantity	Load-Flow Simulation Value	Synchronous Machine Block Initial Conditions Parameter Name	Synchronous Machine Block Initial Conditions Parameter Value
Voltage magnitude	1.020 pu	**Terminal voltage magnitude**	13.8*1.02 kV
Phase angle	0.00 deg	**Terminal voltage angle**	0 deg
Generated real power	31.2 MW	**Terminal active power**	31.2e6 V*A
Generated reactive power	10.4 Mvar	**Terminal reactive power**	10.4e6 V*A

If you print the data to the command line, the *si_torque0* and *si_efd0* data are printed under the `Initial conditions required for steady-state (SI)`:

```
Initial conditions required for steady-state (SI):
    si_efd0 =       85.4468     : V     % Field circuit voltage
    si_ifd0 =       1168.87     : A     % Field circuit current
    si_torque0 =    828709      : Nm    % Mechanical torque
    si_Pm0 =        3.12416e+07 : W     % Mechanical power
```

To initialize correctly, specify `85.4468` V as the value for the field voltage source, and `828709` Nm as the value for the **Shaft torque** Constant block that is connected to the Ideal Torque Source block.

Multiple Load-Flow Simulation Solutions

There are often multiple solutions to the set of load-flow targets specified when initializing an AC electrical network. For example, for a PV bus source where you specify the active power and voltage, there are two solutions for the reactive power. For the desired solution, the magnitude of the reactive

power is typically less than the specified active power magnitude. For the undesired solution, the reactive power magnitude is much larger than the active power magnitude.

If the initialization returns the undesired solution, reconfigure the Load Flow Source or synchronous machine block and increase the value for the minimum boundary of the **Internal source range search range** parameter. For the Load Flow Source block, the parameter is in the **Expected Ranges** settings. For synchronous machine blocks, the parameter is in the **Initial Conditions** settings.

Nonoptimal Local Minimum

The simulation can stop and generate an error if, to satisfy the active and reactive power demands, the optimization decreases the Busbar block voltage, to the point where the solution is closer to an undesired local minimum around zero busbar voltage than to the desired load flow solution. To prevent this type of issue, reconfigure the Load Flow Source or synchronous machine blocks and increase the value of the **Minimum voltage (pu)** parameter. For the Load Flow Source block, the parameter is in the **Expected Ranges** settings. For synchronous machine blocks, the parameter is in the **Initial Conditions** settings.

Frequency and Time Simulation Mode Incompatibility

You can only perform a load-flow analysis by using the frequency and time simulation mode. Replace any blocks that are not compatible with the frequency and time simulation mode. For more information, see "Frequency and Time Simulation Mode".

See Also

Simscape Blocks
Busbar | Induction Machine Squirrel Cage | Induction Machine Wound Rotor | Load Flow Source | Synchronous Machine Model 2.1 | Synchronous Machine Round Rotor | Synchronous Machine Salient Pole

Apps
Load-Flow Analyzer

Related Examples

- 2-Bus Loadflow on page 10-85
- IEEE 9-Bus Loadflow on page 10-83
- Induction Motor Initialization with Loadflow on page 10-87
- Synchronous Machine Initialization with Loadflow on page 10-89

Real-Time Simulation

Prepare Simscape Electrical Models for Real-Time Simulation Using Simscape Checks

If you have a Simulink Real-Time license, you can optimize your model for real-time execution using the `Execute real-time application` activity mode in the Simulink Performance Advisor. This mode includes several checks specific to physical models. For example, the Simulink Performance Advisor identifies Simscape Solver Configuration blocks with settings that are suboptimal for real-time simulation. For optimal results, Solver Configuration blocks should have the **Use local solver** and **Use fixed-cost runtime consistency iterations** options selected.

The checks are organized into folders. You can use the checks in the **Simscape checks** folder for all physical models. Subfolders contain checks that target blocks from Simscape Electrical and other add-on products such as Simscape Driveline and Simscape Multibody.

Before you run the checks, use the processes described in "Real-Time Model Preparation Workflow", "Real-Time Simulation Workflow", and "Hardware-In-The-Loop Simulation Workflow".

To run the Simulink Real-Time Performance Advisor Checks:

1   In the Simulink Editor menu bar, select **Debug > Performance Advisor**.
2   In the Performance Advisor window, under **Activity**, select `Execute real-time application`.
3   In the left pane, expand the **Real-Time** folder, and then the **Simscape checks** folder.
4   Run the top-level Simscape checks and the Simscape Electrical checks. If your model contains blocks from other add-on products, also run the checks in the subfolder corresponding to that product.

See Also

More About

- "Model Preparation Objectives"
- "Real-Time Model Preparation Workflow"
- "Real-Time Simulation Workflow"
- "Use Performance Advisor to Improve Simulation Efficiency"
- "Create and Use Code Generation Reports" (HDL Coder)

9

Simscape to HDL Workflow

Get Started with Simscape Hardware-in-the-Loop Workflow

To perform hardware-in-the-loop (HIL) simulation with smaller timesteps and increased accuracy, deploy the Simscape plant models to the FPGAs on board the Speedgoat I/O modules.

- Generate an HDL implementation model by using the Simscape HDL Workflow Advisor. The implementation model is a Simulink model that replaces the Simscape components with HDL-compatible Simulink blocks.
- Generate HDL code for the implementation model, and then deploy the generated code to generic FPGAs, SoCs, or FPGAs on board Speedgoat FPGA I/O modules by using the HDL Workflow Advisor.

By using this capability, you can model and deploy complex physical systems in Simscape that previously took long time to model by using Simulink blocks.

Simscape Example Models for HDL Code Generation

For HDL code generation, you can design your own Simscape algorithm or choose from a list of example models that are created in Simscape. The example models include:

- Boost converter
- Bridge rectifier
- Buck converter
- Half-wave rectifier
- Three phase rectifier
- Two level converter ideal
- Two level converter IGBT
- Solar power inverter model
- Swiss rectifier
- Vienna rectifier

All examples are prefixed with `sschdlex` and postfixed with `Example`. For example, to open the boost converter model, at the MATLAB command prompt, enter:

```
load_system('sschdlexBoostConverterExample')
open_system('sschdlexBoostConverterExample/Simscape_system')
```

Guidelines for Modeling Simscape for HDL Compatibility

Follow these guidelines when designing your Simscape algorithm for compatibility with Simscape HDL Workflow Advisor. To replace the subsystem that uses Simscape blocks with the corresponding state-space algorithm, follow these additional guidelines as described in "Modeling Guidelines for Simscape Subsystem Replacement" (HDL Coder).

Use Linear and Switched Linear Blocks

Create a Simscape model by using linear and switched linear blocks. Linear blocks are blocks that are defined by a linear relationship such as resistors. Switched linear blocks are blocks such as diodes and switches. These blocks are also defined by a linear relationship such as $V = IR$ where R can switch between two or more values depending on the state of the diodes or switches.

Nonlinear blocks are not supported. To verify that the Simscape model does not contain nonlinear blocks, use the `simscape.findNonlinearBlocks` function. Provide the path to your Simscape model as an argument to this function.

```
simscape.findNonlinearBlocks('current_model')
```

Alternatively, to verify that the model does not contain nonlinear blocks, run the "Check switched linear task" (HDL Coder) of the Simscape HDL Workflow Advisor.

Specify Backward Euler Solver with Discrete Sample Time

Configure the solver options for HDL code generation by using a Solver Configuration block.

In the Block Parameters dialog box of this block:

- Select **Use local solver**.
- Use `Backward Euler` as the **Solver type**.
- Specify a discrete sample time, Ts.

To verify that the solver settings are specified correctly, run the "Check solver configuration task" (HDL Coder) of the Simscape HDL Workflow Advisor.

Run hdlsetup function

After creating the model, configure the model for HDL code generation by running the `hdlsetup` function. `hdlsetup` configures the solver settings such as using a fixed-step solver, specifies the simulation start and stop times, and so on. To run the command for your `current_model`:

```
hdlsetup('current_model')
```

Restrictions for HDL Code Generation from Simscape Models

HDL Coder™ does not support code generation from Simscape networks that contain:

- Events
- Mode charts
- Delays
- Runtime parameters
- Periodic sources
- Simscape Multibody blocks
- Simscape Electrical Specialized Power Systems blocks
- Nonlinear and time-varying Simscape blocks. Time-varying blocks include blocks such as Variable Inductor and Variable Capacitor.
- Nonscalar states or inputs to the network. Split nonscalar inputs into scalar inputs and reduce the second operand of the colon operator by one for error caused by nonscalar states. For example:

```
% Suppose this code generates an error
tmp1 = u(1:4);

% Fix the error by reducing second operand by 1
tmp1 = u(1:3);
```

See Also
makehdl | sschdladvisor

More About
- "Generate HDL Code for Simscape Models" (HDL Coder)
- "Generate Simulink Real-Time Interface Subsystem for Simscape Two-Level Converter Model" (HDL Coder)

Modeling Guidelines for Simscape Subsystem Replacement

To generate HDL code for Simscape algorithms, you generate an HDL implementation model by using the Simscape HDL Workflow Advisor. If you follow certain guidelines when modeling the Simscape algorithm, the Simscape HDL Workflow Advisor replaces the Simscape subsystem with a corresponding `HDL Subsystem` block in the HDL implementation model. The `HDL Subsystem` block contains the state-space algorithm that uses HDL-compatible Simulink blocks instead of Simscape blocks. You can generate HDL code for the `HDL Subsystem` block and deploy the code onto FPGA target devices and FPGAs on board Speedgoat FPGA I/O modules. In this case, when you select the **Generate validation logic for the implementation model** check box in the **Generate implementation model** task of the Simscape HDL Workflow Advisor, the Advisor generates a separate state-space validation model. This model compares the outputs from the `HDL Subsystem` and the original Simscape subsystem to verify that they are functionally equivalent.

If you do not follow the guidelines, the Simscape HDL Workflow Advisor might not be able to perform this replacement. In that case, the HDL implementation model contains the state-space algorithm with the original Simscape subsystem beside it. Before generating code, you modify the implementation model and rearrange the blocks such that it replaces the Simscape subsystem with the state-space algorithm. In this case, when you select the **Generate validation logic for the implementation model** check box, the Advisor places a validation logic subsystem inside the implementation model to verify functional equivalence.

In addition to these guidelines, make sure that the Simscape model is configured for compatibility with Simscape HDL Workflow Advisor. See "Guidelines for Modeling Simscape for HDL Compatibility" (HDL Coder).

Enclose Simscape Blocks Inside a Subsystem

- Enclose the Simscape blocks for which you are generating an HDL implementation model inside a Subsystem block and provide the test inputs. Inside the Subsystem block, your model can have multiple hierarchies that use Simscape blocks.

- Do not use masked subsystems. The Simscape HDL Workflow Advisor cannot replace masked subsystems in the HDL implementation model.

- Inside the Subsystem block that contains Simscape blocks, at the input ports, add Simulink-PS Converter blocks. At the output ports of this subsystem, add PS-Simulink Converter blocks.

  - Use a meaningful name for the Simulink-PS Converter and PS-Simulink Converter blocks.

    The Simscape HDL Workflow Advisor uses the names of the Simulink-PS Converter and PS-Simulink Converter blocks for the input and output ports of the `HDL Subsystem` block. Using a meaningful name makes it easier to identify what the input and output ports in the HDL implementation model correspond to.

  - In the Block Parameters dialog box of the Simulink-PS Converter and PS-Simulink Converter blocks, on the **Input Handling** tab, leave **Filtering and derivatives** set to `Provide signals` and **Provided signals** set to `Input only`.

For example, open the buck converter model. The Simscape_system block contains Simscape blocks. Blocks outside this subsystem form the test environment.

```
open_system('sschdlexBuckConverterExample')
sim('sschdlexBuckConverterExample')
```
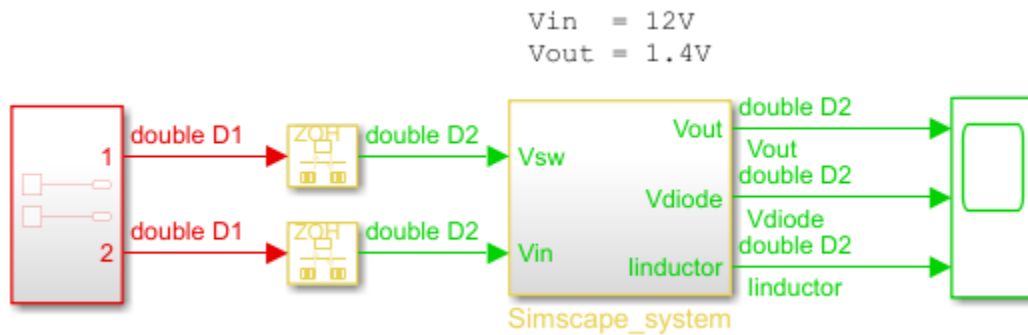
Inside the `Simscape_system` subsystem, the model uses Simscape blocks and physical signals. The model has Simulink-PS Converter and PS-Simulink Converter blocks at the interfaces. Provide unique names for these blocks such that they match the corresponding port names.

```
open_system('sschdlexBuckConverterExample/Simscape_system')
```
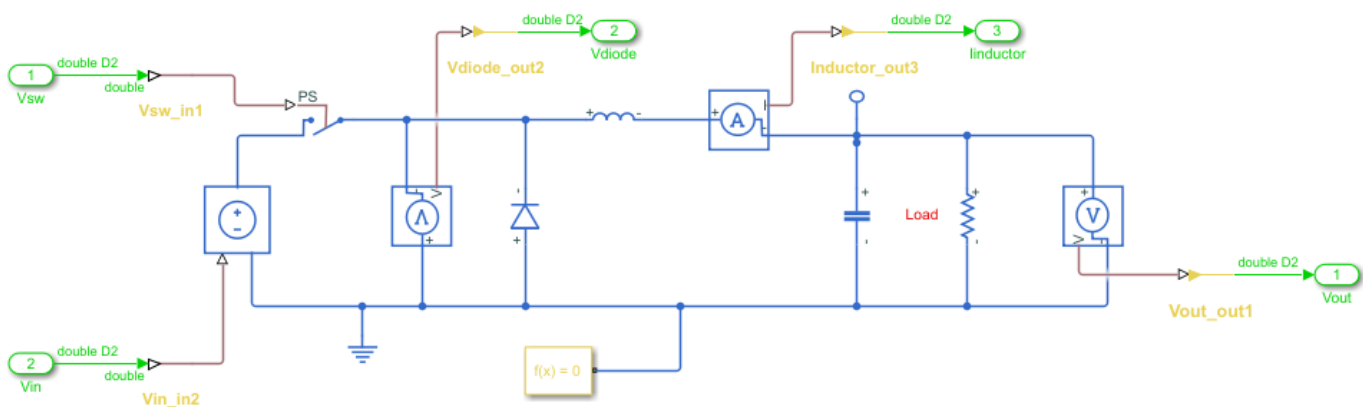

Multiple Simscape Network Considerations

If your Simscape model contains multiple networks:

- Enclose each network inside a subsystem. Add Simulink-PS Converter and PS-Simulink Converter blocks at the subsystem interface.

- Use a Solver Configuration block for each network. Use the same sample time across Solver Configuration blocks inside the different networks.

  For example, this model contains more Simscape networks than Solver Configuration blocks, the Simscape network is not replaced with the HDL subsystem.
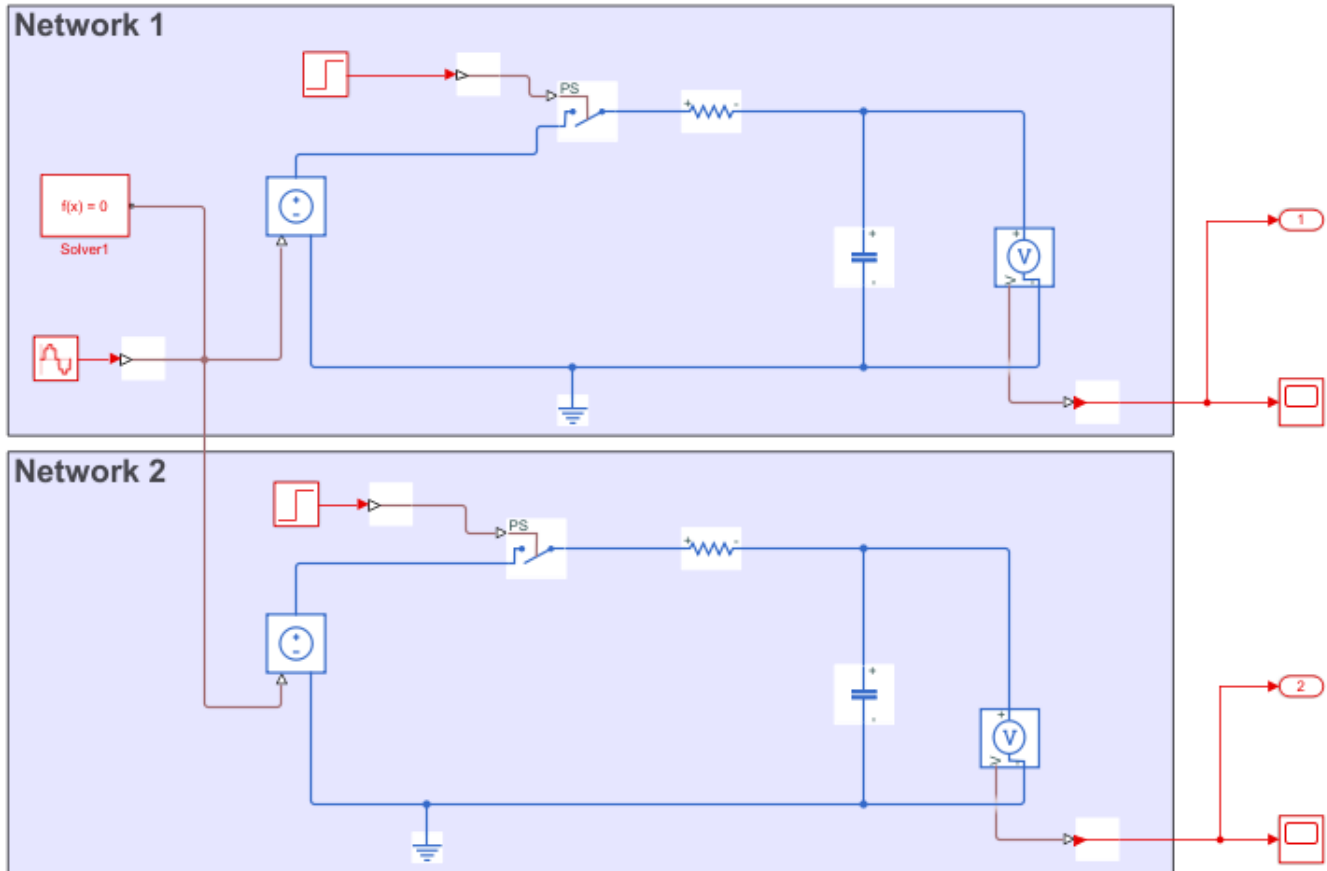
The Simscape HDL Workflow Advisor then replaces each Simscape subsystem with the corresponding `HDL Subsystem`.

For an example that shows how to generate HDL code for a model that has multiple networks, see "Generate HDL Code for Simscape Models with Multiple Networks" (HDL Coder).

Avoid Using Certain Blocks in Simscape Utilities Library

To generate an implementation model that replaces the Simscape subsystem with the state-space algorithm, in your original Simscape model, do not use these blocks from the **Simscape** > **Utilities** Library:

- Simscape Bus
- Connection Port
- Connection Label

For example, this model contains Connection Label and Simscape Bus blocks inside two different subsystems. The Simscape HDL Workflow Advisor cannot replace these subsystems with the state-space algorithm.
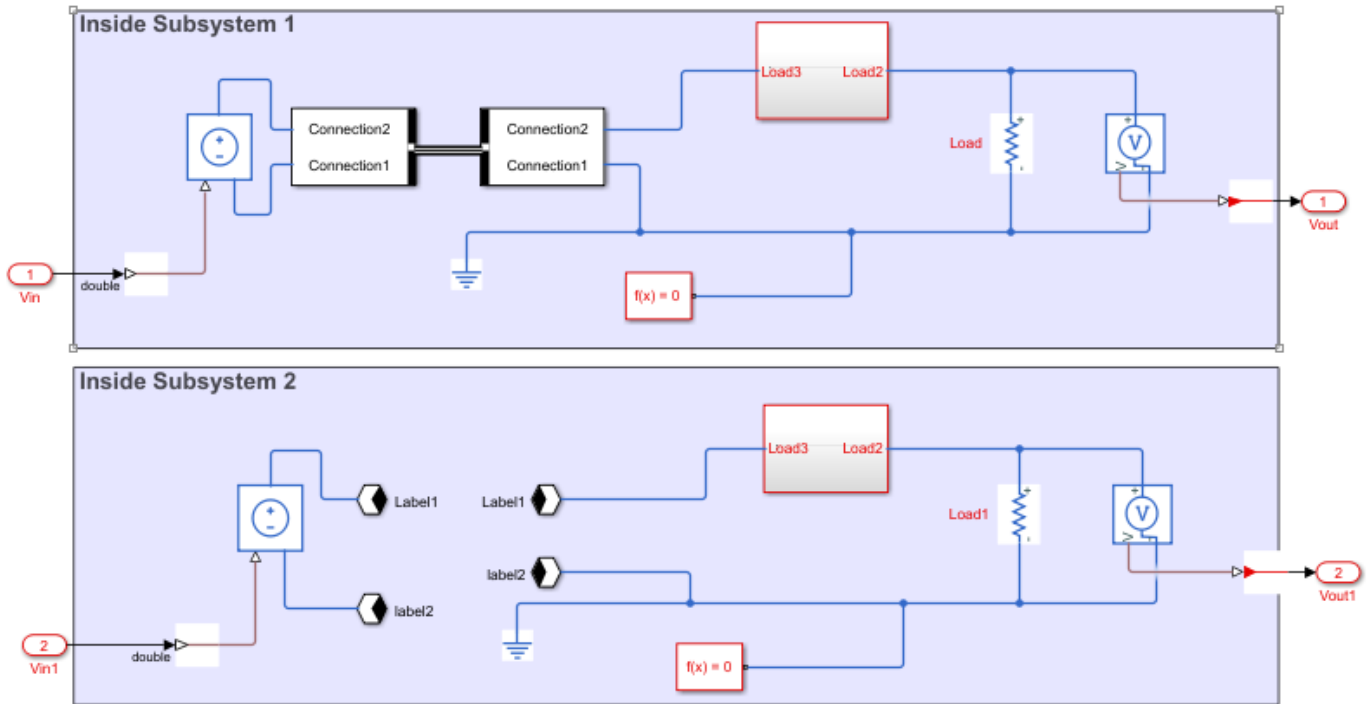
See Also

makehdl | sschdladvisor

More About

- "Get Started with Simscape Hardware-in-the-Loop Workflow" (HDL Coder)
- "Generate HDL Code for Simscape Models" (HDL Coder)
- "Generate Simulink Real-Time Interface Subsystem for Simscape Two-Level Converter Model" (HDL Coder)

Generate HDL Code for Simscape Models

This example shows how to generate HDL code for a halfwave rectifier model that uses Simcape™ blocks. Use the Simscape HDL Workflow Advisor to generate an HDL implementation model. You can then generate HDL code for the implementation model. See "Get Started with Simscape Hardware-in-the-Loop Workflow" (HDL Coder).

The Halfwave Rectifier Model

To open the half-wave rectifier model, at the MATLAB command prompt, enter:

```
open_system('sschdlexHalfWaveRectifierExample')
```

Save this model locally as `HalfWaveRectifier_HDL` to run the workflow.

```
open_system('HalfWaveRectifier_HDL')
set_param('HalfWaveRectifier_HDL', 'SimulationCommand', 'Update')
```
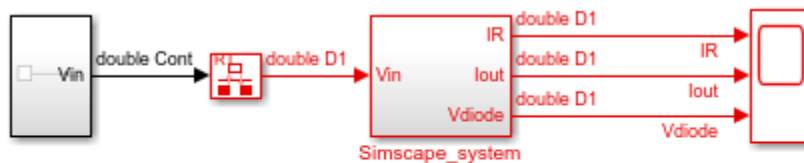

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At the top level of the model, a `Simscape_system` block models the half-wave rectifier algorithm. The model accepts a Sine Wave input, uses a Rate Transition block to discretize the continuous time input, and has a Scope block that calculates the output. To see the half-wave rectifier algorithm, double-click the `Simscape_system` subsystem.

```
open_system('HalfWaveRectifier_HDL/Simscape_system')
```
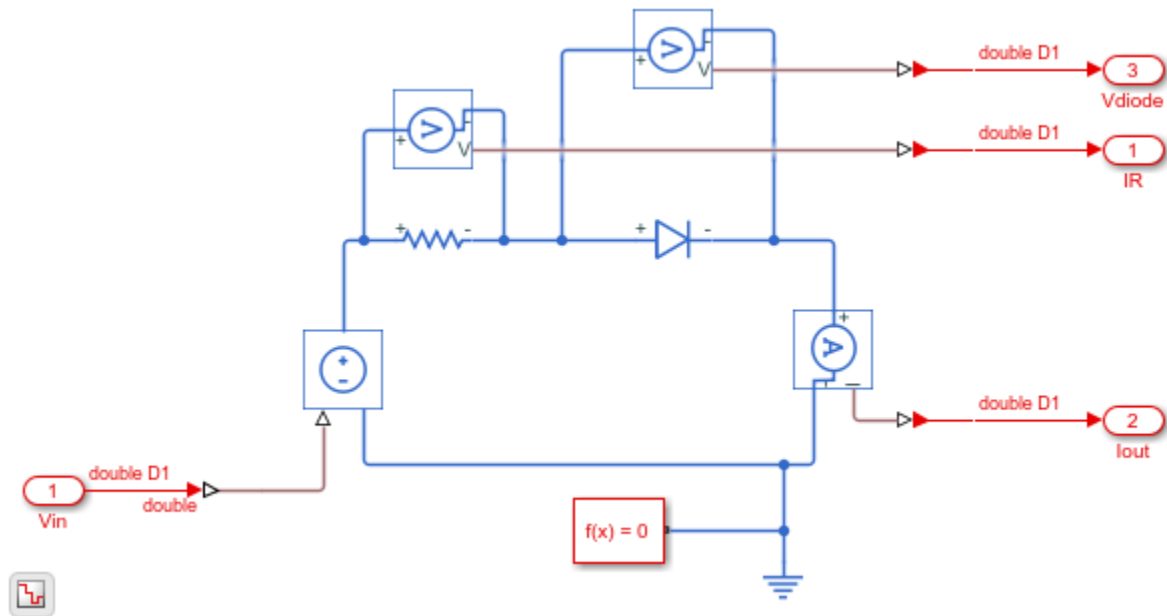
The half-wave rectifier consists of a resistor, which is a linear block, and a diode, which is a switched linear block. The Simscape model is preconfigured for HDL compatibility. At the input and output port interfaces, the model has Simulink-PS Converter and PS-Simulink Converter blocks. The solver settings are configured for compatibility with Simscape HDL Workflow Advisor. If you open the Block Parameters dialog box for the Solver Configuration block, **Use local solver** is selected and `Backward Euler` is specified as the **Solver type**. See "Get Started with Simscape Hardware-in-the-Loop Workflow" (HDL Coder).

To see the functionality, simulate the model and then open the Scope block.

```
sim('HalfWaveRectifier_HDL')
open_system('HalfWaveRectifier_HDL/Scope')
```
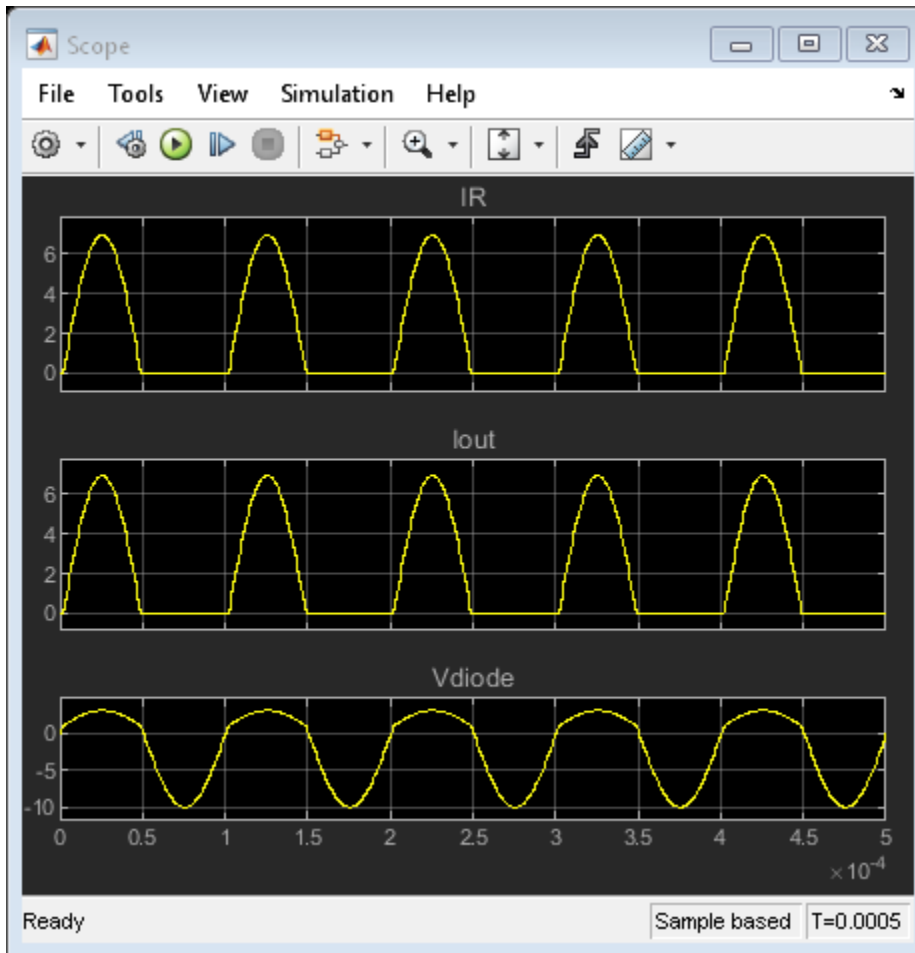
Run Simscape HDL Workflow Advisor

To generate an HDL implementation model from which you generate code, use the Simscape HDL Workflow Advisor. To open the Advisor, run this command:

```
sschdladvisor('HalfWaveRectifier_HDL')
```

```
### Running Simscape HDL Workflow Advisor for <a href="matlab:(HalfWaveRectifier_HDL)">HalfWaveR
```

This command updates the model advisor cache and opens the Simscape HDL Workflow Advisor. To learn more about the Simscape HDL Workflow Advisor and the various tasks, right-click that folder or task, and select **What's This?**. See also "Simscape HDL Workflow Advisor Tasks" (HDL Coder).

To run the workflow and compare functionality of the HDL implementation model with the original Simscape algorithm, select the **Generate implementation model** step, and then select the **Generate validation logic for the implementation model** check box. Use a **Validation logic tolerance** of 0.001. Right-click the **Generate implementation model** step and select **Run to Selected Task**.

The Advisor generates an HDL implementation model and a state-space validation model. The implementation model has the same name as the original Simscape model and uses the prefix gmStateSpaceHDL_. The state-space validation model has the same name as the implementation model and uses the postfix _vnl.

Open and Examine HDL Implementation Model

In the **Generate implementation model task**, click the link to open the implementation model. The model contains a `Simscape_system` subsystem that contains a `HDL Subsystem` block. The `HDL Subsystem` models the state-space representation that you generated from the Simscape model.

```
open_system('gmStateSpaceHDL_HalfWaveRectifier_HDL')
open_system('gmStateSpaceHDL_HalfWaveRectifier_HDL/Simscape_system')
set_param('gmStateSpaceHDL_HalfWaveRectifier_HDL','SimulationCommand','Update')
```
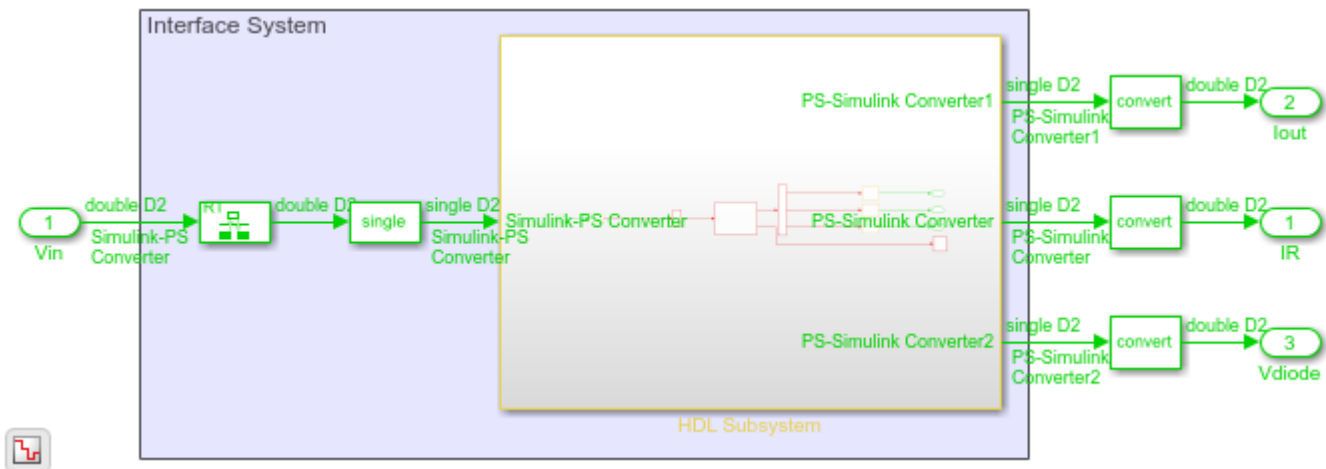

The ports of this subsystem use the same name as the Simulink-PS Converter and PS-Simulink Converter blocks in your original Simscape model. If you navigate inside this subsystem, you see several delays, adders, and Matrix Multiply blocks that model the state-space equations.

```
open_system('gmStateSpaceHDL_HalfWaveRectifier_HDL/Simscape_system/HDL Subsystem/HDL Algorithm')
```
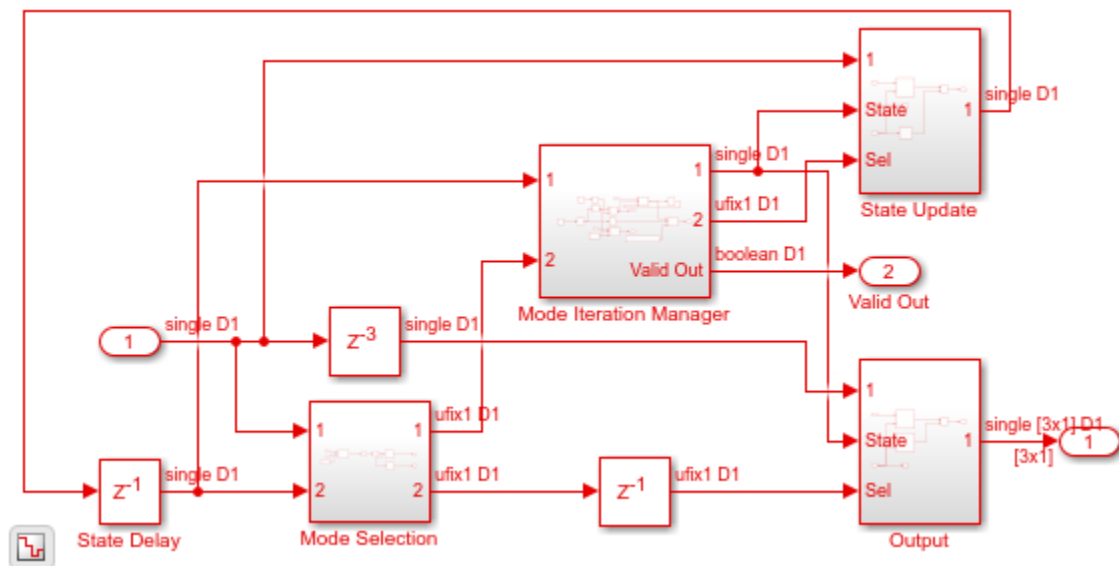

To simulate the HDL Implementation model, enter tese commands. Open the Scope block to view results.

```
sim('gmStateSpaceHDL_HalfWaveRectifier_HDL')
open_system('gmStateSpaceHDL_HalfWaveRectifier_HDL/Scope')
```
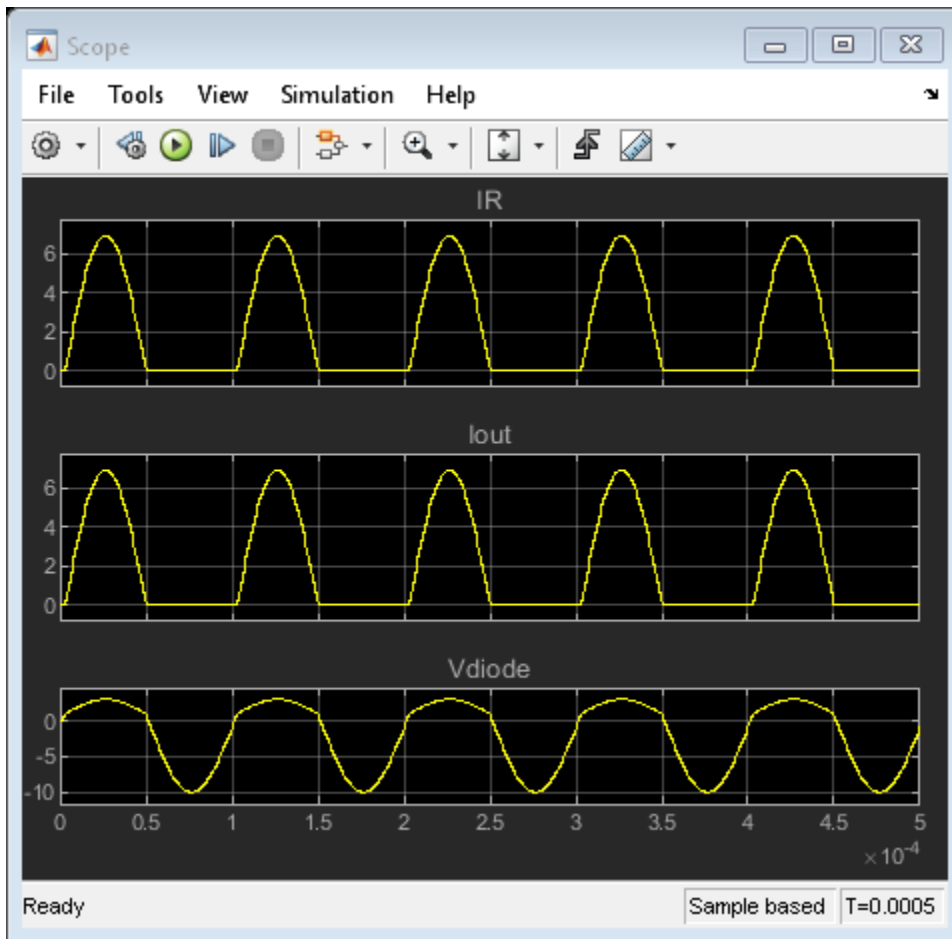

HDL code is generated for the `HDL Subsystem` block inside this model.

Validate HDL Algorithm

To compare functionality of the HDL implementation model with the original Simscape algorithm, open and simulate the state-space validation model.

```
open_system('gmStateSpaceHDL_HalfWaveRectifier_HDL_vnl')
sim('gmStateSpaceHDL_HalfWaveRectifier_HDL_vnl')
```
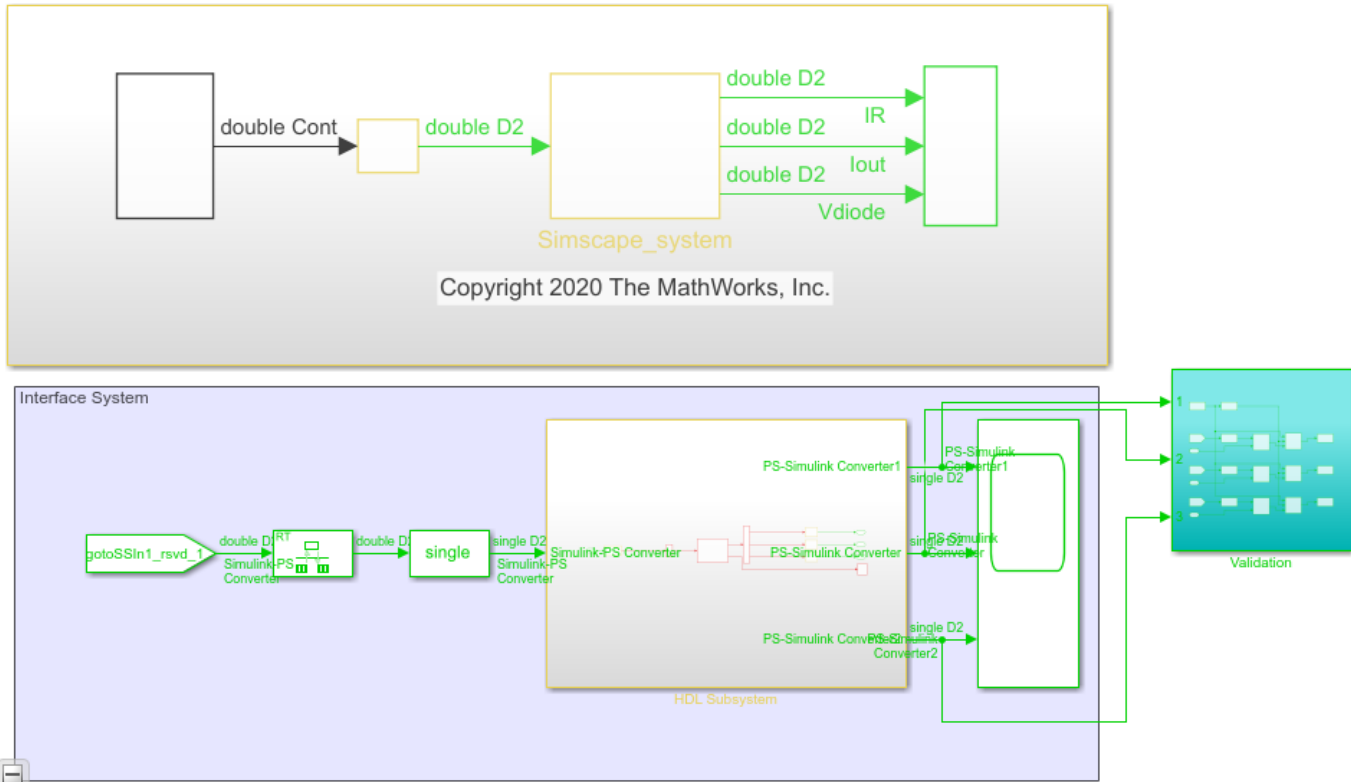
The output of this model matches the original Simscape model. The simulation does not generate assertions, which indicates that the outputs match. For a more systemic verification, see "Validate HDL Implementation Model to Simscape Algorithm" (HDL Coder).

In some cases, your Simscape algorithm might not be compatible for generating an implementation model by using the Simscape HDL Workflow Advisor. In such cases, running certain tasks in the Advisor can result in the task to fail. To learn how you can make it HDL compatible, see:

- "Troubleshoot Conversion of Simscape DC Motor Control to HDL-Compatible Simulink Model" (HDL Coder)
- "Troubleshoot Conversion of Simscape Permanent Magnet Synchronous Motor to HDL-Compatible Simulink Model" (HDL Coder)

Generate HDL Code and Validation Model

The HDL model and subsystem parameter settings are saved using this command:

```
hdlsaveparams('gmStateSpaceHDL_HalfWaveRectifier_HDL');

%% Set Model 'gmStateSpaceHDL_HalfWaveRectifier_HDL' HDL parameters
hdlset_param('gmStateSpaceHDL_HalfWaveRectifier_HDL', 'FloatingPointTargetConfiguration', hdlcode
, 'LatencyStrategy', 'MIN') ...
);
hdlset_param('gmStateSpaceHDL_HalfWaveRectifier_HDL', 'HDLSubsystem', 'gmStateSpaceHDL_HalfWaveRe
hdlset_param('gmStateSpaceHDL_HalfWaveRectifier_HDL', 'MaskParameterAsGeneric', 'on');
hdlset_param('gmStateSpaceHDL_HalfWaveRectifier_HDL', 'Oversampling', 60);

% Set SubSystem HDL parameters
```

```
hdlset_param('gmStateSpaceHDL_HalfWaveRectifier_HDL/Simscape_system/HDL Subsystem', 'FlattenHiera
```

```
hdlset_param('gmStateSpaceHDL_HalfWaveRectifier_HDL/Simscape_system/HDL Subsystem/HDL Algorithm/N
```

The model uses single data types and generates HDL code in `Native Floating Point` mode. Floating-point operators can introduce delays. Because the design contains feedback loops, to allocate sufficient delays for the operators inside the feedback loops, the model uses clock-rate pipelining in conjunction with a large value for the **Oversampling factor**. An **Oversampling factor** of `60` and the clock-rate pipelining optimization is saved on this model.

For more information, see:

- "Clock-Rate Pipelining" (HDL Coder)
- "Oversampling factor" (HDL Coder)
- "Allocate Sufficient Delays for Floating-Point Operations" (HDL Coder)

Before you generate HDL code, enable generation of the validation model. The validation model compares the output of the generated model after code generation and the original model. To learn more, see "Generated Model and Validation Model" (HDL Coder).

Run these commands to save validation model generation settings on your Simulink model:

```
HDLmodelname = 'gmStateSpaceHDL_HalfWaveRectifier_HDL';
hdlset_param(HDLmodelname, 'TargetDirectory', 'C:/Temp/hdlsrc');
hdlset_param(HDLmodelname, 'GenerateValidationModel', 'on');
```

To generate HDL code, run this command:

```
makehdl('gmStateSpaceHDL_HalfWaveRectifier_HDL/HDL Subsystem');
```

The generated HDL code and validation model are saved in `C:/Temp/hdlsrc` directory. The generated code is saved as `HDL_Subsystem_tc.vhd`. To open the validation model, click the link to `gm_gmStateSpaceHDL_HalfWaveRectifier_HDL_vnl.slx` in the code generation logs in the Command Window.

Open the `Compare` block at the output of `HDL Subsystem_vnl` subsystem of the validation model. Then, open the `Assert_Out1` block. To see the simulation results after HDL code generation, open the `Compare: Out1` Scope block. The top graph represents the output of the generated model, and the middle graph represents the output of the implementation model. The botton graph calculates the difference between outputs of both models. As the outputs match, the error is zero.
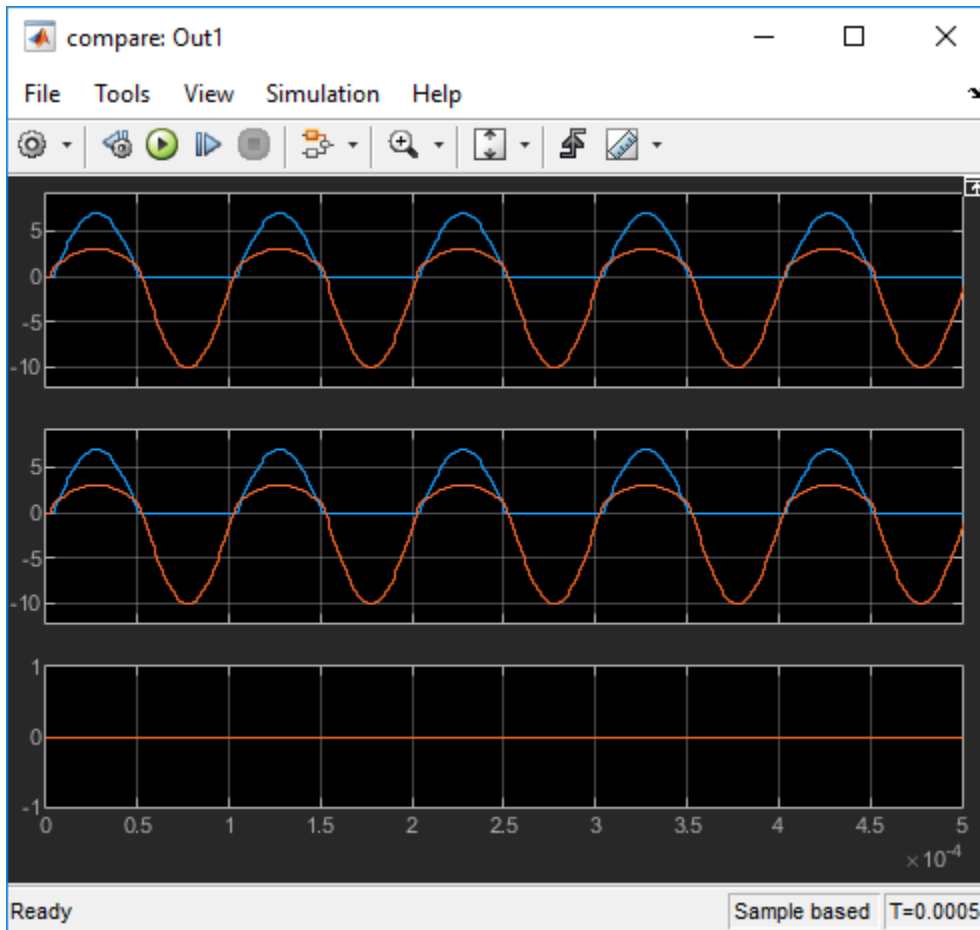
See Also

Functions
checkhdl | makehdl

More About

- "Get Started with Simscape Electrical"
- "Get Started with Simscape Hardware-in-the-Loop Workflow" (HDL Coder)
- "Validate HDL Implementation Model to Simscape Algorithm" (HDL Coder)

Generate Optimized HDL Implementation Model from Simscape

This example shows how you can generate an optimized HDL implementation model for a Simscape™ vienna rectifier model by using optimizations such as resource sharing and RAM mapping.

Why Optimize the HDL Implementation Model

For Simscape models that have many switching elements, the state-space representation contains a large number of configurations. The Simscape HDL Workflow Advisor simulates the Simscape model to calculate the number of relevant configurations. Certain Simscape models can have a large number of configurations that are relevant. The generated HDL implementation model for such a large design can consume a significantly large number of resources. Synthesizing the generated code can cause the design to occupy a large amount of resources on the FPGA device. In some cases, the design might not fit on the target FPGA device. To save resoures and make the design fit on the FPGA, the Simscape HDL Workflow Advisor uses HDL Coder™ optimizations such as clock-rate pipelining, resource sharing, and RAM mapping.

Vienna Rectifier Model

To open the model, at the MATLAB® command prompt, enter:

```
open_system('sschdlexViennaRectifierExample')
```

Save this model as ViennaRectifier_HDL to run the workflow.

```
open_system('ViennaRectifier_HDL')
set_param('ViennaRectifier_HDL', 'SimulationCommand', 'Update')
```
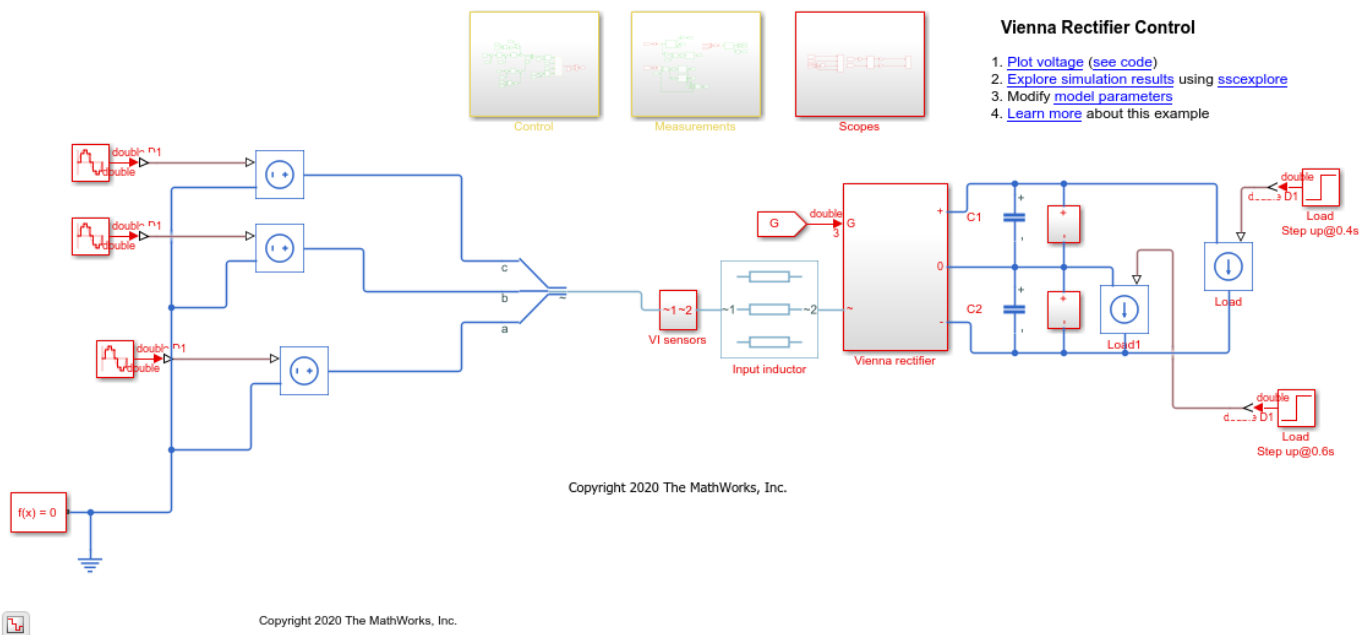

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The `Control` subsystem implements a closed-loop control strategy for the `Vienna rectifier` subsystem by using space-vector modulation. At simulation time `0.1s`, the vienna rectifier is engaged. At times `0.4s` and `0.6s`, the load steps up on the DC side.

The `Vienna rectifier` subsystem consists of three-phase legs. Each leg has one power switch and six power diodes. See "Vienna Rectifier Control" on page 10-611.

```
open_system('ViennaRectifier_HDL/Vienna rectifier')
```
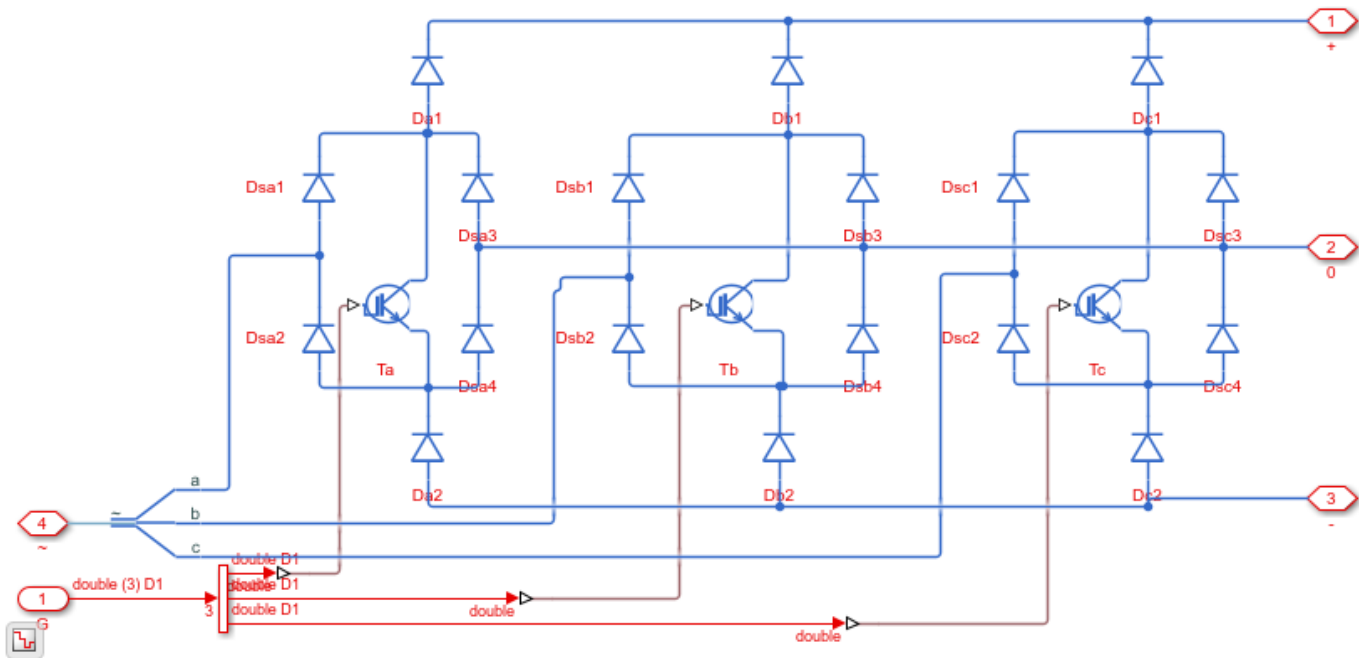

Simulate the model. View the simulation results by double-clicking the Scope blocks inside the `Scopes` subsystem.

```
sim('ViennaRectifier_HDL')
open_system('ViennaRectifier_HDL/Scopes/Scope')
```
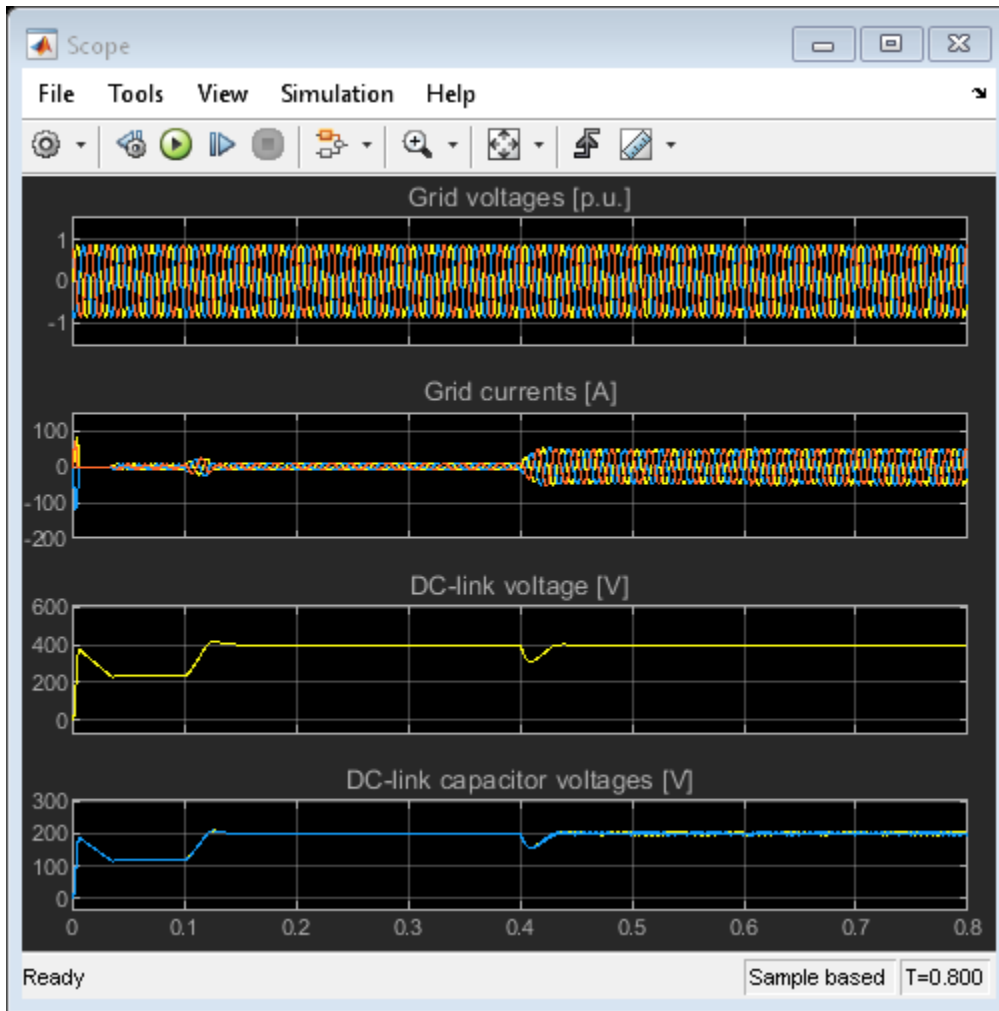
Generate HDL Implementation Model and Validate HDL Algrithm

To generate an HDL implementation model, use the Simscape HDL Workflow Advisor. You can generate HDL code for the implementation model. To open the Advisor, run this command:

```
sschdladvisor('ViennaRectifier_HDL')
```

```
### Running Simscape HDL Workflow Advisor for <a href="matlab:(ViennaRectifier_HDL)">ViennaRecti
```

To run the workflow, right-click the **Generate implementation model** task and select **Run to Selected Task**. After the task passes, you see a link to the HDL implementation model. To see the number of configurations, select the **Extract Equations** task. On the task, you see that simulating the model reaches 558 modes. Such a large number of modes can increase resource consumption of the design on the FPGA.

To open the HDL implementation model, enter these commands:

```
open_system('gmStateSpaceHDL_ViennaRectifier_HDL')
set_param('gmStateSpaceHDL_ViennaRectifier_HDL', 'SimulationCommand', 'Update')
```
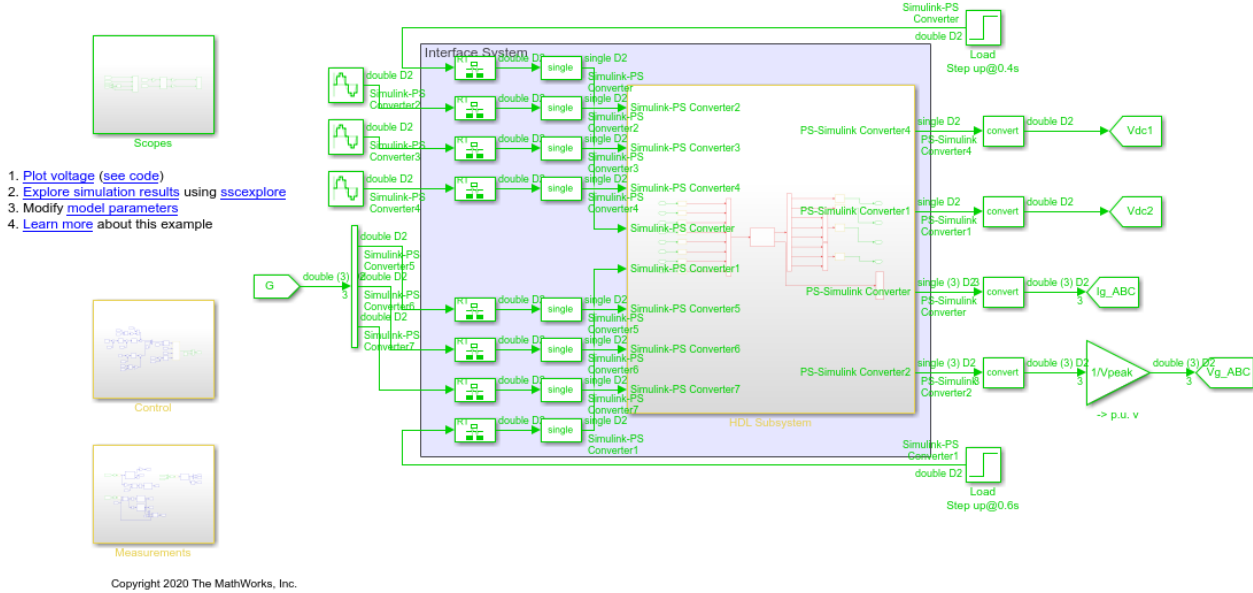
The ports of this subsystem use the same name as the Simulink-PS Converter and PS-Simulink Converter blocks in your original Simscape model. If you navigate inside this subsystem, you see several delays, adders, and Matrix Multiply blocks that model the state-space equations.

```
open_system('gmStateSpaceHDL_ViennaRectifier_HDL/HDL Subsystem/HDL Algorithm')
```
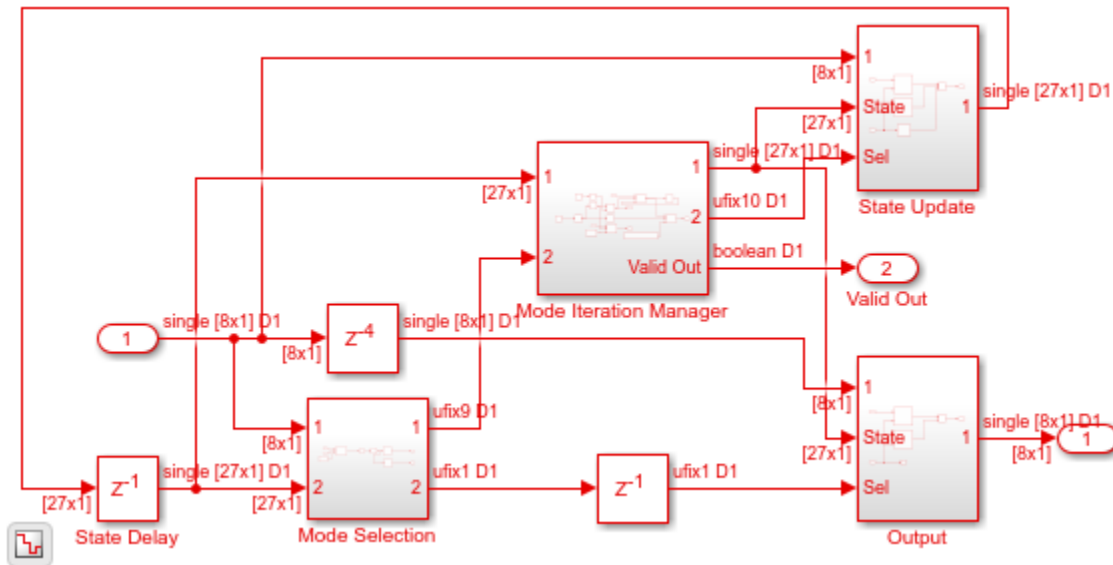

To validate the HDL algorithm, in the **Generate implementation model** task, select the **Generate validation logic for the implementation model** check box, set the **Validation logic tolerance** to `0.001`, and rerun this task. The task generates a state-space validation model that compares the implementation model and the original Simscape model.

```
open_system('gmStateSpaceHDL_ViennaRectifier_HDL_vnl')
set_param('gmStateSpaceHDL_ViennaRectifier_HDL_vnl', 'SimulationCommand', 'Update')
```
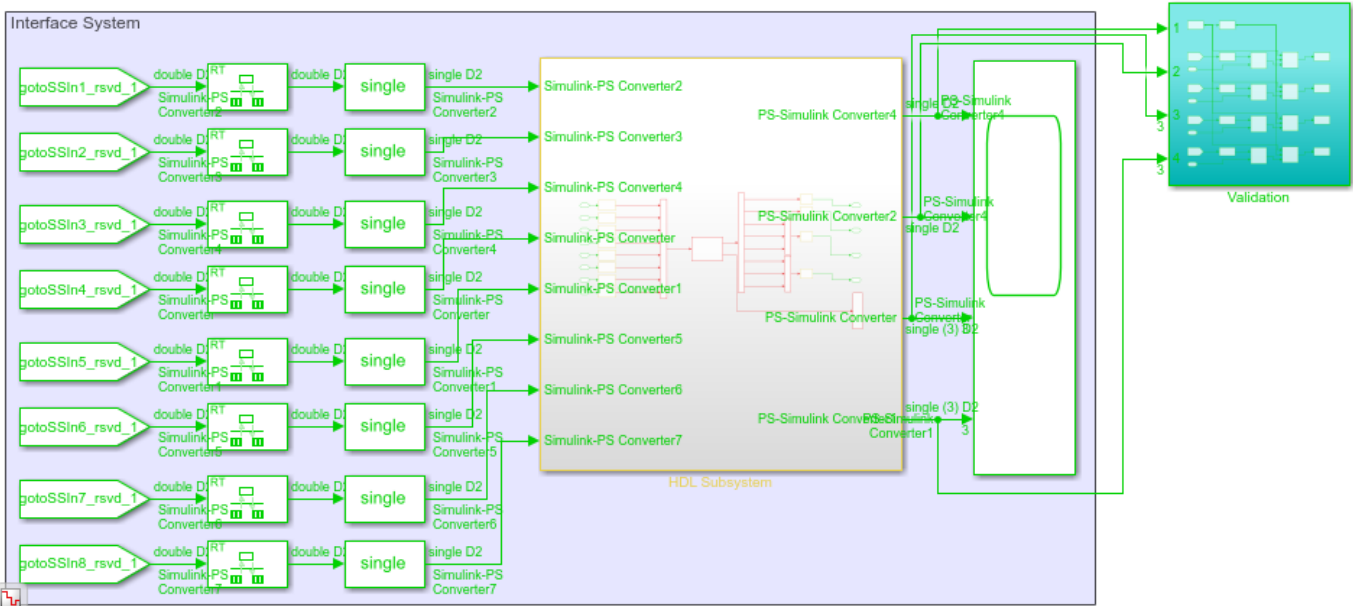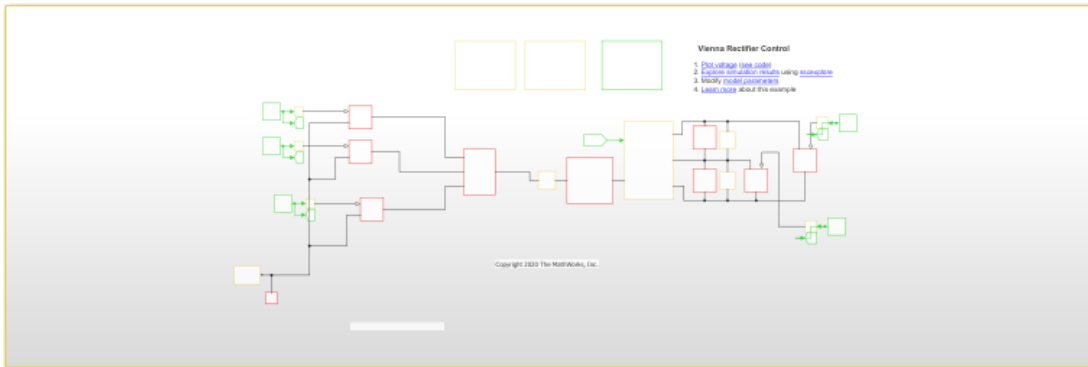

Simulating the model does not display assertions, which indicates that the HDL algorithm matches the original model.

```
sim('gmStateSpaceHDL_ViennaRectifier_HDL_vnl')
```

Map State-Space Parameters in Implementation Model to RAM

The HDL implementation model uses `single` data types and contains large Delay blocks that are inside a feedback loop in the `HDL Algorithm` subsystem. To accommodate the large delays and make the design run at a faster clock rate on the target FPGA, the model uses clock-rate pipelining in conjunction with a large value of **Oversampling factor**.

```
hdlsaveparams('gmStateSpaceHDL_ViennaRectifier_HDL')
```

For more information, see:

- "Clock-Rate Pipelining" (HDL Coder)
- "Oversampling factor" (HDL Coder)

- "Allocate Sufficient Delays for Floating-Point Operations" (HDL Coder)

In the **Generate implementation model** task, the **Map state space parameters to RAMs** setting uses the default value of `Auto`. This setting maps large state-space parameters in the HDL implementation model to RAMs when the number of modes exceed a threshold value of `200`. As the vienna rectifier model uses a large number of modes, the state-space parameters are mapped to RAMs. By mapping to RAMs, you save lookup table resources on the FPGA. To enable the RAM mapping, the "UseRAM" (HDL Coder) parameter is enabled on the masked subsystem blocks that perform the state update and compute the output.

To map the parameters to RAMs irrespective of the threshold, set **Map state space parameters to RAMs** to `on`.

To see the effect of RAM mapping on the vienna rectifier model:

1. Verify the **UseRAM** parameter setting by running the `hdlget_param` function on the `Multiply Input` and `Multiply State` blocks.

```
Multiplysubsys1 = 'gmStateSpaceHDL_ViennaRectifier_HDL/HDL Subsystem/HDL Algorithm/State Update'
Multiplysubsys2 = 'gmStateSpaceHDL_ViennaRectifier_HDL/HDL Subsystem/HDL Algorithm/Output';
UseRAM1 = hdlget_param([Multiplysubsys1 '/Multiply Input'], 'UseRAM')
UseRAM2 = hdlget_param([Multiplysubsys1 '/Multiply State'], 'UseRAM')


UseRAM1 =

    'on'


UseRAM2 =

    'on'
```

2. Enable generation of the resource utilization report.

```
hdlset_param('gmStateSpaceHDL_ViennaRectifier_HDL', 'ResourceReport', 'on')
```

3. Generate HDL code for the implementation model.

```
makehdl('gmStateSpaceHDL_ViennaRectifier_HDL/HDL Subsystem');
```

When you generate code, HDL Coder opens a Code Generation report. The **High-level Resource Report** shows `136` RAMs utilized.

Summary

Multipliers	294
Adders/Subtractors	4960
Registers	29079
Total 1-Bit Registers	278646
RAMs	136
Multiplexers	45795
I/O Bits	516
Static Shift operators	0
Dynamic Shift operators	644

Resource Sharing of State Update and Output Computation Blocks

Before you generate HDL code for the HDL Subsystem, you can optimize the algorithm by using the resource sharing optimization in HDL Coder. Resource sharing is an area optimization that identifies multiple functionally equivalent resources and replaces them with a single, equivalent resource. The data is time-multiplexed over the shared resource to perform the same operations. See "Resource Sharing" (HDL Coder).

In the HDL implementation model, you can share the masked subsystem blocks that perform state updates and compute the output.

To share these subsystems for the vienna rectifier and generate HDL code:

1. Specify a **SharingFactor** of 2 on the Multiply Input and Multiply State subsystems.

```
hdlset_param([Multiplysubsys1 '/Multiply Input'],'SharingFactor', 2)
hdlset_param([Multiplysubsys1 '/Multiply State'],'SharingFactor', 2)
hdlset_param([Multiplysubsys2 '/Multiply Input'],'SharingFactor', 2)
hdlset_param([Multiplysubsys2 '/Multiply State'],'SharingFactor', 2)
```

2. Enable generation of the optimization report

```
hdlset_param('gmStateSpaceHDL_ViennaRectifier_HDL', 'OptimizationReport', 'on')
```

3. Generate HDL code for the HDL Subsystem block in the implementation model.

```
makehdl('gmStateSpaceHDL_ViennaRectifier_HDL/HDL Subsystem');
```

When you generate code, HDL Coder opens a Code Generation report. To see the status of the resource sharing optimization, click the **Streaming and Sharing** section of the report. This sharing group shows the dot products that the optimization shared. When you click the **High-level Resource Report**, you see that the comsumption of adders, multipliers, and registers have decreased.

Subsystem: Multiply Input

SharingFactor: 2

Highlight shared resources and diagnostics

Group Id	Resource Type	I/O Wordlengths	Group Size	Block Name	Color Legend
1			2	dot_product_5	
2			2	dot_product_5	
3			2	dot_product_5	
4			2	dot_product_5	
5			2	dot_product_5	
6			2	dot_product_5	
7			2	dot_product_5	
8			2	dot_product_5	
9			2	dot_product_5	
10			2	dot_product_5	
11			2	dot_product_5	
12			2	dot_product_5	
13			2	dot_product_5	

See Also

Functions
checkhdl | makehdl

More About

- "Get Started with Simscape Hardware-in-the-Loop Workflow" (HDL Coder)
- "Speed and Area Optimizations in HDL Coder" (HDL Coder)
- "Generate HDL Code for Simscape Models" (HDL Coder)

Generate Simulink Real-Time Interface Subsystem for Simscape Two-Level Converter Model

This example shows how to generate a Simulink® Real-Time Interface subsystem for a Simscape™ two-level converter plant model. You can then deploy the interface model on the Speedgoat FPGA IO module. This example uses the Speedgoat IO334-325k module.

Real-Time Simulation

Simulating the plant model on the FPGA provides:

- **Real-time Simulation**: Hardware-in-the-loop provides real-time simulation of your Simscape plant model on the target hardware.
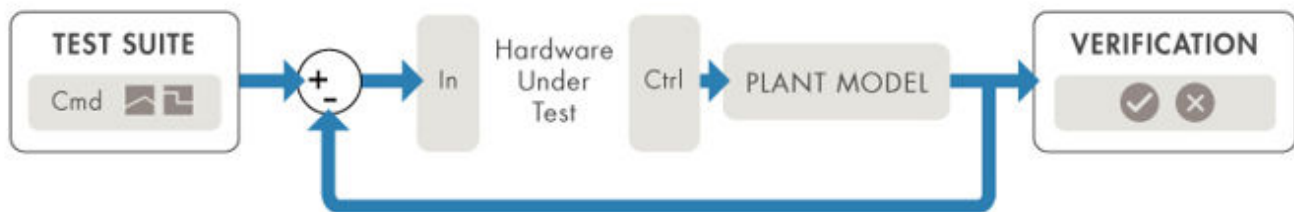
- **Hardware Acceleration**: Accelerated simulation of complex physical systems on hardware while reconfigurable FPGAs provide rapid prototyping.

To use the workflow:

1   Develop the Simscape model and convert it into an implementation model by using the Simscape HDL Workflow Advisor.
2   Generate HDL code and deploy the code to the Speedgoat I/O module by using the HDL Workflow Advisor.

Setup and Configuration

Before deploying your algorithm on the Speedgoat IO module:

1. Install the latest version of Xilinx® Vivado® as listed in "HDL Language Support and Supported Third-Party Tools and Hardware" (HDL Coder).

Then, set the tool path to the installed Xilinx Vivado executable by using the `hdlsetuptoolpath` (HDL Coder) function.

```
hdlsetuptoolpath('ToolName','Xilinx Vivado','ToolPath','C:\Xilinx\Vivado\2019.2\bin\vivado.bat')
```

2. For real-time simulation, set up the development environment and target computer settings. See "Get Started with Simulink Real-Time" (Simulink Real-Time).

3. Install the Speedgoat Library and the Speedgoat HDL Coder Integration packages. See Install Speedgoat HDL Coder Integration Packages.

Two-Level Converter Ideal Model

To open this model, enter:

```
open_system('sschdlexTwoLevelConverterIdealExample')
```

Save this model locally as TwoLevelConverter_HDL.slx to run this workflow.

```
open_system('TwoLevelConverter_HDL')
set_param('TwoLevelConverter_HDL','SimulationCommand','update')
```
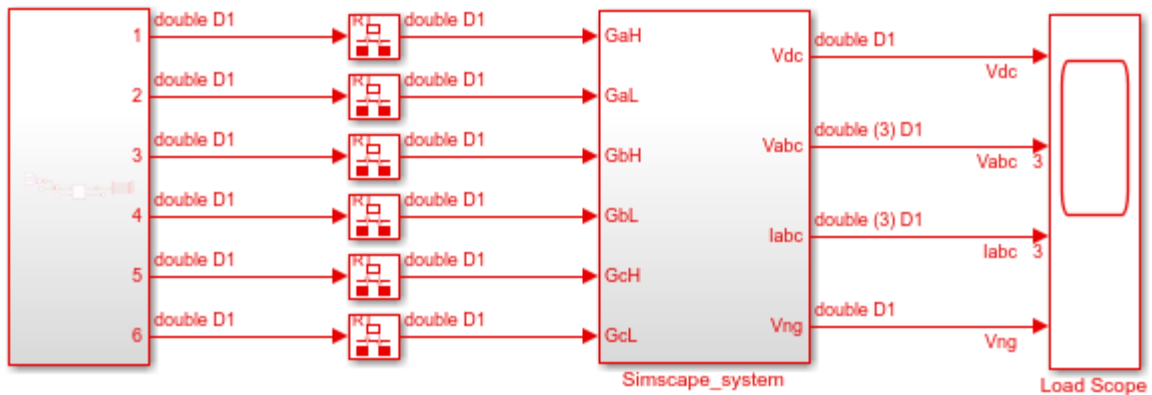

Copyright 2020 The MathWorks, Inc.

```
open_system('TwoLevelConverter_HDL/Simscape_system')
```
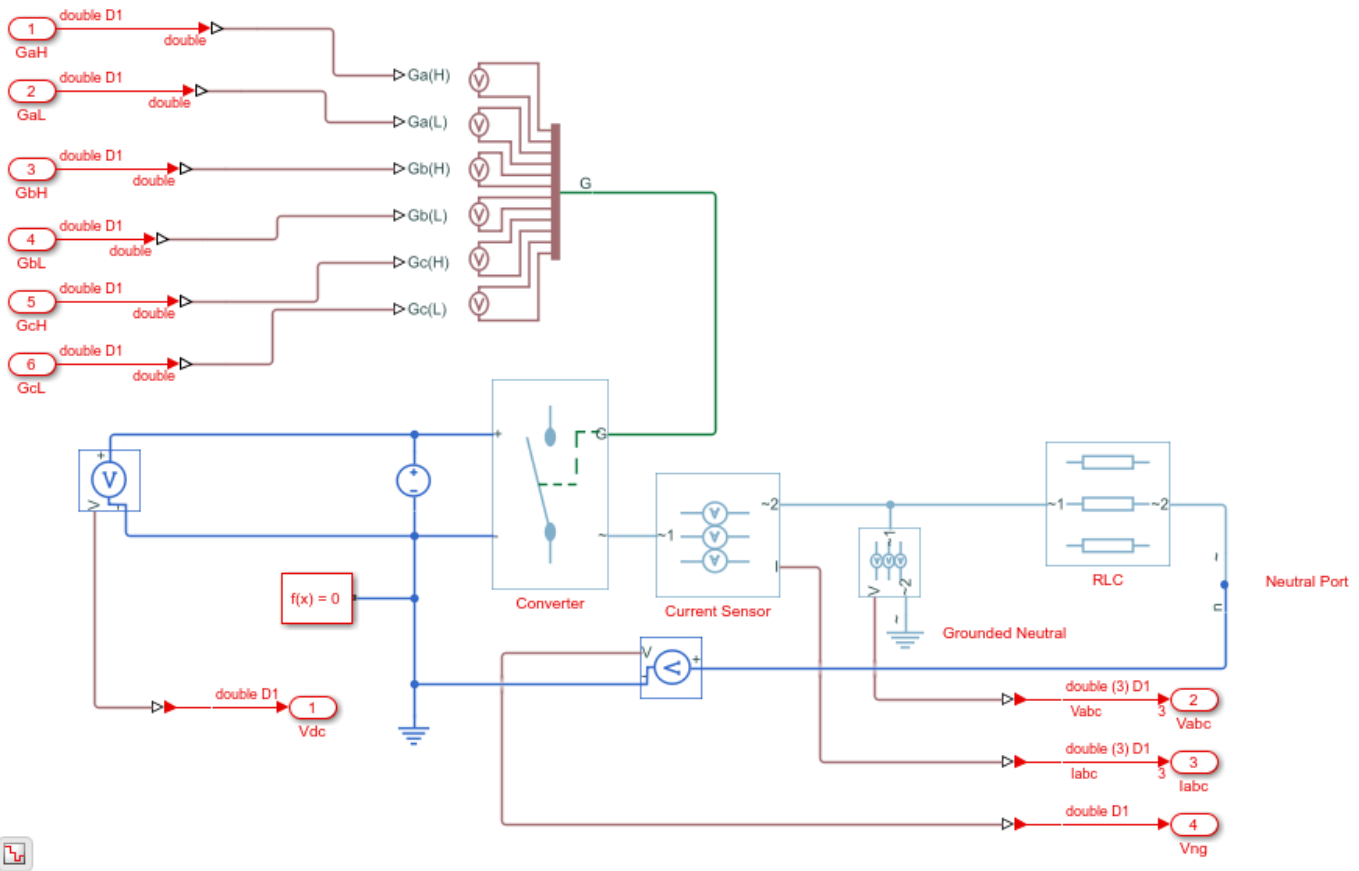
The Simscape subsystem receives six-switch controlling pulses as input. The Simscape subsystem acts as a generator that uses a two-level, carrier-based PWM method to:

1   Sample a reference wave.

2   Compare the sample to a triangular carrier wave.

3   Generate a switch-on pulse if a sample is higher than the carrier signal or a switch-off pulse if a sample is lower than the carrier wave.

Simulate the model.

```
sim('TwoLevelConverter_HDL')
open_system('TwoLevelConverter_HDL/Load Scope')
```
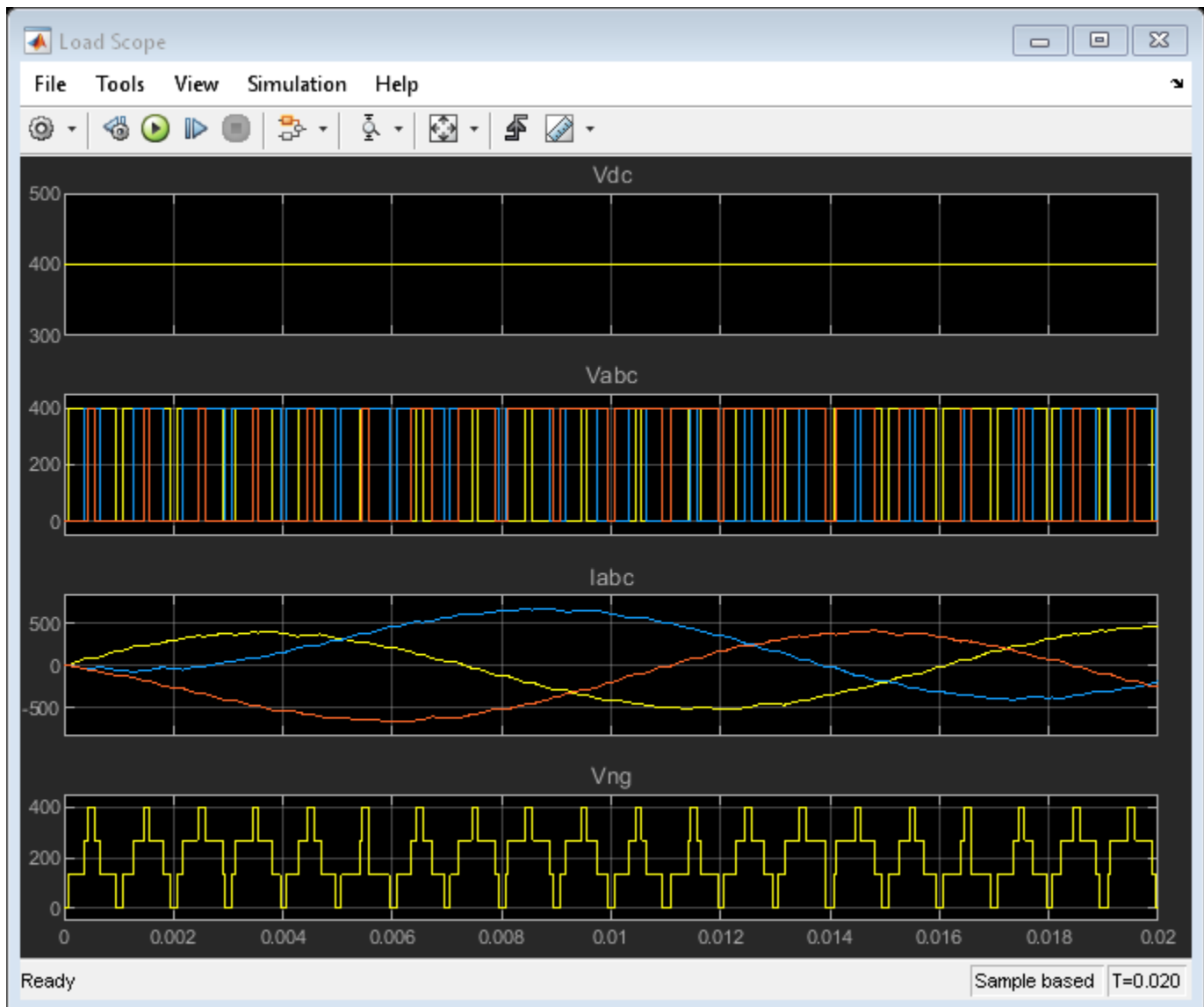
Generate HDL Implementation Model

To generate an implementation model, use the Simscape HDL Workflow Advisor. Run the `sschdladvisor` (HDL Coder) function for your model:

```
sschdladvisor('TwoLevelConverter_HDL')
```

```
### Running Simscape HDL Workflow Advisor for <a href="matlab:(TwoLevelConverter_HDL)">TwoLevelCc
```

To generate the implementation model, in the Simscape HDL Workflow Advisor, keep the default settings for the tasks, and then run the tasks. You see a link to the model in the **Generate implementation model** task. This model has the same name as the original model prefixed with `gmStateSpaceHDL`.

To open the implementation model, enter:

```
load_system('gmStateSpaceHDL_TwoLevelConverter_HDL')
open_system('gmStateSpaceHDL_TwoLevelConverter_HDL/Simscape_system')
set_param('gmStateSpaceHDL_TwoLevelConverter_HDL','SimulationCommand','update')
```
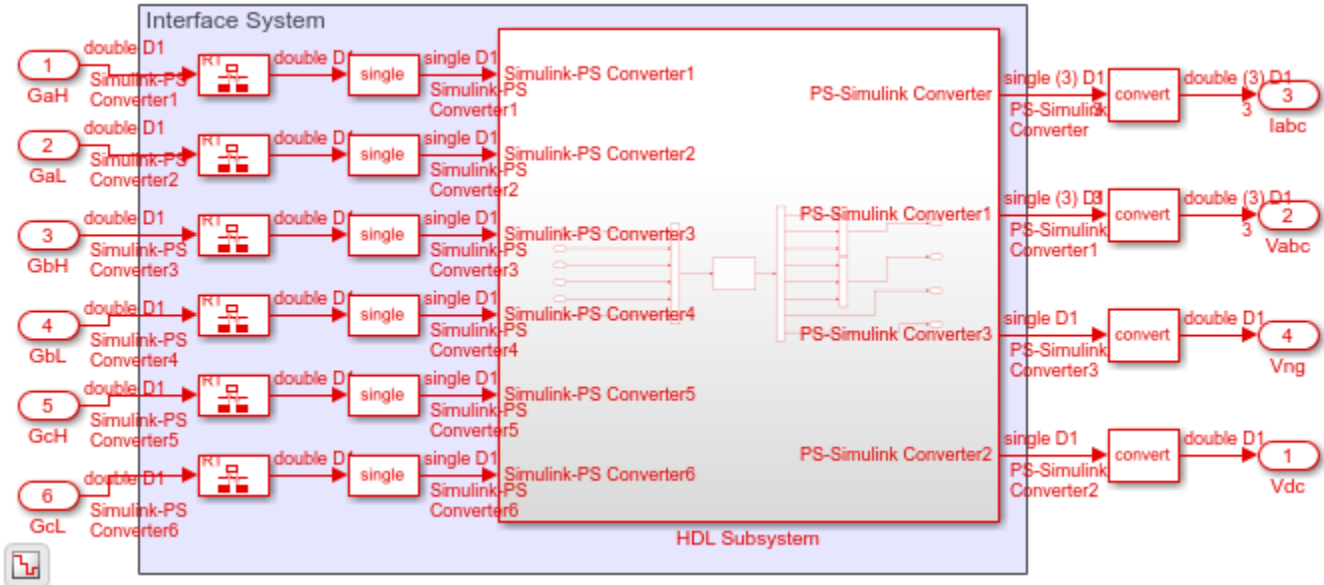

The implementation model replaces the Simscape subsystem with the HDL algorithm that performs the state-space computations. When you navigate inside this subsystem, you see several delays, adders, and Matrix Multiply blocks that model the state-space equations. From and Goto blocks inside this subsystem provide the same input as that of the original model to the `HDL Subsystem`.

```
open_system('gmStateSpaceHDL_TwoLevelConverter_HDL/Simscape_system/HDL Subsystem/HDL Algorithm')
```


HDL Workflow Advisor

The HDL Workflow Advisor guides you through HDL code generation and the FPGA design process. Use the Advisor to:

- Check the model for HDL code generation compatibility and fix incompatible settings.
- Generate HDL code, test bench, and scripts to build and run the code and test bench.

- Perform synthesis and timing analysis.
- Deploy the generated code on SoCs, FPGAs, and Speedgoat I/O modules.

To open the HDL Workflow Advisor for a subsystem inside the model, use the `hdladvisor` (HDL Coder) function.

```
load_system('sschdlexTwoLevelConverterIgbtExample')
hdladvisor('sschdlexTwoLevelConverterIgbtExample/Simscape_system')
```

The left pane contains folders that represent a group of related tasks. Expanding the folders and selecting a task displays information about that task in the right pane. The right pane contains simple controls for running the task to advanced parameters and option settings that control code and test bench generation. To learn more about each task, right-click that task, and select **What's This?**. See "Getting Started with the HDL Workflow Advisor" (HDL Coder).

Deploy Two Level Ideal Converter Model to Speedgoat IO334-325K Module

1. Open the HDL Workflow Advisor for the implementation model.

```
hdladvisor('gmStateSpaceHDL_TwoLevelConverter_HDL/Simscape_system/HDL Subsystem')
```

2. In **Set Target Device and Synthesis Tool** task, specify **Target workflow** as `Simulink Real-Time FPGA I/O` and **Target platform** as `Speedgoat IO334-325K`

3. Run the **Set Target Reference Design** task, select a value of `x4` for the parameter `PCIe lanes`, and select **Run This Task**.

4. In **Set Target Interface** task, map the input and output `single` data type ports to `PCIe Interface` and select **Run This Task**.

Target platform interface table

Port Name	Port Type	Data Type	Target Platform Interfaces	Interface Mapping	Interface Options
Simulink-PS Converte...	Inport	single	PCIe Interface ▼	x"100"	Options...
Simulink-PS Converte...	Inport	single	PCIe Interface ▼	x"104"	Options...
Simulink-PS Converte...	Inport	single	PCIe Interface ▼	x"108"	Options...
Simulink-PS Converte...	Inport	single	PCIe Interface ▼	x"10C"	Options...
Simulink-PS Converte...	Inport	single	PCIe Interface ▼	x"110"	Options...
Simulink-PS Converte...	Inport	single	PCIe Interface ▼	x"114"	Options...
PS-Simulink Converter	Outport	single (3)	PCIe Interface ▼	x"120"	
PS-Simulink Converte...	Outport	single (3)	PCIe Interface ▼	x"140"	
PS-Simulink Converte...	Outport	single	PCIe Interface ▼	x"118"	
PS-Simulink Converte...	Outport	single	PCIe Interface ▼	x"11C"	

5. Right-click **Generate RTL Code and IP Core** task and select **Run to Selected Task**. As the model uses vector data types, the **Generate RTL Code and IP Core** fails because the `ScalarizePorts` property must be set to `dutlevel`. Click the link to change this setting and rerun the task.

6. Run the workflow to the **Generate Simulink Real-Time interface** task. In **Create Project** task, you can open the Vivado project and see the implemented design. After the **Generate Simulink Real-Time interface** task passes, click the link to open the Simulink Real-Time Interface Model.
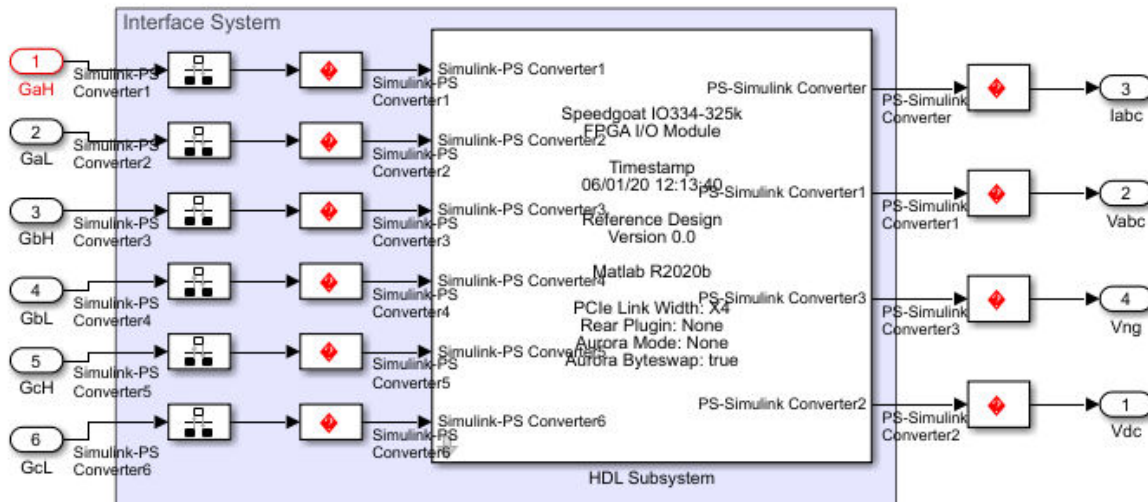
Export HDL Workflow to Script

For rapid prototyping, you can export the HDL Workflow Advisor settings to a script. The script is a MATLAB® file that you can run from the command line. You can then modify and run the script, or import the settings into the HDL Workflow Advisor User Interface.

To export an HDL Workflow script, after you run the tasks in the Advisor, select **File > Export to Script**. For this example, when you export to script, this file shows the settings you saved.

```
edit('hdlworkflow_slrt.m')
```

To import an HDL Workflow script, in the HDL Workflow Advisor, select **File > Import from Script**. Select the script file and click **Open**. The HDL Workflow Advisor updates the tasks with the imported script settings.

For an example that shows how to run the real-time application by deploying the FPGA bitstream, see "Hardware-in-the-Loop Implementation of Simscape Model on Speedgoat FPGA I/O Modules" (HDL Coder).

See Also

Functions
checkhdl | makehdl

More About

- "Run HDL Workflow with a Script" (HDL Coder)
- "IP Core Generation Workflow for Speedgoat Simulink-Programmable I/O Modules" (HDL Coder)
- "FPGA Programming and Configuration on Speedgoat Simulink-Programmable I/O Modules" (HDL Coder)
- "Simscape HDL Workflow Advisor Tasks" (HDL Coder)
- Speedgoat I/O Examples

Deploy Simscape Buck Converter Model to Speedgoat IO Module Using HDL Workflow Script

This example shows how to deploy a Simscape™ buck converter model to a Speedgoat IO334 Simulink®-programmable I/O module and then run the model in real-time at a sample step size as small as 1 microsecond. The example uses a DCDC converter topology to show how to prepare your power electronic converter model for hardware in the loop (HIL) simulation on a Speedgoat real-time target machine.

To use this workflow:

1 Convert your model into an HDL-compatible implementation model by using the Simscape HDL Workflow Advisor

2 Generate HDL code and FPGA bitstream for the IO334 module by using the HDL Workflow Advisor.

3 Deploy the real-time model to the Speedgoat real-time taret machine by using Simulink Real-Time.

The model runs at a sample time of 1us till HDL code generation and then runs at 50us on the CPU in real time. To generate HDL code and FPGA bitstream, the example shows how to run the HDL workflow script from the command line. For an example that shows how you can use the Workflow Advisor User Interface to run this workflow, see "Hardware-in-the-Loop Implementation of Simscape Model on Speedgoat FPGA I/O Modules" (HDL Coder).

Setup and Configuration

Before deploying your algorithm on the Speedgoat IO module:

1. Install the latest version of Xilinx® Vivado® as listed in "HDL Language Support and Supported Third-Party Tools and Hardware" (HDL Coder).

Then, set the tool path to the installed Xilinx Vivado executable by using the `hdlsetuptoolpath` (HDL Coder) function.

```
hdlsetuptoolpath('ToolName','Xilinx Vivado','ToolPath','C:\Xilinx\Vivado\2019.2\bin\vivado.bat')
```

2. For real-time simulation, set up the development environment and target computer settings. See "Get Started with Simulink Real-Time" (Simulink Real-Time).

3. Install the Speedgoat Library and the Speedgoat HDL Coder Integration packages. See Install Speedgoat HDL Coder Integration Packages.

Buck Converter Model

To see the buck converter model, run this command:

```
open_system('sschdlexBuckConverterExample')
```

This model is modified for real-time deployment and saved as `sschdlex_IO334_BuckConverter`. The model has been partitioned into parts that run on the FPGA and parts that run on CPU. Parts inside the green `FPGA` subsystem run on the FPGA. Parts outside this subsystem run on the CPU in real time.

```
open_system('sschdlex_IO334_BuckConverter')
set_param('sschdlex_IO334_BuckConverter', 'SimulationCommand', 'Update')
```
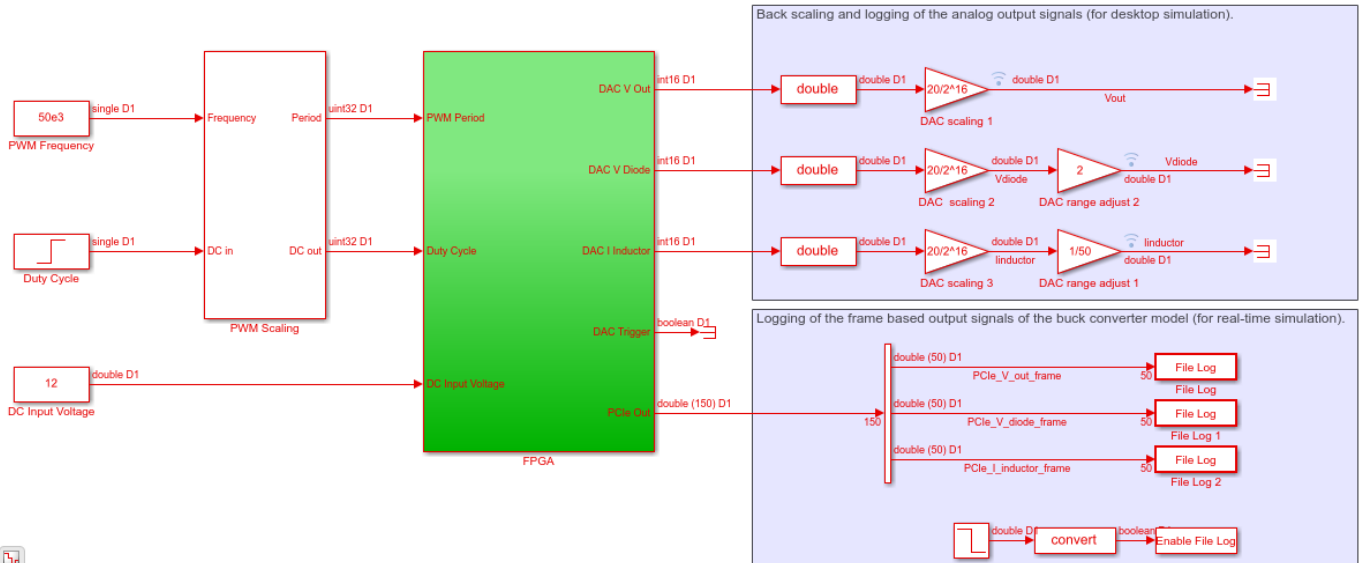

You generate VHDL code for blocks that are inside the green FPGA subsystem that contains the PWM generator and buck converter. The code is then deployed to the FPGA on board the IO334 module. The outputs of the subsytem are mapped to DAC interfaces. The output signals from the Buck Converter subsystem are scaled within a `10V` range and converted to use `uint16` data types. `50` samples are packed together to one frame to log the output signals on the CPU.

```
open_system('sschdlex_IO334_BuckConverter/FPGA')
```
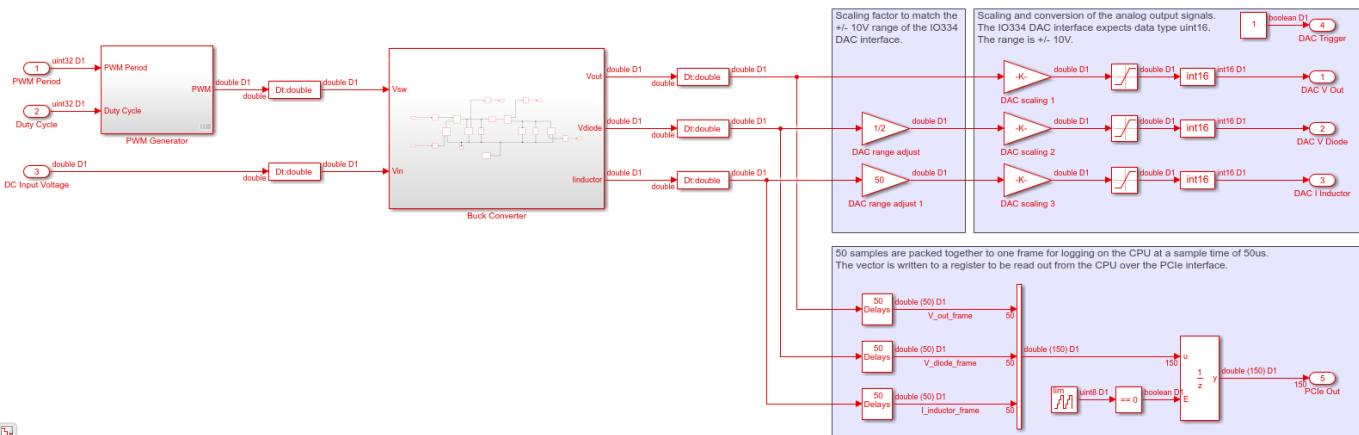

To see the buck converter model, double-click the Buck Converter subsystem. The buck converter is a power converter model that steps down the input voltage at the output. The voltage at the output is stepped down by the duty cycle, D. The output voltage, `Vout`, is calculated as `Vin/D`

```
open_system('sschdlex_IO334_BuckConverter/FPGA/Buck Converter')
```
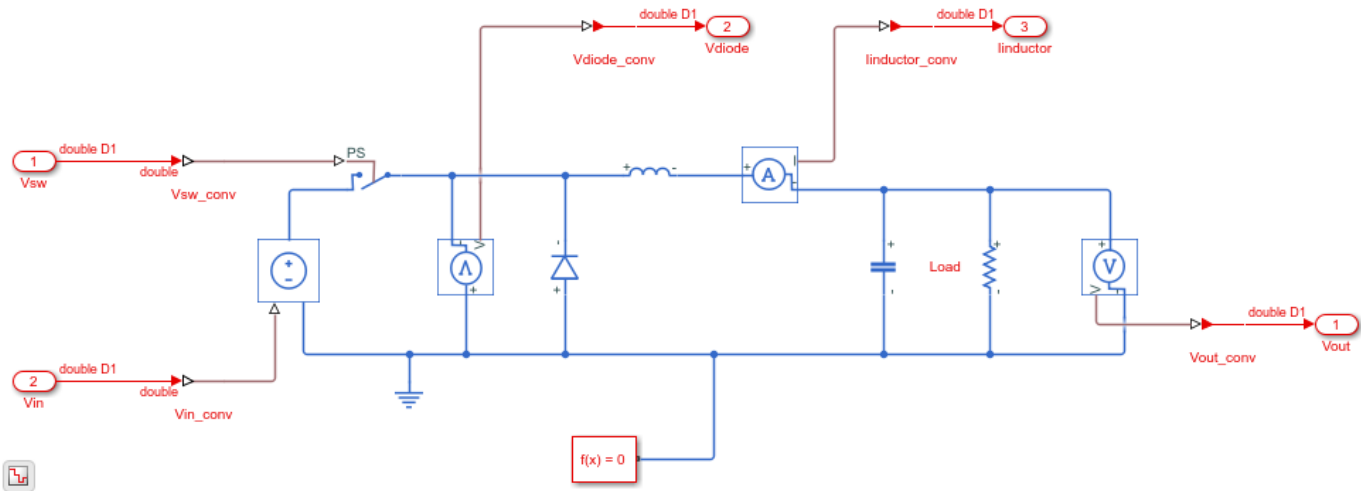
Run Desktop Simulation of Simscape model

The Simulation input is a duty cycle step wave from `0.2` to `0.8`. The input signals that include the DC input voltage, PWM frequency, and duty cycle are generated on the top level of the model. The sample time for the Simscape model is set to `1us`. Signal logging is enabled on the top level of the model.

```
sim('sschdlex_IO334_BuckConverter')

% Display the buck converter output signals in SDI
Simulink.sdi.clearAllSubPlots
Simulink.sdi.setSubPlotLayout(3,1);

allIDs2 = Simulink.sdi.getAllRunIDs;
runID2 = allIDs2(end);
run2 = Simulink.sdi.getRun(runID2);
run2.name = 'Simscape Desktop Simulation';

run2.getAllSignals;
plotOnSubPlot(run2.getSignalsByName('Vout'),1,1,true);
plotOnSubPlot(run2.getSignalsByName('Iinductor'),2,1,true);
plotOnSubPlot(run2.getSignalsByName('Vdiode'),3,1,true);

Simulink.sdi.view;
```
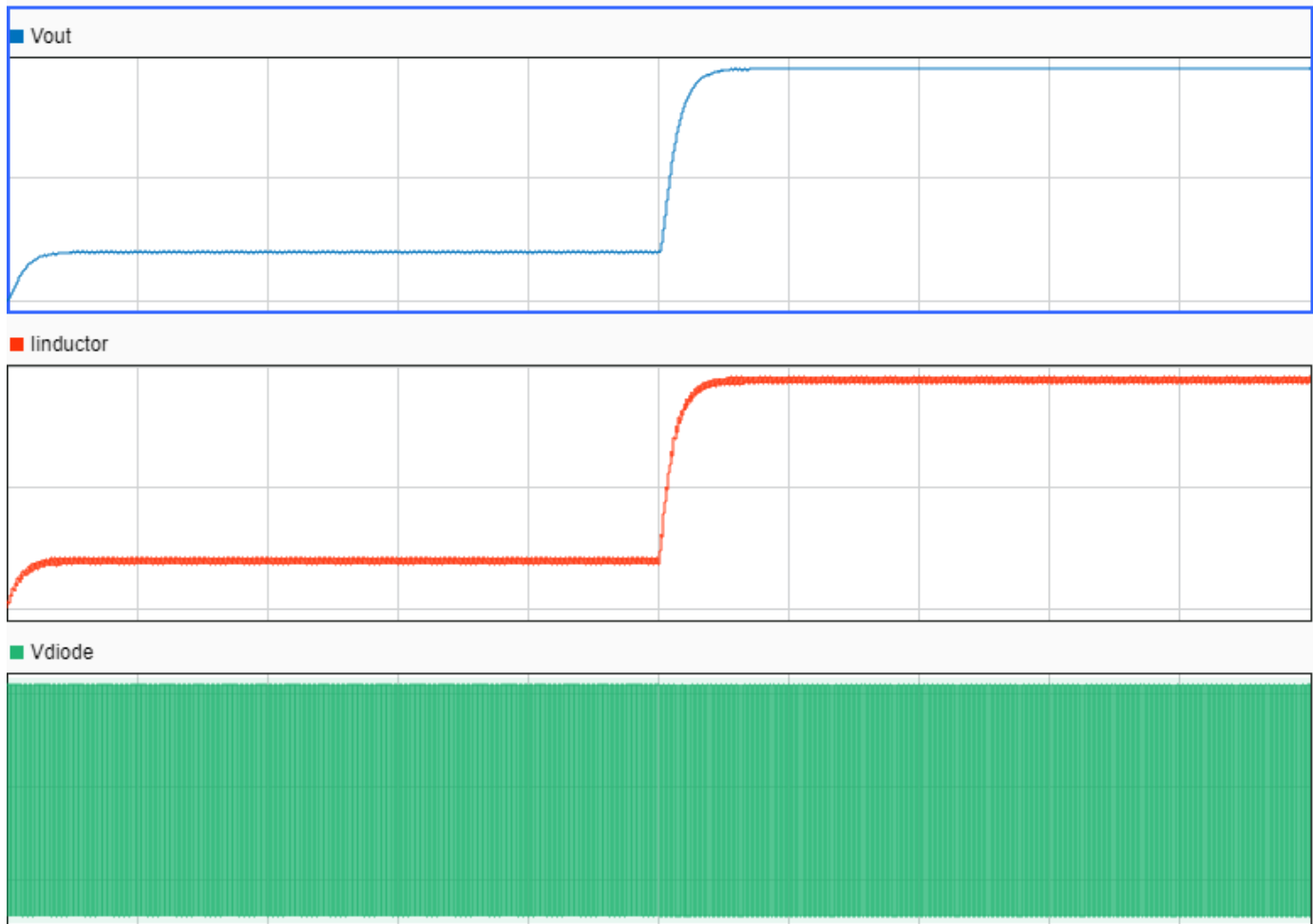
Generate HDL Implementation Model

For HDL code generation compatibility, you run the Simscape HDL Workflow Advisor to generate an HDL implementation model.

The Simscape solver is set to run for two iterations at each sample step. The Simscape HDL Workflow Advisor uses the solver settings in the next step for deterministic real-time behaviour.

```
set_param('sschdlex_IO334_BuckConverter/FPGA/Buck Converter/Solver Configuration','DoFixedCost',
set_param('sschdlex_IO334_BuckConverter/FPGA/Buck Converter/Solver Configuration','MaxNonlinIter
```

To open the Advisor, run the `sschdladvisor` (HDL Coder) function for your model:

```
sschdladvisor('sschdlex_IO334_BuckConverter')
```

```
### Running Simscape HDL Workflow Advisor for <a href="matlab:(sschdlex_IO334_BuckConverter)">ssc
```

To generate the implementation model, in the Simscape HDL Workflow Advisor, keep the default settings for the tasks, and then run the tasks. Run the tasks in the Advisor by clicking the **Run all** button. You see a link to the model in the **Generate implementation model** task. This model has the same name as your original model with the prefix `gmStateSpaceHDL_`.

Prepare Implementation Model for HDL Code Generation

To open the implementation model, click the link in the **Generate implementation model** task.

```
open_system('gmStateSpaceHDL_sschdlex_IO334_BuckConverte');
set_param('gmStateSpaceHDL_sschdlex_IO334_BuckConverte', 'SimulationCommand', 'Update')
```
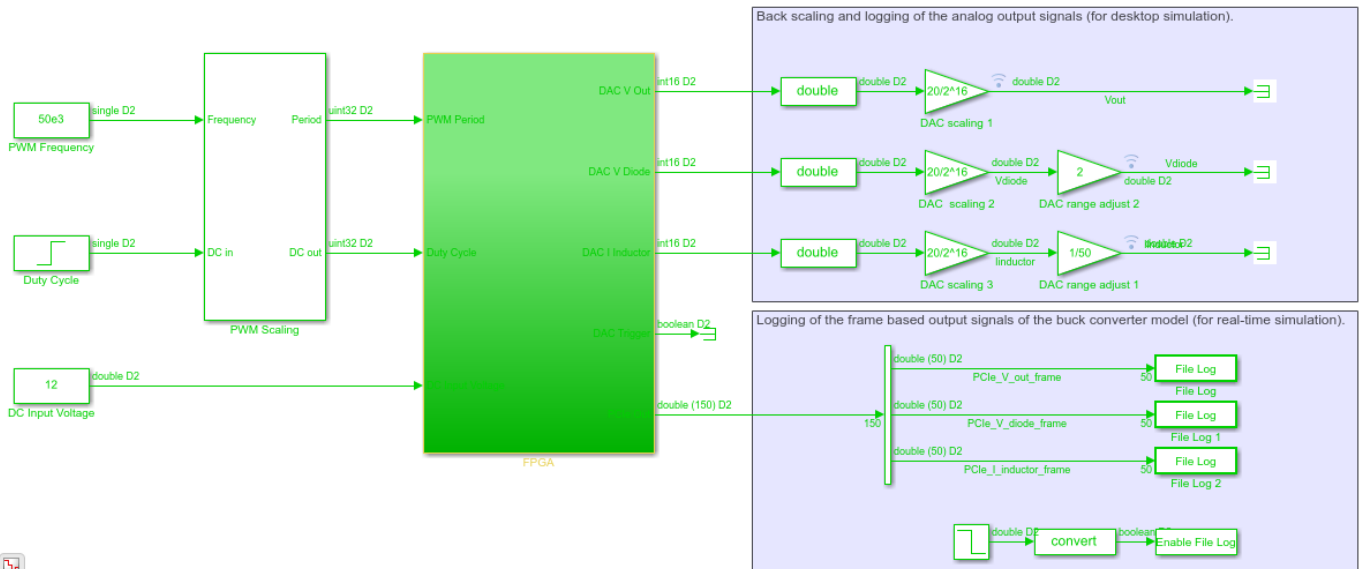

The model contains a switched linear Simulink replacement of the original buck converter model. You see that the Simscape model was replaced.

```
open_system('gmStateSpaceHDL_sschdlex_IO334_BuckConverte/FPGA/Buck Converter');
```
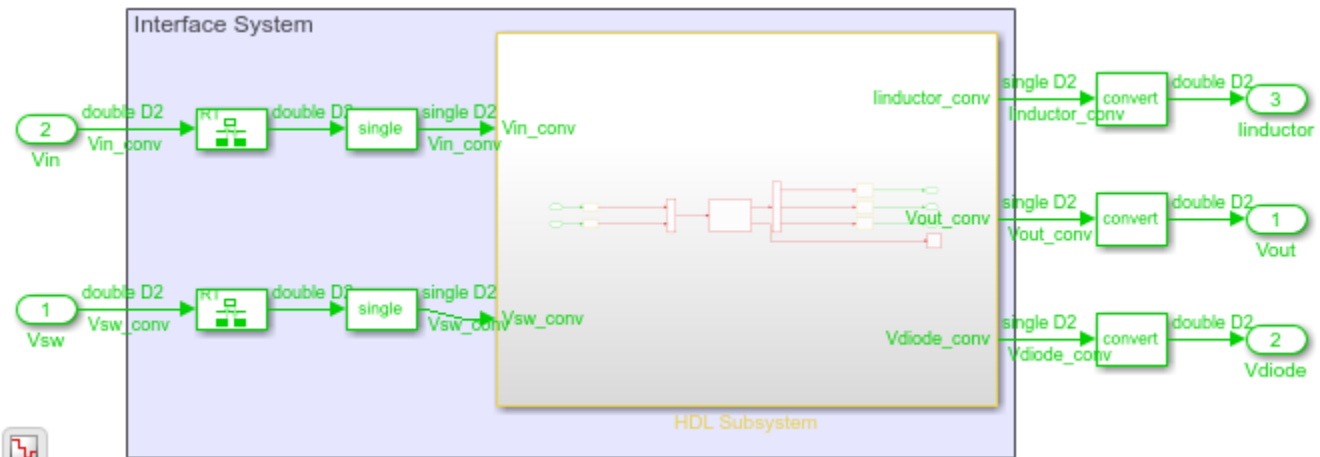

The implementation model replaces the Simscape subsystem with the HDL-compatible algorithm that performs the state-space computations. When you navigate inside this subsystem, you see several delays, adders, and Matrix Multiply blocks that model the state-space equations. From and Goto blocks inside this subsystem provide the same input as that of the original model to the HDL Subsystem.

```
open_system('gmStateSpaceHDL_sschdlex_IO334_BuckConverte/FPGA/Buck Converter/HDL Subsystem/HDL A
```
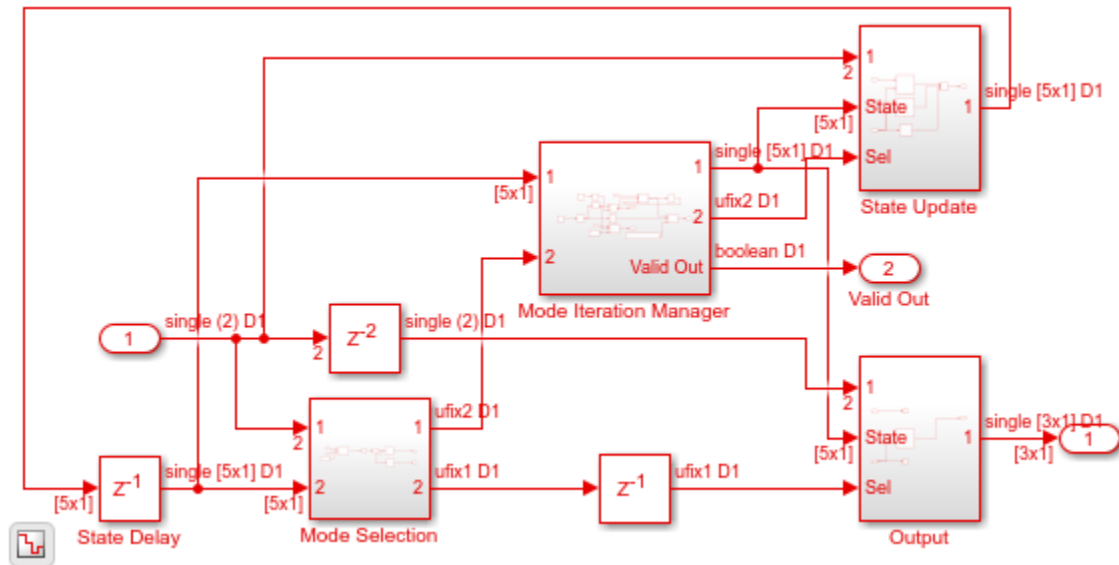

The data type of the buck converter output signals is set to single precision floating point for HDL code generation.

```
set_param('gmStateSpaceHDL_sschdlex_IO334_BuckConverte/FPGA/Signal Specification','OutDataTypeStr
set_param('gmStateSpaceHDL_sschdlex_IO334_BuckConverte/FPGA/Signal Specification1','OutDataTypeS
set_param('gmStateSpaceHDL_sschdlex_IO334_BuckConverte/FPGA/Signal Specification2','OutDataTypeS
set_param('gmStateSpaceHDL_sschdlex_IO334_BuckConverte/FPGA/Signal Specification3','OutDataTypeS
set_param('gmStateSpaceHDL_sschdlex_IO334_BuckConverte/FPGA/Signal Specification4','OutDataTypeS
```

Run Desktop Simulation of HDL Implementation Model and Validate HDL Algorithm

You can simulate the switched linear state-space model of the buck converter in Simulink and display the signals in Simulation Data Inspector. The comparison of the runs show that the numeric results match.

Simulate the HDL implementation model.

```
sim('gmStateSpaceHDL_sschdlex_IO334_BuckConverte')
%
% Display output signals of buck converter Simscape model in Simulation
% Data Inspector.
Simulink.sdi.clearAllSubPlots
Simulink.sdi.setSubPlotLayout(3,1);

allIDs2 = Simulink.sdi.getAllRunIDs;
runID2 = allIDs2(end);
run2 = Simulink.sdi.getRun(runID2);
run2.name = 'HDL Desktop Simulation';

run2.getAllSignals;
plotOnSubPlot(run2.getSignalsByName('Vout'),1,1,true);
plotOnSubPlot(run2.getSignalsByName('Iinductor'),2,1,true);
plotOnSubPlot(run2.getSignalsByName('Vdiode'),3,1,true);

Simulink.sdi.view;
```
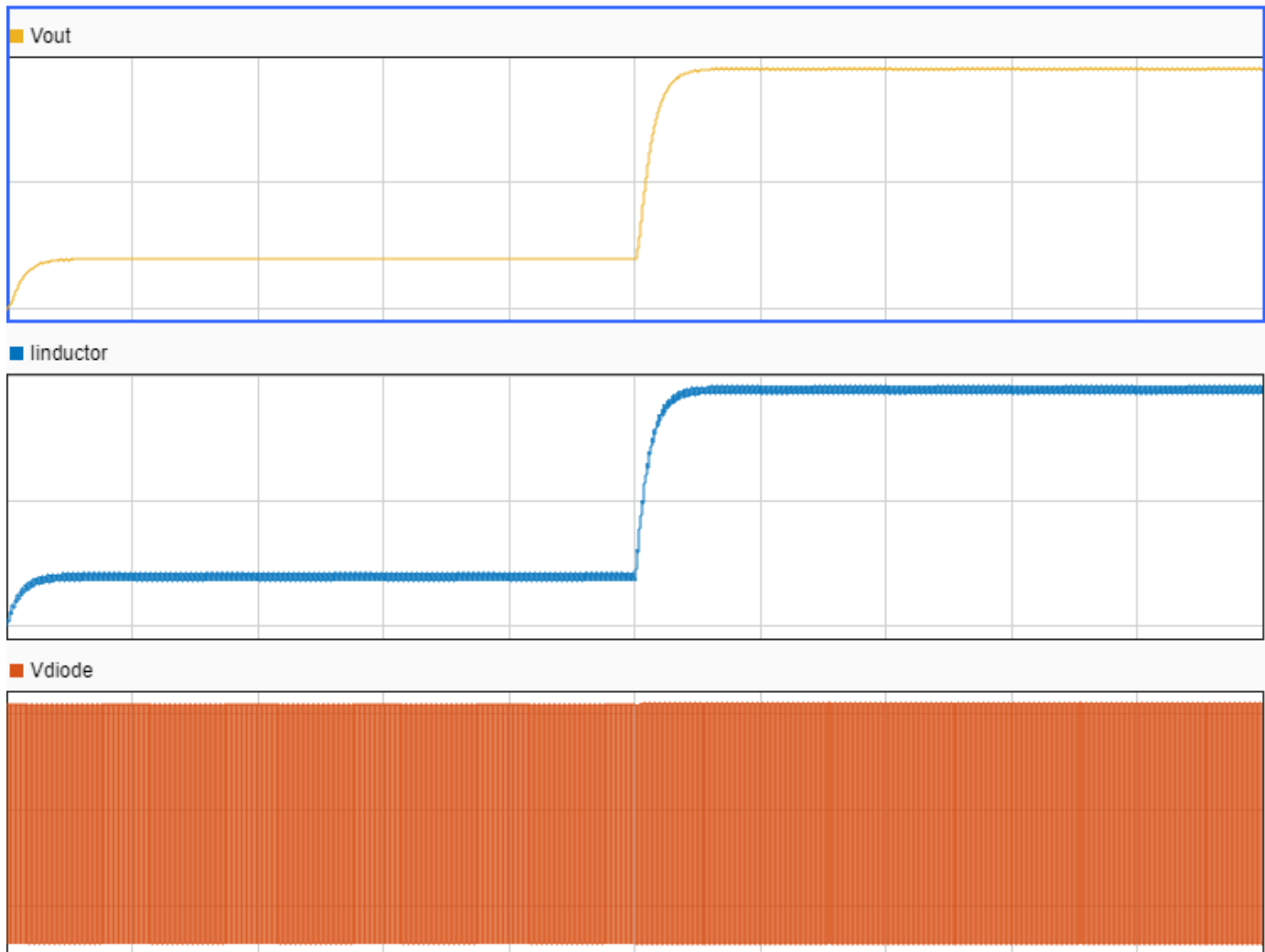
To verify that the HDL implementation model matches the original Simscape model, generate a state-space validation model. In the **Generate implementation model** task, select the **Generate validation logic for the implementation model** check box and then run this task. Simulating the model does not display assertions, which indicates that the numeric results match. See "Validate HDL Implementation Model to Simscape Algorithm" (HDL Coder).

HDL Workflow Advisor

The HDL Workflow Advisor guides you through HDL code generation and the FPGA design process. Use the Advisor to:

- Check the model for HDL code generation compatibility and fix incompatible settings.
- Generate HDL code, test bench, and scripts to build and run the code and test bench.
- Perform synthesis, timing analysis, and deploy the generated code on SoCs, FPGAs, and Speedgoat I/O modules.

You run the Advisor for the FPGA subsystem in your model. To open the HDL Workflow Advisor for the subsystem inside the model, use the `hdladvisor` (HDL Coder) function. For example:

```
hdladvisor('gmStateSpaceHDL_sschdlex_IO334_BuckConverte/FPGA')
```

To learn about the tasks in the Advisor, right-click that task, and select **What's This?**. See "Getting Started with the HDL Workflow Advisor" (HDL Coder).

Run Workflow Script to Generate Simulink Real-Time Interface Model

For rapid prototyping, export the HDL Workflow Advisor settings to a script. The script is a MATLAB® file that you run from the command line. You can modify and run the script, or import the settings into the HDL Workflow Advisor User Interface. See "Run HDL Workflow with a Script" (HDL Coder).

This example shows how to run the HDL Workflow script. To generate a Simulink Real-Time Interface model, open and run this MATLAB script.

```
edit('hdlworkflow_buck_IO334')
```

```matlab
%% ------------------------------------------------------------------------
% This script contains the model, target settings, interface mapping, and
% the Workflow Configuration settings for generating HDL code for the HDL
% implementation model generated for the buck converter model, and for
% deploying the code to the FPGA on board the Speedgoat IO334-325K module.
%% ------------------------------------------------------------------------

%% Set Parameters for HDL Code Generation

% Model HDL parameters
% ------------------------------------------------------------------------
hdlset_param('gmStateSpaceHDL_sschdlex_IO334_BuckConverte', 'FloatingPointTargetConfiguration', 
hdlset_param('gmStateSpaceHDL_sschdlex_IO334_BuckConverte', 'HDLSubsystem', 'gmStateSpaceHDL_ssch
hdlset_param('gmStateSpaceHDL_sschdlex_IO334_BuckConverte', 'Oversampling', 100);
hdlset_param('gmStateSpaceHDL_sschdlex_IO334_BuckConverte', 'ScalarizePorts', 'DUTLevel');
hdlset_param('gmStateSpaceHDL_sschdlex_IO334_BuckConverte', 'TargetFrequency', 200);
hdlset_param('gmStateSpaceHDL_sschdlex_IO334_BuckConverte', 'Workflow', 'Simulink Real-Time FPGA
hdlset_param('gmStateSpaceHDL_sschdlex_IO334_BuckConverte', 'TargetPlatform', 'Speedgoat IO334-32
hdlset_param('gmStateSpaceHDL_sschdlex_IO334_BuckConverte', 'AdaptivePipelining', 'off');

%% Map DUT Ports to Target Interfaces

% Input port mapping
% ------------------------------------------------------------------------
% All input signals to the "FPGA" subsystem are mapped to the PCIe
% interface. I.e. these signals will be transferred from the CPU of the
% real-time target machine to the IO334 FPGA over the PCIe bus.

hdlset_param('gmStateSpaceHDL_sschdlex_IO334_BuckConverte/FPGA/PWM Period', 'IOInterface', 'PCIe
hdlset_param('gmStateSpaceHDL_sschdlex_IO334_BuckConverte/FPGA/Duty Cycle', 'IOInterface', 'PCIe
hdlset_param('gmStateSpaceHDL_sschdlex_IO334_BuckConverte/FPGA/DC Input Voltage', 'IOInterface',

% Output port mapping
% ------------------------------------------------------------------------
% The scaled output signals of the converter are mapped to the analog
% output interface of the IO334.
```

```matlab
hdlset_param('gmStateSpaceHDL_sschdlex_IO334_BuckConverte/FPGA/DAC V Out', 'IOInterface', 'IO334
hdlset_param('gmStateSpaceHDL_sschdlex_IO334_BuckConverte/FPGA/DAC V Out', 'IOInterfaceMapping',

hdlset_param('gmStateSpaceHDL_sschdlex_IO334_BuckConverte/FPGA/DAC V Diode', 'IOInterface', 'IO33
hdlset_param('gmStateSpaceHDL_sschdlex_IO334_BuckConverte/FPGA/DAC V Diode', 'IOInterfaceMapping

hdlset_param('gmStateSpaceHDL_sschdlex_IO334_BuckConverte/FPGA/DAC I Inductor', 'IOInterface', ']
hdlset_param('gmStateSpaceHDL_sschdlex_IO334_BuckConverte/FPGA/DAC I Inductor', 'IOInterfaceMappi

hdlset_param('gmStateSpaceHDL_sschdlex_IO334_BuckConverte/FPGA/DAC Trigger', 'IOInterface', 'IO33
hdlset_param('gmStateSpaceHDL_sschdlex_IO334_BuckConverte/FPGA/DAC Trigger', 'IOInterfaceMapping

% The signal frames are mapped to PCIe registers. They are read by the CPU for data logging.
hdlset_param('gmStateSpaceHDL_sschdlex_IO334_BuckConverte/FPGA/PCIe Out', 'IOInterface', 'PCIe I

%% Workflow Configuration Settings

% HDL Workflow Advisor is opened with the following settings.
hWC = hdlcoder.WorkflowConfig('SynthesisTool','Xilinx Vivado','TargetWorkflow','Simulink Real-Tin

% Specify the top level project directory.
hWC.ProjectFolder = 'hdl_prj';
hWC.ReferenceDesignToolVersion = '2019.2';

% Set Workflow tasks to run.
hWC.RunTaskGenerateRTLCodeAndIPCore = true;
hWC.RunTaskCreateProject = true;
hWC.RunTaskBuildFPGABitstream = true;
hWC.RunTaskGenerateSimulinkRealTimeInterface = true;

%% Run the Workflow
hdlcoder.runWorkflow('gmStateSpaceHDL_sschdlex_IO334_BuckConverte/FPGA', hWC);
```

Prepare Simulink Real-Time Interface Model for Real-Time Simulation

Running the workflow script generates RTL code and IP core, creates a Vivado project, builds the FPGA bitstream, and then generates the Simulink Real-Time Interface model.
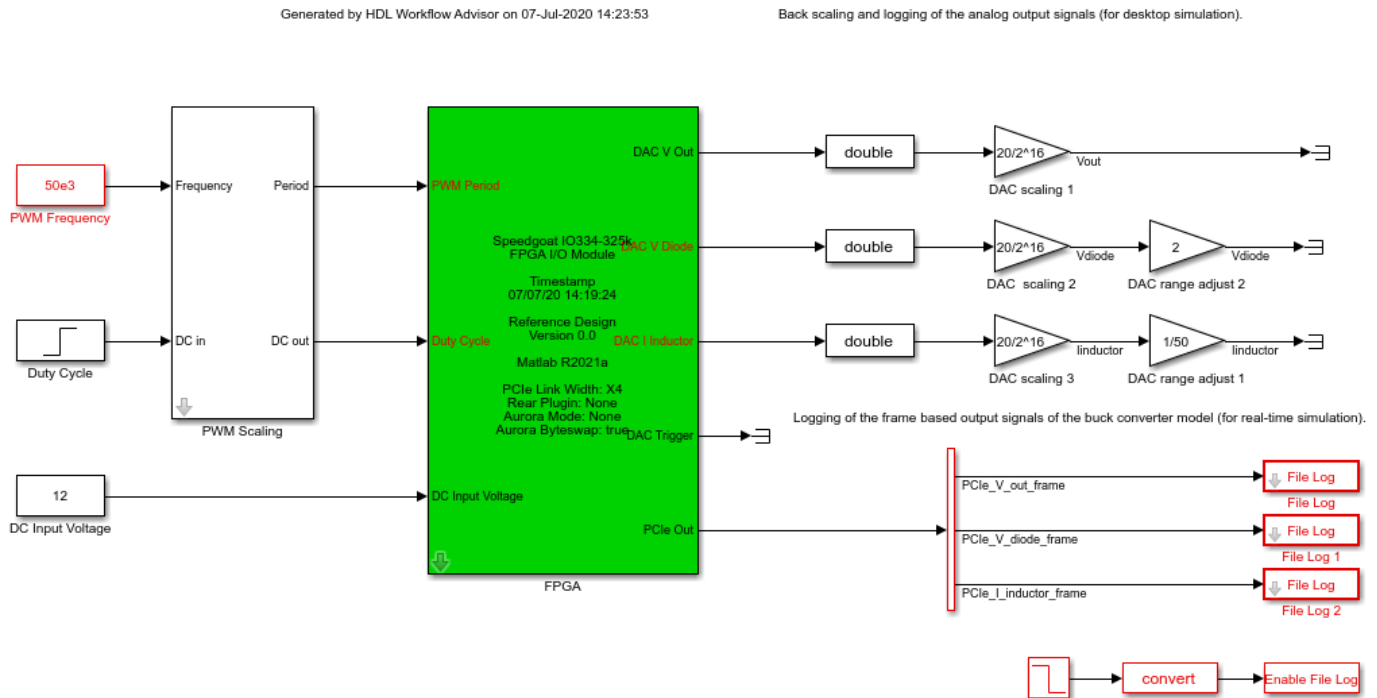
Before deploying the model to the Speedgoat real-time target machine:

1. Set the sample time for all blocks running on the CPU of the Speedgoat real-time target machine to 50us (including driver blocks for the FPGA).

```
generated_model = gcs;
Ts = 50e-6;
set_param([generated_model,'/FPGA'],'ts','Ts');
```

2. Set the Simulation Data Inspector setting **Input processing** to `Columns as channels (frame based)` for the signals `PCIe_V_out_frame`, `PCIe_V_diode_frame` and `PCIe_I_inductor_frame`. Inside the mask of the `File Log` blocks, right-click the logging symbol and navigate to the Instrumentation Properties dialog box. To make the logging signal appear, you might have to update the model.

```
set_param(generated_model, 'SimulationCommand', 'update');
```
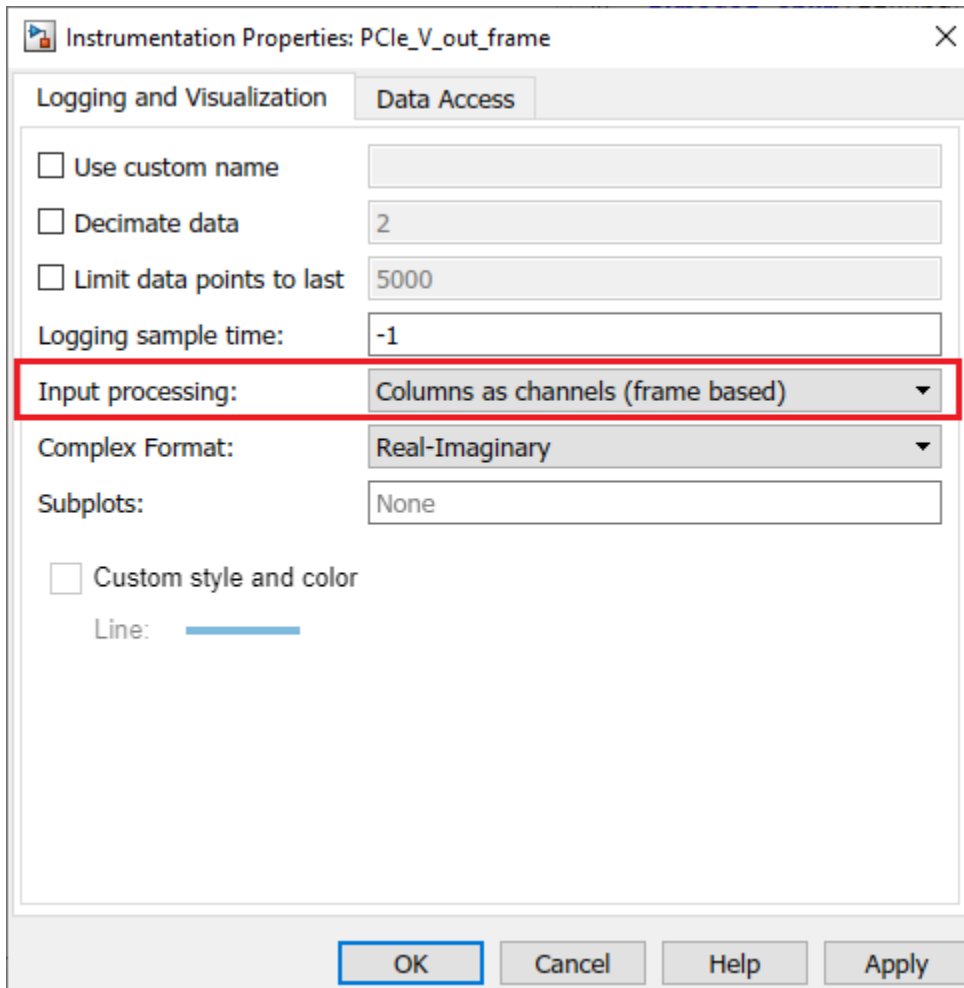
Alternatively, you can set signal logging to frame based mode by using these commands.

```
Simulink.sdi.setSignalInputProcessingMode([generated_model,'/File Log/Demux'], 1,'frame');
Simulink.sdi.setSignalInputProcessingMode([generated_model,'/File Log 1/Demux'], 1,'frame');
Simulink.sdi.setSignalInputProcessingMode([generated_model,'/File Log 2/Demux'], 1,'frame');
```

Connect to Target Machine and Run Real-Time Simulation

The model can now be deployed to the Speedgoat real-time target machine. The buck converter model is automatically loaded to the FPGA on the IO334.

Connect to the Speedgoat real-time target machine.

```
tg = slrealtime;
tg.connect;
```

Build and download the model to the target machine.

```
rtwbuild(generated_model);
tg.load(generated_model);
```

Start the model execution.

```
tg.start;
pause(10);
```

The file logging blocks store the signals on the SSD of the target-machine. The data is automatically uploaded to the host computer once the model is stopped. The data is visualized in Simulation Data Inspector. You can verify that the results of the real-time simulation matches the original Simscape model.

```
Simulink.sdi.setSubPlotLayout(3,1);

allIDs = Simulink.sdi.getAllRunIDs;
runID = allIDs(end);
run = Simulink.sdi.getRun(runID);
run.name = 'Real Time Simulation on IO334';

run.getAllSignals
plotOnSubPlot(run.getSignalsByName('PCIe_V_out_frame'),1,1,true);
plotOnSubPlot(run.getSignalsByName('PCIe_I_inductor_frame'),2,1,true);
plotOnSubPlot(run.getSignalsByName('PCIe_V_diode_frame'),3,1,true);

Simulink.sdi.view
```
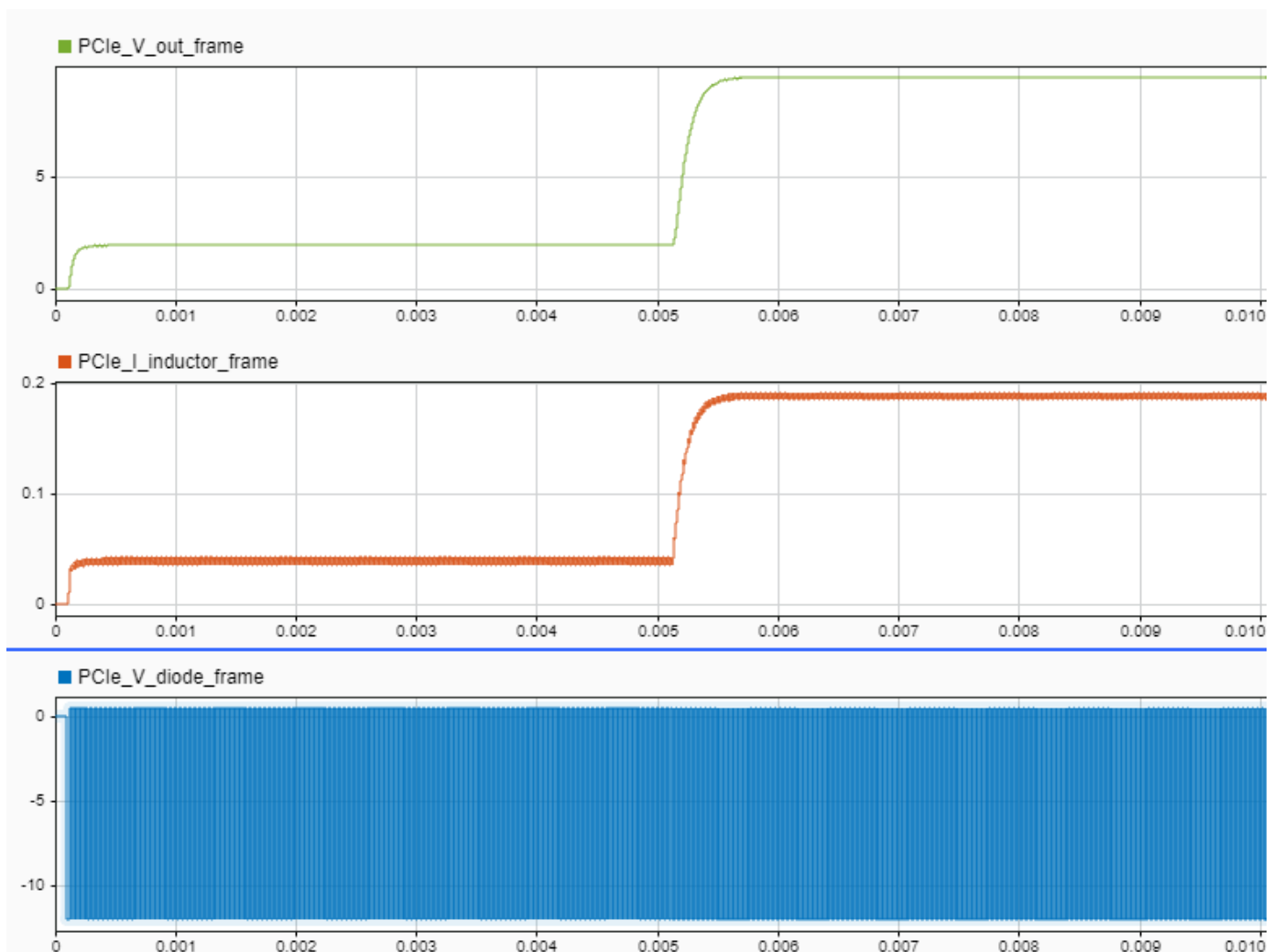
Alternatively, you can measure the signals at the analog output of the IO334. This figure shows a plot of the signals in MATLAB.
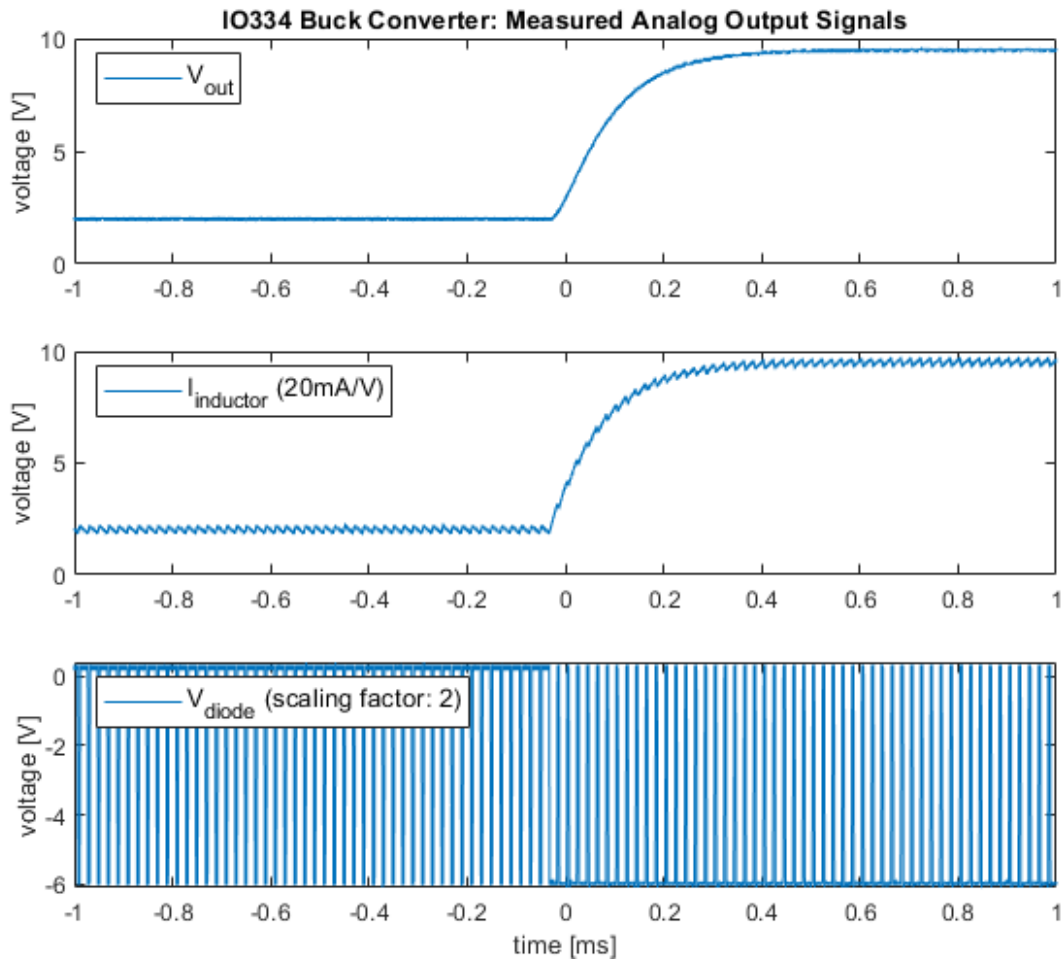
See Also

Functions
checkhdl | makehdl

More About

- "Run HDL Workflow with a Script" (HDL Coder)
- "IP Core Generation Workflow for Speedgoat Simulink-Programmable I/O Modules" (HDL Coder)
- "FPGA Programming and Configuration on Speedgoat Simulink-Programmable I/O Modules" (HDL Coder)

- "Simscape HDL Workflow Advisor Tasks" (HDL Coder)
- Speedgoat I/O Examples

Partition Simscape Models Containing a Large Network into Multiple Smaller Networks

This example shows how to partition a solar power inverter model that contains a single, large Simscape™ network into multiple networks. After you partition the network, you can run the Simscape HDL Workflow Advisor to generate the HDL implementation model. To learn how you run the Advisor for the model, see "Generate HDL Code for Simscape Models with Multiple Networks" (HDL Coder).

Why Partition a Simscape Network

When your Simscape model contains many switching elements, the state-space representation can contain a large number of modes. The Simscape HDL Workflow Advisor simulates the Simscape model to calculate the number of modes that are relevant. Certain Simscape models can have a large number of modes that are relevant. The generated HDL implementation model for such a large design can consume a significantly large number of resources, and the generated HDL implementation md may even fail to synthesize on the target FPGA device. To reduce the number of modes, you can partition the Simscape network in your model into multiple networks, and then run the Simscape HDL Workflow Advisor.

Solar Power Inverter Model with Single Network

To open the solar power inverter example model, run:

```
open_system('sschdlexSolarInverterSingleNetworkExample')
```

For this example, the model is saved as Solar_Power_Inverter_Single_Network_HDL. This model is the same as sschdlexSolarInverterSingleNetworkExample but has the subsystems rearranged and the logic for the solar panel placed inside a Solar_Panel subsystem.

```
open_system('Solar_Power_Inverter_Single_Network_HDL')
set_param('Solar_Power_Inverter_Single_Network_HDL', 'SimulationCommand', 'Update')
```
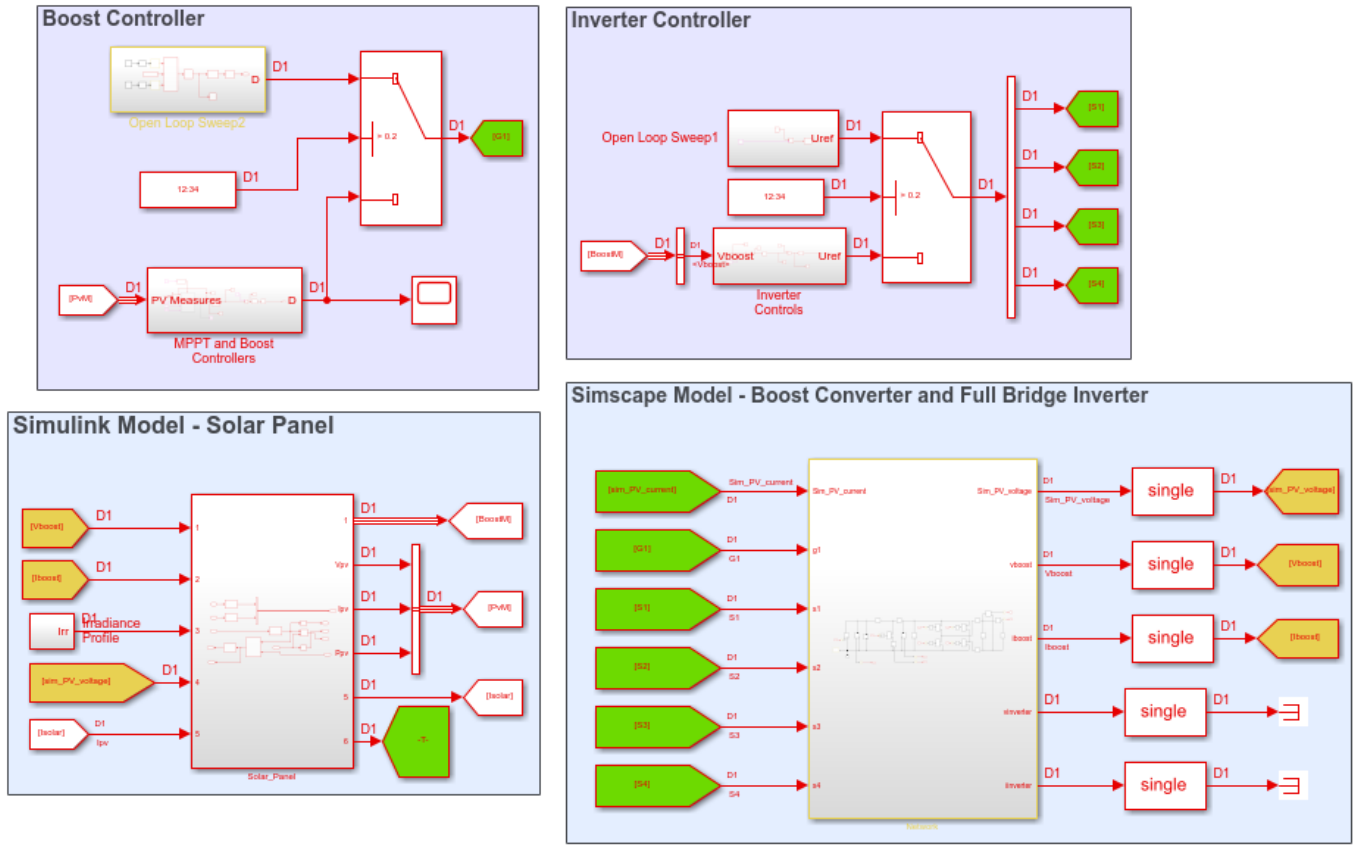
Copyright 2019 The MathWorks, Inc

The model consists of four parts: solar panel, boost controller, inverter controller, and a boost converter and full bridge inverter. The solar panel is modeled in Simulink® by using lookup tables. The boost controller and inverter controller provide the control signals for the boost converter and the full bridge inverter which is an H-bridge.

To see the boost converter and inverter, open the Network subsystem.

```
open_system('Solar_Power_Inverter_Single_Network_HDL/Network')
```
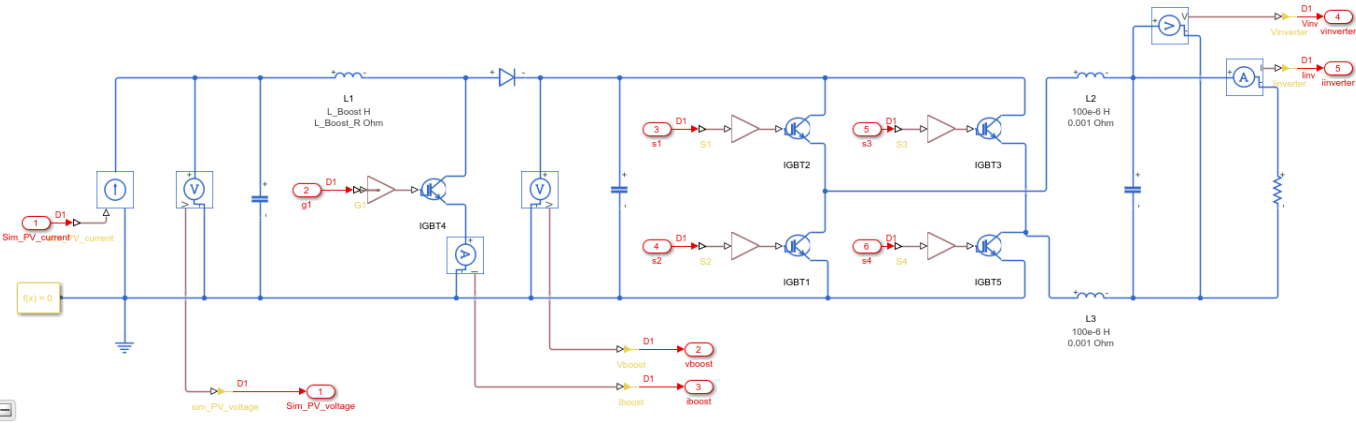
Run Simscape HDL Workflow Advisor

1. To open the Simscape HDL Workflow Advisor for the model, enter:

```
sschdladvisor('Solar_Power_Inverter_Single_Network_HDL')
```

```
### Running Simscape HDL Workflow Advisor for <a href="matlab:(Solar_Power_Inverter_Single_Networ
```

2. Run the workflow to the **Discretize Equations** task. You see that the state-space representation uses around 173 modes, which is a large number of modes.

Summary of the state-space representation:

Details related to the Simscape network Solar_Power_Inverter_Single_Network_HDL/Network/Solver Configuration

Parameter	Parameter size
A	18 x 18 x 173
B	18 x 6 x 173
F0	18 x 1 x 173
C	5 x 18 x 173
D	5 x 6 x 173
Y0	5 x 1 x 173

Such a large number of modes can consume a significantly large number of hardware resources, and may even cause the DUT subsystem in the HDL implementation model to fail to synthesize on the target FPGA device.

Generate HDL Implementation Model and View Resource Consumption

To see the resource consumption:

1. Run the **Generate implementation model** task. Click the link to open the HDL implementation model.

The model contains a HDL Subsystem block that models the state-space equations for the Simscape network. Save the model as Solar_Power_Inverter_Single_Network_StateSpace.slx.

```
open_system('Solar_Power_Inverter_Single_Network_StateSpace')
set_param('Solar_Power_Inverter_Single_Network_StateSpace', 'SimulationCommand', 'Update')
```
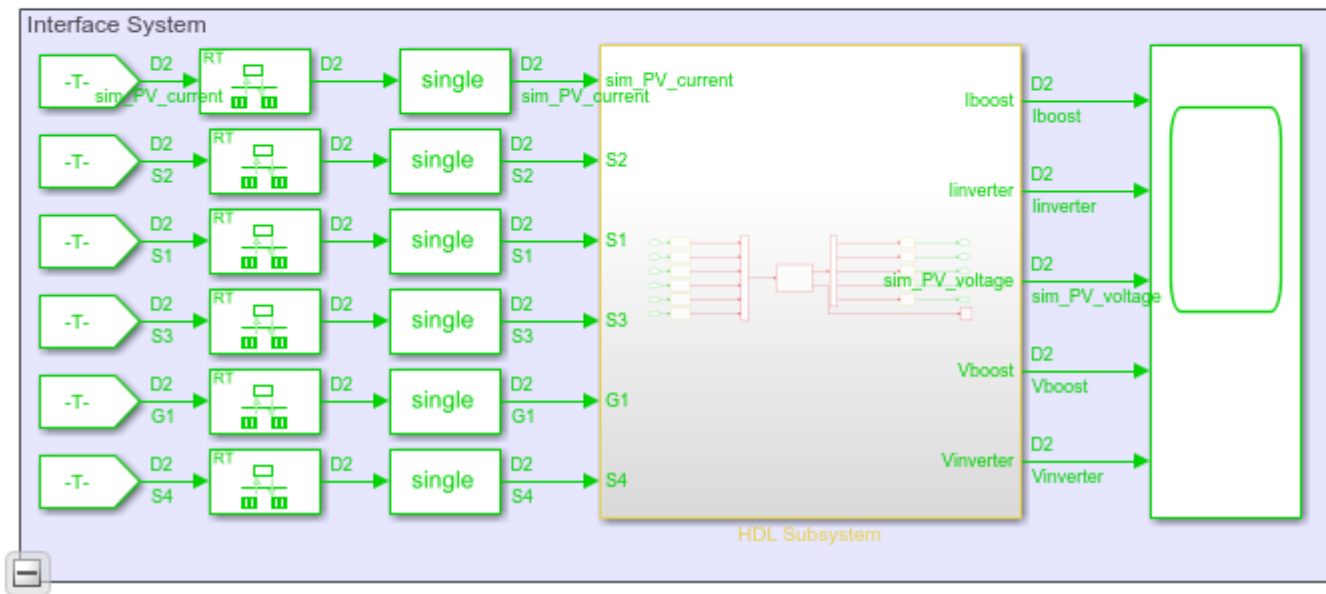
2. Enable generation of the resource utilization report.

```
hdlset_param('Solar_Power_Inverter_Single_Network_StateSpace', 'ResourceReport', 'on')
```

3. Run the `makehdl` function to generate code for the HDL Subsystem block.

```
makehdl('Solar_Power_Inverter_Single_Network_StateSpace/HDL Subsystem')
```

If HDL Coder™ generates an error that it is unable to allocate delays, increase the **Oversampling factor**. Start by increasing the **Oversampling factor** to 100, and then generate HDL code. If HDL Coder is still unable to allocate delays, then further increase the **Oversampling factor**.

```
hdlset_param('Solar_Power_Inverter_Single_Network_StateSpace', 'Oversampling', 100)
```

4. As you generate HDL code, open the Code Generation Report. The resource utilization report indicates a large amount of multipliers, adders, and registers that might be consumed on the target FPGA device.

Summary

Multipliers	173
Adders/Subtractors	2959
Registers	16218
Total 1-Bit Registers	161776
RAMs	0
Multiplexers	25511
I/O Bits	356
Static Shift operators	0
Dynamic Shift operators	352

Partition Solar Inverter Network into Multiple Simscape Networks

To reduce the number of modes, you can partition the Simscape network inside the Network subsystem into two Simscape networks. To partition the network into multiple networks:

1. Identify the boundary for partitioning the network into multiple networks. An energy storage element such as a capacitor or an inductor makes a good candidate for partitioning the network. To produce a Simscape model that contains multiple networks and effectively reduces the number of modes in the state-space representation, choose a boundary that produces identical or near identical partitions. That is, the number of switching elements on either side of the boundary are identical or nearly identical.

For the solar power inverter, you can choose the DC link capacitor between the H-bridge inverter and the boost converter as the boundary for partitioning the network.
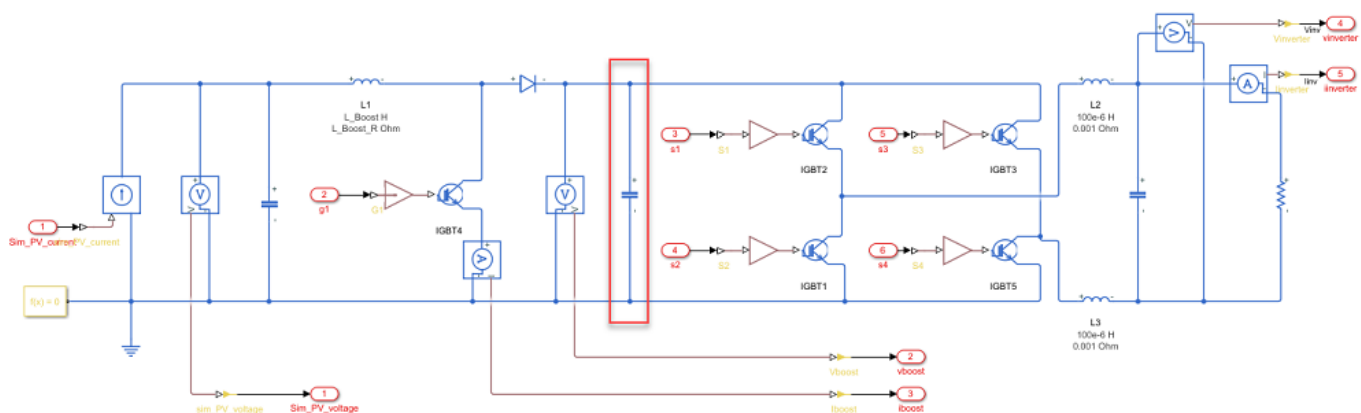
2. After you partition the network, prepare the modified Simscape model for compatibility with the Simscape HDL Workflow Advisor. Place each partitioned network inside a subsystem and use a Solver Configuration block for each network.

The Simscape HDL Workflow Advisor uses the Solver Configuration block to identify each unique network in your Simscape model.

3. For the simulation to converge when using multiple networks, in the network containing the boost converter, add a snubber resistance and a controlled current source in parallel to the capacitor for the current output to the inverter network.
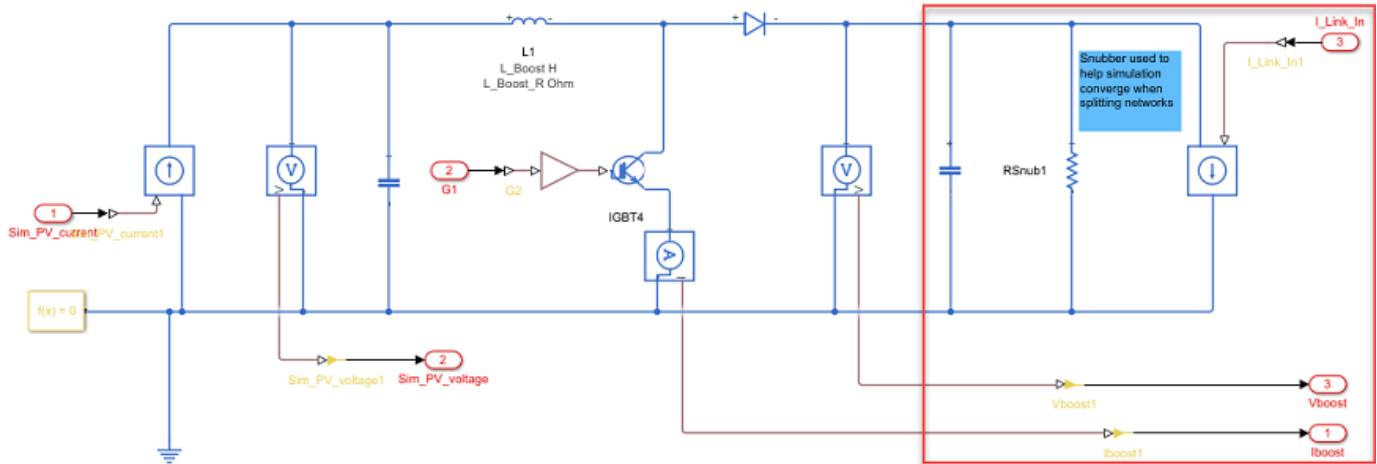
4. In the inverter network, add a controlled voltage source to the voltage input to the network.
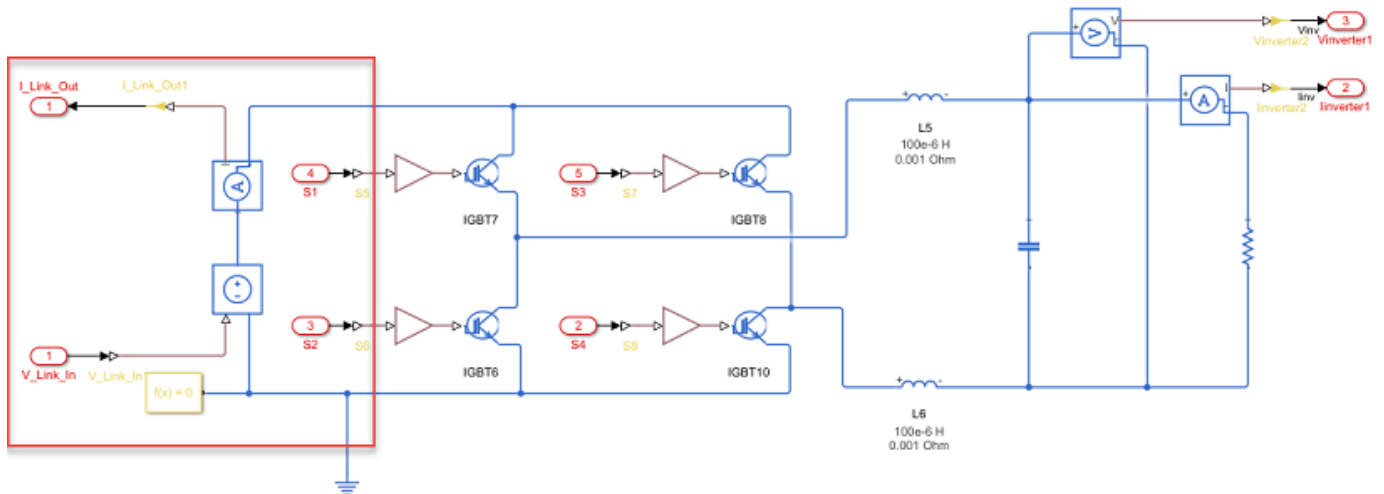
5. To break the algebraic loops in the system, add Delay blocks between the signal lines that connect the output of one subsystem to the input of the other subsystem. For higher accuracy, add Data Type Conversion blocks to provide double data types as inputs to the networks.
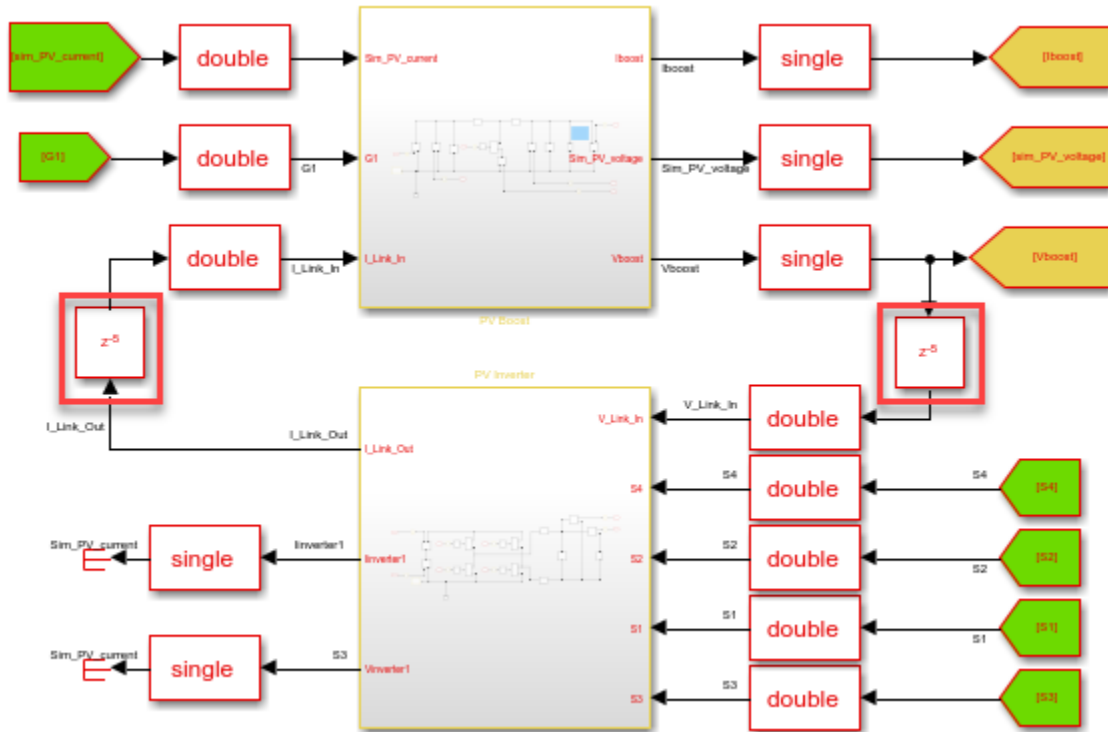
Solar Power Inverter Model with Multiple Networks

The single network model is now partitioned into multiple networks. To open the model containing multiple networks, enter:

```
open_system('sschdlexSolarInverterPartitionedNetworkExample')
```

To learn how you run the Simscape HDL Workflow Advisor and generate HDL code for this model, see "Generate HDL Code for Simscape Models with Multiple Networks" (HDL Coder).

See Also

Functions
checkhdl | makehdl | sschdladvisor

More About

- "Generate HDL Code for Simscape Models" (HDL Coder)
- "Simscape HDL Workflow Advisor Tasks" (HDL Coder)
- "Simscape HDL Workflow Advisor Tips and Guidelines" (HDL Coder)
- "Validate HDL Implementation Model to Simscape Algorithm" (HDL Coder)

Generate HDL Code for Simscape Models with Multiple Networks

This example shows how you can run the Simscape HDL Workflow Advisor to generate the HDL implementation model for a Simscape™ model that contains multiple networks. You can also generate a validation logic that numerically compares each Simscape network with the corresponding state-space implementation in the HDL implementation model. The Simscape model in this example is a solar power inverter partitioned into two networks. To learn how this network is partitioned, see "Partition Simscape Models Containing a Large Network into Multiple Smaller Networks" (HDL Coder).

Why Use a Simscape Model with Multiple Networks

When your Simscape model contains many switching elements, the state-space representation can contain a large number of modes. The generated HDL implementation model for such a large design can consume a significantly large number of resources, and may even fail to synthesize on the target FPGA device. To reduce the number of modes, you can partition the Simscape network in your model into multiple networks, and then run the Simscape HDL Workflow Advisor.

Solar Power Inverter Model with Multiple Networks

To open the model that contains multiple networks, run:

```
open_system('sschdlexSolarInverterPartitionedNetworkExample')
```

For this example, the model is saved as Solar_Power_Inverter_Multiple_Network_HDL. This model is the same as sschdlexSolarInverterPartitionedNetworkExample but has the subsystems rearranged and the logic for the solar panel placed inside a Solar_Panel subsystem.

```
open_system('Solar_Power_Inverter_Multiple_Network_HDL')
set_param('Solar_Power_Inverter_Multiple_Network_HDL', 'SimulationCommand', 'Update')
```
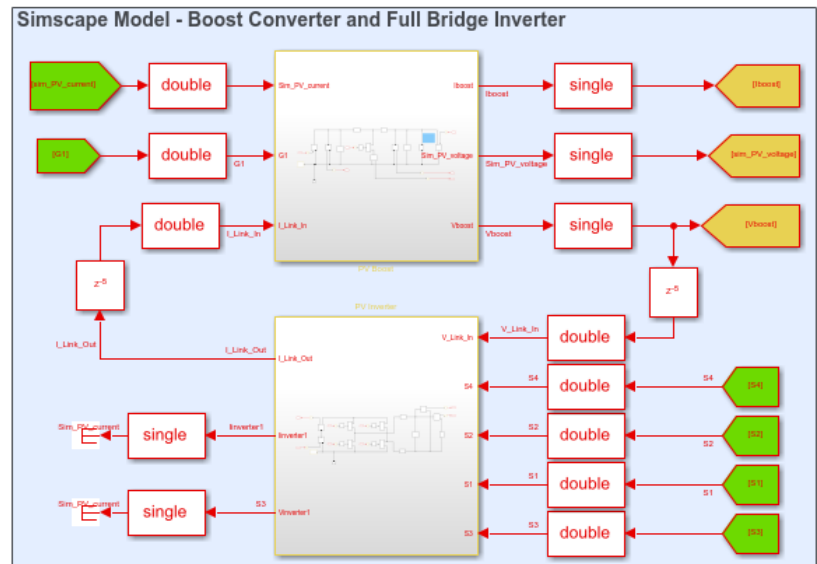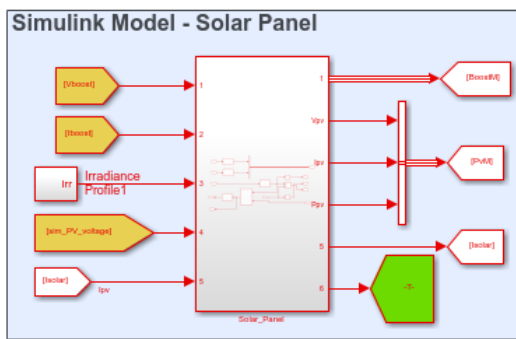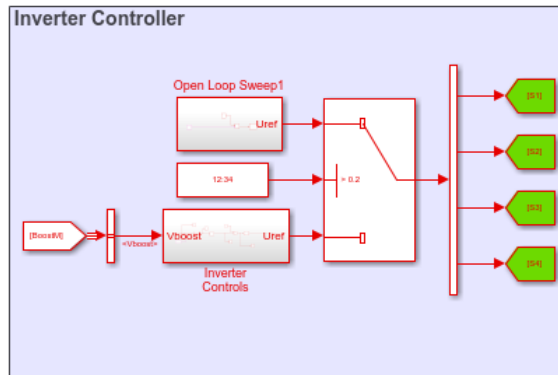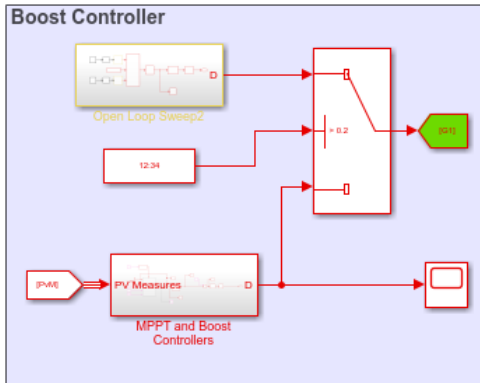
The model consists of four parts: solar panel, boost controller, inverter controller, and a boost converter and full bridge inverter. The solar panel is modeled in Simulink® by using lookup tables. The boost controller and inverter controller provide the control signals for the boost converter and the full bridge inverter which is an H-bridge.

The original model contains the boost converter and full bridge inverter as a single network inside one subsystem. To see this model, enter:

```
open_system('sschdlexSolarInverterSingleNetworkExample')
```

The partitioned model contains the two networks inside separate subsystems. To see the boost converter, open the PV Boost subsystem.

```
open_system('Solar_Power_Inverter_Multiple_Network_HDL/PV Boost')
```
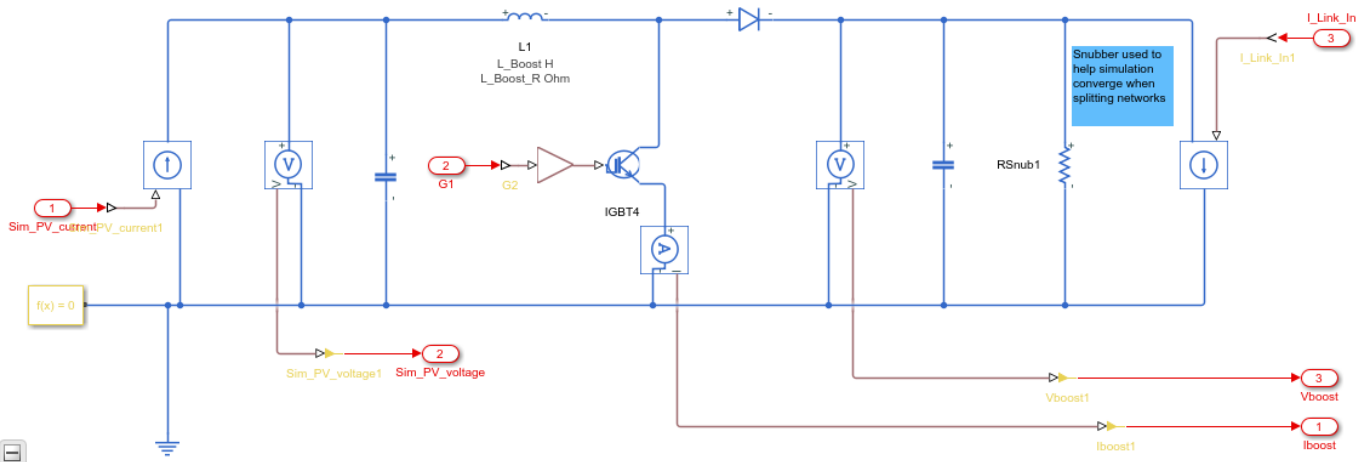
To see the full bridge inverter, open the PV Inverter subsystem.

```
open_system('Solar_Power_Inverter_Multiple_Network_HDL/PV Inverter')
```
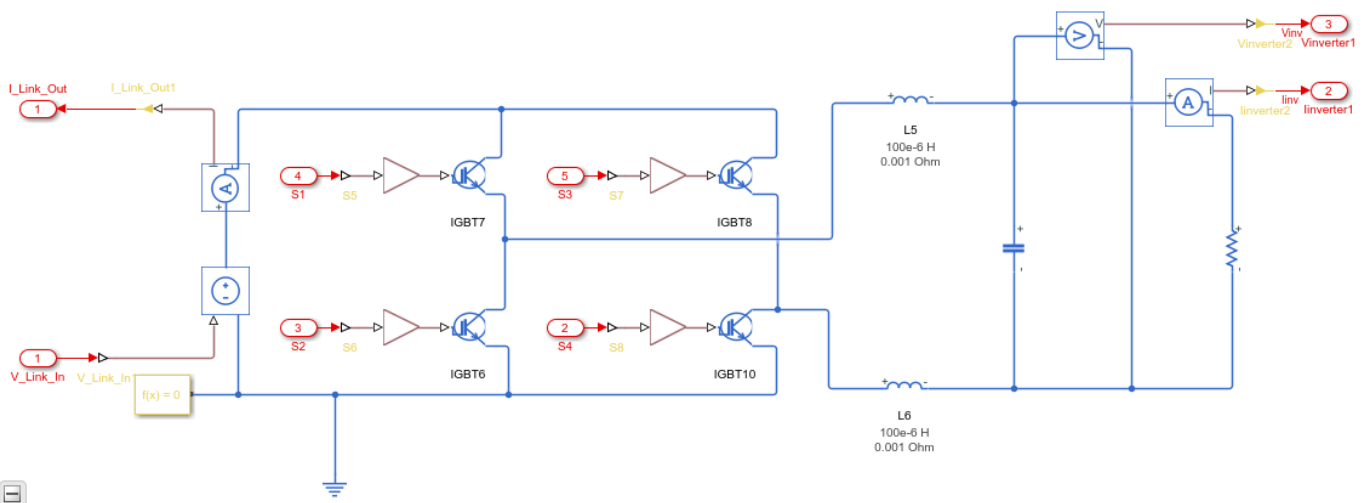

Run Simscape HDL Workflow Advisor for Model with Multiple Networks

1. To open the Simscape HDL Workflow Advisor for the model, enter:

```
sschdladvisor('Solar_Power_Inverter_Multiple_Network_HDL')
```

```
### Running Simscape HDL Workflow Advisor for <a href="matlab:(Solar_Power_Inverter_Multiple_Net
```

2. Run the workflow to the **Check switched linear** task.

The Simscape HDL Workflow Advisor lists the number of networks present in the model and the number of algebraic and differential variables for each network. The Advisor uses the Solver Configuration block to identify each unique network in your model.

Number of Simscape networks present in the model: 2

Details related to the Simscape network Solar_Power_Inverter_Multiple_Network_HDL/PV Inverter/Solver Configuration1

Details
Number of Discrete Variables: 12

Number of Differential Variables: 3

Source	Value
PV_Inverter.Capacitor4.vc	Capacitor voltage
PV_Inverter.L5.i_L	Inductor current
PV_Inverter.L6.i_L	Inductor current

Number of Algebraic Variables: 9

Source	Value
PV_Inverter.IGBT10.C.v	Voltage
PV_Inverter.IGBT10.ideal_switch.i	i
PV_Inverter.IGBT6.C.v	Voltage
PV_Inverter.IGBT6.ideal_switch.i	i
PV_Inverter.IGBT7.diode.i	Current
PV_Inverter.IGBT7.ideal_switch.i	i
PV_Inverter.IGBT8.diode.i	Current
PV_Inverter.IGBT8.ideal_switch.i	i
PV_Inverter.L6.v	Voltage

Details related to the Simscape network Solar_Power_Inverter_Multiple_Network_HDL/PV Boost/Solver Configuration

Details
Number of Discrete Variables: 6

Number of Differential Variables: 3

Source	Value
PV_Boost.Capacitor.vc	Capacitor voltage
PV_Boost.Capacitor2.vc	Capacitor voltage
PV_Boost.L1.i_L	Inductor current

3. Run the **Extract equations** task.

The task displays the number of modes, states, inputs, outputs, and differential variables for each Simscape network.

Passed

Details related to the Simscape network Solar_Power_Inverter_Multiple_Network_HDL/PV Inverter/Solver Configuration1

- Number of states: 12
- Number of inputs: 5
- Number of outputs: 3
- Number of modes: 58
- Number of differential variables: 3

Details related to the Simscape network Solar_Power_Inverter_Multiple_Network_HDL/PV Boost/Solver Configuration

- Number of states: 6
- Number of inputs: 3
- Number of outputs: 3
- Number of modes: 9
- Number of differential variables: 3

4. Run the **Discretize equations** task.

The state-space representation now uses fewer modes. The number of modes is 58 for the boost converter and 9 for the full bridge inverter, which results in a total number of 67 modes. The reduction in the number of modes saves area of the HDL implementation model on the target device.

Details related to the Simscape network Solar_Power_Inverter_Multiple_Network_HDL/PV Boost/Solver Configuration

Parameter	Parameter size
A	12 x 12 x 58
B	12 x 5 x 58
F0	12 x 1 x 58
C	3 x 12 x 58
D	3 x 5 x 58
Y0	3 x 1 x 58

Details related to the Simscape network Solar_Power_Inverter_Multiple_Network_HDL/PV Inverter/Solver Configuration1

Parameter	Parameter size
A	6 x 6 x 9
B	6 x 3 x 9
F0	6 x 1 x 9
C	3 x 6 x 9
D	3 x 3 x 9
Y0	3 x 1 x 9

5. Change the **Validation logic tolerance** to `1e-4` and select the **Generate validation logic for the implementation model** check box. Run the **Generate implementation model** task.

Generate implementation model

Solver Settings

Solver method: Iterative   Number of solver iterations 5   How to Change This?

Implementation Model Settings

Floating-point precision

- ⦿ Single
- ○ Double
- ○ Single coefficient, double computation   What's This?

Map state space parameters to RAMs Auto ▾

Verification Settings

☑ Generate validation logic for the implementation model   Validation logic tolerance 1e-4

Run This Task

Result: ✅ Passed

Passed
Generated implementation model 'gmStateSpaceHDL_Solar_Power_Inverter_Multip'.

Open HDL Implementation Model and Validate HDL Algorithm

To open the implementation model, click the link in the **Generate implementation model** task log. Rename the model as Solar_Power_Inverter_Multiple_Network_StateSpace.

```
open_system('Solar_Power_Inverter_Multiple_Network_StateSpace')
set_param('Solar_Power_Inverter_Multiple_Network_StateSpace', 'SimulationCommand', 'Update')
```
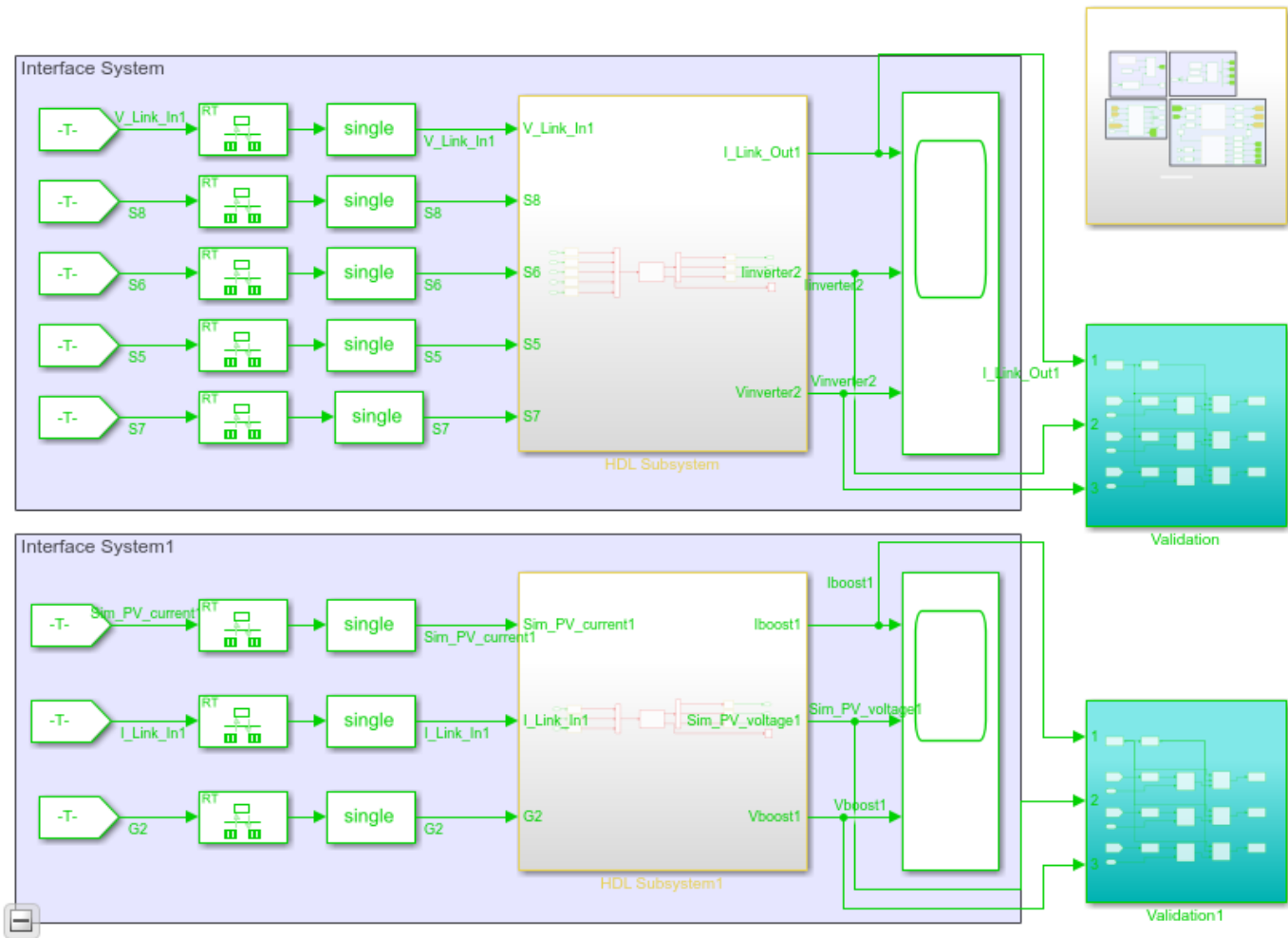
The model contains two HDL Subsystems. The HDL Subsystem block models the state-space equations for the boost converter. The HDL Subsystem1 block models the state-space equations for the full bridge inverter. The Validation and Validation1 subsystems compare functional equivalence of the state space representation of the boost converter and full bridge inverter with the corresponding Simscape network in the original model.

The state-space parameters are saved in a MAT file Solar_multiple_network_stateSpaceParameters.mat. The file contains a cell array of two structures. One structure contains the parameters for the boost converter. The other structure contains the parameters for the full bridge inverter.

To compare the functional equivalence, simulate the model. If simulating the model produces assertions, you can resolve the validation mismatch by modifying a combination of various settings in the **Generate implementation model** task until the HDL implementation model matches the Simscape algorithm. The settings include increasing the validation logic tolerance, increasing the number of solver iterations, and changing the floating-point precision. For more information, see "Validate HDL Implementation Model to Simscape Algorithm" (HDL Coder).

Generate HDL Code and Validation Model

1. Enable generation of the resource utilization report.

```
hdlset_param('Solar_Power_Inverter_Multiple_Network_StateSpace', 'ResourceReport', 'on')
```

2. Before you generate HDL code, it is recommended that you enable generation of the validation model. The validation model compares the output of the generated model after code generation to the output of the original model. To learn more, see "Generated Model and Validation Model" (HDL Coder).

```
HDLmodelname = 'Solar_Power_Inverter_Multiple_Network_StateSpace';
hdlset_param(HDLmodelname, 'TargetDirectory', 'C:/Temp/hdlsrc');
hdlset_param(HDLmodelname, 'GenerateValidationModel', 'on');
```

3. Run the `makehdl` function to generate code. To generate HDL code for both HDL Subsystem blocks, you can place the blocks inside another top level subsystem and then generate HDL code. Name this subsystem as HDL_DUT.

```
makehdl('Solar_Power_Inverter_Single_Network_StateSpace/HDL_DUT')
```

The generated HDL code and validation model are saved in C:/Temp/hdlsrc directory. The generated code is saved as HDL_DUT_tc.vhd. To open the validation model, click the link to gm_Solar_Power_Inverter_Multiple_Network_StateSpace_vnl.slx in the code generation logs in the Command Window.

4. As you generate HDL code, open the Code Generation Report. The resource utilization report indicates a large amount of adders, multipliers, and registers that might be consumed on the target FPGA device.

Summary

Multipliers	103
Adders/Subtractors	1751
Registers	9957
Total 1-Bit Registers	100465
RAMs	0
Multiplexers	15562
I/O Bits	452
Static Shift operators	0
Dynamic Shift operators	214

The overall resource consumption of the two networks is significantly less than the resource consumption of a single, large network. To learn about the resource consumption of the single solar power inverter network, see "Partition Simscape Models Containing a Large Network into Multiple Smaller Networks" (HDL Coder).

See Also

Functions
checkhdl | makehdl | sschdladvisor

More About

- "Generate HDL Code for Simscape Models" (HDL Coder)
- "Simscape HDL Workflow Advisor Tasks" (HDL Coder)
- "Simscape HDL Workflow Advisor Tips and Guidelines" (HDL Coder)
- "Validate HDL Implementation Model to Simscape Algorithm" (HDL Coder)

Troubleshoot Conversion of Simscape DC Motor Control to HDL-Compatible Simulink Model

This example shows how to modify a Simscape™ plant model to generate an HDL-compatible Simulink® model with HDL Coder™. HDL code is then generated from this Simulink model.

Introduction

The Simscape plant model is converted to an HDL-compatible Simulink model by using the Simscape HDL Workflow Advisor. To run the Advisor, you run the `sschdladvisor` function for the model.

The Simscape HDL Workflow Advisor generates an HDL implementation model from which you generate HDL code. Before you generate the implementation model, configure the Simscape plant model for generation of the implementation model using the Simscape HDL Workflow Advisor. For more information, see "Generate HDL Code for Simscape Models" (HDL Coder).

In some cases, the Simscape plant model may not be compatible for generation of the implementation model using the Simscape HDL Workflow Advisor. For HDL compatibility, you modify the Simscape plant model and then run the Simscape HDL Workflow Advisor. This example illustrates the DC Motor Control plant model. The model contains a nonlinear Friction block. You can use the approach in this example to convert Simscape models with few nonlinear blocks to a HDL-compatible Simulink model.

DC Motor Control Model

The DC Motor Control model is a physical model developed in Simscape. The model contains nonlinear elements and must be modified for implementation model generation.

```
open_system('ee_dc_motor_control')
```

Enclose the DC Motor and Friction block inside a Subsystem and save the model as `ee_dc_motor_control_original`.

```
open_system('ee_dc_motor_control_original')
set_param('ee_dc_motor_control_original','SimulationCommand','Update')
```
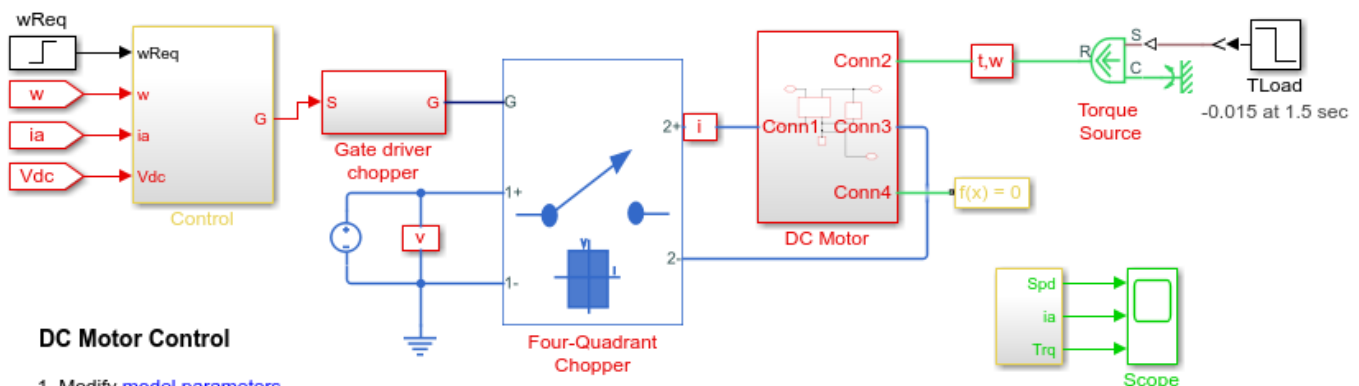

DC motor control is used as a speed control structure. A PWM controlled four-quadrant Chopper is used to feed the DC motor. The DC motor consists of Rotational Electromechanical Converter,

Resistor, Inductance, Friction block and an Inertia block. The control subsystem includes the outer speed-control loop, the inner current-control loop and the PWM generation.

```
sim('ee_dc_motor_control_original')
open_system('ee_dc_motor_control_original/Scope')
```
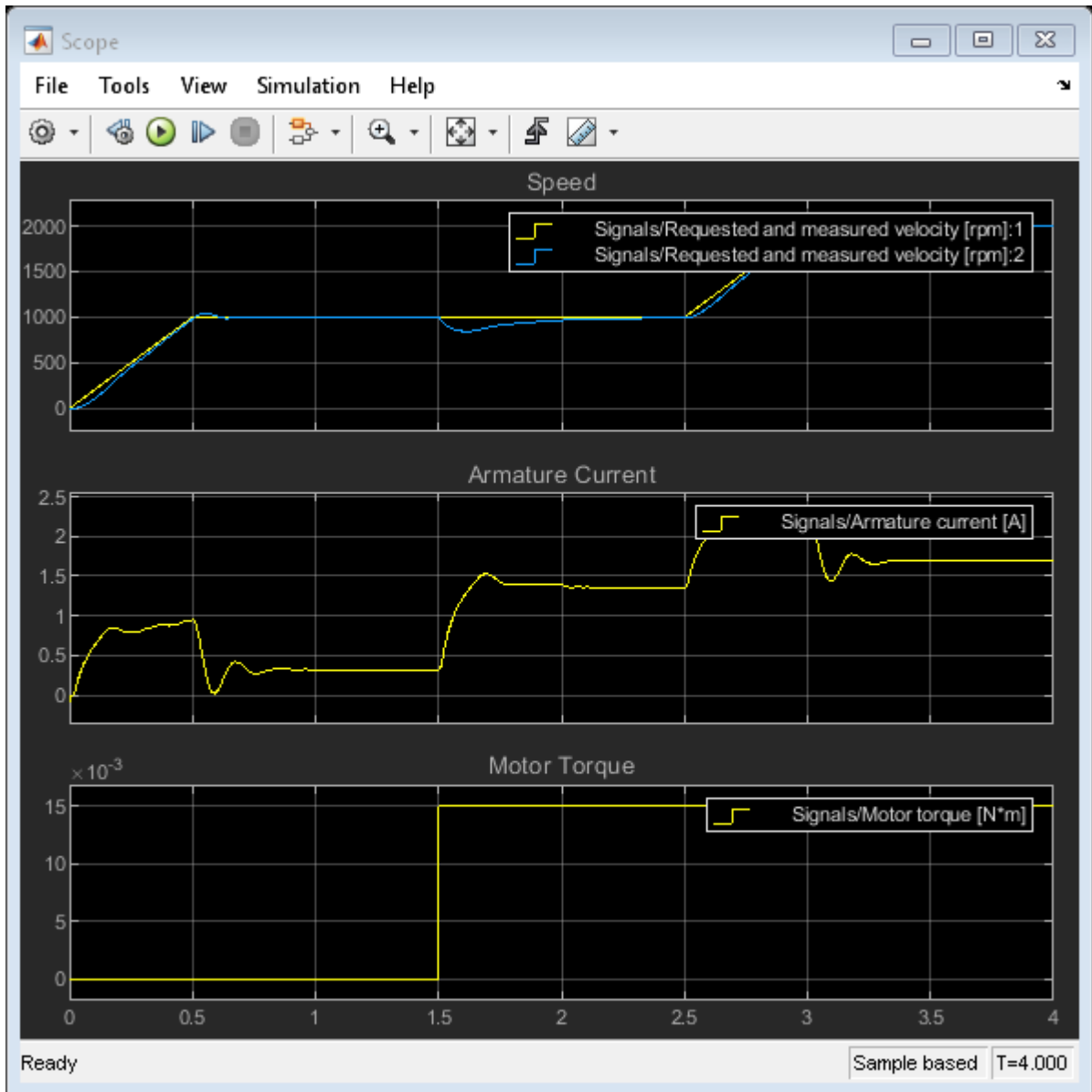

Make DC Motor Model HDL-Compatible

To convert the model to a model that is compatible for conversion with Simscape HDL Workflow Advisor:

1. Detect presence of nonlinear components or blocks in the model. To verify the presence of nonlinear blocks in Simscape plant model, enter:

```
simscape.findNonlinearBlocks('ee_dc_motor_control_original')
```

```
Found network that contains nonlinear equations in the following blocks:
    {'ee_dc_motor_control_original/DC Motor/Friction'}

The number of linear or switched linear networks in the model is 0.
The number of nonlinear networks in the model is 1.

ans =

  1x1 cell array

    {'ee_dc_motor_control_original/DC Motor/Friction'}
```

The Simscape plant model has a nonlinear block, which is the Friction block.

2. For HDL compatibility, the model must not contain nonlinear elements. Remove the Friction block from the model.

3. To simulate the model faster and to reduce the time that the Simscape HDL Workflow Advisor takes to extract the state-space equations, reduce the stop time of this model. In the Simulink Toolstrip, on the Simulation tab, change **Stop Time** to 1.

Save the changes into a new model as ee_dc_motor_control_modified.

```
open_system('ee_dc_motor_control_modified')
set_param('ee_dc_motor_control_original','SimulationCommand','Update')
```
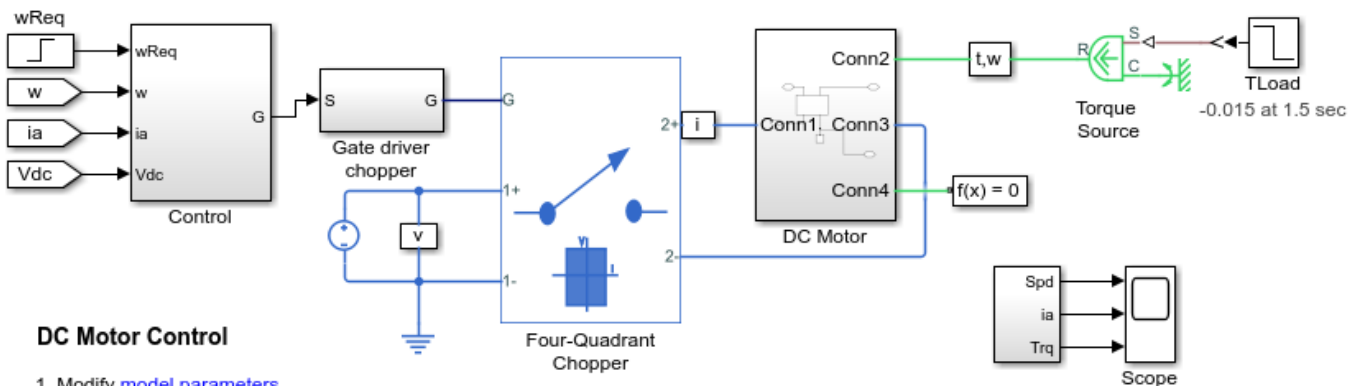

To see the simulation results of the modified model, run these commands:

```
sim('ee_dc_motor_control_modified')
open_system('ee_dc_motor_control_modified/Scope')
```
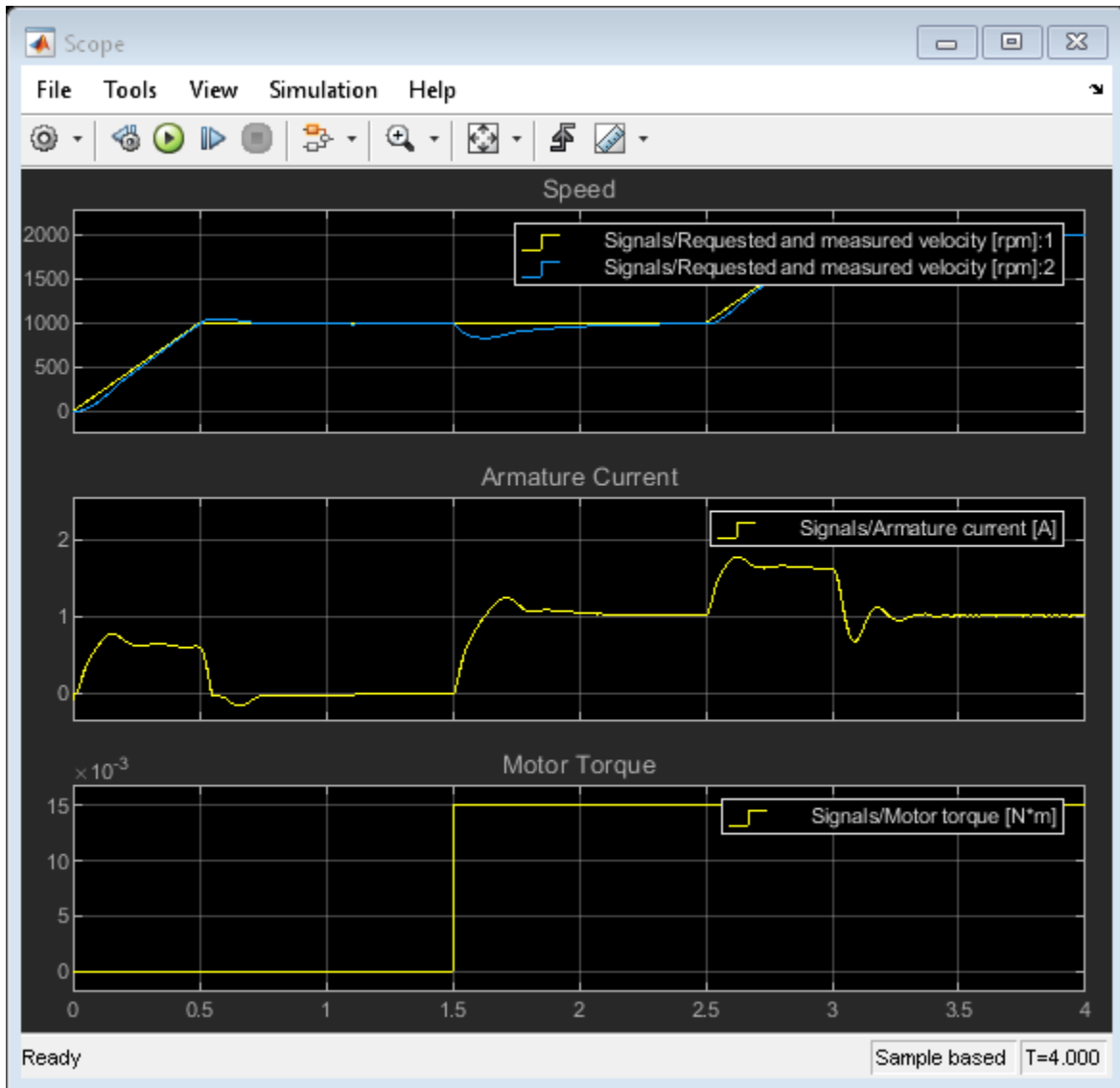
Run Simscape HDL Workflow Advisor and Verify Simulation Results

To open the Simscape HDL Workflow Advisor, run the `sschdladvisor` for your model.

```
sschdladvisor('ee_dc_motor_control_modified')
```

```
### Running Simscape HDL Workflow Advisor for <a href="matlab:(ee_dc_motor_control_modified)">ee_
Updating Model Advisor cache...
Model Advisor cache updated. For new customizations, to update the cache, use the Advisor.Manager
```

To generate the implementation model, in the Simscape HDL Workflow Advisor, leave all tasks to the default settings and then run the tasks. Click the link in the **Generate implementation model** task to open the model.
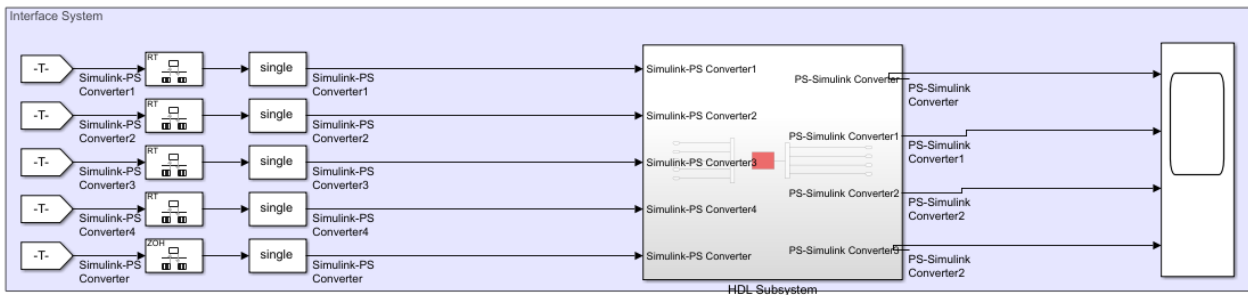
Simulate Implementation model and Generate HDL code

Before you can generate HDL code from the model, you must change the sample time and specify certain settings that make the model compatible for HDL code generation. The sample time of modified plant model is Ts and the number of solver iterations to compute the modes is 3. Therefore, you must change the sample time of the model. To specify the HDL-compatible settings:

1 In the Configuration Parameters dialog box:

- On the **Solver** pane, set **Fixed-step size (fundamental sample time)** to `Ts/3` and select **Treat each discrete rate as a separate task**.

- On the **Diagnostics > Sample Time** pane, set **Multitask rate transition** and **Single task rate transition** to `error`.

1 Add a Rate Transition block in your Simscape model that is placed inside the `Subsystem` block in your implementation model as illustrated in figure below.
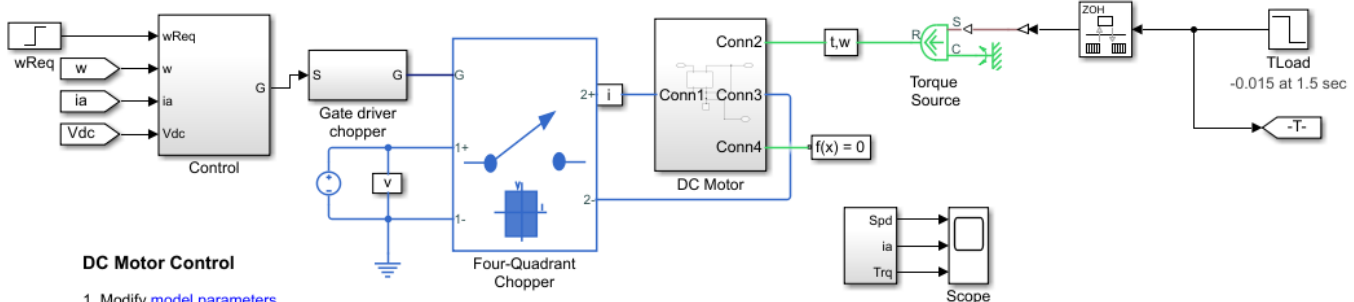
To simulate the model, run this command and then open the Scope block to see the results:

```
sim('gmStateSpaceHDL_ee_dc_motor_control_modifie')
```
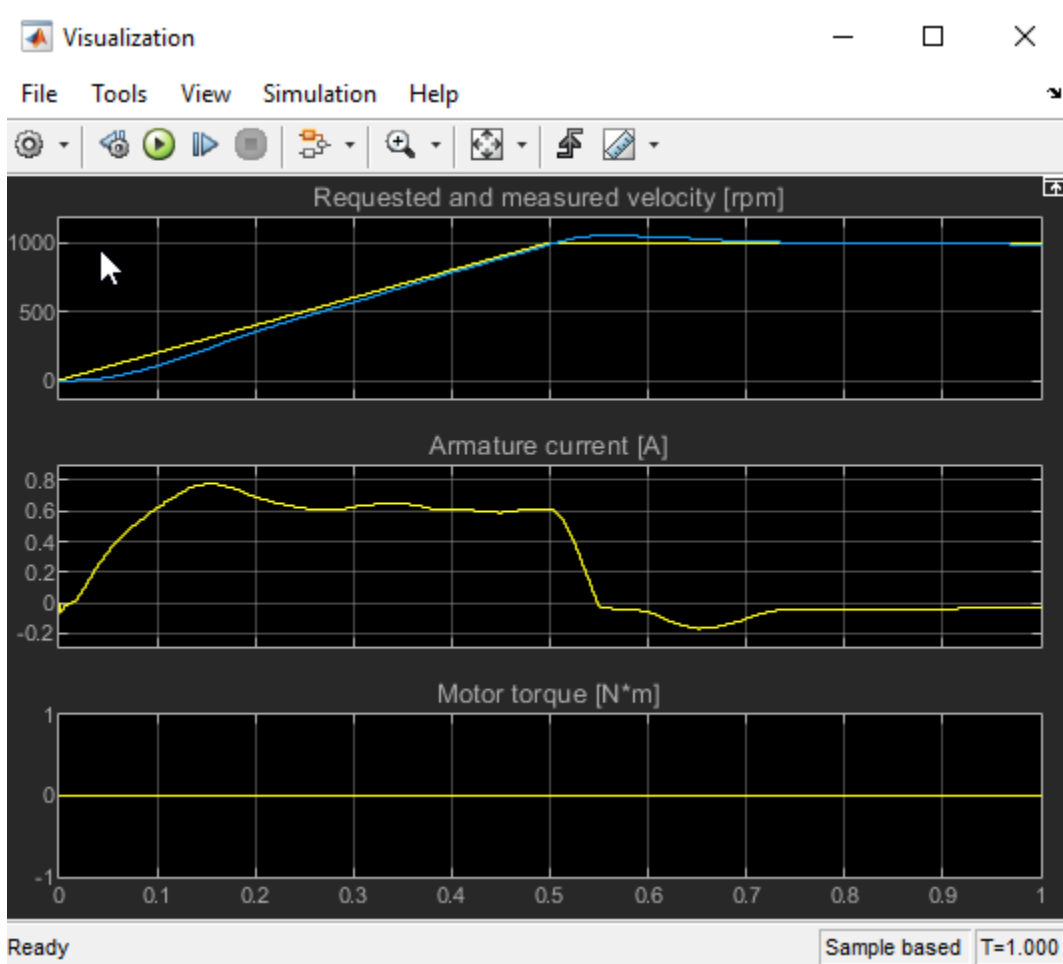
You see that the output generated by the modified Simscape plant model matches the output generated by the implementation model.

Generate HDL Code and Validation Model

Before you generate HDL code, it is recommended that you enable generation of the validation model. The validation model compares the output of the generated model after code generation and the modified Simscape plant model. To learn more, see "Generated Model and Validation Model" (HDL Coder).

To save validation model generation settings on your Simulink model, run this command:

```
hdlset_param('gmStateSpaceHDL_ee_dc_motor_control_modifie', 'GenerateValidationModel', 'on');
```

To generate HDL code, run this command:

```
makehdl('gmStateSpaceHDL_ee_dc_motor_control_modified/HDL Subsystem')
```

By default, HDL Coder generates VHDL code. To generate Verilog code, run this command:

```
makehdl('gmStateSpaceHDL_ee_dc_motor_control_modifie/HDL Subsystem', 'TargetLanguage', 'Verilog')
```

The generated HDL code and the validation model is saved in the `hdlsrc` directory. The generated code is saved as `HDL_Subsystem_tc.vhd`. You can also verify the simulation results by running the validation model `gm_gmStateSpaceHDL_ee_dc_motor_control_modifie_vnl.slx`.

See Also

Functions
`checkhdl` | `makehdl`

More About

- "Generate HDL Code for Simscape Models" (HDL Coder)
- "Get Started with Simscape Electrical"
- "Simscape HDL Workflow Advisor Tasks" (HDL Coder)
- "Validate HDL Implementation Model to Simscape Algorithm" (HDL Coder)

Troubleshoot Conversion of Simscape Permanent Magnet Synchronous Motor to HDL-Compatible Simulink Model

This example shows how to modify a Simscape™ plant model that is continuous time and contains nonlinear elements to generate an HDL-compatible Simulink® model. You can then generate HDL code for this Simulink model.

Introduction

The Simscape HDL Workflow Advisor converts the Simscape plant model to an HDL-compatible implementation model from which you generate HDL code. In some cases, the Simscape plant model might not be compatible for implementation model generation. In such cases, you first modify the Simscape plant model and then run the Advisor.

This example illustrates how to modify a permanent magnet synchronous motor model for compatibility with Simscape HDL Workflow Advisor. The model is continuous time and contains many nonlinear components. You modify this model to a discrete-time switched linear model and then run the Simscape HDL Workflow Advisor.

Permanent Magnet Synchronous Motor Model

The permanent magnet synchronous motor model is a physical system in Simscape. To open the model, run this command:

```
open_system('ee_pmsm_drive')
```

Save this model as ee_pmsm_drive_original.slx.

```
open_system('ee_pmsm_drive_original')
set_param('ee_pmsm_drive_original','SimulationCommand','Update')
```
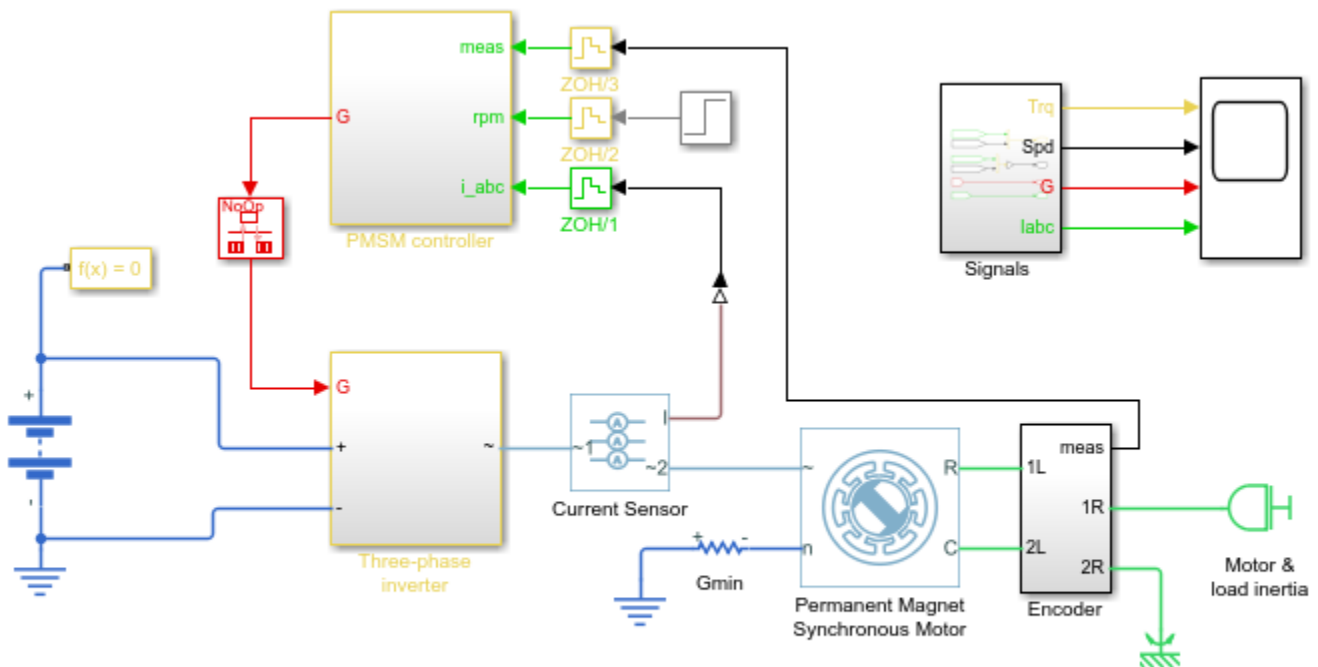
Three-Phase PMSM Drive

1. Explore simulation results using sscexplore
2. Learn more about this example

The model contains a Permanent Magnet Synchronous Machine (PMSM) and inverter sized that you can use in a typical hybrid vehicle. The inverter is connected to the vehicle battery. To see how the model works, simulate the model.

```
sim('ee_pmsm_drive_original')
open_system('ee_pmsm_drive_original/Scope')
```
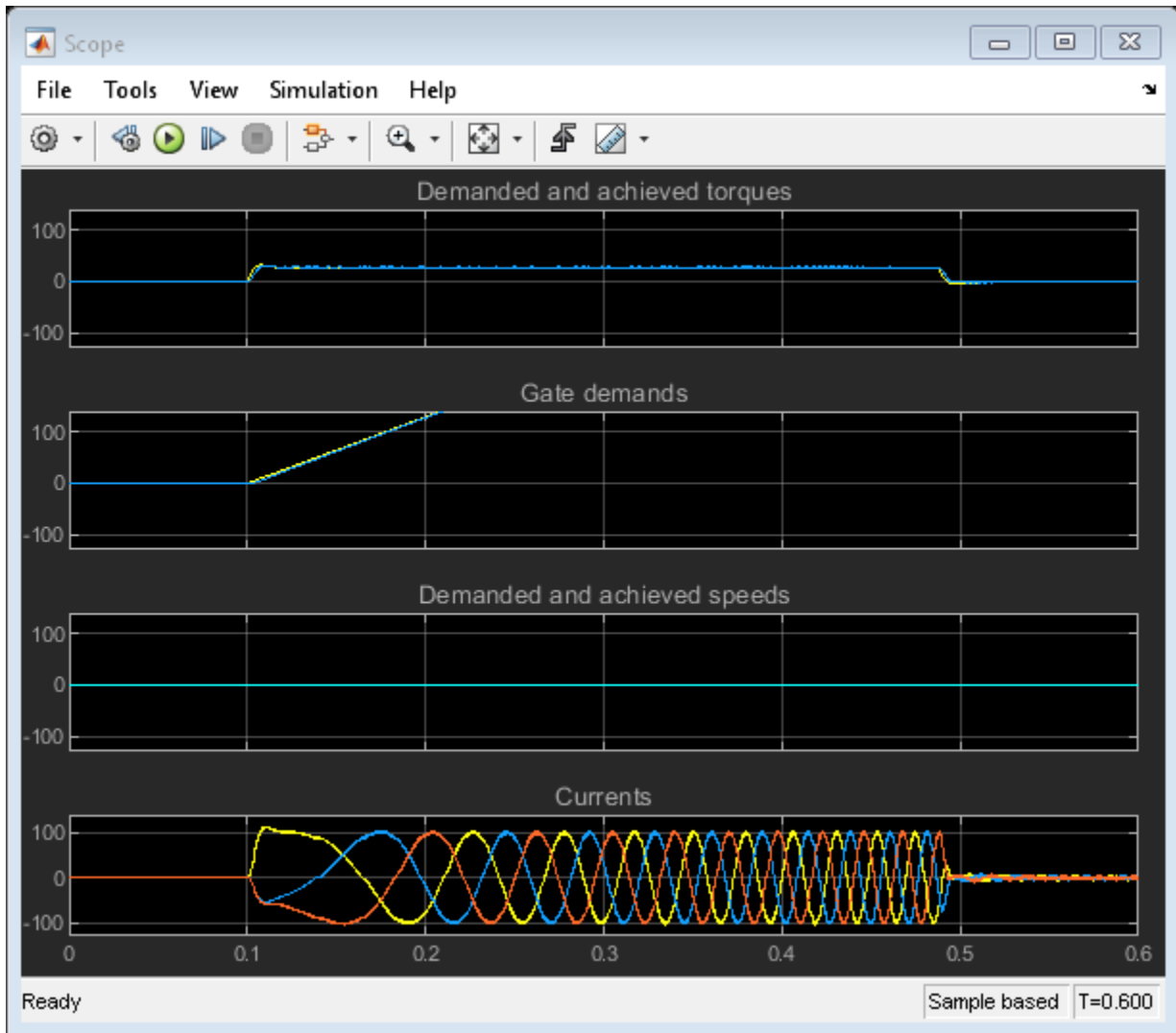
This model is a continuous time system. To use this model with Simscape HDL Workflow Advisor, convert the model into a discrete system. You then modify the model to use blocks that are compatible for the Simscape to HDL workflow.

Convert Continuous-Time Model to Fixed-Step Discrete Model

1. Configure the solver options for HDL code generation by using a Solver Configuration block. In the block parameters:

- Select **Use local solver**.
- Use `Backward Euler` as the **Solver type**.
- Specify a discrete **Sample time**, `Ts`.

2. Modify the Solver settings of the model. On the **Modeling** tab, click **Model Settings**. On the **Solver** pane:

- Set **Solver selection type** to `Fixed-Step`.

- Set **Solver** to `discrete (no continuous states)`.

- Set **Fixed-step size (fundamental sample time)** to `Ts`.

- In the section **Tasking and sample time options**, clear **Treat each discrete rate as a separate task**.

3. Modify the display settings of your model. On the **Debug** tab, select **Information Overlays > Sample Time > Colors**. Review the Sample Time Legend for blocks that have a sample time other than `Ts`, or run at a continuous time scale. Double-click the Step block and set the **Sample time** to `Ts`.

4. For faster simulation, ignore the zero-sequence parameters of the PMSM. Double-click the Permanent Magnet Synchronous Motor block and set **Zero Sequence** to `Exclude`.

The model is now a fixed-step discrete system. Simulate the model and compare the **Torque Demand** and **Motor Torque** signals in the Simulation Data Inspector. The signals differ by more than the tolerance levels toward the end of simulation but are within acceptable limits.

You use a two-step process to convert the Simscape plant model to a HDL-compatible implementation model:

1   Implement a Simulink model that replaces the nonlinear part of the Simscape algorithm by using equivalent Simulink blocks.

2   Modify this model to use blocks that are compatible for Simscape to HDL workflow.

Replace Nonlinear Simscape Blocks with Equivalent Simulink Implementation

1. To make the Simscape plant model HDL-Compatible, identify the presence of any nonlinear components or blocks in the model:

```
simscape.findNonlinearBlocks('ee_pmsm_drive_original')

Found network that contains nonlinear equations in the following blocks:
    {'ee_pmsm_drive_original/Permanent Magnet Synchronous Motor'}

The number of linear or switched linear networks in the model is 0.
The number of nonlinear networks in the model is 1.

ans =

  1x1 cell array

    {'ee_pmsm_drive_original/Permanent Magnet Synchronous Motor'}
```

The Simscape plant model has a nonlinear block, which is the PMSM block.

2. The PMSM block, Encoder block, Gmin resistor, and Motor & Load Inertia block are replaced with Simulink blocks that perform the equivalent algorithm.

To implement the Electrical Interface block, you use Controlled Current Sources.
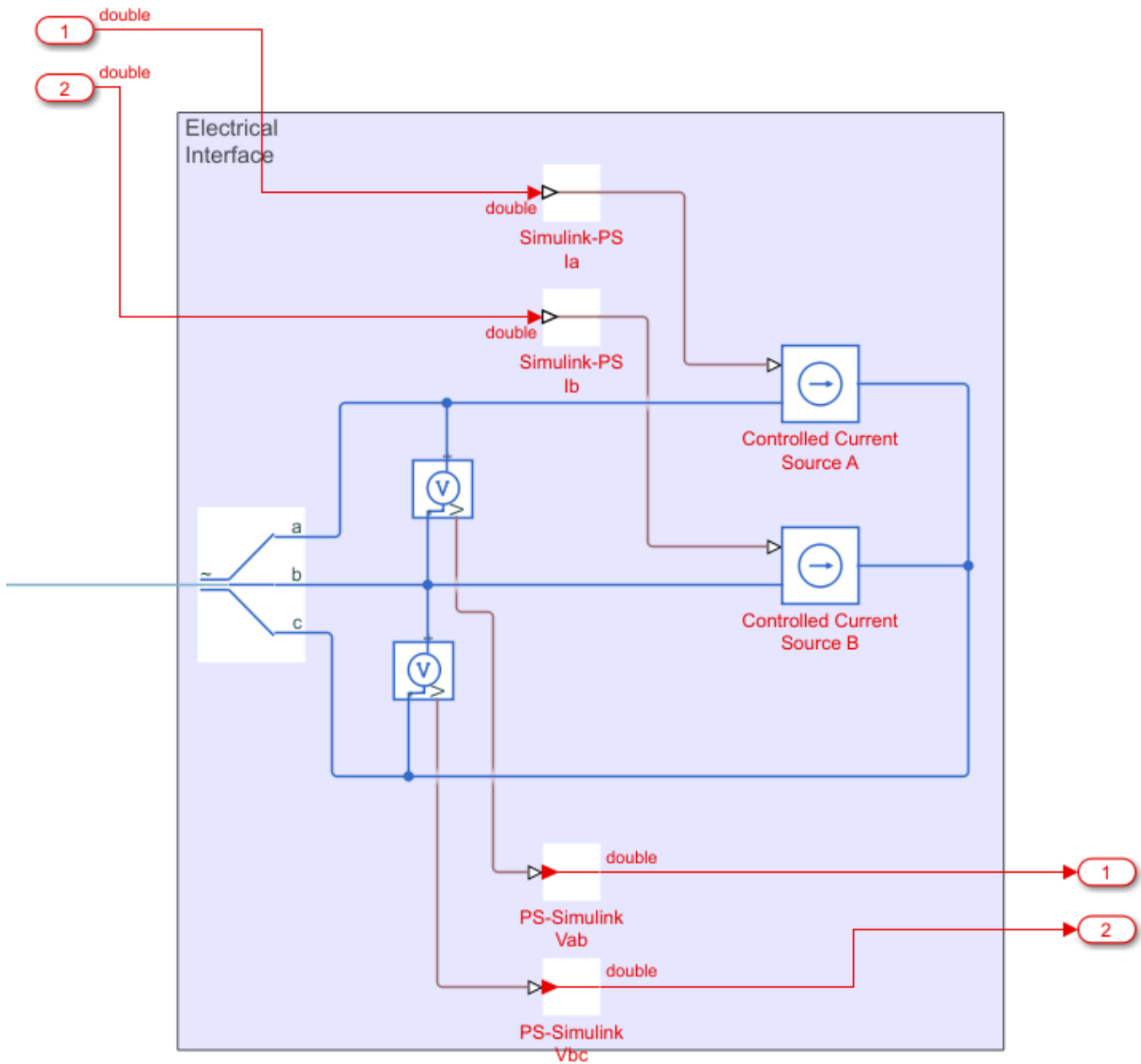
The interface to the PMSM is isolated from the implementation. To implement the PMSM by using Simulink blocks, you use Electrical Equations and Mechanical Equations. Inside the Park Transform and Inverse Park Transform blocks, eliminate the Sine and Cosine blocks.
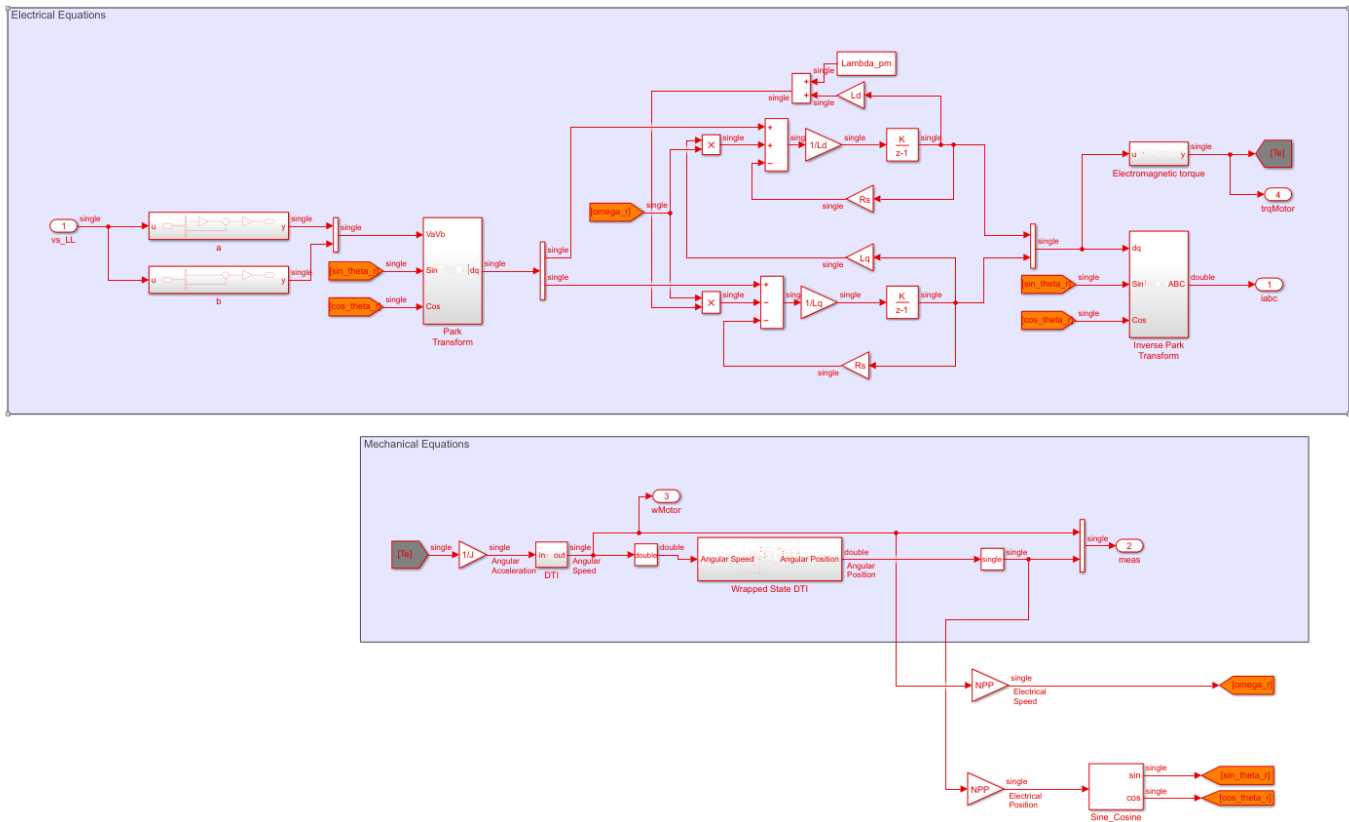
Identify Simscape Blocks that Run on FPGA and Restructure Simscape Model

The ee_pmsm_drive_singleSL model illustrates how you modify the original model ee_pmsm_drive_original and prepare the model for readiness with Simscape HDL Workflow Advisor.

1. To modify the Simscape model for compatibility with HDL implementation model generation, identify the part of the Simscape algorithm that you want to run on the FPGA. In this example, you can run the three-phase inverter, electrical interface, PWM, and the Permanent Magnet Synchronous Motor (Simulink) on the FPGA.

2. After blocks to run on the FPGA have been identified, the blocks are placed inside a top-level subsystem. This subsystem is the DUT (Design Under Test) and contains blocks you run on the FPGA after generating the HDL implementation model. Afer running the Simscape HDL Workflow Advisor, this subsystem is replaced with the HDL algorithm. This part of the Simscape model must run at the fastest sample rate. Rate Transition blocks are added to upsample the design.

3. To save resource usage on the target hardware, Data Type Conversion blocks are added to convert the model to use `single` data types.

The ee_pmsm_drive_singleSL model shows how these blocks are placed inside a top-level subsystem Subsystem1, which is the DUT. The blocks inside the subsystem are running at a faster rate.

```
load_system('ee_pmsm_drive_singleSL')
set_param('ee_pmsm_drive_singleSL','SimulationCommand','update')
open_system('ee_pmsm_drive_singleSL/Subsystem1')
```
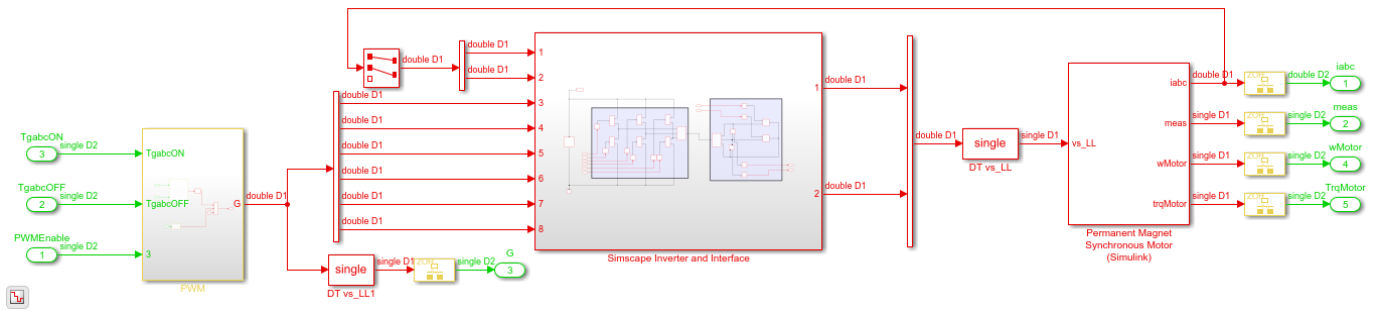
In the ee_pmsm_drive_singleSL model, the three-phase inverter and electrical interface are placed inside the Simscape Inverter and Interface subsystem.

```
open_system('ee_pmsm_drive_singleSL/Subsystem1/Simscape Inverter and Interface')
```
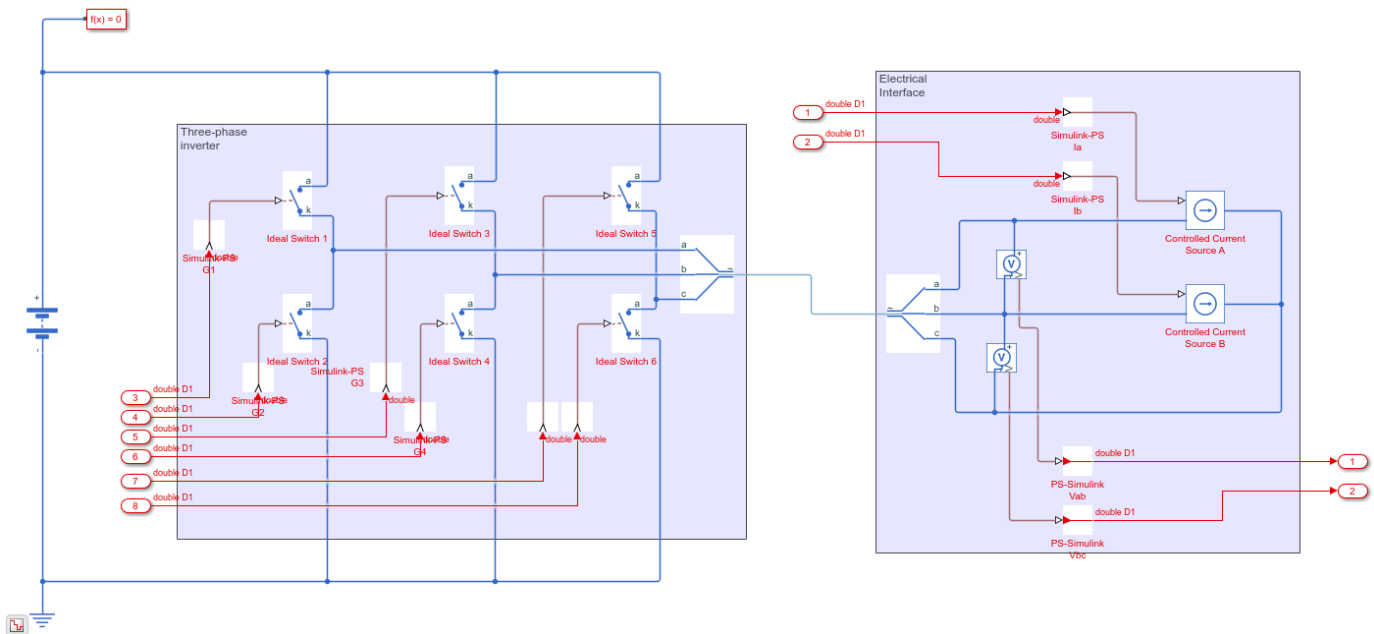

Modify Permanent Magnet Synchronous Motor Subsystem for HDL Compatibility

The preceding section describes the changes that have been applied to the masked subsystem, Permanent Magnet Synchronous Motor (Simulink).

1. The Integrator with Wrapped State (Discrete or Continuous) block is not compatible for HDL code generation. This block has been replaced with a Wrapped State DTI subsystem.

```
PMSMSubsystem = 'ee_pmsm_drive_singleSL/Subsystem1/Permanent Magnet Synchronous Motor (Simulink)'
open_system(PMSMSubsystem, 'force')
```
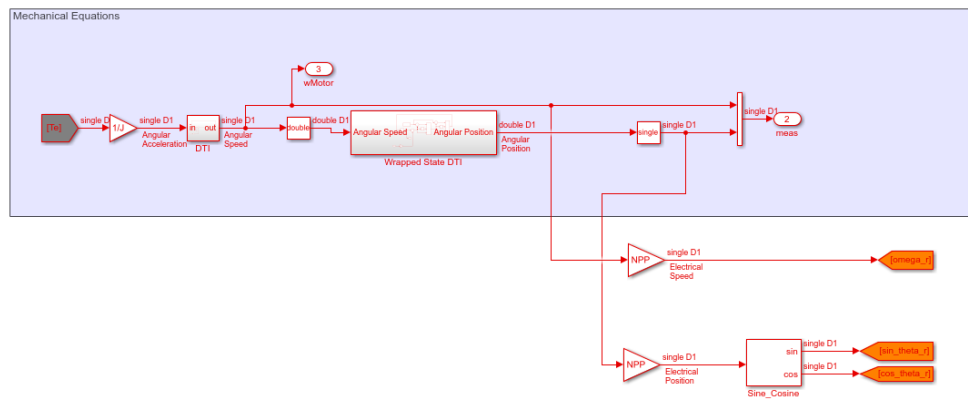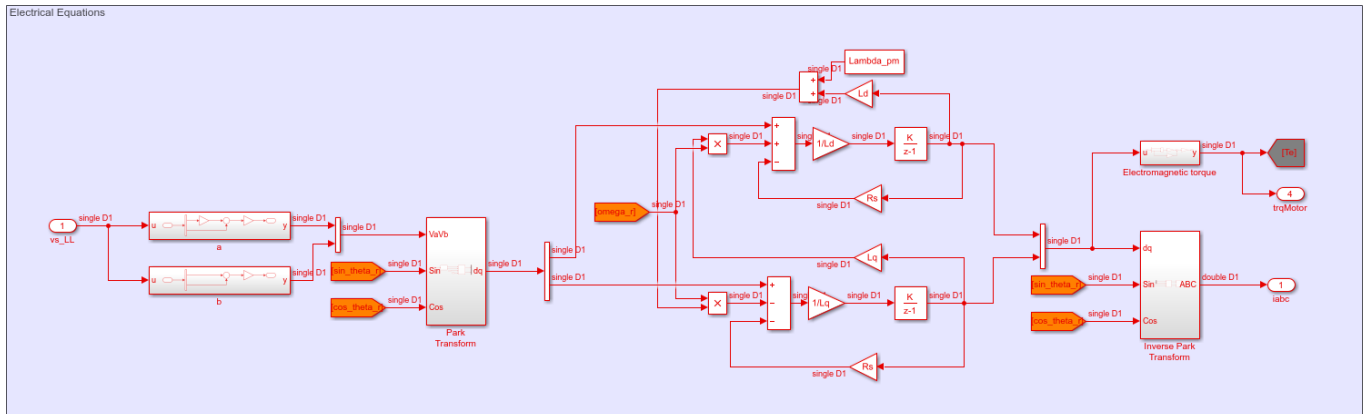
```
open_system([PMSMSubsystem, '/Wrapped State DTI'])
```
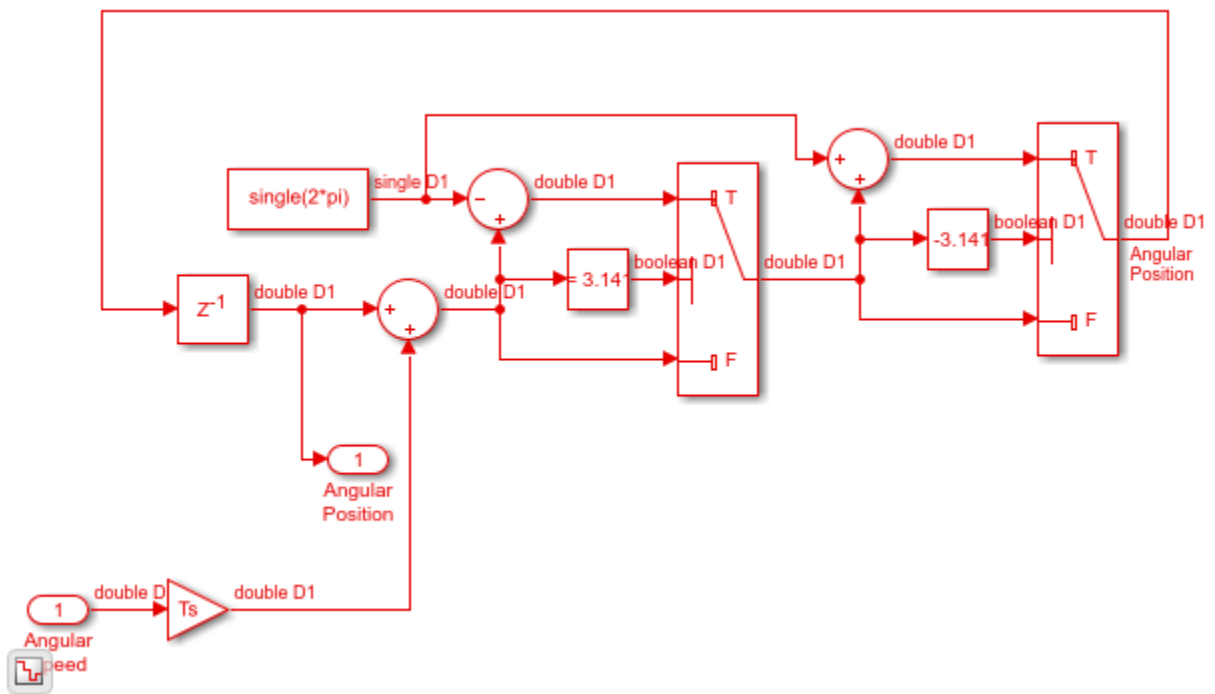
2. To reduce the FPGA area footprint for the:

- Park Transform block, Clarke Transform and Clarke to Park Angle Transform blocks are added.
- Inverse Park Transform block, Inverse Park to Clarke Angle Transform and Inverse Clarke Transform blocks are added.

```
open_system([PMSMSubsystem, '/Park Transform'])
```
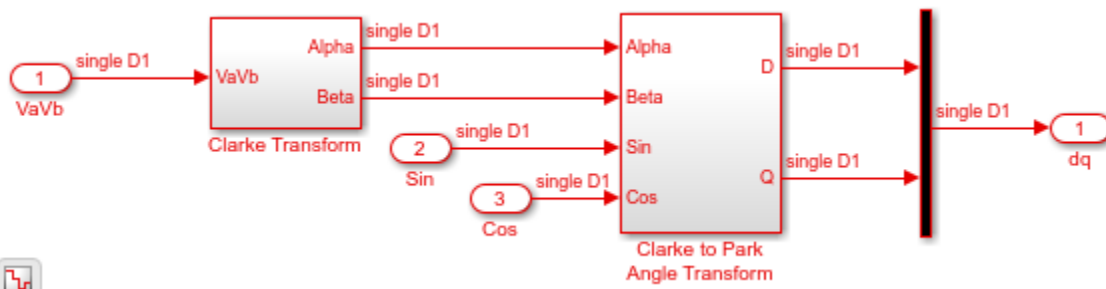


```
open_system([PMSMSubsystem, '/Inverse Park Transform'])
```
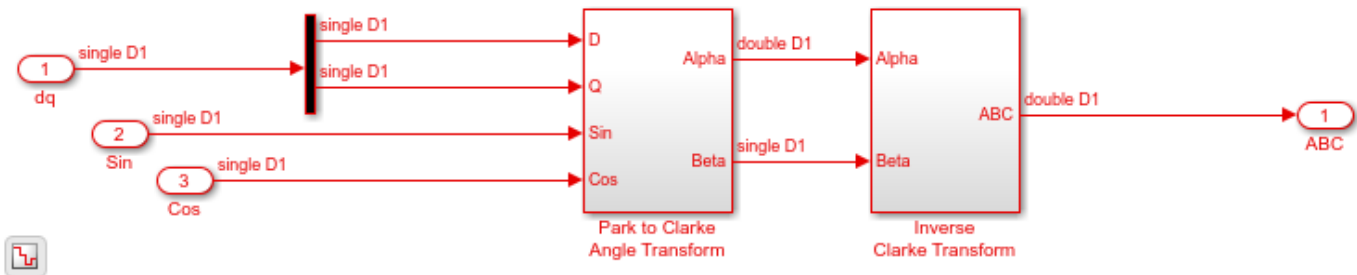

3. For the Discrete-Time Integrator blocks inside this subsystem, the **Sample time** is set to -1, **Gain value** to Ts, and **Integrator method** to Accumulation:Forward Euler. You can view these block parameters programmatically by running these commands.

```
blockDTI = find_system(PMSMSubsystem, 'LookUnderMasks', 'on', ...
                                  'blocktype', 'DiscreteIntegrator');
 for  n  =  1:numel(blockDTI)
    Integpath = blockDTI(n);
    Integname = get_param(Integpath, 'Name');
    stime = num2str(get_param(blockDTI{n}, 'SampleTime'));
    gval = num2str(get_param(blockDTI{n}, 'gainval'));
    integmethod = num2str(get_param(blockDTI{n}, 'IntegratorMethod'));
    disp('---------------------------------------------------')
    disp(Integpath)
    disp(['Sample time: ', stime, '    Gain: ', gval, ...
                   '      Integration method: ', integmethod])
 end
disp('---------------------------------------------------')

---------------------------------------------------
    {'ee_pmsm_drive_singleSL/Subsystem1/Permanent Magnet...'}

Sample time: -1    Gain: Ts    Integration method: Accumulation: Forward Euler
---------------------------------------------------
```

```
    {'ee_pmsm_drive_singleSL/Subsystem1/Permanent Magnet...'}

Sample time: -1    Gain: Ts    Integration method: Accumulation: Forward Euler
----------------------------------------------------
```

Prepare Model and Run Simscape HDL Workflow Advisor

To the top level of the model:

1 A Digital Clock that has **Sample time** Ts has been added and connected to a Display block.
2 The Three-Phase Current Sensor Simscape block is replaced by feeding the controller with three-phase currents coming from the PMSM model.

This figure illustrates the top level of the model with the above changes.

```
open_system('ee_pmsm_drive_singleSL')
```
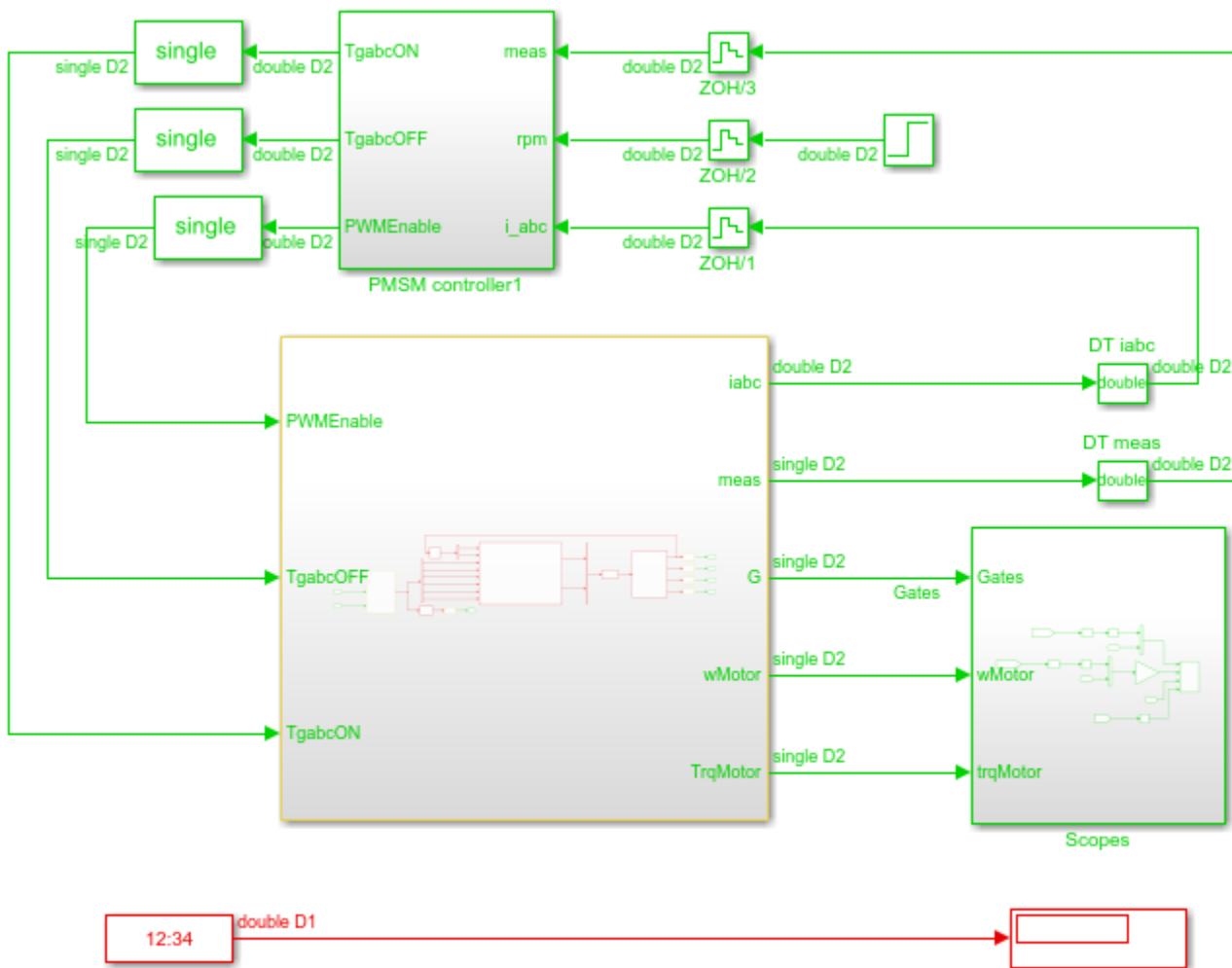

To open the Simscape HDL Workflow Advisor, run the sschdladvisor function for your model:

```
sschdladvisor('ee_pmsm_drive_singleSL')
```

To generate the implementation model, in the Simscape HDL Workflow Advisor, leave the default settings and then run the tasks. To open the implementation model, in the **Generate implementation model** task, click the link.
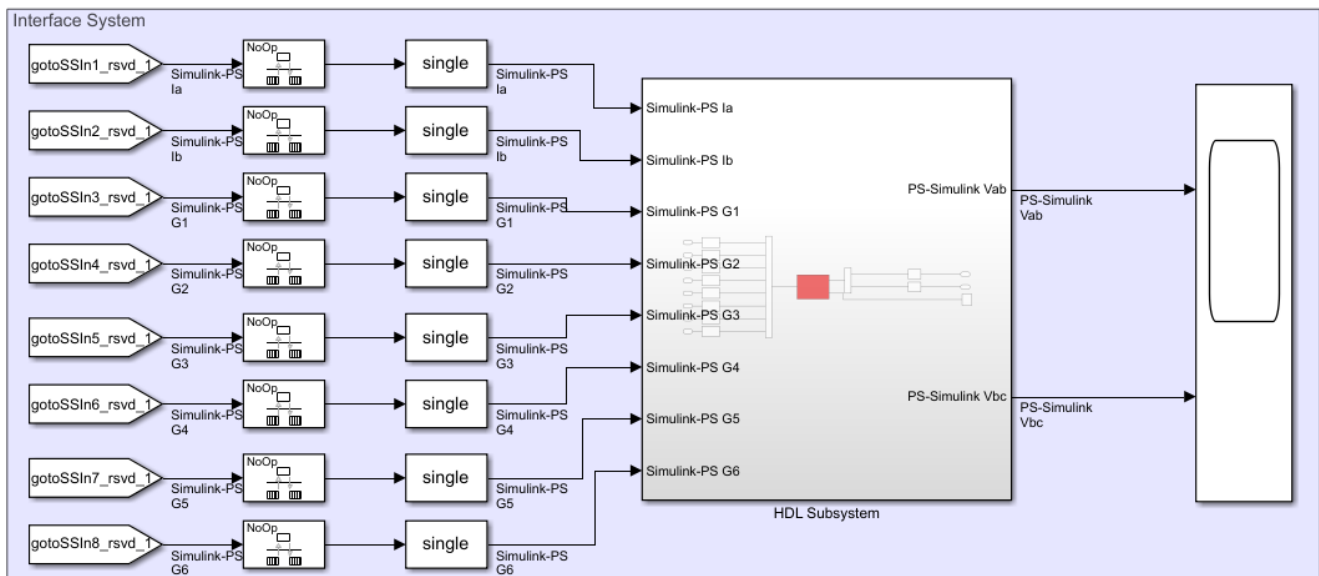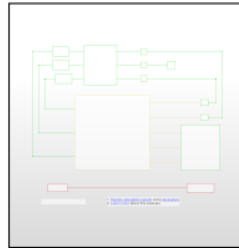
Reconfigure Implementation Model for HDL Code Generation

In this example, the implementation model has been modified for deployment to Speedgoat FPGA I/O platforms. The model is resaved as gmStateSpaceHDL_ee_pmsm_drive_GenerateHDL.

To reconfigure the single-precision implementation model for HDL code generation:

1. Run the `hdlsetup` (HDL Coder) function on the model.

```
hdlsetup('gmStateSpaceHDL_ee_pmsm_drive_GenerateHDL')
```

2. The model solver setting, **Fixed-step size**, is modified to `Ts/5` because the default **Number of solver iterations** is 5.

3. The Subsystem1 block contains blocks that you run on the FPGA. The Simscape Inverter and Interface subsystem is replaced with the HDL Subsystem block. The HDL Subsystem block contains the HDL Algorithm that contains the HDL implementation of the Simscape algorithm. To see the HDL algorithm implementation, open this block.

```
model_name = 'gmStateSpaceHDL_ee_pmsm_drive_GenerateHDL';
dut_name = 'gmStateSpaceHDL_ee_pmsm_drive_GenerateHDL/Subsystem1';
```

```
load_system(model_name)
set_param(model_name,'SimulationCommand','Update')
open_system([dut_name, '/HDL Subsystem'])
```
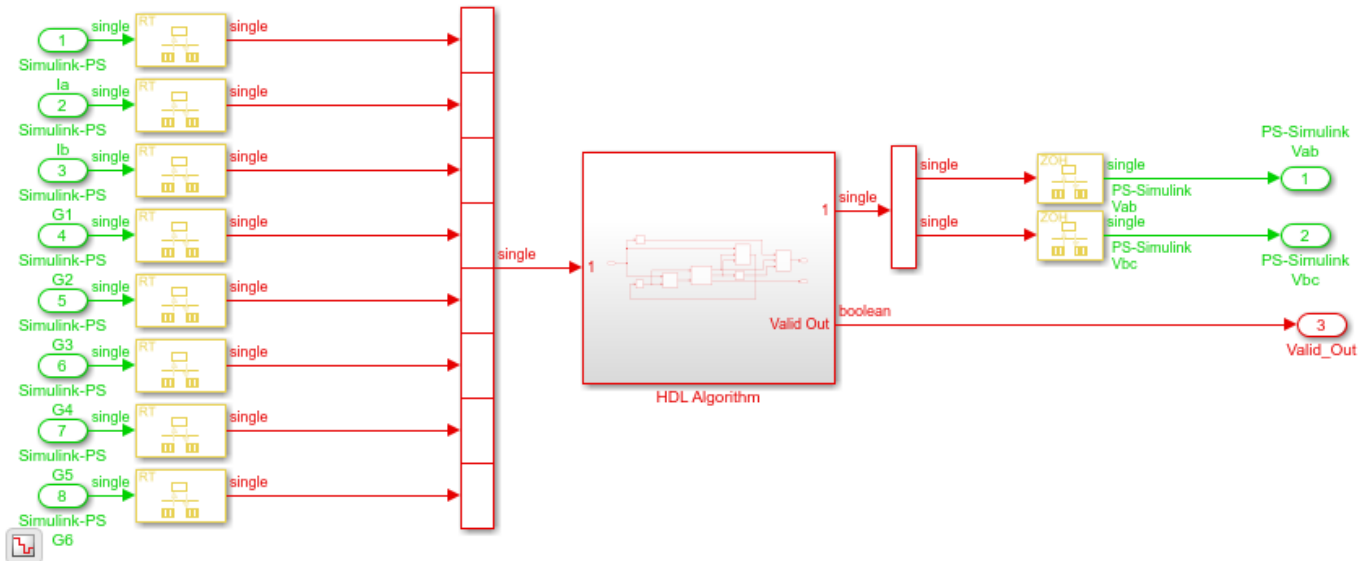


```
open_system([dut_name, '/HDL Subsystem/HDL Algorithm'])
```
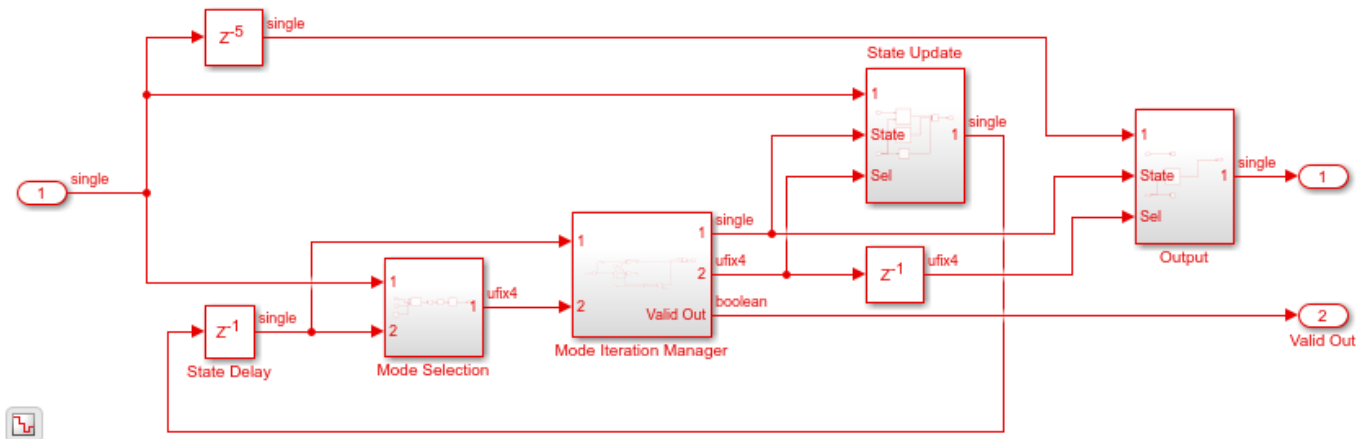

4. The HDL Algorithm Subsystem has a Valid Out signal. The Permanent Magnet Synchronous Motor (Simulink) subsystem is placed inside an Enabled Subsystem and the vs_LL input port is connected to the Valid Out signal.

```
open_system(dut_name)
```
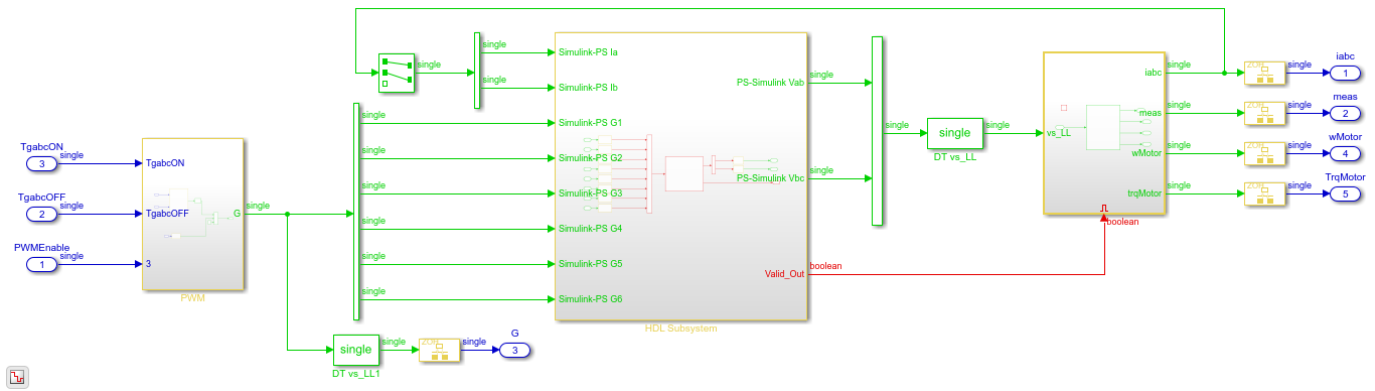
5. Move the block inside the subsystem that originally contained the Simscape algorithm to the top level of the model.

This figure illustrates the top level of the model with the above changes.

```
open_system(model_name)
```
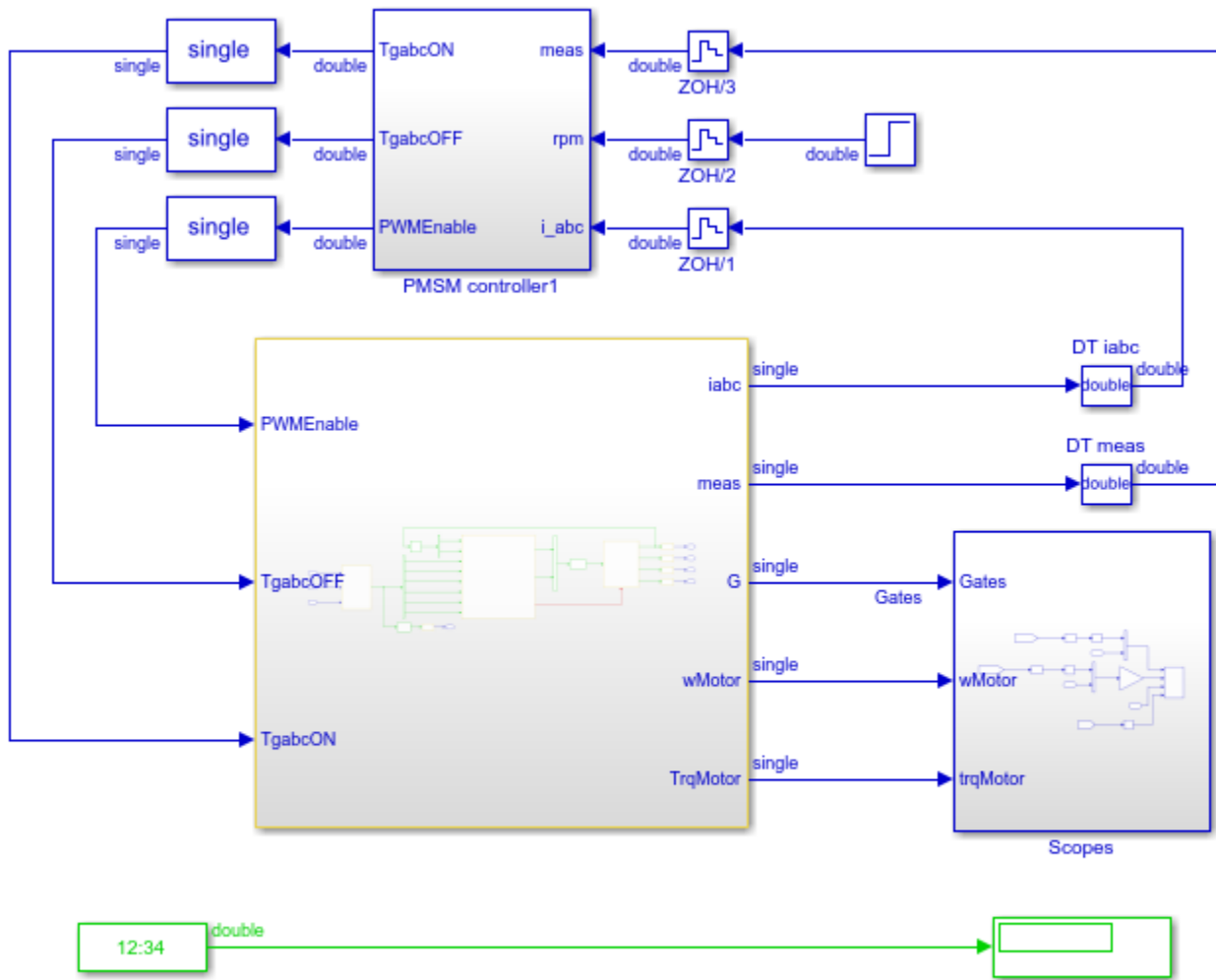
Three-Phase PMSM Drive

1. Explore simulation results using sscexplore
2. Learn more about this example

Generate HDL Code

Before you generate HDL code, to compare the output of the generated model after code generation with the modified Simscape plant model, specify validation model generation.

```
hdlset_param(model_name, 'GenerateValidationModel', 'on');
```

To learn more, see "Generated Model and Validation Model" (HDL Coder).

To generate HDL code, run this command:

```
makehdl('gmStateSpaceHDL_ee_pmsm_drive_GenerateHDL/HDL Subsystem')
```

By default, HDL Coder generates VHDL code. To generate Verilog code, run this command:

```
makehdl('gmStateSpaceHDL_ee_pmsm_drive_GenerateHDL/HDL Subsystem', 'TargetLanguage', 'Verilog')
```

The code generator saves the generated HDL code and the validation model in the `hdlsrc` folder. The generated code is saved as `HDL_Subsystem_tc.vhd`. To see the resource usage information of your design, view the Code Generation Report.

To open the validation model, after you generate HDL code, open the gm_gmStateSpaceHDL_ee_pmsm_drive_GenerateHDL_vnl.slx model.

Deploy Permanent Magnet Synchronous Motor to Speedgoat FPGA I/O Modules

In the HDL implementation model, Subsystem1 contains blocks you run on the FPGA. You can run the HDL Workflow Advisor on this Subsystem to deploy the HDL algorithm onto FPGA boards in Speedgoat target platforms. For an example, see "Hardware-in-the-Loop Implementation of Simscape Model on Speedgoat FPGA I/O Modules" (HDL Coder).

See Also

Functions
checkhdl | makehdl

More About

*   "Generate HDL Code for Simscape Models" (HDL Coder)
*   "Generate Simulink Real-Time Interface Subsystem for Simscape Two-Level Converter Model" (HDL Coder)
*   "Validate HDL Implementation Model to Simscape Algorithm" (HDL Coder)
*   "Simscape HDL Workflow Advisor Tips and Guidelines" (HDL Coder)
*   "Get Started with Simscape Electrical"

Replacing Variable Resistors

This example shows how to convert a model that is nonlinear due to a variable resistor into a switched linear model making it compatible with Simscape to HDL Workflow.

Introduction

Simscape to HDL Workflow supports conversion of Simscape switched linear models to functionally-equivalent Simulink models that are compatible for HDL code generation. Due to the nature of the equations that result from variable resistors, blocks such as Piecewise-Constant Resistor can lead to nonlinear behavior and must be replaced with equivalent switched linear components. Specifically the Piecewise-Constant Resistor contains events that are not supported by the Simscape HDL Workflow Advisor.

Open The Simscape™ Model. In the MATLAB® command prompt, enter:

```
nonlinearModel = 'sschdlexVariableResistorExample';
load_system(nonlinearModel)
open_system([nonlinearModel,'/Simscape Subsystem'])
```
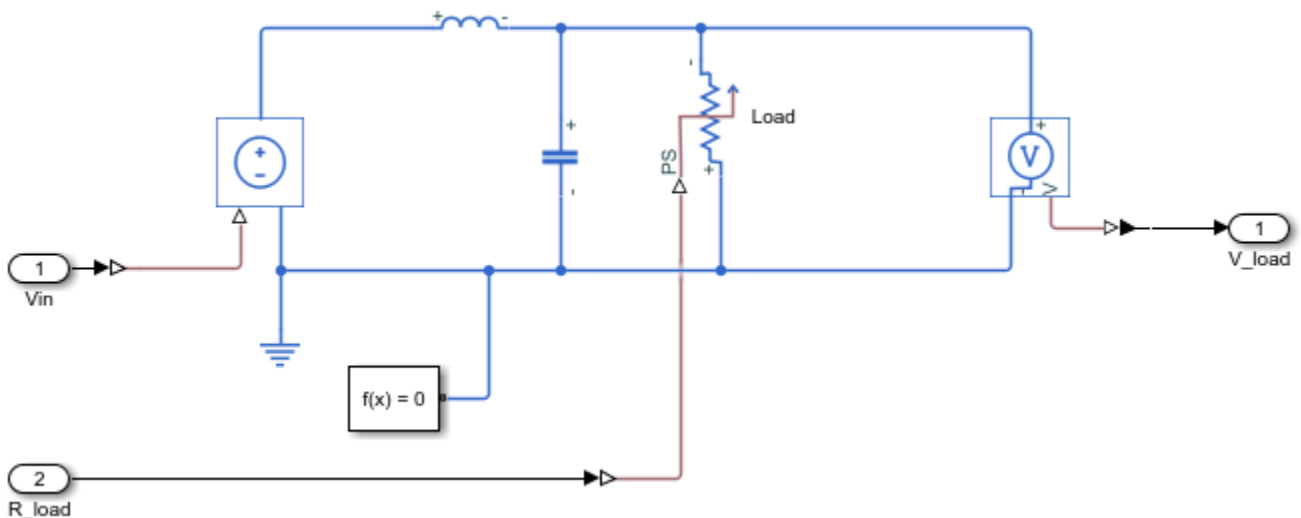

This model is an RLC circuit with a Piecewise-Constant Resistor acting as the resistor or 'load'. For the Piecewise-Constant Resistor, the relationship between voltage V and current I is V=I*R where R is the numerical value presented at the physical signal port R.

To ensure a positive value for the resistance, any value below 1e-6 is replaced by 1e-6. This resistor is Piecewise-Constant because the resistance only changes when the input value differs from the current resistance by more than a set tolerance. Thus, a continuously changing input would be converted to a discrete set of resistances.

In this model the signal going into the Piecewise-Constant Resistor is a step function that changes from 2 to 3 at t=0.1 thus changing the load resistance from 2 Ω to 3 Ω.
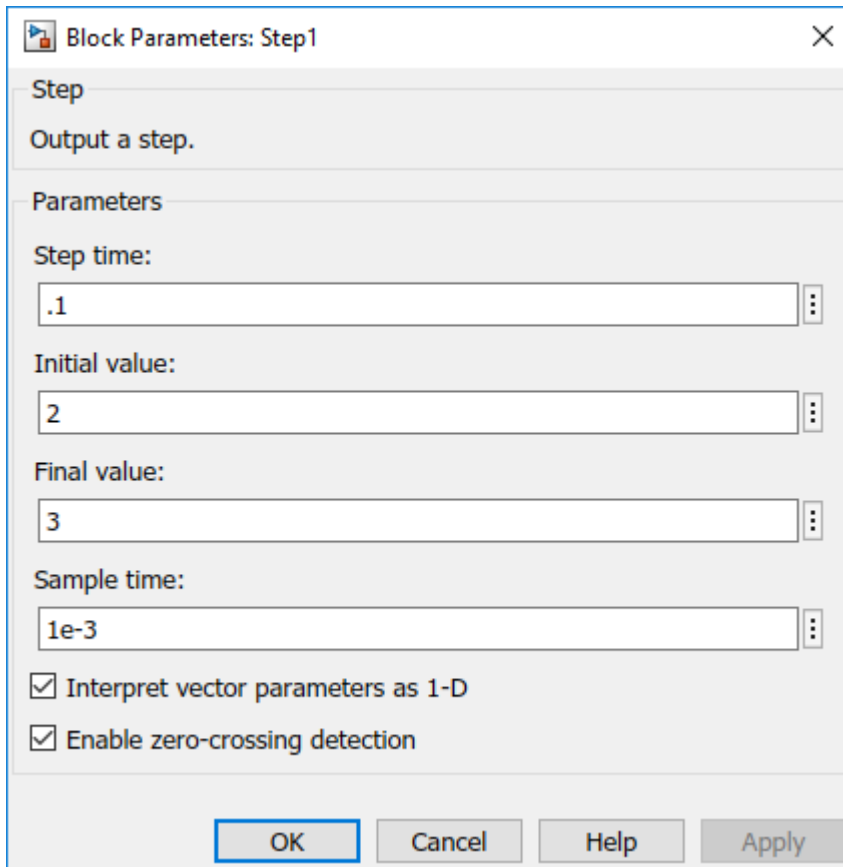
To open the Simscape HDL Workflow Advisor at the command-line, enter:
sschdladvisor(nonlinearModel)

Run the workflow to the Get state-space parameters task. This task fails because of the presence of the Piecewise-Constant Resistor.
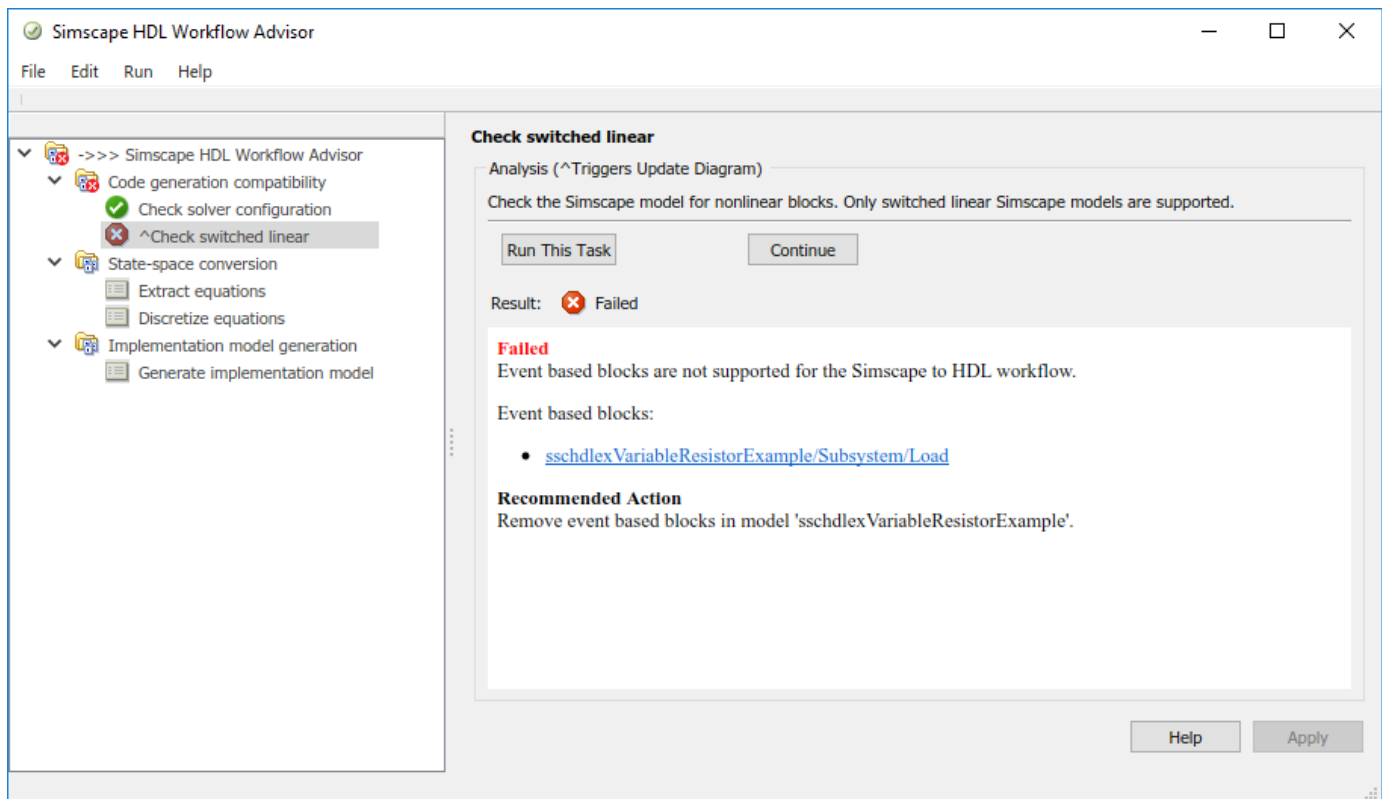
Replace Variable Resistor with switches and constant resistors.

To convert this model to an equivalent switched linear model, replace the Piecewise-Constant Resistor with a set of switches and resistors for each desired value. To open the switched linear version in the MATLAB® command prompt, enter:

```
switchedLinearModel = 'sschdlexVariableResistorSwitchedLinearExample';
load_system(switchedLinearModel)
open_system(switchedLinearModel)
```
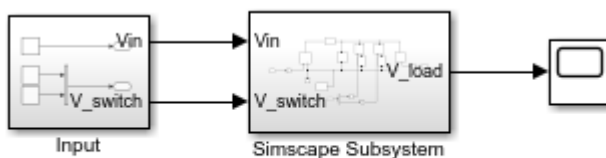

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The variable resistor has been replaced by a resistor and switch for each desired resistance. To recreate the behavior of a load resistance that changes from 2 Ω to 3 Ω at t=0.1 two resistors are used, one with a resistance of 2 Ω and the other with a resistance of 3 Ω. By closing and opening the switches the load resistance switches from 2 Ω to 3 Ω.

Controlling the Switches

The switches must be turned on and off to provide the correct load resistance. To view the control signals for the switches in the MATLAB® command prompt, enter:

```
open_system([switchedLinearModel,'/Input'])
```
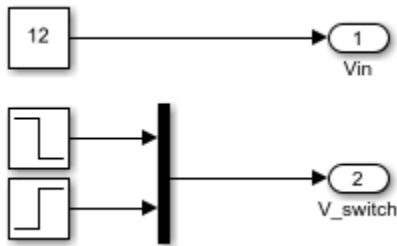

To achieve the correct resistance, create two step functions. One to open the switch in series with the 2 Ω resistor at t=0.1 and another to close the switch in series with the 3 Ω resistor at the same time.

Now that the variable resistor has been replaced with switched linear components run the Simscape HDL Workflow Advisor and see that all the tasks run to completion.
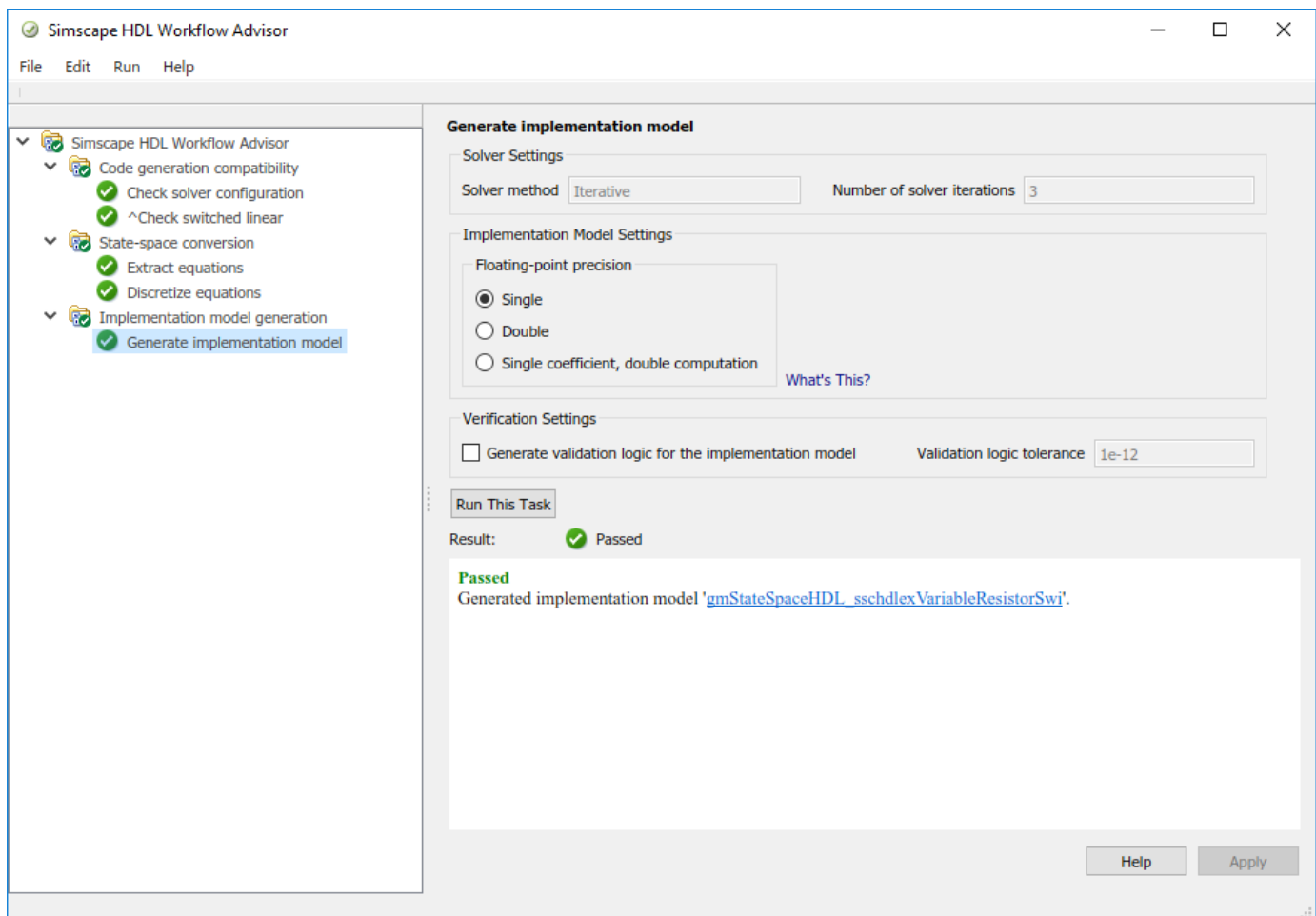
By changing the variable piecewise resistor to a number of specified resistors that switch on and off the model has been changed to a form that is compatible with the Simscape to HDL workflow.

Hardware-in-the-Loop Implementation of Simscape Model on Speedgoat FPGA I/O Modules

This example shows how to synthesize and generate FPGA bitstream from a Simscape™ half-wave rectifier model and download the bitstream to a Speedgoat FPGA I/O 334-325K target for Hardware-in-the-Loop (HIL) implementation.

Hardware-in-the-Loop Workflow

1. Generate a HDL implementation model from the Simscape model by using the Simscape HDL Workflow Advisor. The HDL implementation model is a Simulink® model that replaces the Simscape algorithm with HDL-compatible blocks

2. Generate FPGA bitstream for the HDL implementation model by using the HDL Workflow Advisor

3. Download the bitstream to the Speedgoat FPGA I/O module by using the Simulink Real-Time Explorer for Hardware-in-the-Loop Simulation.

Half Wave Rectifier Model

Open the Simscape half wave rectifier model. In the MATLAB® command prompt, enter:

```
ModelName = 'sschdlexHalfWaveRectifierExample';
open_system(ModelName)
set_param(ModelName, 'SimulationCommand', 'update');
```


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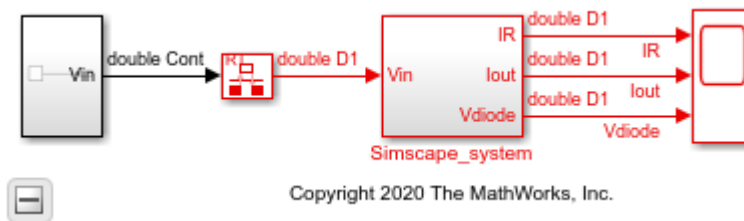
```
open_system([ModelName, '/Simscape_system'])
```
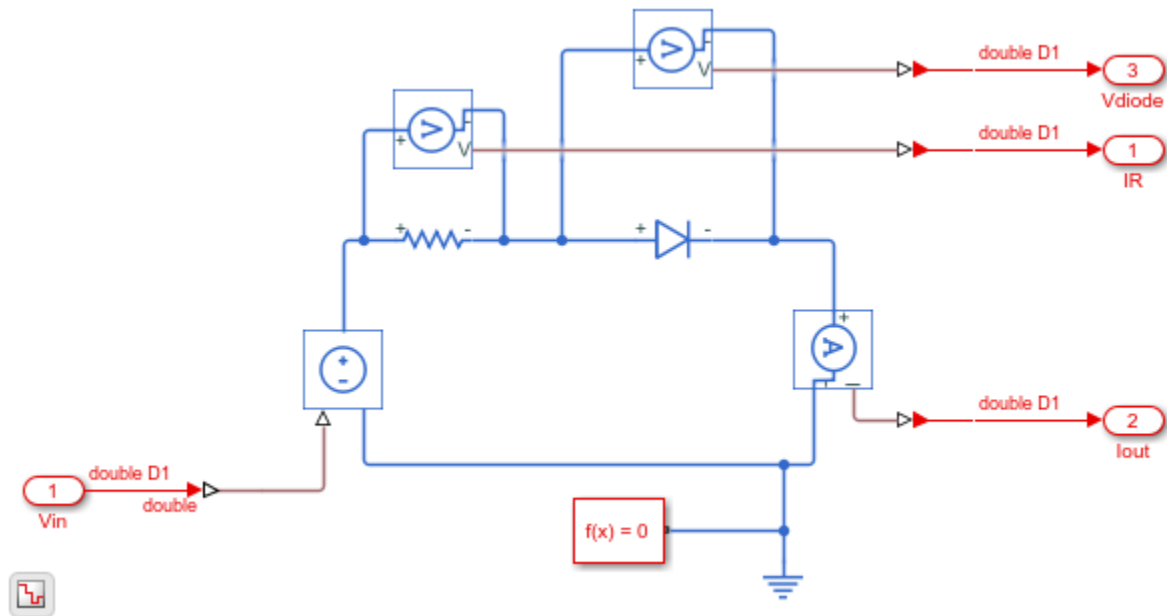
The half-wave rectifier consists of a Resistor, which is a linear block, and a Diode, which is a switched linear block. At the input and output port interfaces, the model has Simulink-PS Converter and PS-Simulink Converter blocks. The solver settings are configured for compatibility with Simscape HDL Workflow Advisor. If you open the Block Parameters dialog box for the Solver Configuration block, **Use local solver** is selected and `Backward Euler` is specified as the **Solver type**. See "Get Started with Simscape Hardware-in-the-Loop Workflow" (HDL Coder).

To see the algorithm functionality, simulate the model.

```
sim(ModelName)
open_system([ModelName, '/Scope'])
```
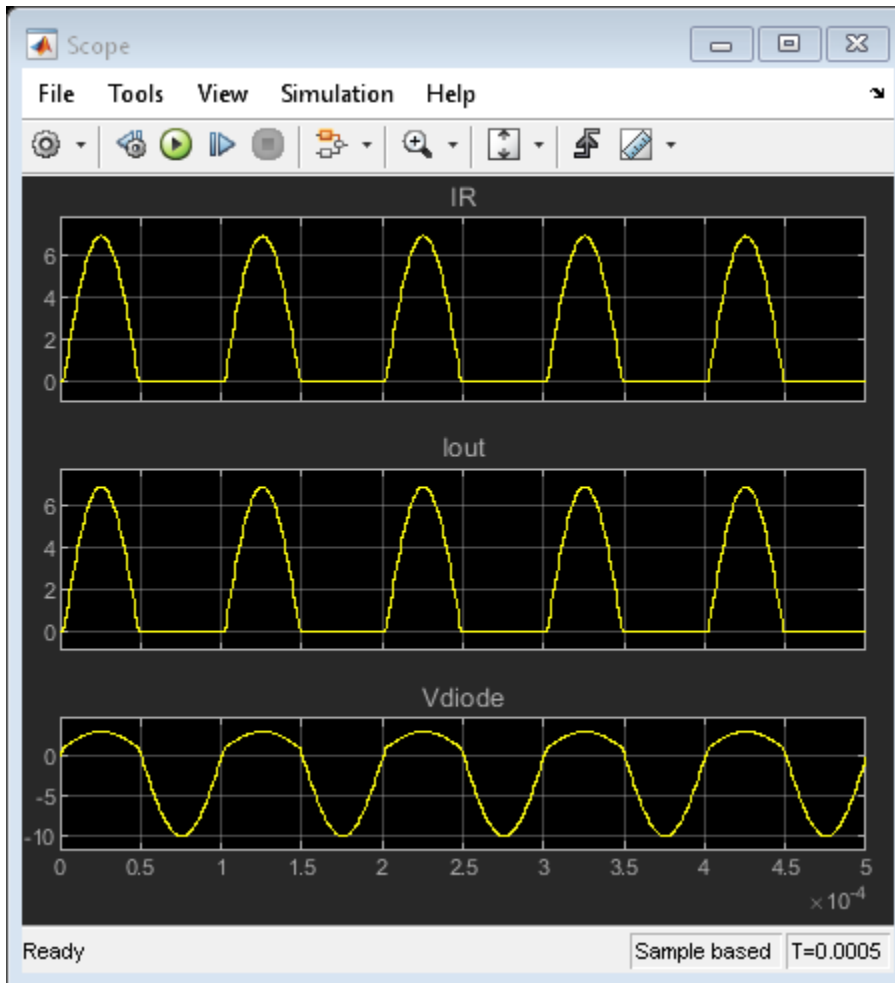
2. Configure the Simscape Model for HDL compatibility by using the `hdlsetup` function:

```
hdlsetup('sschdlexHalfWaveRectifierExample')
```

Generate HDL Implementation Model

To generate the HDL implementation model:

1. Open the Simscape HDL Workflow Advisor:

```
sschdladvisor('sschdlexHalfWaveRectifierExample')
```

2. To compare functionality of the HDL implementation model with the original Simscape algorithm, select the **Generate implementation model** step, and then select the **Generate valiadtion logic for the implementation model** check box. Use a **Validation logic tolerance** of `0.001`. Right-click the **Generate implementation model** step and select **Run to Selected Task**.

The Advisor generates an HDL implementation model and a state-space validation model. To compare functionality of the HDL implementation model with the original Simscape algorithm, open and simulate the state-space validation model. The output of this model matches the original Simscape model. For a more systemic verification, see "Validate HDL Implementation Model to Simscape Algorithm" (HDL Coder).

See also "Simscape HDL Workflow Advisor Tasks" (HDL Coder).

Setup and Configuration

The Speedgoat IO334-325K FPGA module uses Xilinx® Vivado® and *IP Core Generation* workflow infrastructure. Before you deploy the HDL implementation model on the Speedgoat IO module:

1. **Install Xilinx Vivado and Setup Tool Path**

Install the latest version of Xilinx® Vivado® as listed in "HDL Language Support and Supported Third-Party Tools and Hardware" (HDL Coder). Then, set the tool path to the installed Xilinx Vivado executable by using the `hdlsetuptoolpath` (HDL Coder) function.

```
hdlsetuptoolpath('ToolName','Xilinx Vivado','ToolPath','C:\Xilinx\Vivado\2019.2\bin\vivado.bat')
```

2. **Install Speedgoat Library and Speedgoat - HDL Coder Integration Packages**

Install the Speedgoat Library and the Speedgoat - HDL Coder Integration packages. See Install Speedgoat HCIP.

3. **Setup I/O Module**

For real-time simulation, set up the I/O module. See Xilinx HDL Software for Speedgoat I/O Hardware.

HDL Workflow Advisor

The HDL Workflow Advisor guides you through HDL code generation and the FPGA design process. Use the Advisor to:

- Check the model for HDL code generation compatibility and fix incompatible settings.
- Generate HDL code, test bench, and scripts to build and run the code and test bench.
- Perform synthesis and timing analysis.
- Deploy the generated code on SoCs, FPGAs, and Speedgoat I/O modules.

To open the HDL Workflow Advisor, use the `hdladvisor` (HDL Coder) function.

```
hdladvisor('gmStateSpaceHDL_sschdlexHalfWaveRectifierEx/Simscape_system/HDL Subsystem')
```

The left pane contains folders that represent a group of related tasks. Expanding the folders and selecting a task displays information about that task in the right pane. The right pane can contain simple controls for running the task to advanced parameters and option settings that control code and test bench generation. To learn more about each task, right-click that task, and select **What's This?**. See "Getting Started with the HDL Workflow Advisor" (HDL Coder).

Generate FPGA Bitstream for Speedgoat Platform

1. Open the HDL implementation model, and then open the HDL Workflow Advisor for the implementation model.

```
open_system('gmStateSpaceHDL_sschdlexHalfWaveRectifierEx')
hdladvisor('gmStateSpaceHDL_sschdlexHalfWaveRectifierEx/HDL Subsystem')
```

2. In **Set Target Device and Synthesis Tool** task, specify **Target workflow** as `Simulink Real-Time FPGA I/O` and **Target platform** as `Speedgoat IO334-325K`.

3. In the **Set Target Reference Design** task, select a value of x4 for the parameter PCIe lanes, and select **Run This Task**.

4. In **Set Target Interface** task, map the input and output single data type ports to PCIe Interface and select **Run This Task**.

5. In the **Set Target Frequency** task, set the **Target Frequency (MHz)** as 100.

6. Right-click the **Generate Simulink Real-Time Interface** task and select **Run to Selected Task** to generate the HDL IP core, FPGA bitstream, and download the bitstream onto the Speedgoat IO334 target.

A Simulink Real-Time Interface model is generated, and named as **gm_gmStateSpaceHDL_sschdlexHalfWaveRectifierEx_slrt**.
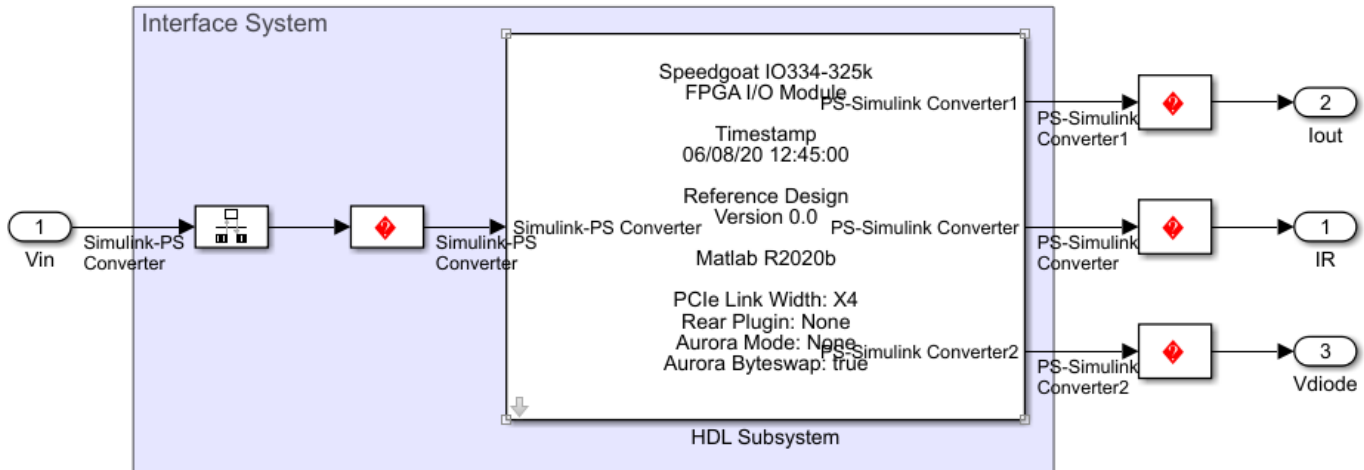
For rapid prototyping, you can export the Workflow Advisor settings to a script. The script is a MATLAB file that you run from the command line. You can modify and run the script, or import the settings into the HDL Workflow Advisor User Interface. To save the workflow, in the HDL Workflow Advisor User Interface, select **File > Export to Script**. Save the file as hdlworkflow_slrt_IO334.m.

To import this file, in the HDL Workflow Advisor User Interface, select **File > Import from Script**. In the Import Workflow Configuration dialog box, select the hdlworkflow_slrt_IO334.m file. The HDL Workflow Advisor updates the tasks according to the imported script. See "Run HDL Workflow with a Script" (HDL Coder).

Deploy Bitstream to Speedgoat IO334-325k Target

1. **Connect Development Computer to Target**

Connect the development computer to the target by using a cross-over network cable. The Speedgoat Target IP address is 10.10.10.15. Set the IP address of the communication link between the development computer and target computer to a value 10.10.10.12 because the communication link must be in the same network.

2. **Setup and Configure Simulink Real-Time Explorer**

You download the bitstream by using the Simulink Real-Time Explorer. To open the Simulink Real-Time Explorer, enter the command slrtExplorer. Alternatively, you can open the Explorer from the **REAL-TIME** tab of the Simulink Toolstrip.

slrtExplorer

a. In the **TARGET** pane click the **Add Target** button, and then click the **Properties** button on the toolbar. In the **Target Properties** Workspace, click **Host-to-Target Communication**.

• Set **IP Address** as 10.10.10.15, **Port** as 22222, **Subnet mask** as 255.255.255.0, and **Gateway** as 10.10.10.10.

• Set **Target driver** as Auto and **Bus type** as PCI.

b. In the **Target Properties** Workplace, click *Target Settings.

- Select **USB Support** and **Graphics mode**.
- Click **Boot Configuration** in **Target Properties** workspace.
- Select **Boot mode** as `Network` and click on **Create boot disk**. The Target MAC address appears in the **MAC address** field.

Save the configuration by clicking the **save** button.

3. **Create Real-Time Application**

Open the Simulink Real-Time Interface model. Add a Scope block to the model and connect it to the outputs. Log the output signals to view the simuation results on the Simulation Data Inspector.
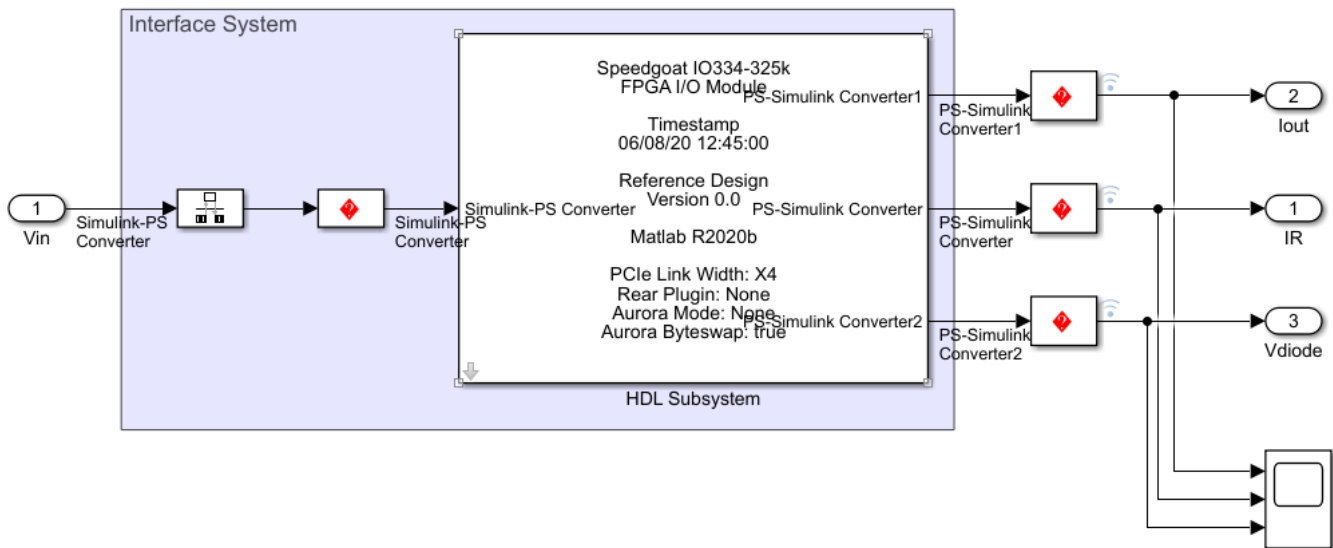
4. **Build and Run Real-Time Application**

Click the **Run on Target** button on the **REAL_TIME** tab to compile and download the model onto Speedgoat IO334-325k target.

A target object name `tg` is created in the MATLAB workspace and the model is run on the target. Observe the output simulation results on the Simulation Data Inspector. The simulation results of the downloaded model match the original Simscape model simulation.

Validate HDL Implementation Model to Simscape Algorithm

If you design your algorithm by using Simscape switched linear blocks, you can run the Simscape HDL Workflow Advisor to generate an HDL implementation model. The HDL implementation model represents the Simscape algorithm by using Simulink blocks that are compatible for HDL code generation.

Before you prototype the implementation model on an FPGA or target Speedgoat FPGA I/O modules, you can verify the functionality of your design in the Simulink modeling environment. To verify the functionality, specify insertion of validation logic in the HDL implementation model when you run the Simscape HDL Workflow Advisor. This logic verifies whether the numeric results of the HDL implementation model match the original Simscape algorithm.

In some cases, there can be a mismatch in simulation results between the Simscape algorithm and the corresponding HDL implementation. Such mismatches generate warnings or assertions when you simulate the implementation model. To resolve the warnings, use a combination of various settings in the **Generate implementation model** task as illustrated below.

Bridge Rectifier Model

This example uses the bridge rectifier model to illustrate how to generate an implementation model with validation logic inserted in the model, and how you can resolve any assertions that may be generated when you simulate the implementation model.

1   Open the bridge rectifier model. In the MATLAB Command Window, enter:

```
open_system('sschdlexBridgeRectifierExample')
open_system('sschdlexBridgeRectifierExample/Simscape_system')
```

Inside the `Simscape_system`, you see four diodes arranged in a bridge configuration. For both positive and negative input values, this configuration provides a positive, rectified output.
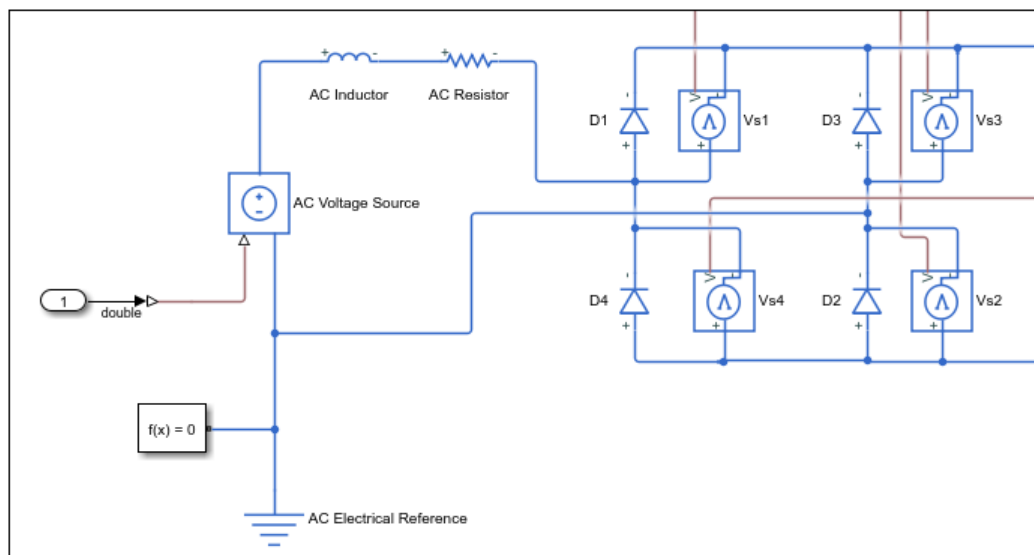
2   Open the Simscape HDL Workflow Advisor for your model:

```
sschdladvisor('sschdlexBridgeRectifierExample')
```

3 Right-click the **Get state-space parameters** task and select **Run to Selected Task** to run all tasks in the Advisor except for the **Generate implementation model** task.

4 In the **Generate implementation model** task, select the **Generate validation logic for the implementation model** check box. Leave the other options with their default values and select **Run This Task**.

Generate implementation model

Solver Settings

Solver method: Iterative   Number of solver iterations 3   How to Change This?

Implementation Model Settings

Floating-point precision

◉ Single
○ Double   Map state space parameters to RAMs Auto ▾
○ Single coefficient, double computation   What's This?

Verification Settings

☑ Generate validation logic for the implementation model   Validation logic tolerance 1e-12

Run This Task

Result: ✅ Passed

After running this task, keep the UI window for this task open. If simulating the HDL implementation model generates warnings, you modify the settings in the **Generate implementation model** task and then rerun this task. You do not have to modify or rerun other tasks.

5 Click the link to open the HDL implementation model. You see a `Validation` Subsystem that compares the simulation results of the Simscape model to the HDL implementation model. Simulate the implementation model.
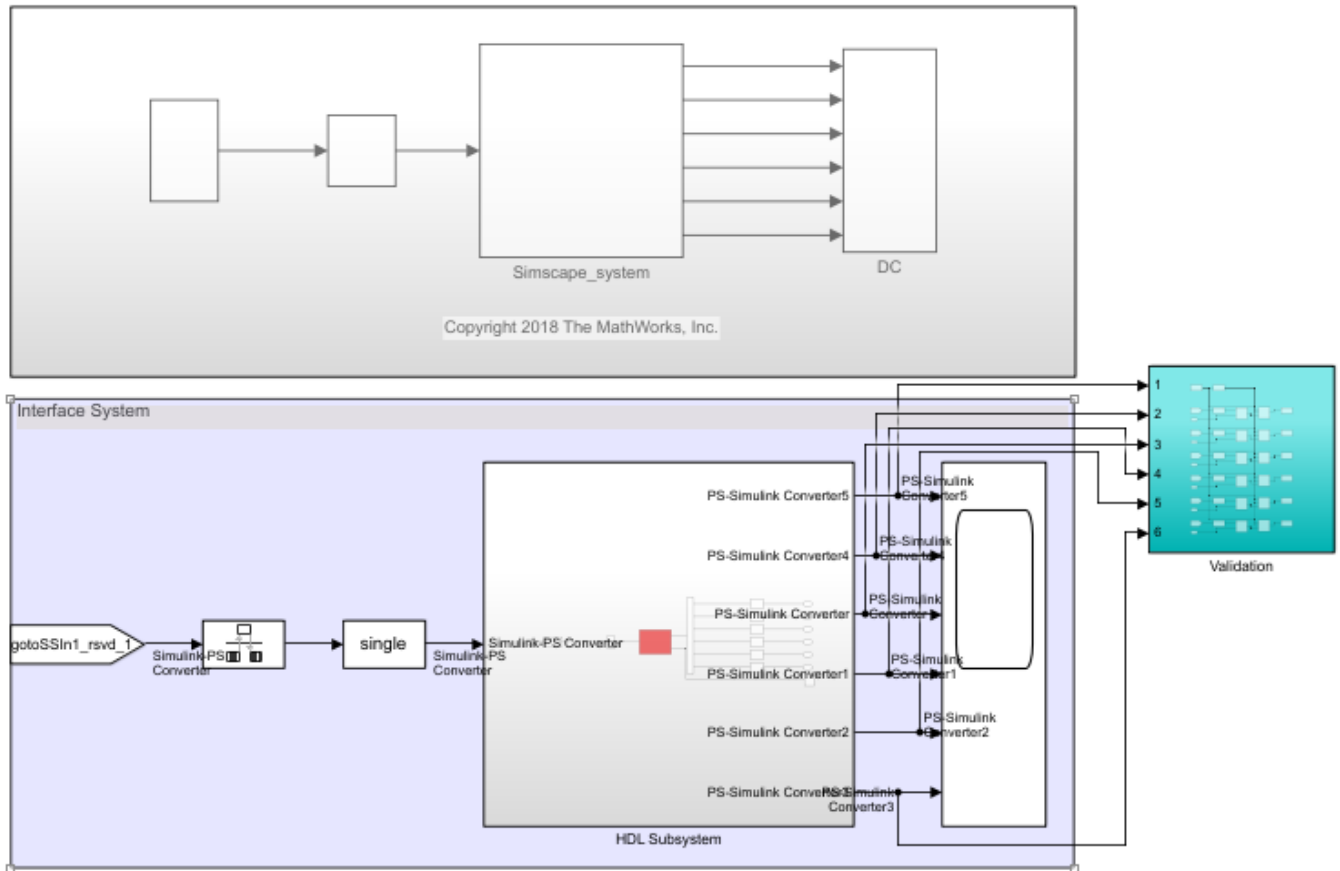
You see that simulating the model generates multiple assertions indicating a mismatch in the simulation results. If you open the Diagnostic Viewer, you see this message:

```
Assertion detected in 'gmStateSpaceHDL_BridgeRectifier_HDL_SimMismatch/
Validation/Check Static Range1' at time 0.04186 [4982 similar]
```

The message indicates that the Simscape™ algorithm does not match the equivalent HDL implementation. To resolve the validation mismatch, you can modify various settings in the **Generate implementation model** task until the HDL implementation model matches the Simscape algorithm. In most cases, to resolve the numeric mismatch, you may want to use a combination of these settings.

Increase Validation Logic Tolerance

Conversion of a Simscape algorithm to an equivalent HDL implementation leads to rounding errors. The default tolerance value is relatively small and can be difficult to achieve especially with single-precision data types in the HDL implementation model. To resolve the mismatch:

1 Start by increasing the **Validation logic tolerance** to an initial value such as 1e-4.
2 Select **Generate validation logic for the implementation model** and run the task to generate the HDL implementation model that includes validation logic.
3 Simulate the model and check whether the simulation displays assertions in the Diagnostic Viewer. If the simulation results produce warnings, proceed to the next step to increase the number of solver iterations.

Increase Number of Solver Iterations

For each mode in the physical system, the switched linear workflow arrives at a state-space representation. The solver method is iterative and performs multiple computations to determine the correct mode for the next time step. After a certain number of iterations, the output value from the next time step becomes the same as the value from the previous time step. This consistency in the output value indicates the correct number of solver iterations.

The Advisor by default chooses an optimal value for the number of solver iterations. See "Using Number of Solver Iterations" (HDL Coder). If increasing the tolerance value does not improve accuracy of the HDL implementation model, you can resolve the numeric mismatch by increasing the number of solver iterations.

When you increase the number of solver iterations, the code generator changes the sample time of the generated HDL implementation model. A large number of iterations can increase the simulation time significantly. See "Reducing Number of Solver Iterations" (HDL Coder). This flowchart illustrates how to change the **Number of solver iterations**.
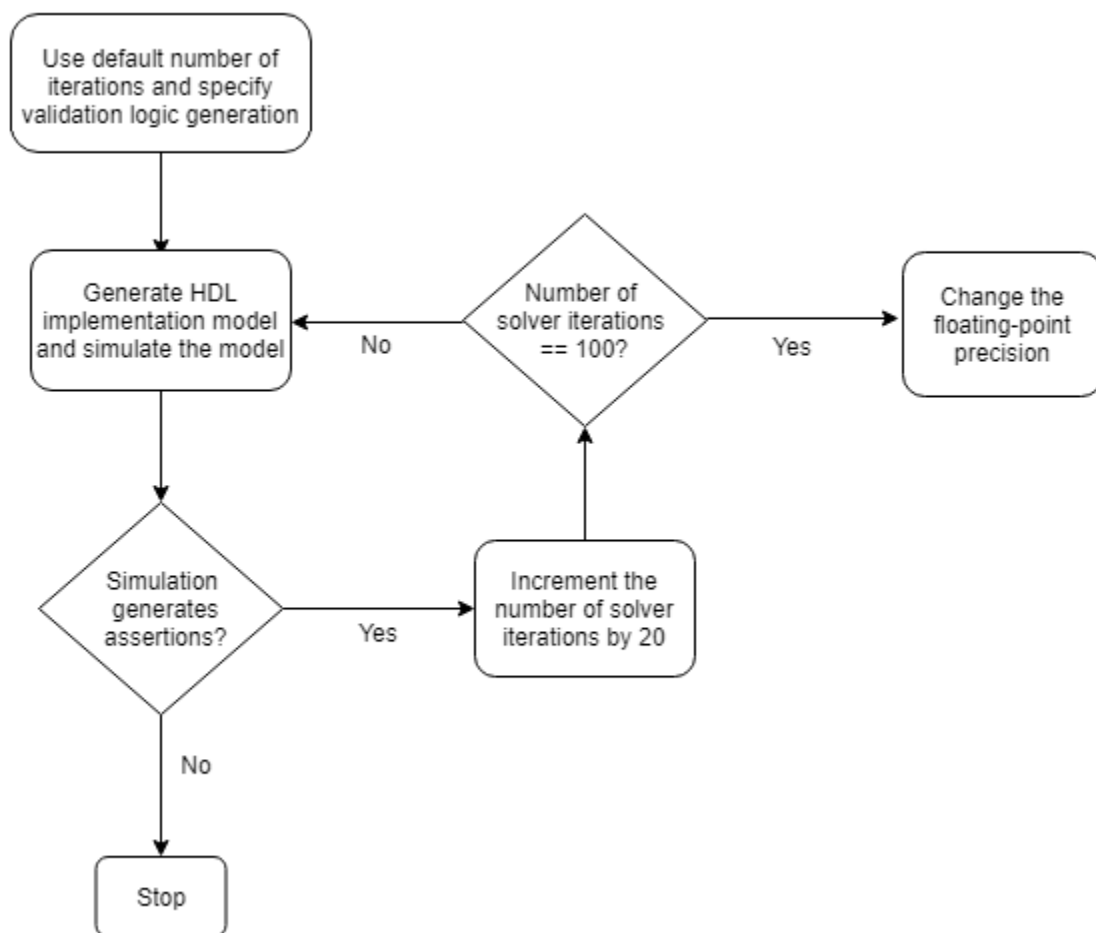
Use Larger Floating-Point Precision

You can use the **Floating-point precision** setting in the **Generate implementation model** task to specify the floating-point data type you want to use for the algorithm inside the HDL Subsystem. Specify whether you want to store the matrix coefficients in `single` or `double` data types and whether to use `single` or `double` when performing the computations.

Floating-Point Precision	Description
Double	Using `double` floating-point precision increases the numerical accuracy of the generated model and the maximum achievable target frequency. However, the area consumption and pipeline latency are also increased.
Single	This is the default setting for floating-point precision.
Single coefficient, double computation	This mode offers a tradeoff between `Single` and `Double` modes of floating-point precision. To save memory usage, the coefficients that are stored in `single`. The matrix computations are then performed in `double` for improved accuracy.

This flowchart illustrates how to change the **Floating Point Precision** and improve the numeric accuracy of the generated HDL implementation model.
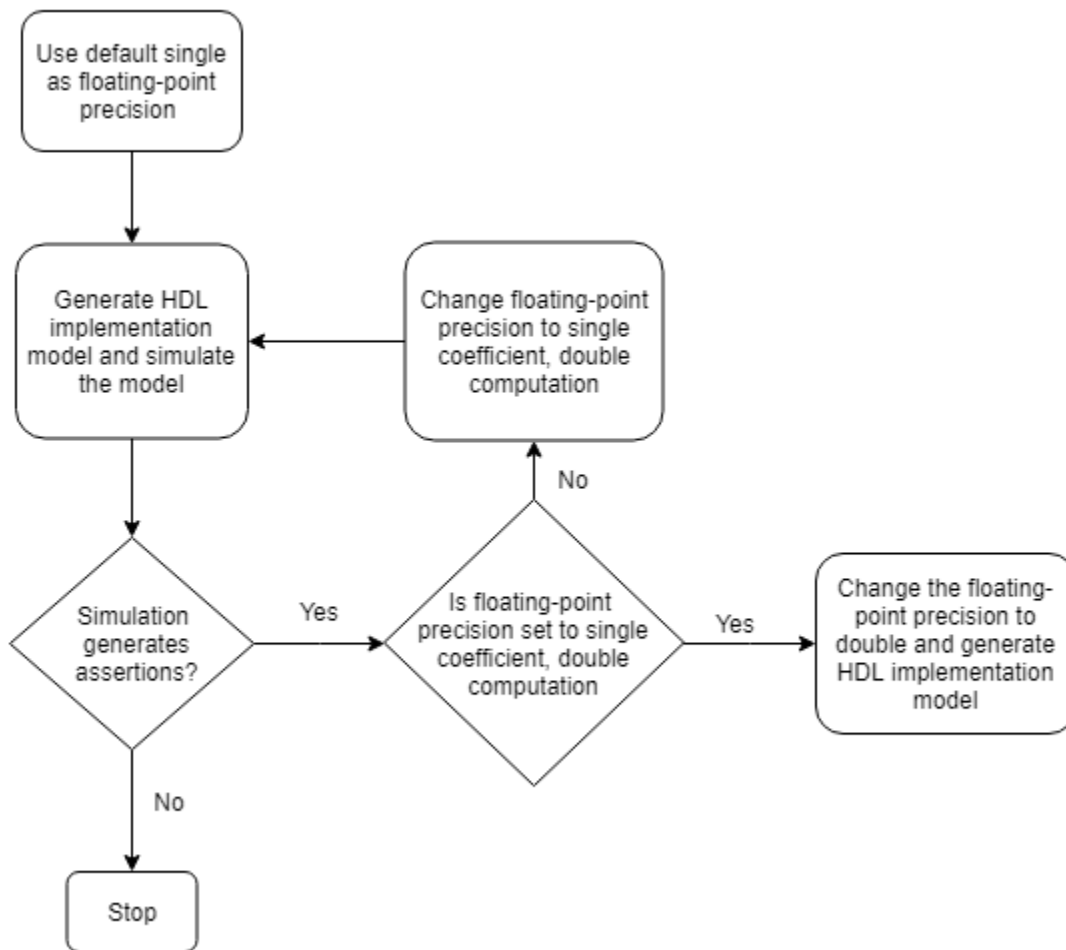
Note Double-precision operations have large latencies and require a large **Oversampling factor** to allocate sufficient delays for the floating-point operations, which reduces the sampling frequency. For a tradeoff between accuracy and precision, use `Single coefficient, double computation` as the **Floating Point Precision**.

After specifying double data types, if the simulation results still produce warnings:

1 Proceed to the first step to further increase the validation logic tolerance. Use a tolerance value of `1e-03` and then simulate the model to see if the numeric accuracy requirements are met.

2 Increase the number of solver iterations if you still see warnings in the Diagnostic Viewer. Continue iterating between these steps till the HDL implementation model numerically matches the Simscape algorithm.

For the bridge rectifier model, to resolve the warnings, set the **Validation logic tolerance** to `1e-4` and specify the **Floating Point Precision** as `double`. After you generate the implementation model with the validation logic, you see that simulating the model does not display warnings in the Diagnostic Viewer.

See Also

Functions
`simscape.findNonlinearBlocks` | `sschdladvisor`

More About

- "Generate HDL Code for Simscape Models" (HDL Coder)
- "Simscape HDL Workflow Advisor Tasks" (HDL Coder)
- "Simscape HDL Workflow Advisor Tips and Guidelines" (HDL Coder)
- "Generate Simulink Real-Time Interface Subsystem for Simscape Two-Level Converter Model" (HDL Coder)

Improve Sampling Rate of HDL Implementation Model Generated from Simscape Algorithm

If you design your algorithm by using Simscape switched linear blocks, you can run the Simscape HDL Workflow Advisor to generate an HDL implementation model. When you open the HDL implementation model, you see the HDL algorithm that models the state-space representation by using Simulink blocks that are compatible for HDL code generation. To learn more about the Simscape HDL Workflow Advisor, see "Simscape HDL Workflow Advisor Tasks" (HDL Coder).

Sampling Frequency

When you generate HDL code and deploy the plant model onto an FPGA, you may want to improve the sampling frequency. The sampling frequency depends on these parameters:

- FPGA clock frequency
- Oversampling factor
- Number of solver iterations

$$Sampling\ Frequency = FPGA\ clock\ frequency\ /\ (Oversampling\ factor\ *\ Number\ of\ solver\ iterations)$$

To improve the sampling rate, you want to maximize the FPGA clock frequency, and minimize the oversampling factor and number of solver iterations. As you improve the sampling rate, make sure that the updated sampling frequency is equivalent to the fixed sample time that you specify for your original Simscape model by using the Solver Configuration block. To learn more about how this block is used in your model before running the Simscape HDL Workflow Advisor, see "Generate HDL Code for Simscape Models" (HDL Coder).

The preceding section uses the boost converter model as an example to illustrate how you can modify the oversampling factor and the number of solver iterations to improve the sampling rate.

Boost Converter Model

This example uses the boost converter model to illustrate the change in sample time in the generated HDL implementation model and the oversampling factor that is saved on the model.

1   Open the boost converter model. To learn how the boost converter is implemented, open the `Simscape_system` Subsystem. To open the boost converter model, in the MATLAB Command Window, enter:

```
open_system('sschdlexBoostConverterExample')
open_system('sschdlexBoostConverterExample/Simscape_system')
```
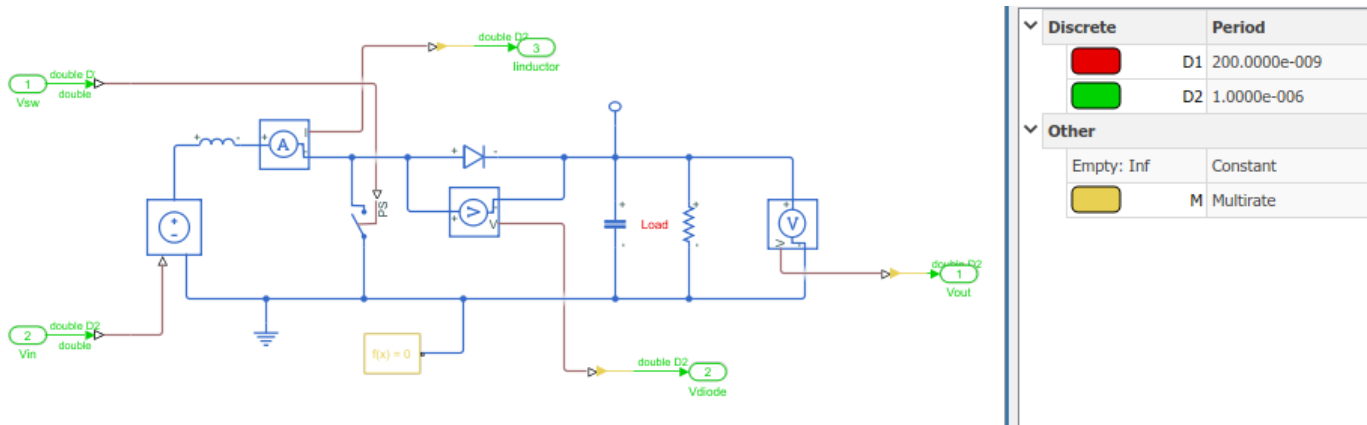
You see that the model runs at a sample time `1e-6`. The sample time of `200e-9` corresponds to the sample time of the sources that drive the Simscape algorithm.

2   Open the Simscape HDL Workflow Advisor for your model:

    `sschdladvisor('sschdlexBoostConverterExample')`

3   Run the workflow to the **Generate implementation model** task.

    After running this task, you see a link to the generated HDL implementation model. Click the link to open the HDL implementation model.

4   Simulate the HDL implementation model. When you navigate the model to the `HDL Algorithm` Subsystem, you see that the model uses `single` data types and runs at a sample time `200e-9`, which is 5 times faster than the original Simscape model.
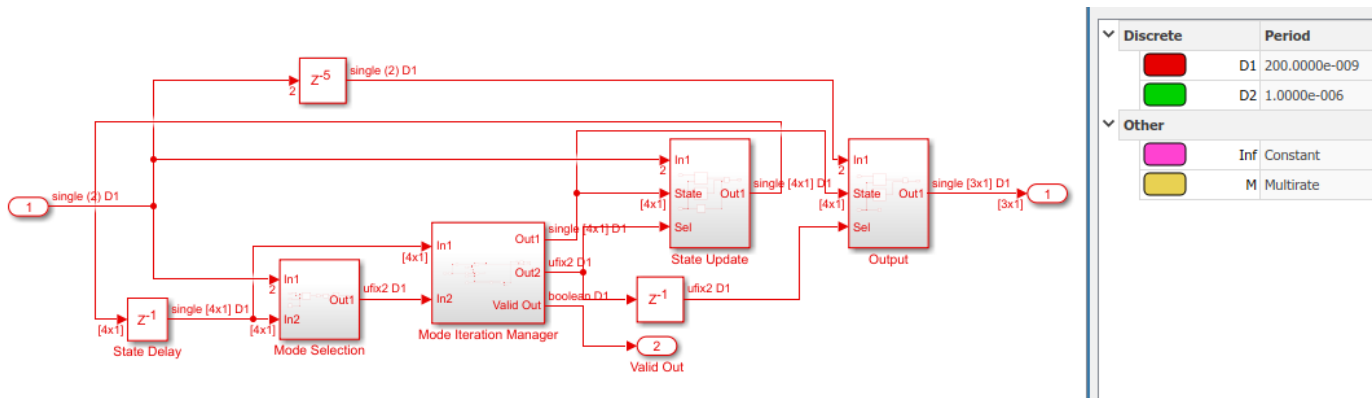
5   Run this command to see the HDL parameter settings that are saved on the model:

    `hdlsaveparams('gmStateSpaceHDL_sschdlexBoostConverterExamp')`

```
%% Set Model 'gmStateSpaceHDL_BoostConverter_HDL' HDL parameters
hdlset_param('gmStateSpaceHDL_BoostConverter_HDL', 'FloatingPointTargetConfiguration', ...
    hdlcoder.createFloatingPointTargetConfig('NativeFloatingPoint' ...
  , 'LatencyStrategy', 'MIN') ...
);
hdlset_param('gmStateSpaceHDL_BoostConverter_HDL', 'HDLSubsystem', ...
                                    'gmStateSpaceHDL_BoostConverter_HDL');
hdlset_param('gmStateSpaceHDL_BoostConverter_HDL', 'MaskParameterAsGeneric', 'on');
hdlset_param('gmStateSpaceHDL_BoostConverter_HDL', 'Oversampling', 60);

% Set SubSystem HDL parameters
hdlset_param('gmStateSpaceHDL_BoostConverter_HDL/HDL Subsystem', 'FlattenHierarchy', 'on');
```

```
% Set SubSystem HDL parameters
hdlset_param('gmStateSpaceHDL_BoostConverter_HDL/HDL Subsystem/HDL Algorithm/State Update/Multiply State', ...
'SharingFactor', 1);
```

The HDL parameters that are saved indicate that the model has the native floating-point mode enabled and uses an **Oversampling factor** of 60 and has **Latency Strategy** set to MIN. This default values chosen for number of solver iterations and combination of HDL parameters offers an optimal trade-off between oversampling factor and the target FPGA clock frequency and improves the sampling frequency. To further improve the sampling frequency, reduce the number of iterations and the oversampling factor.

Reducing Number of Solver Iterations

For each mode in the physical system, the switched linear workflow arrives at a state-space representation. The solver method is iterative and performs multiple computations to determine the correct mode for the next time step. After a certain number of iterations, the output value from the next time step becomes the same as the value from the previous time step. This consistency in the output value indicates the correct number of solver iterations.

The Advisor by default chooses an optimal value for the number of solver iterations. See "Using Number of Solver Iterations" (HDL Coder). To improve the sampling rate, reduce the number of solver iterations. The number of solver iterations depends on various factors such as the complexity of your design, the number of modes in the design that the workflow calculates, and so on.

In the **Generate implementation model** task of the Simscape HDL Workflow Advisor:

1 Start by reducing the **Number of solver iterations** to a value such as 3
2 Select **Generate validation logic for the implementation model**, and then generate the HDL implementation model.
3 Simulate the HDL implementation model and open the Diagnostic Viewer to verify that the model does not display warnings or assertions.

If you see warnings or assertions, it indicates a simulation mismatch because the number of solver iterations that you specified is not adequate to compute the required number of modes in the state-space design. Resolve the mismatch by increasing the validation logic tolerance value or the number of solver iterations. Changing **Floating-point precision** to double is not recommended. Double-precision operations have large latencies and require a large **Oversampling factor** to allocate sufficient delays, which reduces the sampling frequency. See "Validate HDL Implementation Model to Simscape Algorithm" (HDL Coder).

Using Oversampling Factor and Latency Strategy

The **Oversampling factor** specifies the factor by which the FPGA clock rate is a multiple of the HDL implementation model base sample rate. The HDL implementation model contains feedback loops and performs multiplication of large matrices that have floating-point data types inside the feedback loops. To accommodate the large latency introduced by these floating-point operations inside the feedback loops, the code generator uses a large value of oversampling factor in conjunction with the clock-rate pipelining optimization on the model. For more information, see "Strategy 1: Global Oversampling" (HDL Coder).

You vary the oversampling factor and latency strategy of the floating-point operator in conjunction. The default oversampling factor of 60 and minimum latency strategy gives an optimal sampling

frequency. To achieve the maximum FPGA clock frequency, use the maximum latency strategy. When you specify this latency strategy, the floating-point operations introduce the maximum number of delays. To allocate these delays, increase the oversampling factor. If the increase in FPGA clock frequency outweighs the increase in oversampling factor, you achieve a higher sampling frequency.

To change the latency strategy and oversampling factor in conjunction from the Configuration parameters dialog box:

1   On the **HDL Code Generation** > **Floating Point** pane, change the **Latency Strategy** to Max .
2   On the **HDL Code Generation** > **Global Settings** pane, increase the **Oversampling factor** to a value such as 100 depending on the complexity of your HDL design.

For the boost converter model, the default settings of **Number of solver iterations** set to 5, **Oversampling factor** set to 60, and **Latency Strategy** set to Min provides the optimal sampling frequency.

See Also

Functions
simscape.findNonlinearBlocks | sschdladvisor

More About

*   "Solvers for Real-Time Simulation"
*   "Simscape HDL Workflow Advisor Tips and Guidelines" (HDL Coder)
*   "Latency Considerations with Native Floating Point" (HDL Coder)
*   "Generate Simulink Real-Time Interface Subsystem for Simscape Two-Level Converter Model" (HDL Coder)

Examples

Tolerance Study Using Monte Carlo Simulations in Resonant LLC DC-DC Converter

This example shows how to use Simscape™ Electrical™ to perform a Monte Carlo analysis to optimize the design of an LLC resonant DC-DC converter when some of its components have tolerances.

Motivation

When designing an electrical system, some components might possess certain tolerance on some key parameters (for example the inductance of an inductor). Looser tolerance components usually have a lower cost, so it is cost-effective to choose them.

However, component tolerance impacts system performance, introducing a variability on some key specifications. This example shows you how to choose design options that minimize the variability of the entire system.

In this example, an LLC resonant DC-DC converter comprises capacitors and inductors with a given tolerance, and there are several choices for design parameters that provide the desired gain output. The goal is to find which of these design choices have the minimum output gain tolerance, or global system variability.

Monte Carlo Method

The components' statistical variability influences the global system statistical variability. Monte Carlo method consists of inferring an estimation of this influence by statistical repetition. A given known distribution generates the component parameter values and then a simulation is performed to obtain system level results. Simulations are repeated many times. If the number of simulations is large enough, the system output distribution is reasonably well inferred.

Model
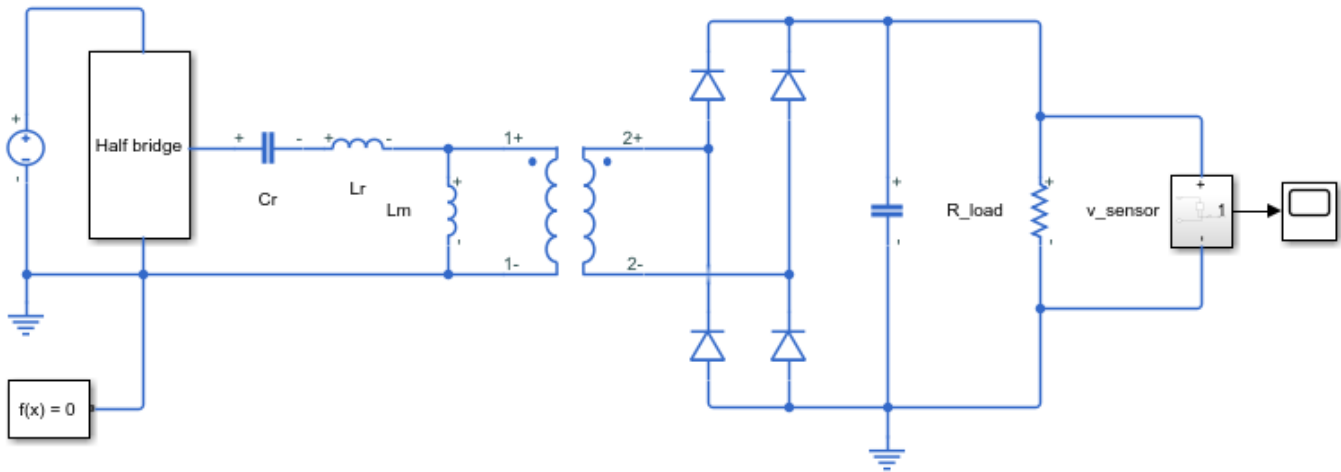
Tolerance Study Using Monte Carlo Simulations in Resonant LLC DC-DC Converter

1. Run Monte Carlo study over inductance ratio (see code)
2. Run Monte Carlo study over inductance ratio and quality factor (computationally intensive) (see code)
3. Explore simulation results using sscexplore
4. Learn more about this example

In this particular design, the control system has been chosen to be very simple: a fixed switching frequency in open-loop.

Theoretical Approximation for Output Voltage Gain

For this converter, a first harmonic approximation to compute output voltage gain from design parameters is:

$$M = \frac{1}{2\sqrt{\left(1 + L_N - \frac{L_N}{f_N^2}\right)^2 + Q^2\left(f_N - \frac{1}{f_N}\right)^2}}$$

in which L_N and Q are the inductance ratio and quality factor, which directly depend on the inductances and capacitance of the components of interest in this study.

We will use this formula to compute an approximation to the expected output gain when components are perfect (do not have any tolerance).

Specifying Component Tolerance

You can specify the component tolerances in the block mask. For example, in the inductor, you can apply a tolerance value with a uniform distribution on a certain range of inductances.

Block Parameters: Lr ✕

Inductor

This block models an inductor with optional tolerance, operational limits and fault modeling.

Source code

Settings

| Main | Operating Limits | Faults | Variables |

Inductance:	Lr	H ⌄
Tolerance (%):	Lr_tol	
Tolerance application:	Random tolerance ▾
Tolerance distribution:	Uniform ▾
Series resistance:	0	Ohm ⌄
Parallel conductance:	1e-9	1/Ohm ⌄

OK   Cancel   Help   Apply
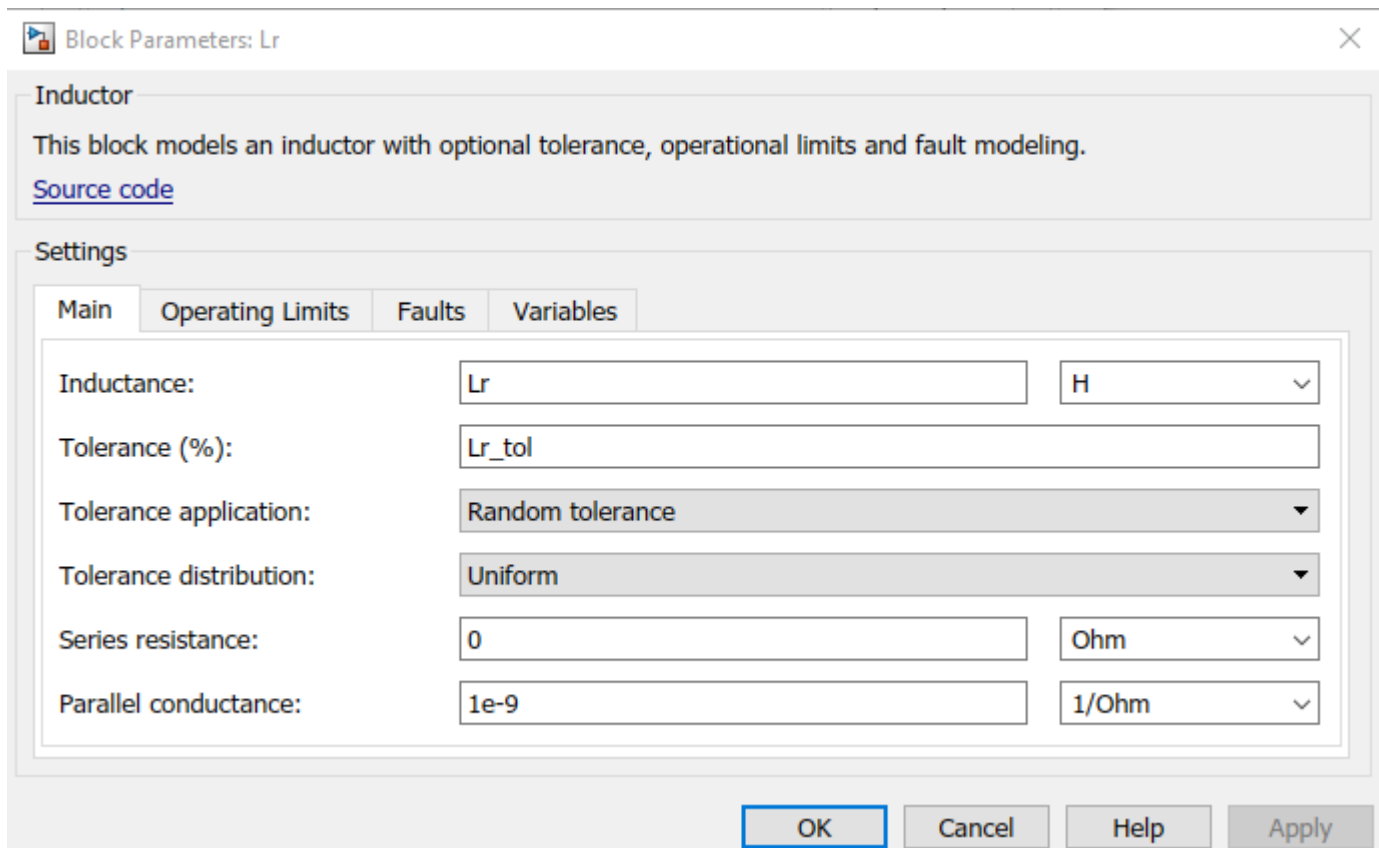
System Tolerance Study (Over Ln Only)

After you apply the tolerances (20% for the inductors and 7% for the capacitor in this example), you run multiple simulations during which the inductances and capacitance are randomly generated at t=0 with a uniform distribution. The output gain measured on the load varies each time, as the system is different during each simulation due to the tolerances. For a fixed design choice (fixed L_N and fixed Q), multiple repeated Monte Carlo simulations infer a statistical distribution of the output voltage.

In this section, to find out which configuration gives a lower output voltage gain variability, you fix a value of Q and change the value of L_N over a range you want to study.

```
This script will run 50 simulations.
```

Gain tolerance for normalized switching frequency of 0.7, and Q = 1
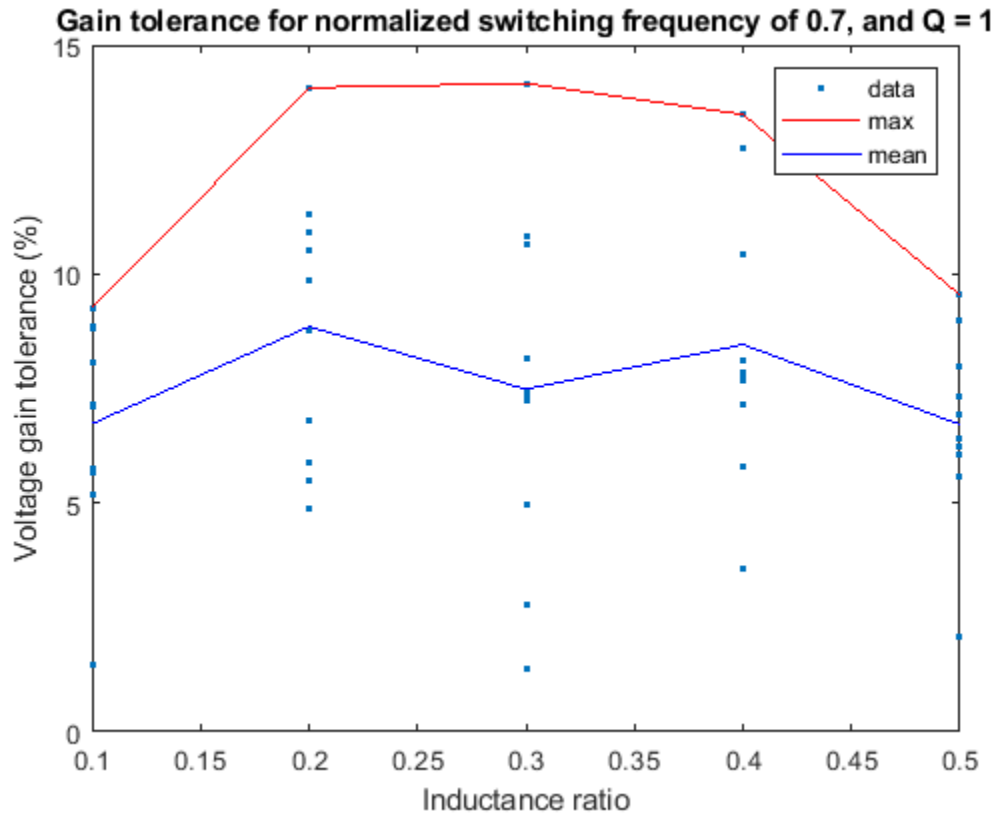
System Tolerance Study (Over Ln and Quality Factor)

To study the system tolerance over L_N and the quality factor, change the value of Q as well. A grid of values for L_N and Q is then selected and, for every configuration, the model estimates the output gain distribution through repetition.

```
This script will run 160 simulations.
```

Max gain tolerance for normalized switching frequency of 0.7
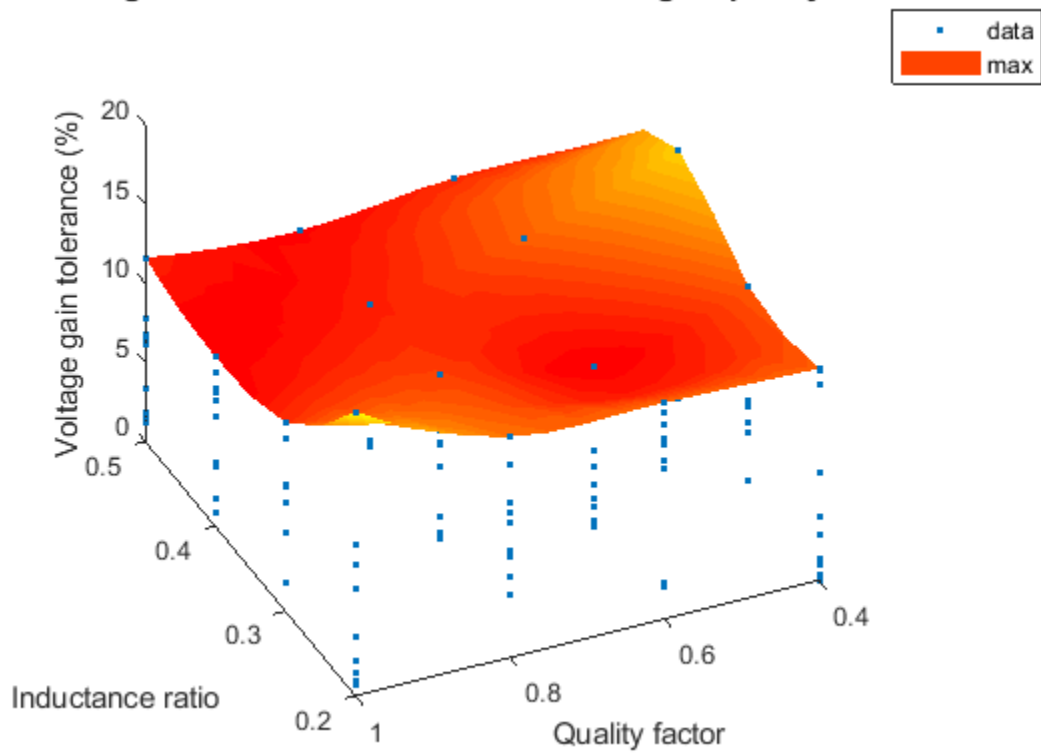
Mean gain tolerance for normalized switching frequency of 0.7
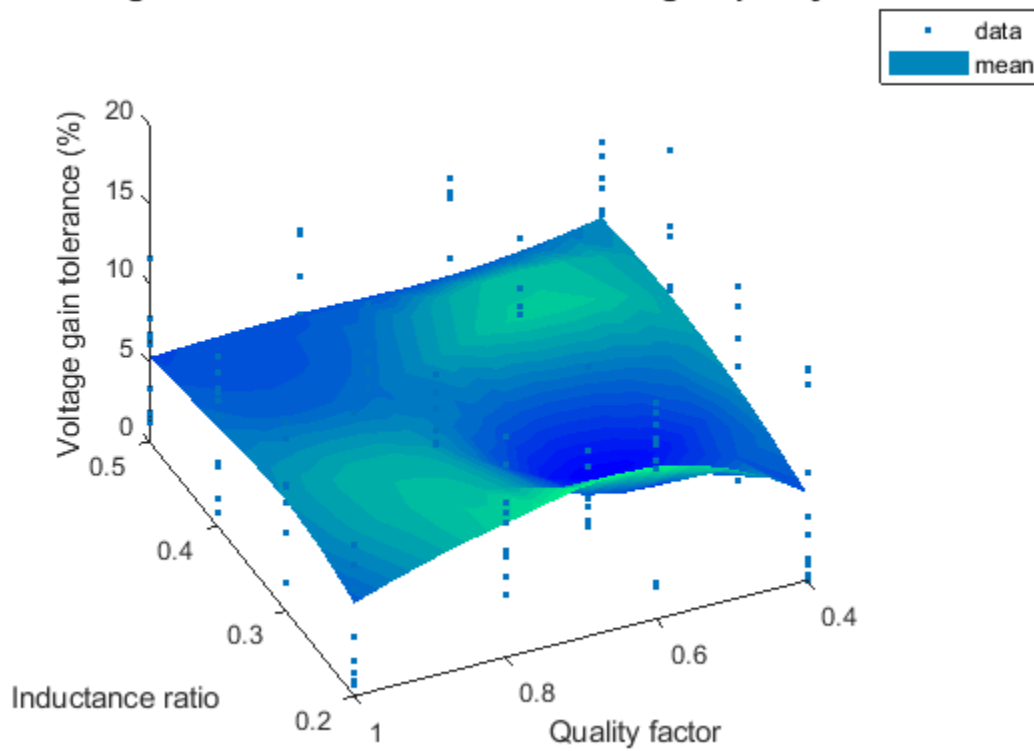
Advantages of Parallel Computing

Simulating hundreds or thousands of times is computationally expensive and time consuming. To reduce computation time, you can run multiple simulations in parallel. This is especially advantageous for independent Monte Carlo simulations.

For this workflow, Parallel Computing Toolbox™ is used to speed up the process and scale it up to a higher number of simulations.

Up until this point, this example worked on a grid of 4x4 configurations with 10 simulations each. This figure shows an animation of the same workflow scaled up to a grid of 17x17 configurations with 100 simulations each.

Voltage gain tolerance for fn=0.7
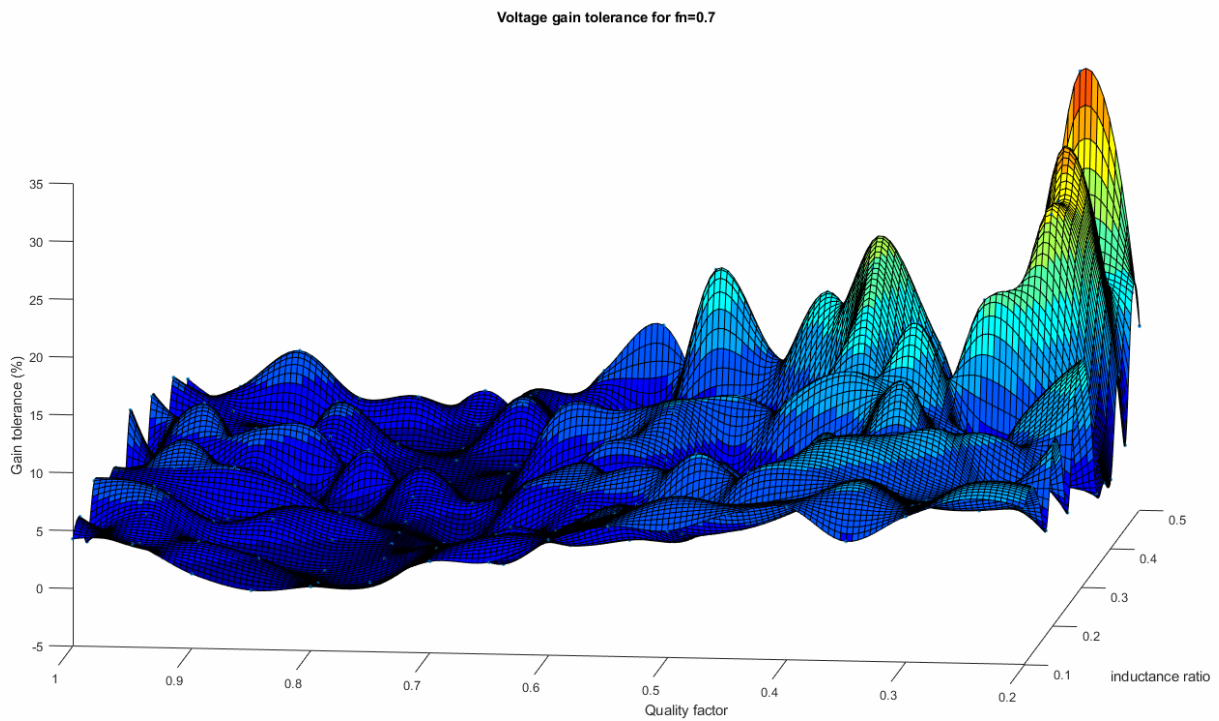
The surface plot shows the maximum tolerance found for each configuration (the worst case scenario). As more and more simulations are performed, the surface plot shapes to a more reliable representation of the distribution of output gain tolerances, and we can find a particular configuration ($Q = 0.8$ and $L_N = 0.3$) that tends to have the lowest possible output gain tolerance, of around 9%.

Six-Phase PMSM Torque Control

This example shows how to control the torque in an electrical-traction drive based on a six-phase permanent magnet synchronous machine (PMSM). A DC voltage source feeds the PMSM through two controlled three-phase converters. The PMSM operates in both motoring and generating modes according to the load. An ideal angular velocity source provides the load. The Control subsystem uses an open-loop approach to control the torque and a closed-loop approach to control the current. At each sample instant, the torque request is converted to a relevant q-axis current reference. The current control is PI-based. The simulation uses several torque steps in both motor and generator modes. The Scopes subsystem contains scopes that allow you to see the simulation results.

Model

Simulation Results from Simscape Logging

The plot below shows the requested and measured torque for the test, as well as the phase currents in the electric drive.

Six-Phase PMSM Velocity Control

This example shows how to control the rotor angular velocity in an electrical-traction drive based on a six-phase permanent magnet synchronous machine (PMSM). A DC voltage source feeds the PMSM through two controlled three-phase converters. The PMSM operates in both motoring and generating modes according to the load. An ideal torque source provides the load. The Scopes subsystem contains scopes that allow you to see the simulation results. The Control subsystem includes a PI-based cascade control structure that has an outer angular-velocity-control loop and four inner current-control loops. During the one second simulation, the angular velocity demand is 0 rpm, 500 rpm, 2000 rpm, and then 3000 rpm.

Model

Simulation Results from Simscape Logging

The plot below shows the requested and measured speed for the test, as well as the phase currents in the electric drive.

Three-Phase Grid-Connected Rectifier Control

This example shows how to control the DC-link voltage using a grid-connected rectifier. The Rectifier control subsystem uses a PI-based cascade control structure. The Scopes subsystem contains scopes that allow you to see the simulation results. If you have a license for HDL Coder™, you can generate VHDL code for an FPGA using the Simscape™ HDL Workflow Advisor.

Model

Three-Phase Grid-Connected Rectifier Control

1. Plot voltage and current (see code)
2. Modify model parameters
3. Learn more about this example

Simulation Results from Simscape Logging

The plot below shows the DC voltage and AC phase currents.
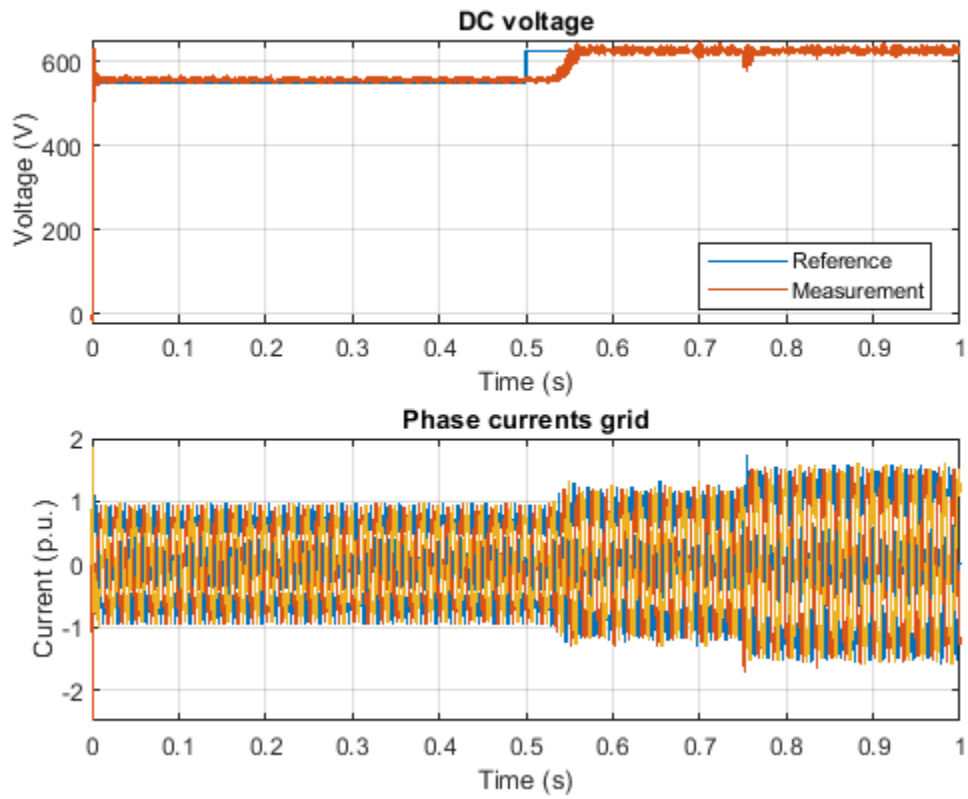
BLDC Position Control with Thermal Model

This example shows how to control the rotor angle in a BLDC based electrical drive. The BLDC includes a thermal model and empirical iron losses. An ideal torque source provides the load. The Control subsystem uses a PI-based cascade control structure with three control loops: an outer position control loop, a speed control loop, and an inner current control loop. The BLDC is fed by a controlled three-phase inverter. The gate signals for the inverter are obtained from hall signals. The simulation uses step references. The initial temperature of the stator windings and rotor is set to 25 degrees Celsius. Ambient temperature is 27 degrees Celsius. The Scopes subsystem contains scopes that allow you to see the simulation results.

Model
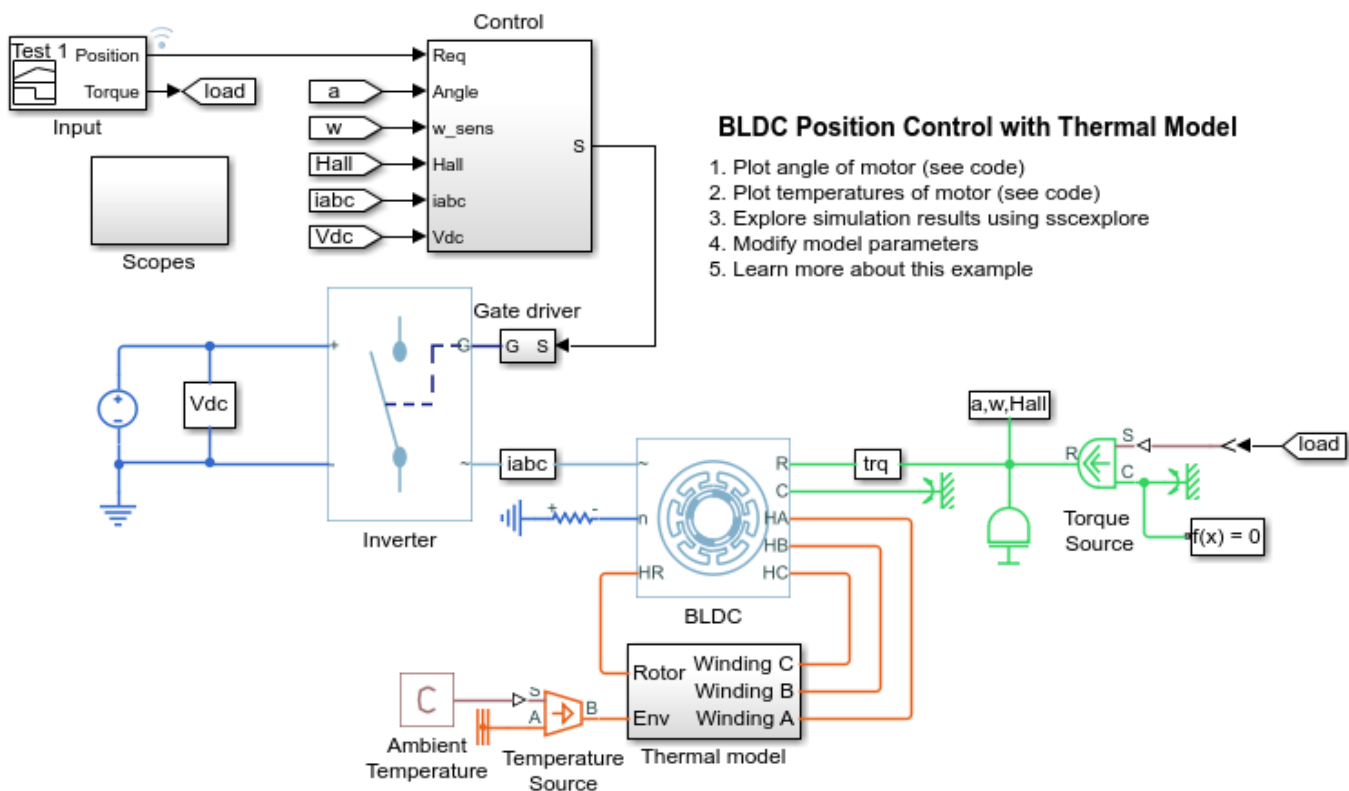
Simulation Results from Simscape Logging

The plot below shows the requested and measured angle for the test and the rotor speed in the electric drive.
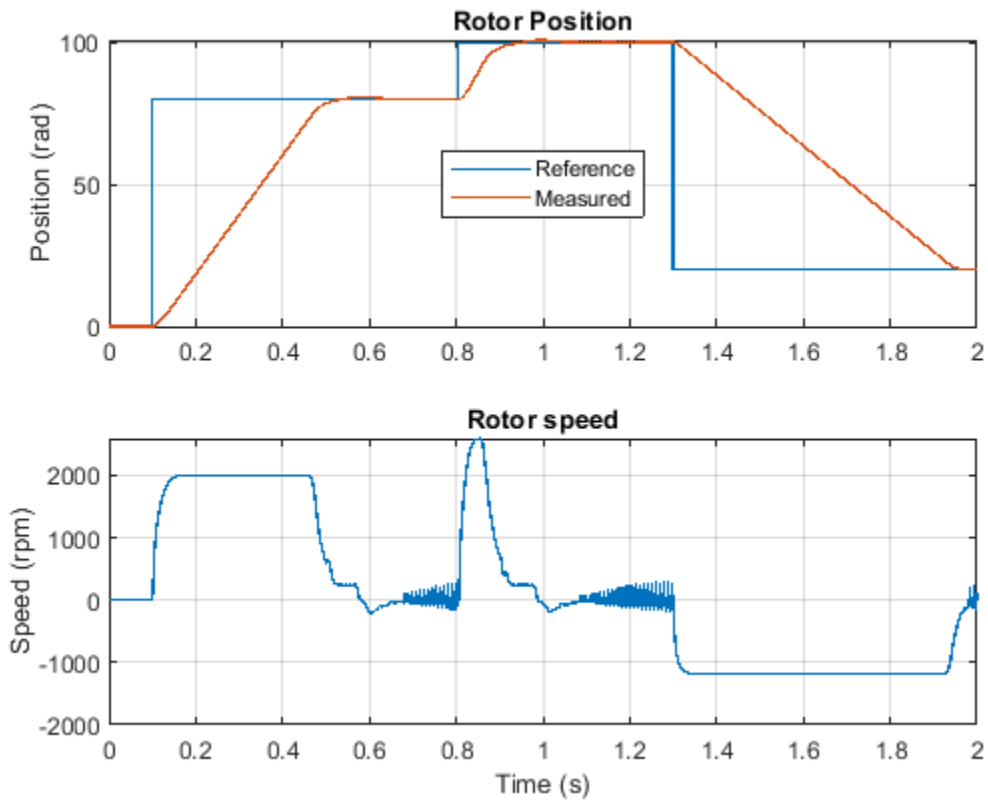
The plot below shows the winding and rotor temperatures of the motor.
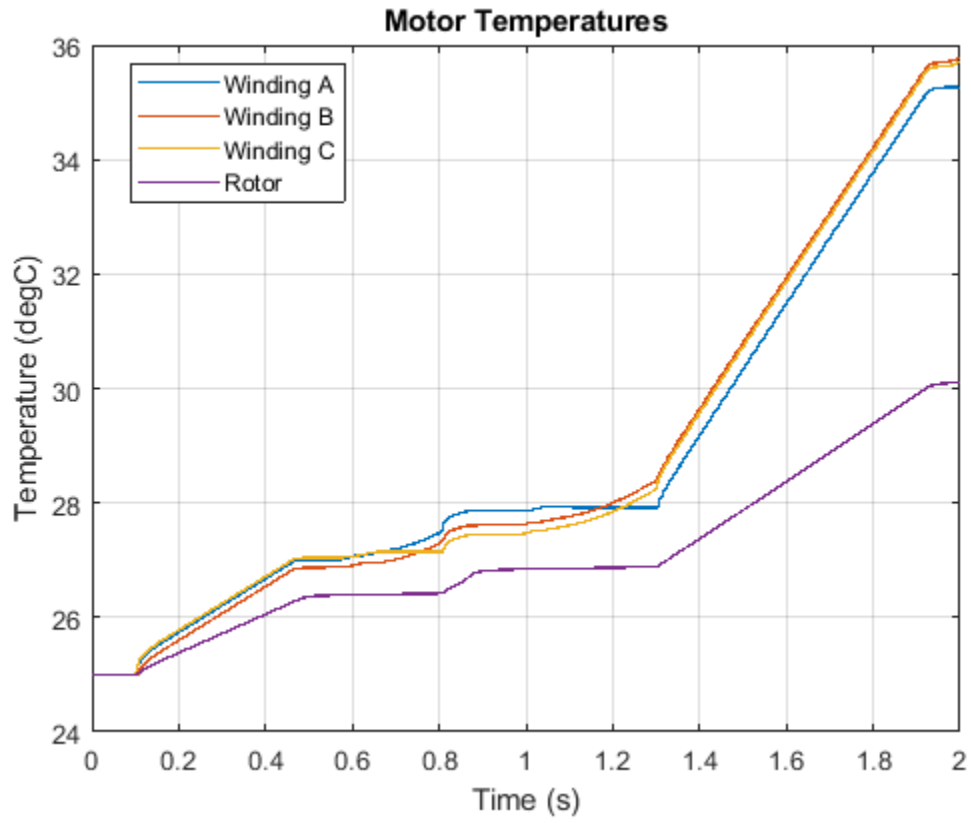
Three-Phase PMSM Drive with Thermal Model

This example shows a Permanent Magnet Synchronous Machine (PMSM) and an inverter sized for use in a typical hybrid vehicle. The PMSM includes a thermal model and empirical iron losses. The inverter is connected directly to the vehicle battery, but you can also implement a DC-DC converter stage in between. You can use this model to design the PMSM controller, by selecting the architecture and the gains to achieve the desired performance. The initial temperature of the stator windings and rotor is set to 25 degrees Celsius. Ambient temperature is 27 degrees Celsius. The Scopes subsystem contains scopes that allow you to see the simulation results.

Model
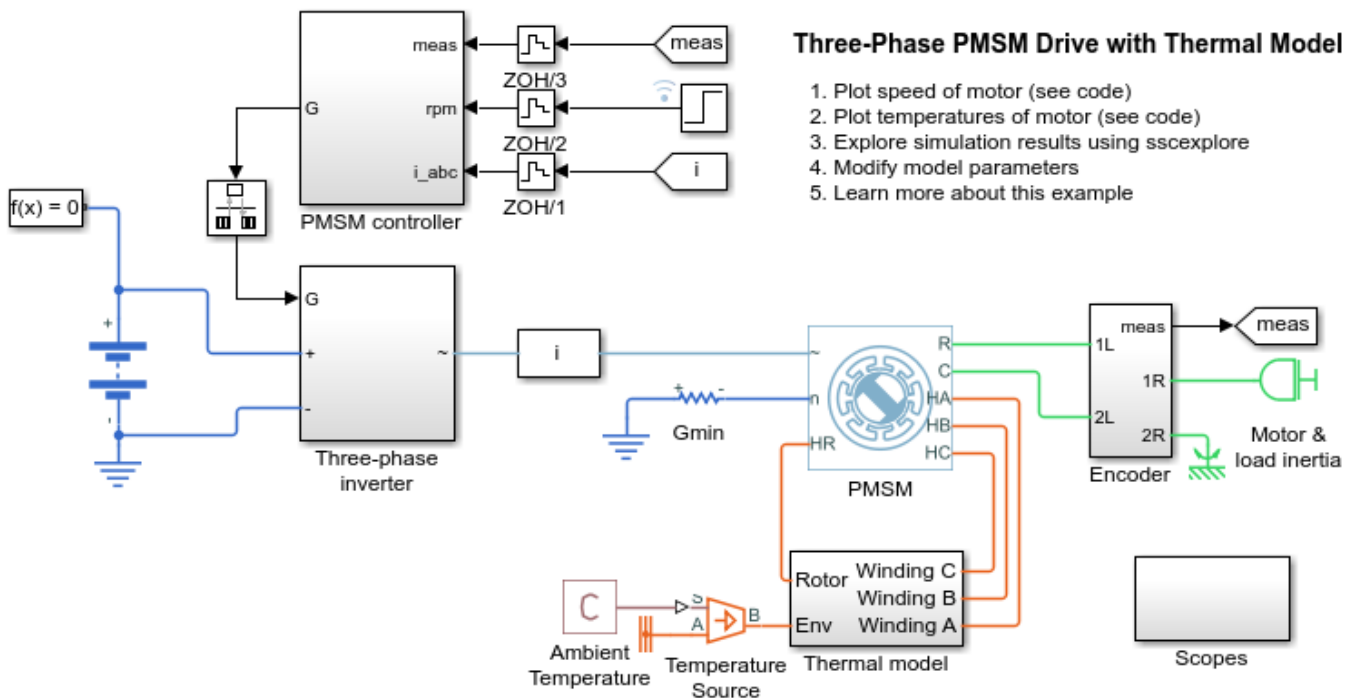
Simulation Results from Simscape Logging

The plot below shows the requested and measured rotor speed for the test and the torque in the electric drive.
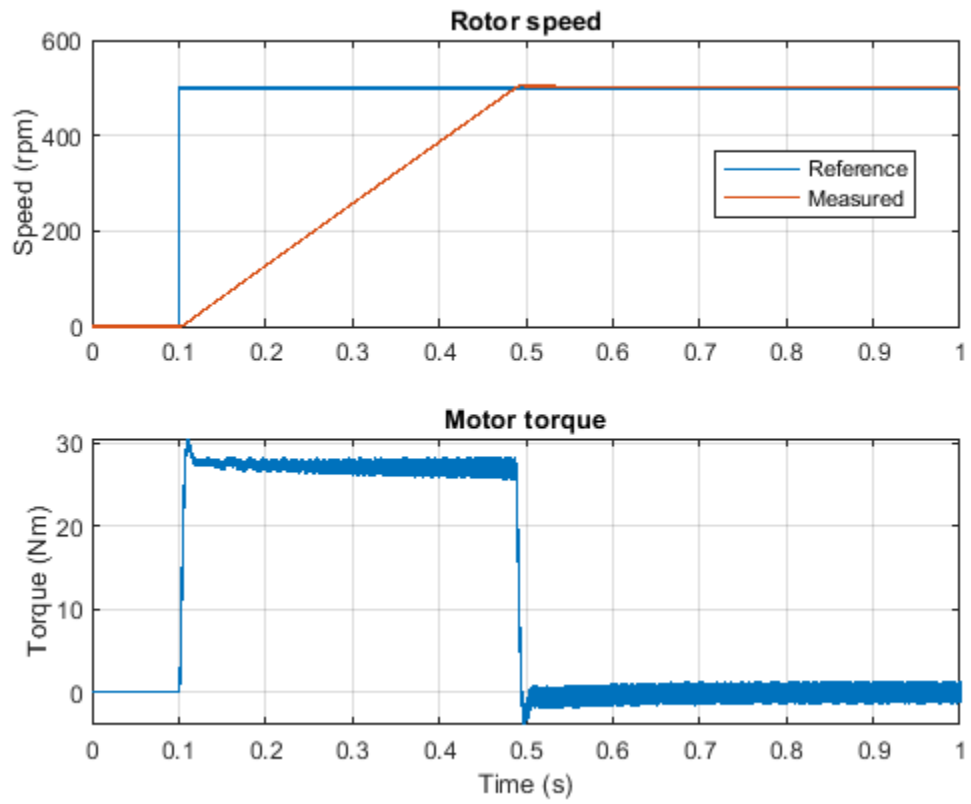
The plot below shows the winding and rotor temperatures of the motor.

Motor Temperatures
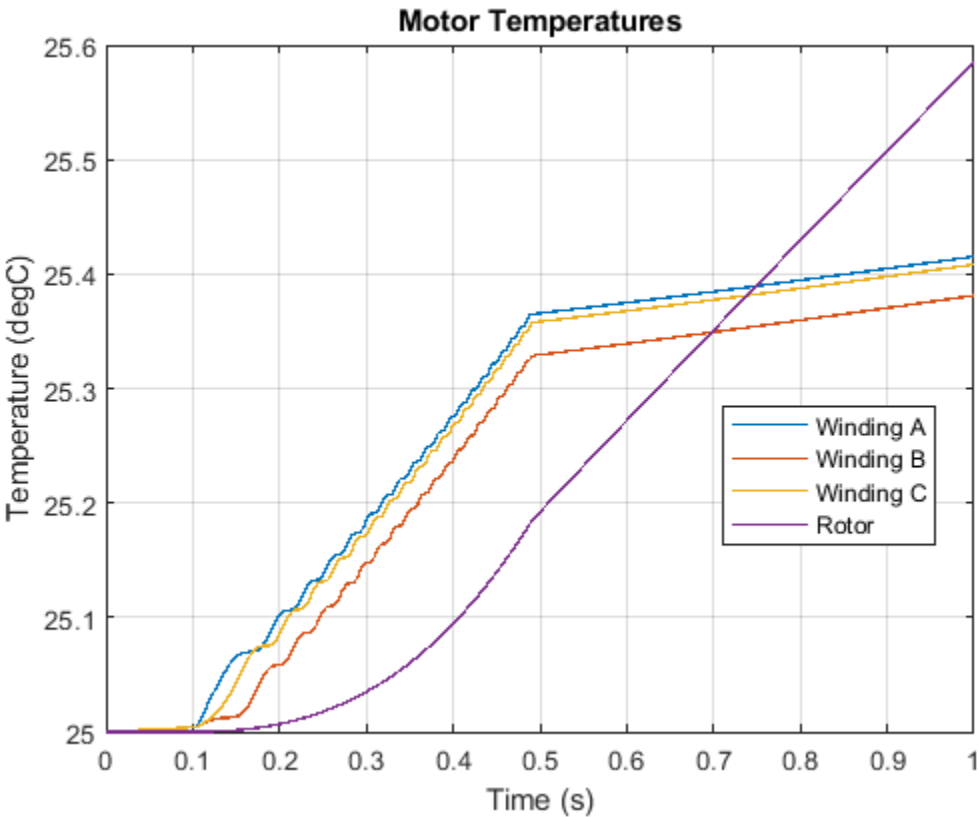
Single-Phase Inverter Current Control

This example shows how to control the current in a single-phase inverter system. The single-phase inverter uses averaged switches fed by modulation waveforms. This example is suitable for real-time evaluation on a dedicated real-time emulator.

Model
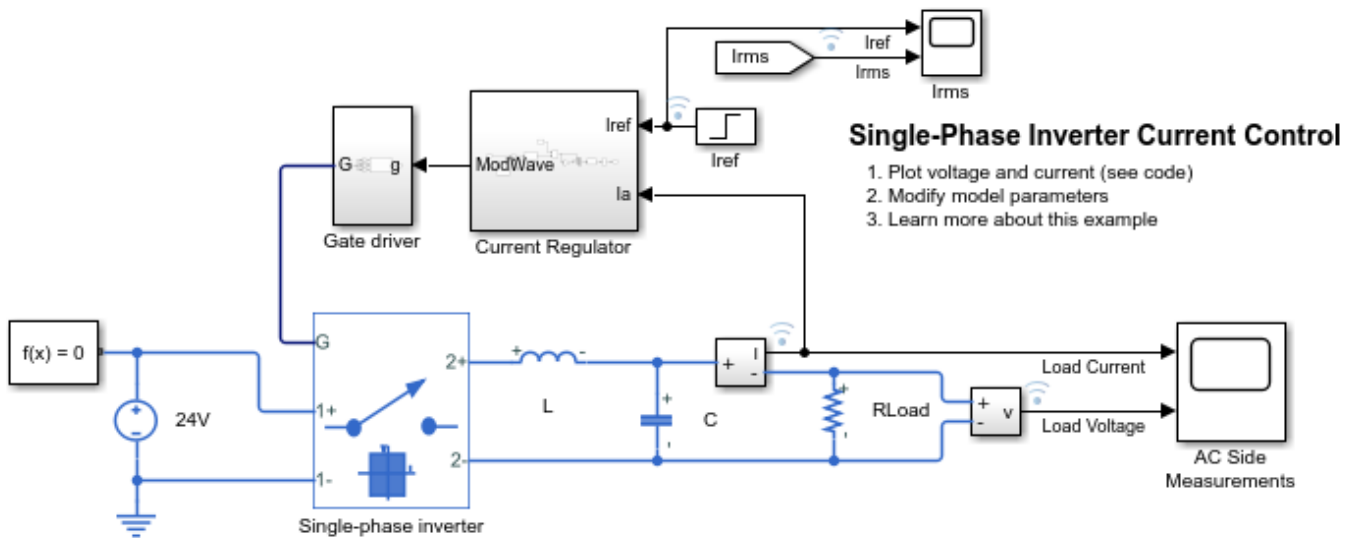
Simulation Results from Simscape Logging

The plot below shows the load current and voltage.
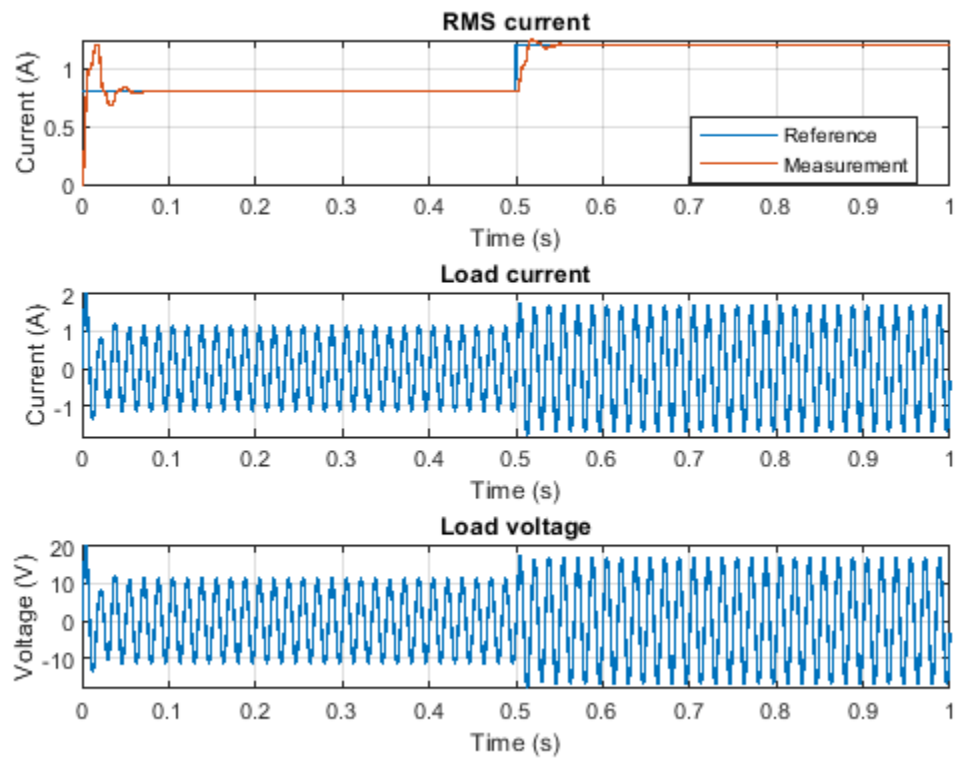
Three-Phase Grid-Tied Inverter

This example shows how to control the voltage in a grid-tied inverter system. The Voltage regulator subsystem implements the PI-based control strategy. The three-phase inverter is connected to the grid via a Circuit Breaker. The Circuit Breaker is open at the beginning of the simulation to allow synchronization. At time 0.15 seconds, the Circuit breaker closes, and the inverter is connected to the grid. The Scopes subsystem contains scopes that allow you to see the simulation results. The inverter is implemented using IGBTs. To speed up simulation, or for real-time deployment, the IGBTs can be replaced with Averaged Switches. In this way the gate signals can be averaged over a specified period or replaced with modulation waveforms.

Model
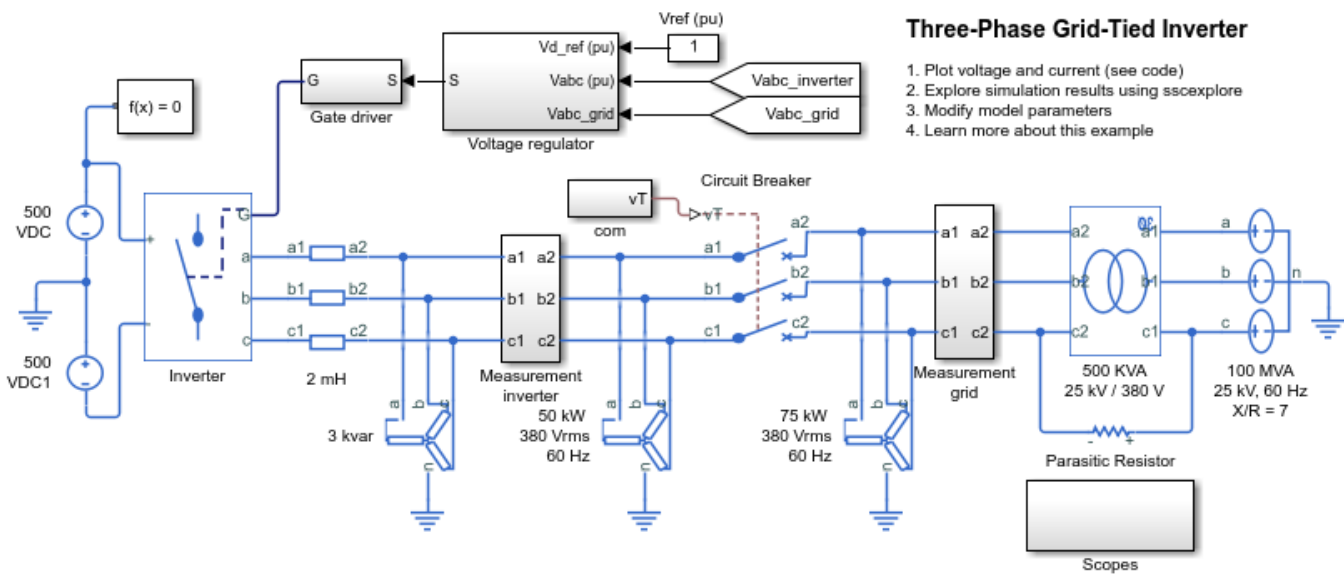
Simulation Results from Simscape Logging

The plot below shows the line voltage and phase currents in the inverter and grid.
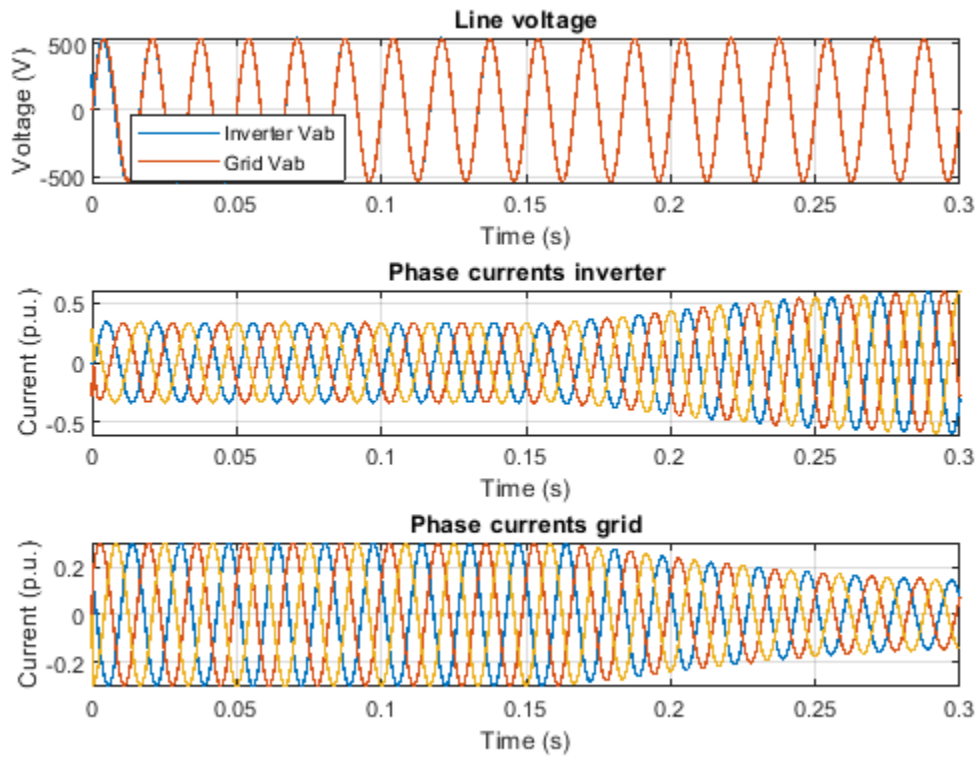
Three-Phase Inverter Voltage Control

This example shows how to control the voltage in a three-phase inverter system. The inverter is implemented using IGBTs. To speed up simulation, or for real-time deployment, the IGBTs can be replaced with Averaged Switches. In this way the gate signals can be averaged over a specified period or replaced with modulation waveforms.

Model
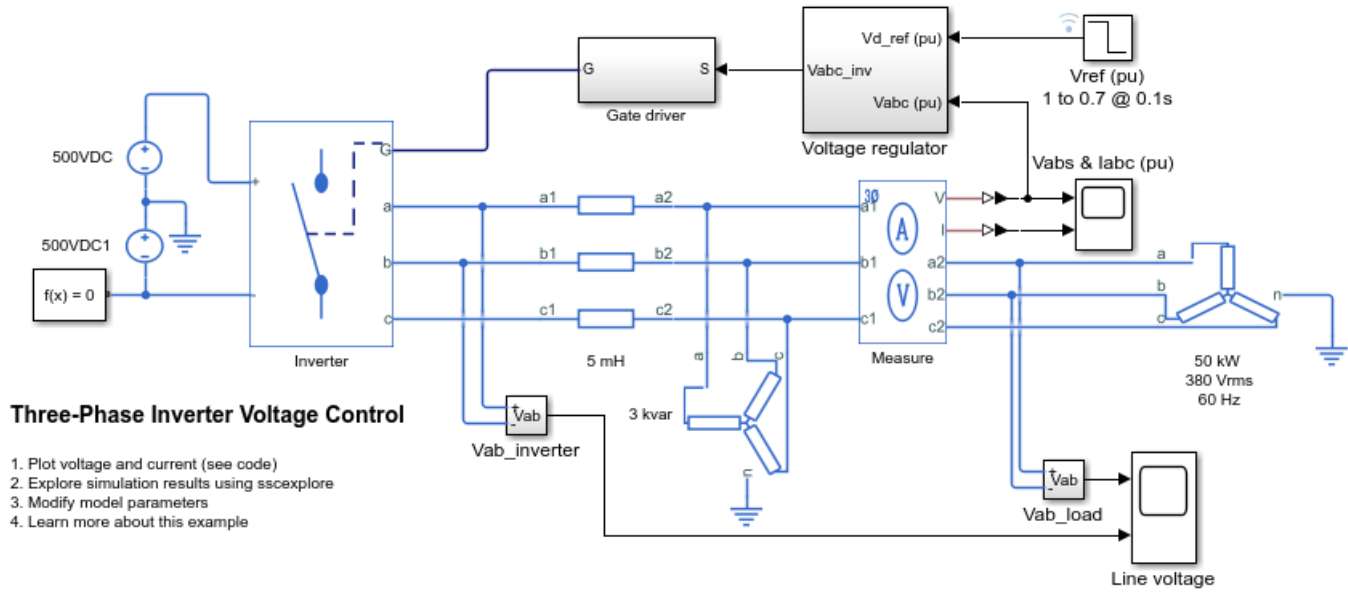
Simulation Results from Simscape Logging

The plot below shows the phase voltages and currents.
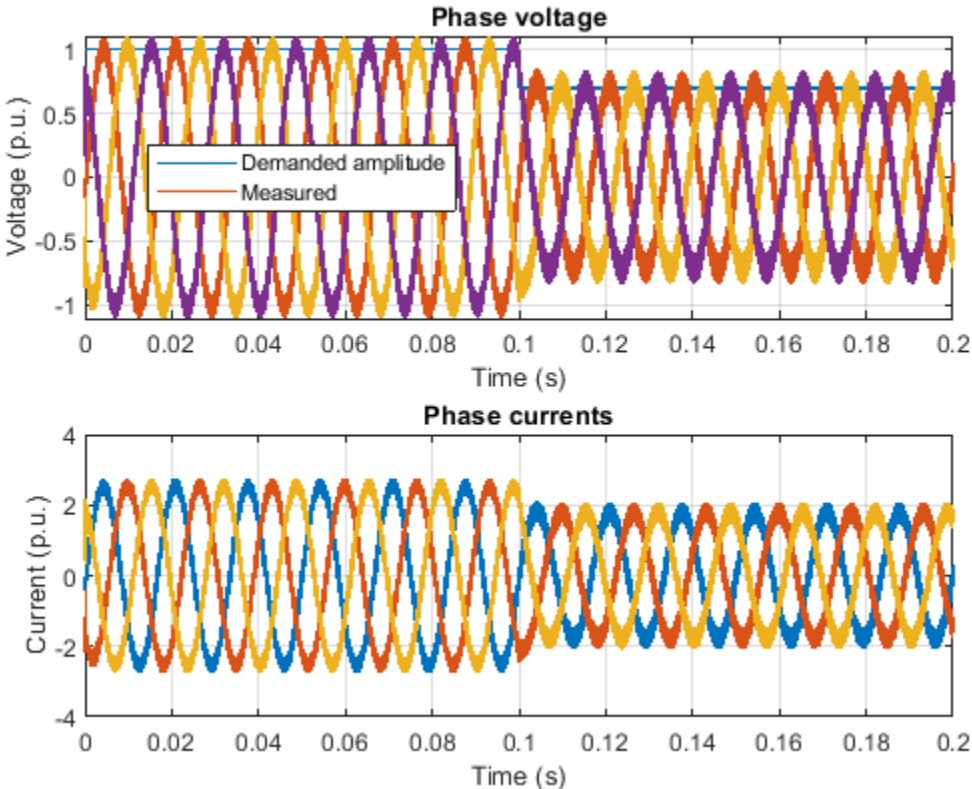
SPICE Conversion of a CMOS Voltage Comparator

This example shows a typical implementation of a CMOS voltage comparator and how you can convert a SPICE subcircuit to a Simscape™ component using the `subcircuit2ssc` function. You can use CMOS voltage comparators in analog to digital converter (ADC) or relaxation oscillator circuits.

In order to obtain the small-signal frequency response of the system, this example includes input u, output y, and includes a Solver Configuration block with the parameter **Start simulation from steady state** selected. You can use the `linmod` function to linearize this model.

Open the CMOS Voltage Comparator Subcircuit

In this example, you will convert this voltage comparator subcircuit to a Simscape component and analyze it in an existing model. The SPICE netlist named ee_CMOS_comparator.cir is a netlist that describes the model of a CMOS voltage comparator.

```
% Display ee_CMOS_comparator.cir
type('ee_CMOS_comparator.cir')


* CMOS Voltage Comparator
* Simulation of Two-stage comparator

* Input Signals
VIN VP 0 AC 1V
VOS VN 0 DC 0V

* Power Supplies
VDD VDD 0 DC 5V
VSS VSS 0 DC -5V

* External Components
CL   VOUT  0          2pF

X1 VDD VSS VP VN VOUT COMPARATOR1
* Subcircuit for CMOS Voltage Comparator
.SUBCKT COMPARATOR1 VDD VSS VP VN VOUT
M1   N1   VN N2 VSS     NMOS1  W=680u    L=5u
M2   N3   VP N2 VSS     NMOS1  W=680u    L=5u
M3   N1   N1 VDD VDD    PMOS1  W=5u      L=5u
M4   N3   N1 VDD VDD    PMOS1  W=5u      L=5u
M5   N2   N4 VSS VSS    NMOS1  W=5u      L=5u
M6   VOUT  N3 VDD VDD    PMOS1  W=60u     L=5u
M7   VOUT  N4 VSS VSS    NMOS1  W=30u     L=5u
M8   N4   N4 VSS VSS    NMOS1  W=30u     L=5u
IS   0   N4      20u

.MODEL NMOS1 NMOS VTO=1 KP=17U
+ LEVEL=1
+ GAMMA=0.8 LAMBDA=0.015 PHI=0.6
+ LD=0.5U CJ=5E-4 CJSW=10E-10
+ U0=425 MJ=0.5 MJSW=0.5 CGSO=0.4E-9 CGDO=0.4E-9
.MODEL PMOS1 PMOS VTO=-1 KP=8U
+ LEVEL=1
+ GAMMA=0.4 LAMBDA=0.02 PHI=0.6
+ LD=0.8U CJ=5E-4 CJSW=10E-10
+ U0=200 MJ=0.5 MJSW=0.5 CGSO=0.4E-9 CGDO=0.4E-9
```

```
.ENDS

* Analysis
*.DC VIN  -1e-3 1e-3 1e-5
.AC DEC 10 1e-1 1e6
.PROBE
.END
```

Open the CMOS Comparator Subsystem

Open the `ee_CMOS_comparator` model, which models the SPICE netlist by using SPICE blocks from the Simscape Electrical™ Additional Components library. This model includes a subsystem called CMOS Comparator. This subsystem is a manual implementation of the voltage comparator subcircuit, which you will replace with a Simscape component converted from the SPICE subcircuit.

```
% Open model
open_system('ee_CMOS_comparator')
set_param(find_system('ee_CMOS_comparator','FindAll', 'on','type','annotation','Tag','ModelFeatur
```
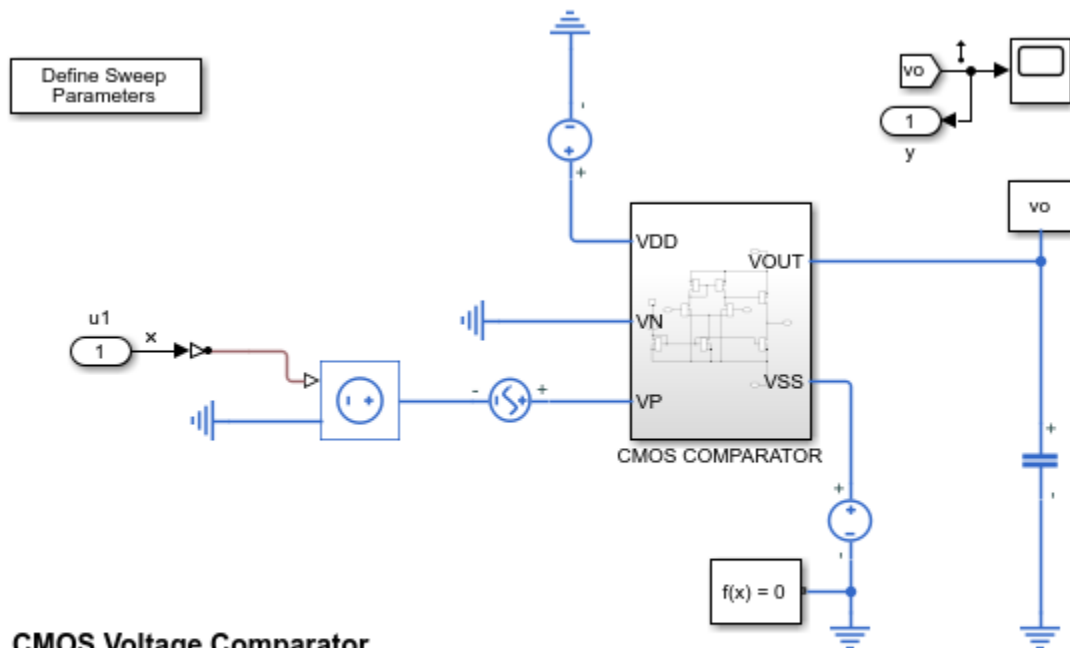

CMOS Voltage Comparator

1. Characteristics: Define Sweep Parameters, Plot DC AC simulation results (see code).
2. SPICE subcircuit to Simscape component (see convertion code), (see SPICE netlist), (see ssc).
3. Simulation verification, (see code).
4. Explore simulation results using sscexplore
5. Learn more about this example

```
% Display CMOS Voltage Comparator Subsystem
open_system('ee_CMOS_comparator/CMOS COMPARATOR','force');
```

The subsystem describes the comparator in a schematic way. The plot below shows the outputs of the CMOS comparator circuit.

```
ee_CMOS_comparator_plot;
```
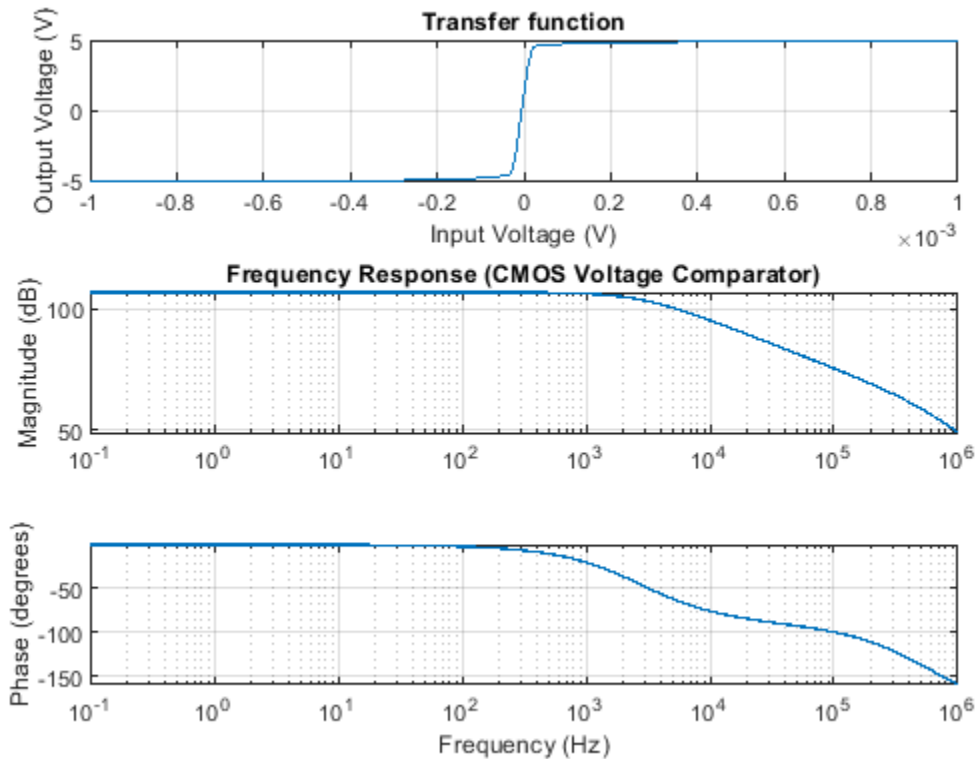
The gain is around 100 dB and the bandwidth is around 3 kHz. To define the minimum and maximum DC sweep input voltages and AC sweep frequencies, return to the top level of the model and double-click the block labeled Define Sweep Parameters. Click Plot DC AC simulation results in the model to run the simulation and plot the results.

However, for large SPICE netlists with multiple subcircuits, a manual conversion can be inefficient, time consuming, and error-prone. Simscape Electrical provides you with a way to automatically convert a SPICE netlist by using the `subcircuit2ssc` function.

Convert the SPICE Subcircuit to Simscape Component

You can convert SPICE components with either `.cir` or `.lib` extensions into Simscape components using the `subcircuit2ssc` function. The `subcircuit2ssc` function converts each `.subckt` section inside the SPICE netlist into a single component.

Create a temporary directory that contains validation files and change the present working directory.

`ee_CMOS_comparator_temporary_directory;`

The validation files `ee_comparator_traf.mat` and `ee_comparator_freq.mat` are MATLAB files that store data for the SPICE transfer characteristics and the small-signal frequency response.

Use the `subcircuit2ssc` function to convert the voltage comparator subcircuit, `ee_CMOS_comparator.cir`, to a Simscape file and place it in a new directory called `+myComparator`.

`subcircuit2ssc('ee_CMOS_comparator.cir','+myComparator');`

```
Netlist converted. Review Simscape component files and make
manual edits for any unsupported items before building the
Simscape library located at:
+myComparator.
```

Generate the Simscape library using the `ssc_build` function.

```
ssc_build myComparator;
```

```
Generating Simulink library 'myComparator_lib' in the current directory 'C:\TEMP\Bdoc20b_1465442
```

Verify Simulation Results

You must replace the CMOS Comparator subsystem with the converted component and rewire the terminals. To see the verification code, in the MATLAB Command Window, enter edit ee_CMOS_comparator_verification. The script automatically connects the Simscape component generated from the conversion into the model. Then it obtains the transfer characteristics and the small-signal frequency response of the Simscape model by using a linearization method. The input voltage of the transfer characteristics is in the range of -1 V to 1 V, while the frequency range of the Bode plot is in the range of 0.1 Hz to 1 GHz. Finally, the script plots and compares the simulation results between a pure SPICE netlist model, the original model with the Simscape subsystem, and the model with the converted Simscape component obtained using `subcircuit2ssc`.

The plot below shows the simulation results from the three different models. The transfer characteristics and frequency response of the model with the converted Simscape component match those of the original SPICE netlist.

```
ee_CMOS_comparator_verification;
```
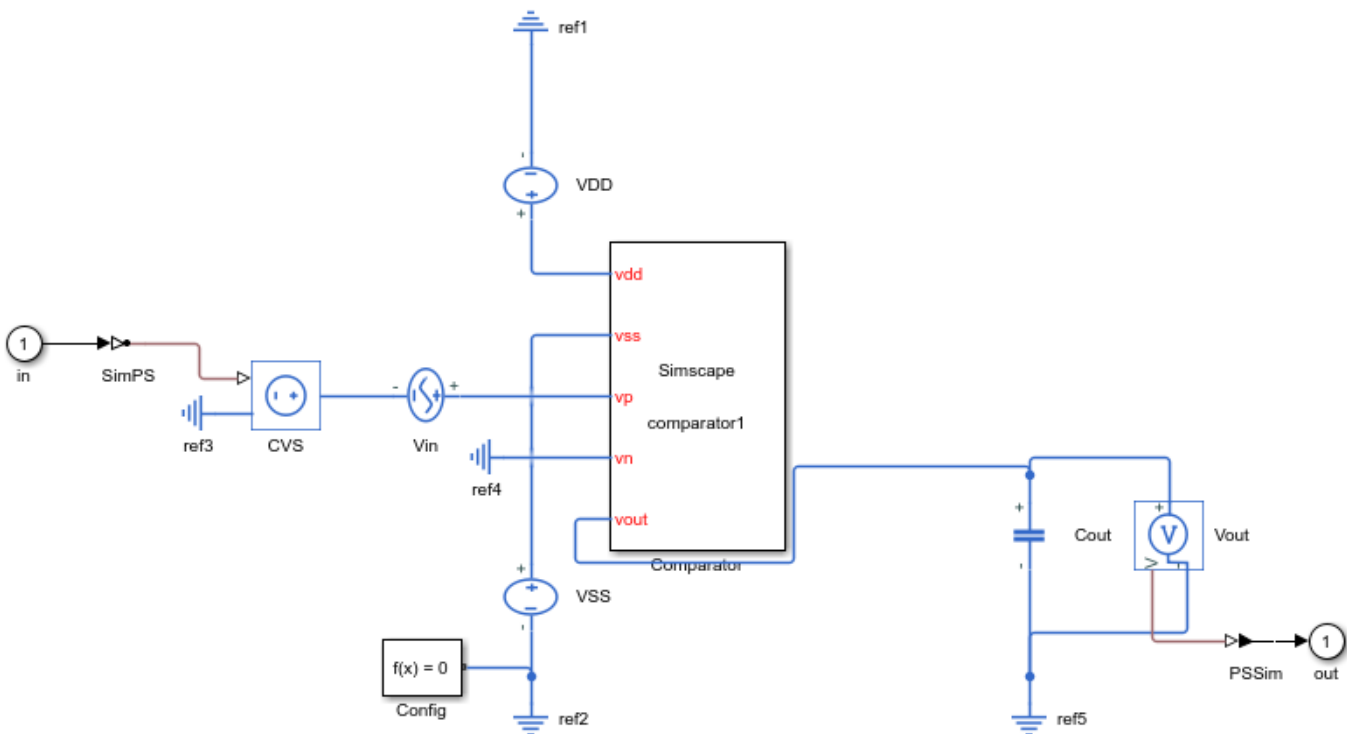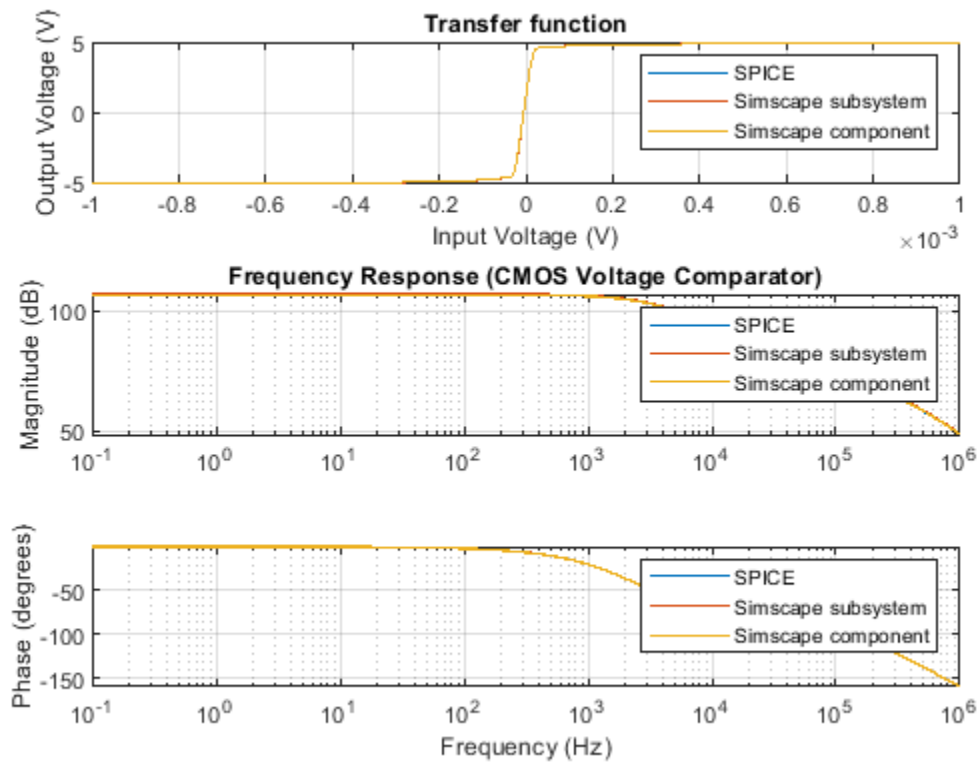
Clean Up

Finally, delete the temporary directory and all its subdirectories.

```
% Delete temporary directory
if exist('originalDirectory','var')
    cd (originalDirectory);
    rmdir(temporaryDirectory,'s')
    clear temporaryDirectory originalDirectory;
end
```

Troubleshoot a SPICE Conversion of an IGBT Subcircuit

This example shows how to troubleshoot the conversion of a SPICE insulated gate bipolar transistor (IGBT) subcircuit and convert it into an equivalent Simscape™ component. The `subcircuit2ssc` function converts all the subcircuit components inside a SPICE netlist file into one or more equivalent Simscape files. However, in some cases, you must manually edit the original SPICE netlist.

Open the IGBT Subcircuit

In this example, you will convert this IGBT to a Simscape component, add it to a model, and simulate the model. The IGBT subcircuit combines the bipolar junction transistor (BJT) and SPICE Level 1 metal-oxide-semiconductor field-effect transistor (MOSFET) models and equations with a drain-gate capacitance model. To open the SPICE netlist, in the MATLAB command window, enter edit ee_igbt_subckt_original.cir.

```
% Display ee_igbt_subckt_original.cir
type('ee_igbt_subckt_original.cir')

% Create a temporary directory and change present working directory
ee_igbt_subckt_temporary_directory;

% Create a subdirectory called +myIGBT
if ~exist('+myIGBT', 'dir')
    mkdir('+myIGBT')
end


* Fast and accurate SPICE IGBT model

.SUBCKT IGBTMODEL C G 0
.FUNC WDSJ(X1,X2) {SQRT(12.422*{ESI}*VDS(X1)/(X2))}
.FUNC VDS(X) {SQRT(X^2+0.01^2)/2}
M1 N1 N2 0 0 MOSN
RG G N2 {5.7-7M*(125-TEMP)}
Q1 0 N1 C BJTP
CGDOX N3 N2 0.1N
VICGDOX N1 N3 0
GCGD N1 N2
+VALUE = {(COXD*AGD*ESI/(WDSJ(V(N1,N2),{NB})*COXD+AGD*ESI))*I(VICGDOX)}

.PARAM TEMP=25
.PARAM AGD=50 ESI=1.0359 NB=2 COXD=175 VTO={4+7M*(125-TEMP)}
.MODEL MOSN NMOS (IS=20N KP=20 VTO={VTO} RD=0.5M CGSO=160U)
.MODEL BJTP PNP (BF=0.26 TF=1.5U CJC=0.6N RE=1M BR=0.44 IS=2E-15 NF=2 XTI=6 XTB=5)

.ENDS IGBTMODEL
```

Modify the IGBT SPICE Subcircuit

Not all SPICE subcircuits can be directly converted with the `subcircuit2ssc` function. If you run the `subcircuit2ssc` conversion directly on this file you will receive the following error:

```
"Error using spiceSubckt/extractSubcktDefinitionStrings (line 1182)"
"Connecting public nodes to ground is illegal."

"Error in spiceSubckt/loadSubckt (line 379)"
                "this.extractSubcktDefinitionStrings; % parse the subckt line"
```

```
"Error in spiceSubckt (line 69)"
                "this.loadSubckt(fullNetlist); % populate the object properties"

"Error in subcircuit2ssc (line 55)"
    "subcircuitArray(ii) = spiceSubckt(file,subcctName(ii));"

"Error in subcircuit2ssc (line 31)"
        "subcircuit2ssc(netlist,target);"
```

The conversion generates an error because the SPICE subcircuit connected a public node directly to node 0. In SPICE, node 0 is ground. To resolve the error, update the SPICE netlist file by renaming node 0 to node E.

If you now run `subcircuit2ssc` and then `ssc_build` you will receive the following error:

```
"Failed to generate 'myIGBT_lib'"

"Caused by:"
    "Error using ne_parselibrarypackage (line 66)"
    "Error: Class member redefinition has been found:"
        "Error using myIGBT.igbtmodel> (line 22)"
        "Class member 'temp' is defined."
        "Error using myIGBT.igbtmodel> (line 31)"
        "Class member 'temp' is defined."
```

The build generates an error because the SPICE subcircuit includes the definition of a local parameter named TEMP which is a reserved SPICE variable name for global temperature. To resolve the error and use the global temperature, remove the local parameter definition of TEMP from the SPICE library file. Alternatively, if you want to specify a local temperature, choose a name other than TEMP for the local parameter.

Save the changes into a new file named `ee_igbt_subckt_modified.cir`.

```
% Display ee_igbt_subckt_modified.cir
type('ee_igbt_subckt_modified.cir')


* Fast and accurate SPICE IGBT model

.SUBCKT IGBTMODEL C G E
.FUNC WDSJ(X1,X2) {SQRT(12.422*{ESI}*VDS(X1)/(X2))}
.FUNC VDS(X) {SQRT(X^2+0.01^2)/2}
M1 N1 N2 E E MOSN
RG G N2 {5.7-7M*(125-TEMP)}
Q1 E N1 C BJTP
CGDOX N3 N2 0.1N
VICGDOX N1 N3 0
GCGD N1 N2
+VALUE = {(COXD*AGD*ESI/(WDSJ(V(N1,N2),{NB})*COXD+AGD*ESI))*I(VICGDOX)}

.PARAM AGD=50 ESI=1.0359 NB=2 COXD=175 VTO={4+7M*(125-TEMP)}
.MODEL MOSN NMOS (IS=20N KP=20 VTO={VTO} RD=0.5M CGSO=160U)
.MODEL BJTP PNP (BF=0.26 TF=1.5U CJC=0.6N RE=1M BR=0.44 IS=2E-15 NF=2 XTI=6 XTB=5)

.ENDS IGBTMODEL
```

To see the differences between the two files, in the MATLAB command window, enter visdiff('ee_igbt_subckt_original.cir','ee_igbt_subckt_modified.cir').

Run this command to convert the SPICE subcircuit of the `ee_igbt_subckt_modified.cir` model to a Simscape component and place the generated files in the newly created directory called `+myIGBT`:

```
subcircuit2ssc('ee_igbt_subckt_modified.cir','+myIGBT')
```

```
Netlist converted. Review Simscape component files and make
manual edits for any unsupported items before building the
Simscape library located at:
+myIGBT.
```

Generate the Simscape library using the `ssc_build` function.

```
ssc_build myIGBT
```

```
Generating Simulink library 'myIGBT_lib' in the current directory 'C:\TEMP\Bdoc20b_1465442_5924\:
```

Open the Converted IGBT Model

The `igbtmodel.ssc` file stored in the `+myIGBT` directory is the converted Simscape component obtained by running the `subcircuit2ssc` function on the modified SPICE netlist. The `subcircuit2ssc` function also converted all the functions implemented in the SPICE subcircuit and stored them in a subdirectory named `+igbtmodel_simscape_functions`. To edit the generated simscape component, in the MATLAB command window, enter edit +myIGBT/igbtmodel.ssc.

```
open_system('ee_igbt_subckt')
set_param(find_system('ee_igbt_subckt','FindAll', 'on','type','annotation','Tag','ModelFeatures')
```
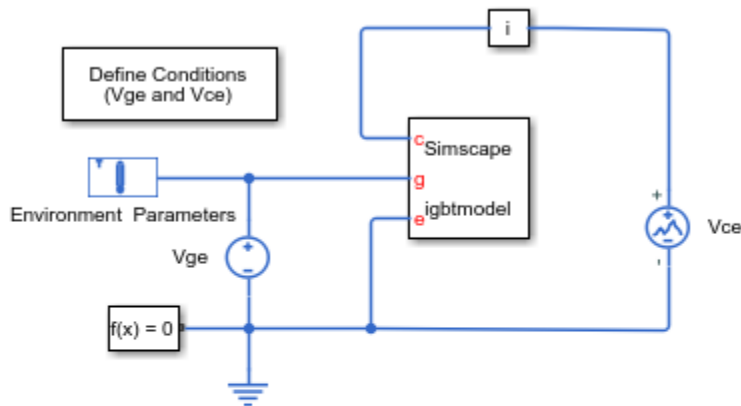

IGBT Subcircuit2ssc Characteristics

1. IGBT curves: Define Vge and Vce, plot curves (see code)
2. SPICE subcircuit to Simscape component (see SPICE netlist), (see ssc)
3. Simulation verification, (see code)
4. Explore simulation results using sscexplore
5. Learn more about this example

To adjust the Simscape global temperature, in the Environment Parameters block, modify the **Temperature** parameter.

Verify Simulation Results

The `ee_igbt_subckt_verification` script compares collector current versus collector-emitter voltage curves for a range of gate-emitter voltages with the SPICE netlist simulation results, which are stored in the `ee_igbt_subckt_data.mat` MAT-file. The gate-emitter voltages, Vge, are 8 V, 10 V, 12 V, 15 V, and 20 V. The Vce voltage sweeps from 0 V to 10 V.

```
ee_igbt_subckt_verification;
```
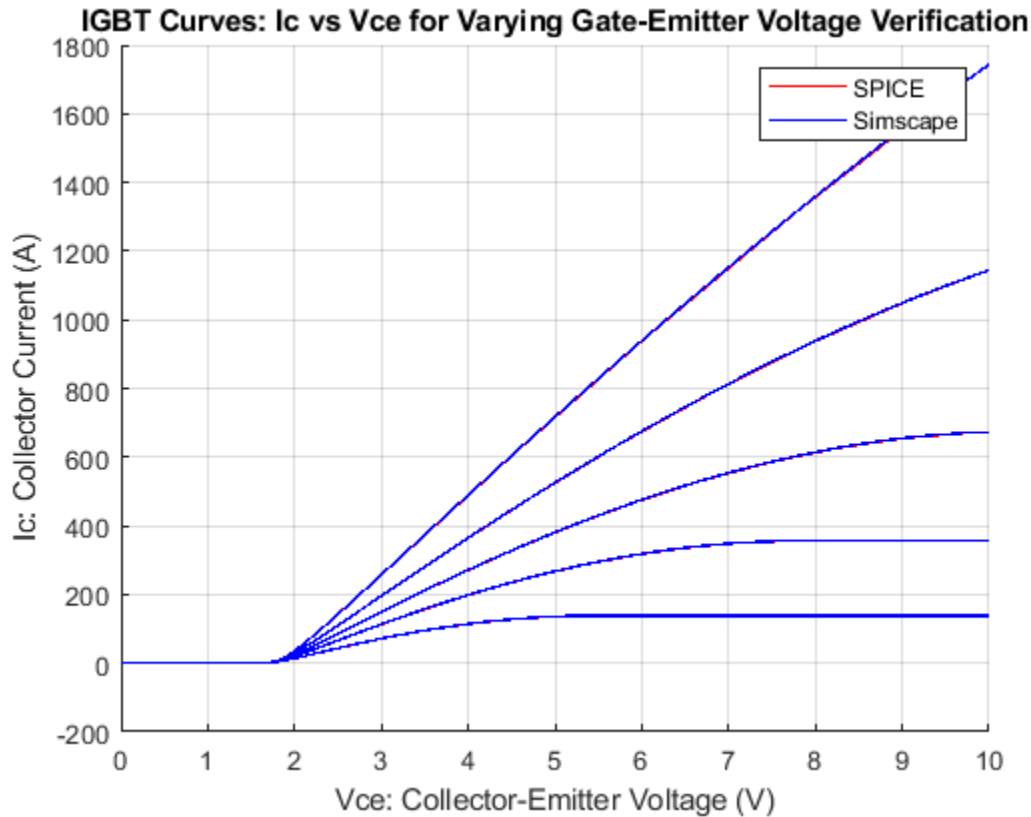

To plot these characteristics with different settings, double-click the block labeled Define Conditions (Vge and Vce) and define the vector of Vge and the minimum and maximum Vce voltages. In the model, to run the simulations and plot the results, click **plot curves**.

Clean Up

Finally, delete the temporary directory and all its subdirectories.

```
% Delete temporary directory
if exist('originalDirectory','var')
    cd (originalDirectory);
    rmdir(temporaryDirectory,'s')
    clear temporaryDirectory originalDirectory;
end
```

Calculate Performance Curves of BLDC

This example shows how to calculate performance curves for a brushless DC (BLDC) motor. The simulation includes a speed ramp. Ideal trapezoidal modulation waves are used to drive an average-value converter. A triggered subsystem is used to determine the peak torque, power, current, and efficiency values for a given speed.

Model
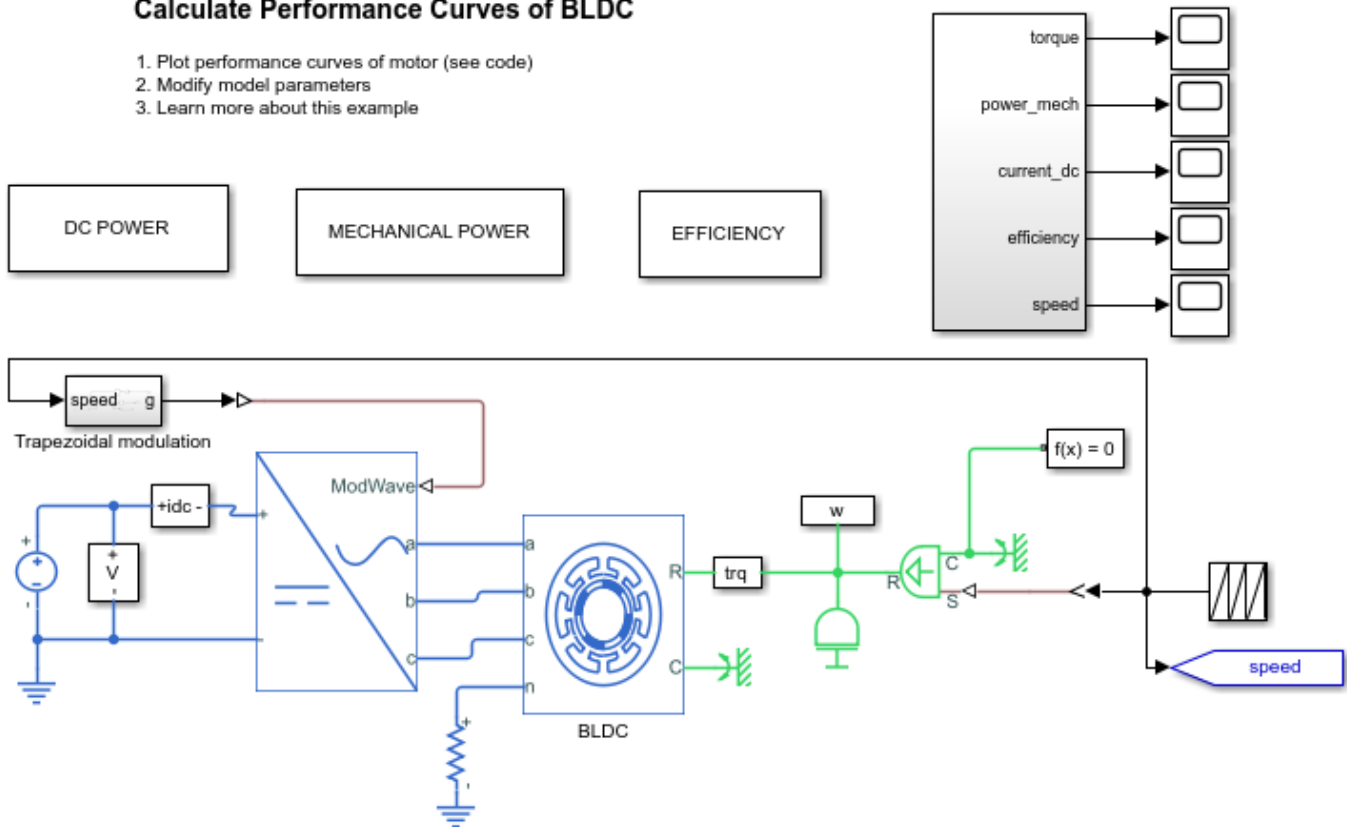
Simulation Results

The plot below shows the performance curves of BLDC using ideal input.
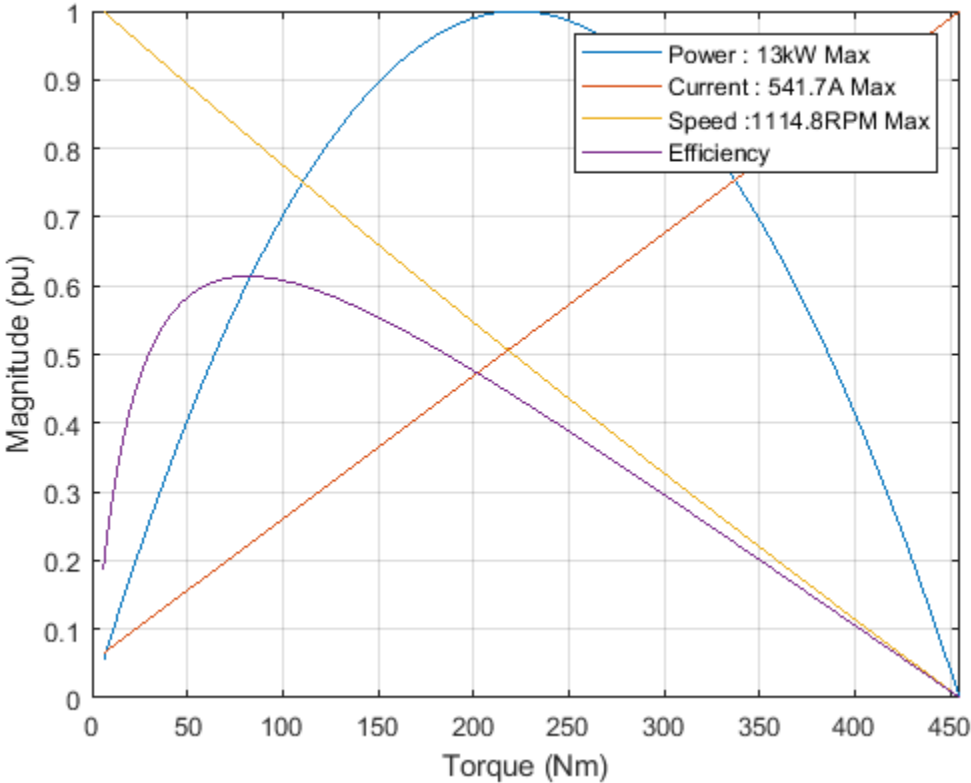
Single-Phase PMSM Control

This example shows how to control the rotor speed in an electrical drive based on a single-phase permanent magnet synchronous motor by using open loop control. An ideal torque source provides the load. The Commutation subsystem uses a Hall signal to compute the gate signals. An H-Bridge feeds the SPPMSM.

Model
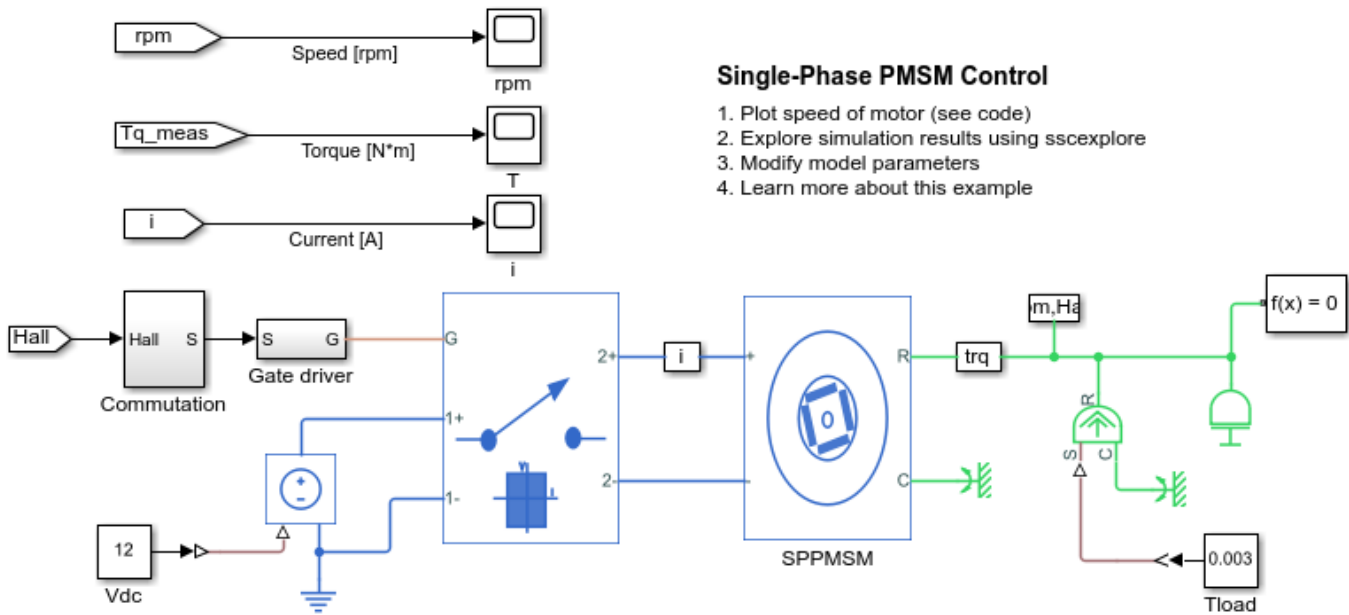
Simulation Results from Simscape Logging

The plot below shows the measured speed for the test.

Five-Phase PMSM Velocity Control

This example shows how to control the rotor angular velocity in an electrical-traction drive based on a five-phase permanent magnet synchronous machine (PMSM). A DC voltage source feeds the PMSM through a controlled five-phase converter. The PMSM operates in both motoring and generating modes according to the load. An ideal torque source provides the load. The Scopes subsystem contains scopes that allow you to see the simulation results. The Control subsystem includes a PI-based cascade control structure that has an outer angular-velocity-control loop and four inner current-control loops. During the one second simulation, the angular velocity demand is 0 rpm, 500 rpm, 2000 rpm, and then 3000 rpm.

Model

Simulation Results from Simscape Logging

The plot below shows the requested and measured speed for the test, as well as the phase currents in the electric drive.
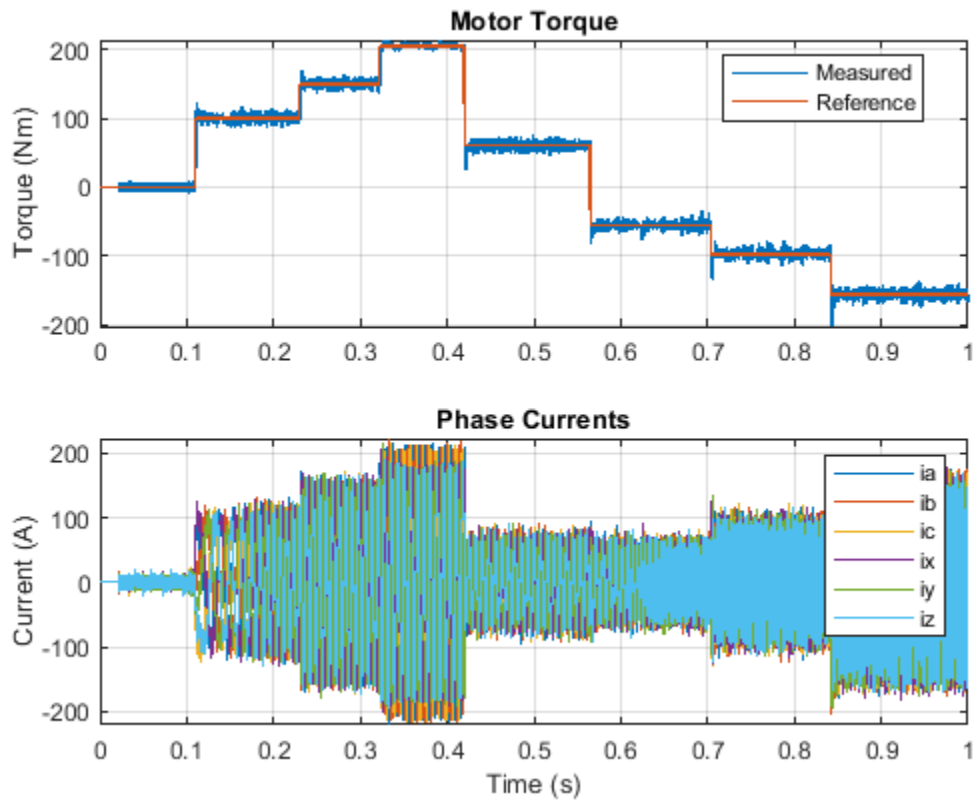
Five-Phase PMSM Torque Control

This example shows how to control the torque in an electrical-traction drive based on a five-phase permanent magnet synchronous machine (PMSM). A DC voltage source feeds the PMSM through a controlled five-phase converter. The PMSM operates in both motoring and generating modes according to the load. An ideal angular velocity source provides the load. The Control subsystem uses an open-loop approach to control the PMSM torque and a closed-loop approach to control the current. At each sample instant, the torque request is converted to a relevant q-axis current reference. The current control is PI-based. The simulation uses several torque steps in both motor and generator modes. The Scopes subsystem contains scopes that allow you to see the simulation results.

Model
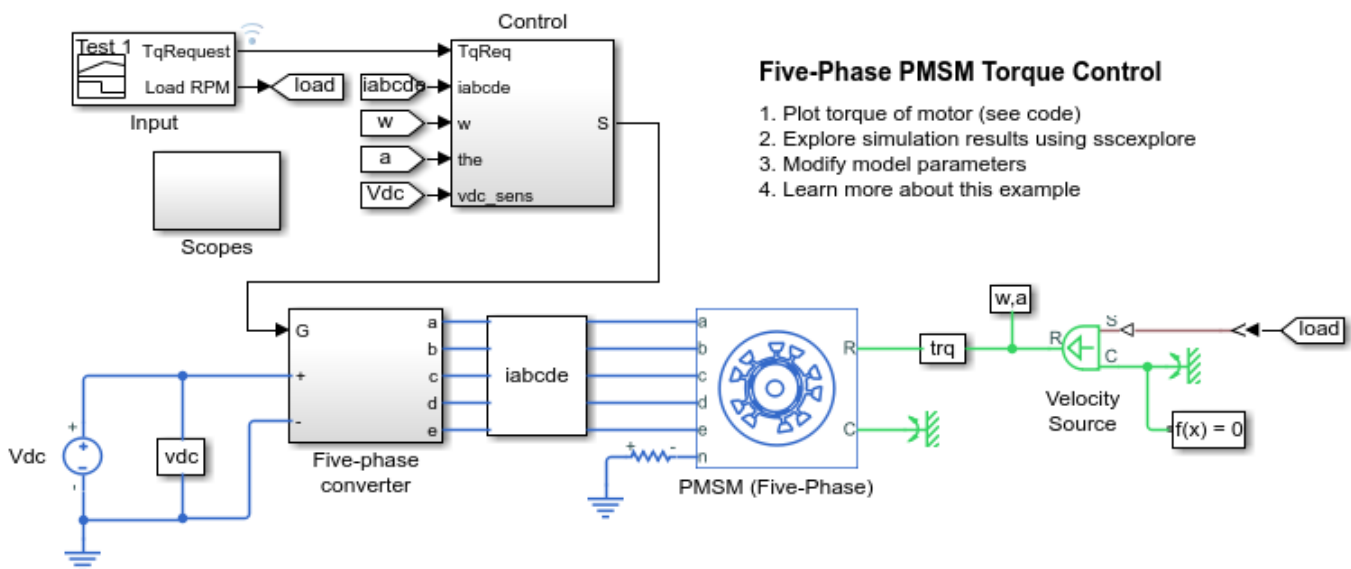
Simulation Results from Simscape Logging

The plot below shows the requested and measured torque for the test, as well as the phase currents in the electric drive.
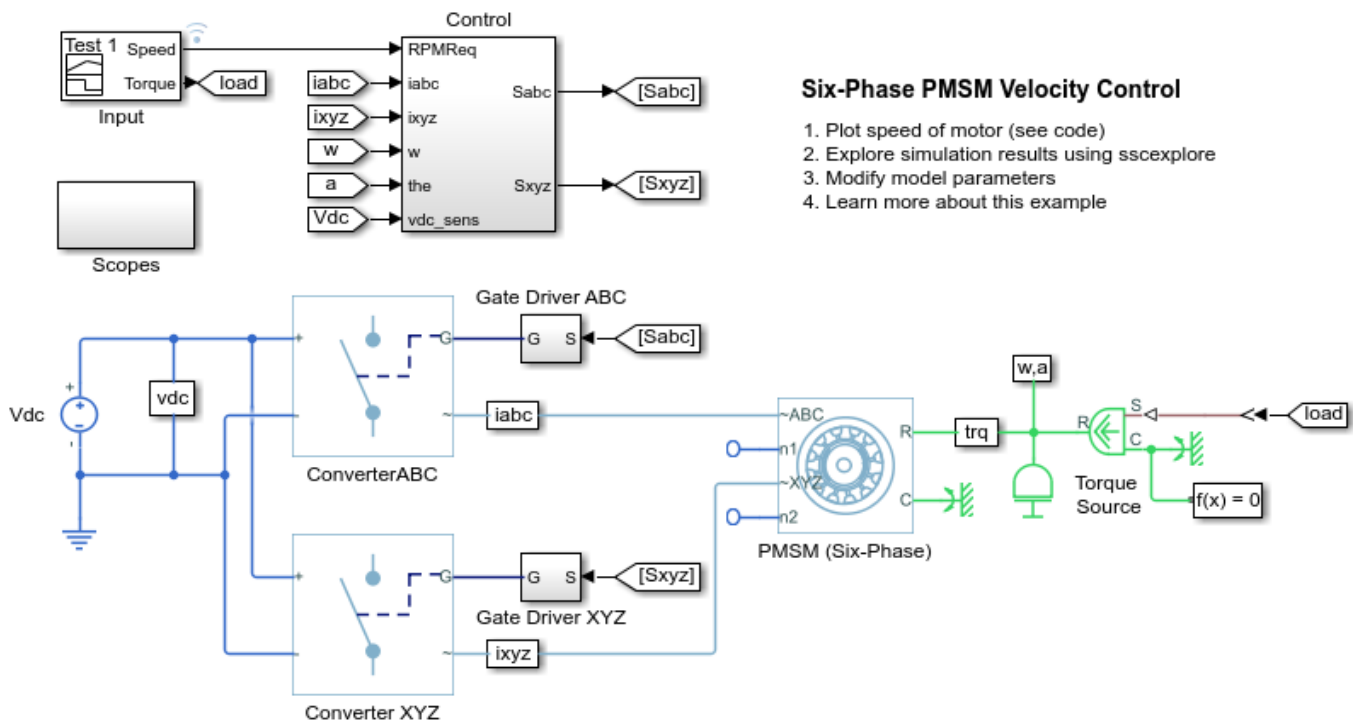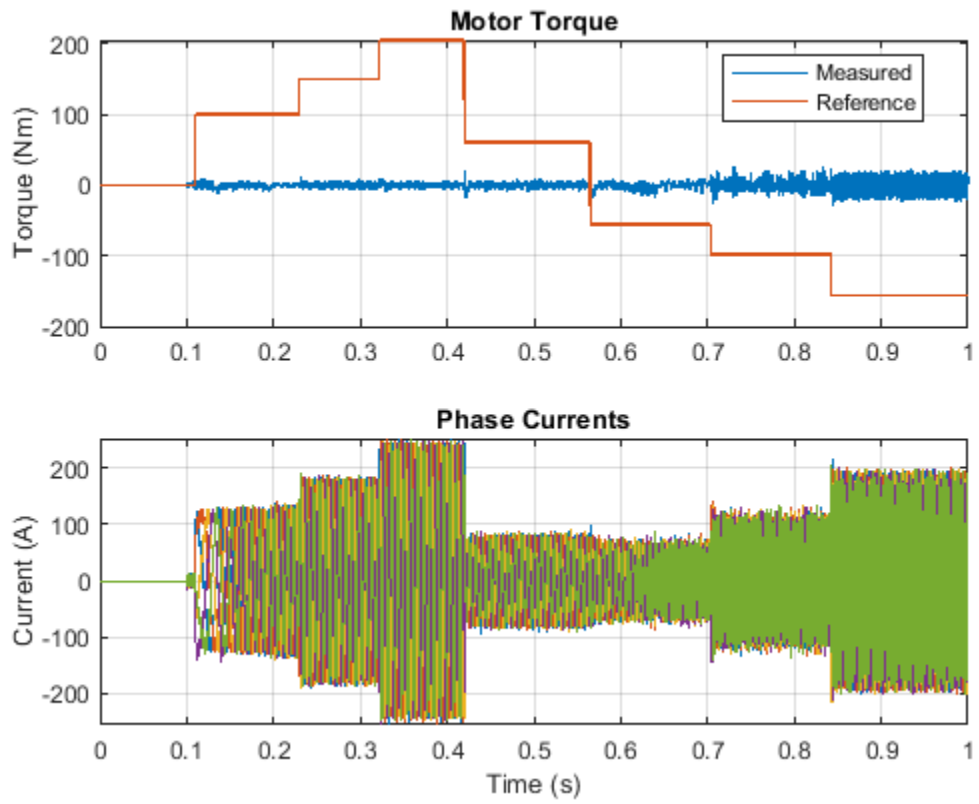
Three-Phase PMLSM Drive

This example shows how to control the position in a three-phase permanent magnet linear synchronous machine (PMLSM) drive. The C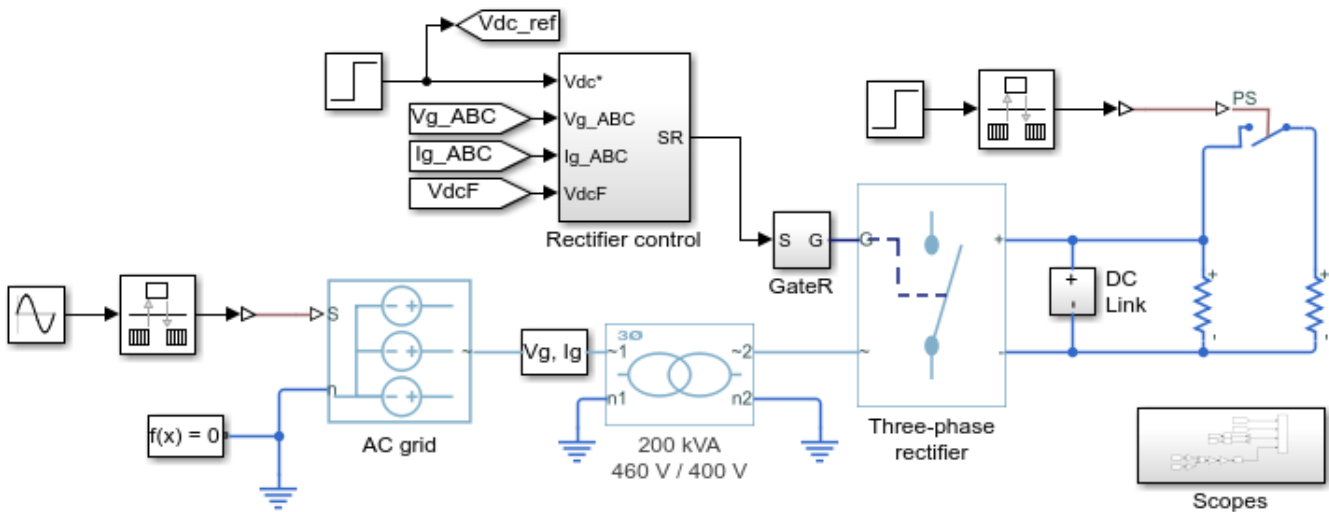ontrol subsystem uses a PI-based cascade control structure with three control loops, an outer position control loop, a speed control loop and two inner current control loops. A controlled three-phase converter feeds the PMLSM. The simulation uses step references. The Scopes subsystem contains scopes that allow you to see the simulation results.
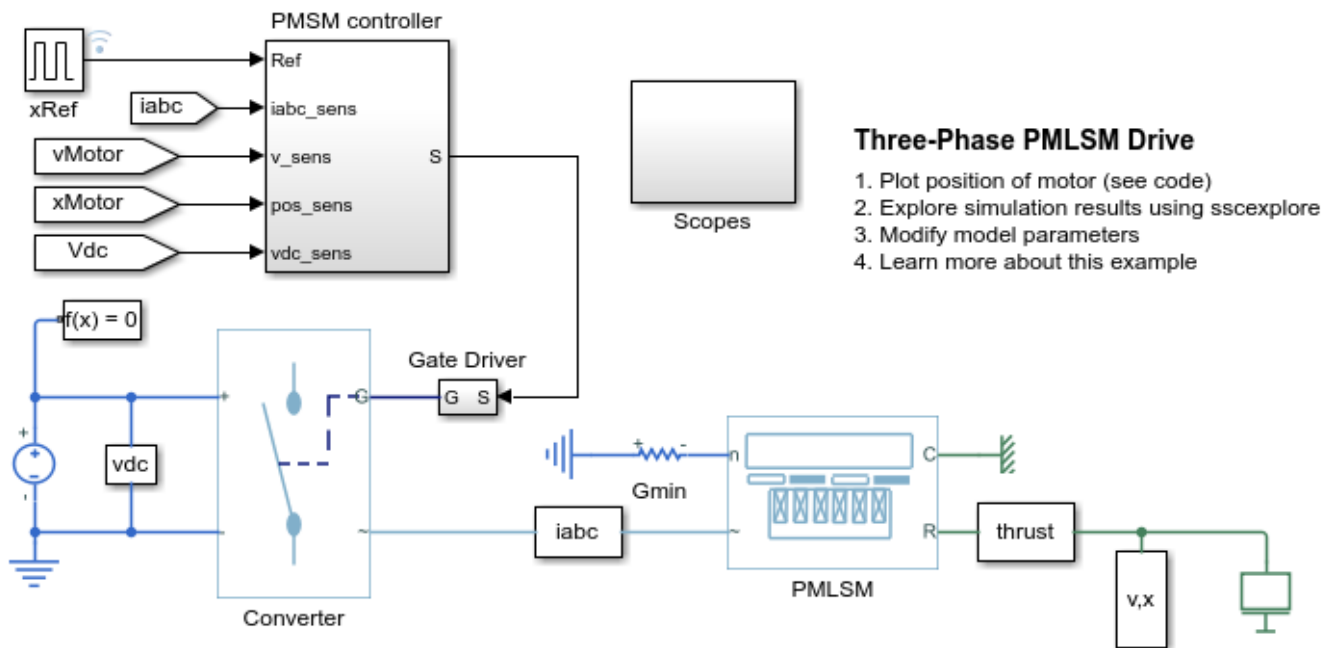
Model

Simulation Results from Simscape Logging

The plot below shows the requested and measured position for the test, as well as the linear speed in the electric drive.
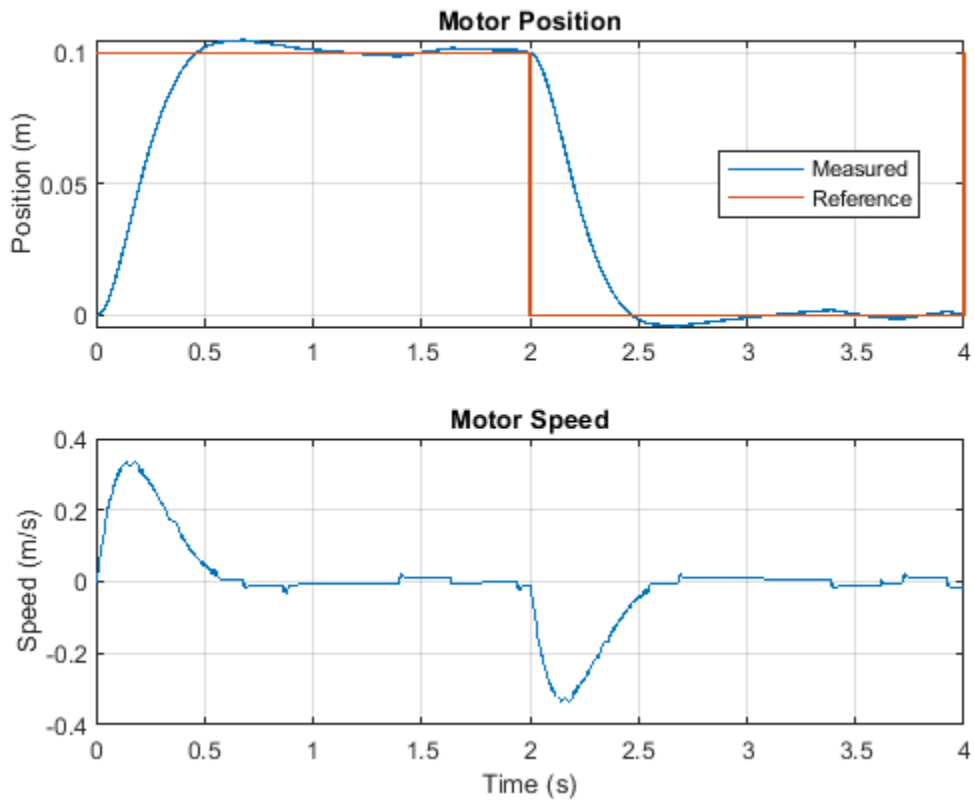
DC-DC Converter Model Fidelity Comparison

This example shows how to use different levels of fidelity in DC-DC converters. The system contains three buck-boost converters. The top converter uses an ideal switch at a sample time of 10 us. To yield accurate results even though the model is under sampled at a sample time of 50 us, the middle converter uses an averaged switch with averaged pulse. To further increase the sample rate and to operate as an ideal averaged converter, the bottom converter uses an averaged switch and duty cycle instead of gate pulse. The Control subsystem contains a PWM generator. The Scopes subsystem contains Scope blocks that allow you to see the simulation results.

Model

Simulation Results from Simscape Logging

The plot below shows the comparison of the currents on both sides of the converter.

The plot below shows the comparison of the voltages on both sides of the converter.

Chopper Model Fidelity Comparison

This example shows how to use different levels of fidelity in chopper converters. The system contains three four-quadrant choppers. The top converter uses ideal switches at a sample time of 10 us. To yield accurate results even though the model is under sampled at a sample time of 50 us, the middle chopper uses averaged switches with averaged pulses. To further increase the sample rate and to operate as an ideal averaged chopper, the bottom converter uses averaged switches and duty cycles instead of gate pulses. The Control subsystem contains a PWM generator. The Scopes subsystem contains Scope blocks that allow you to see the simulation results.
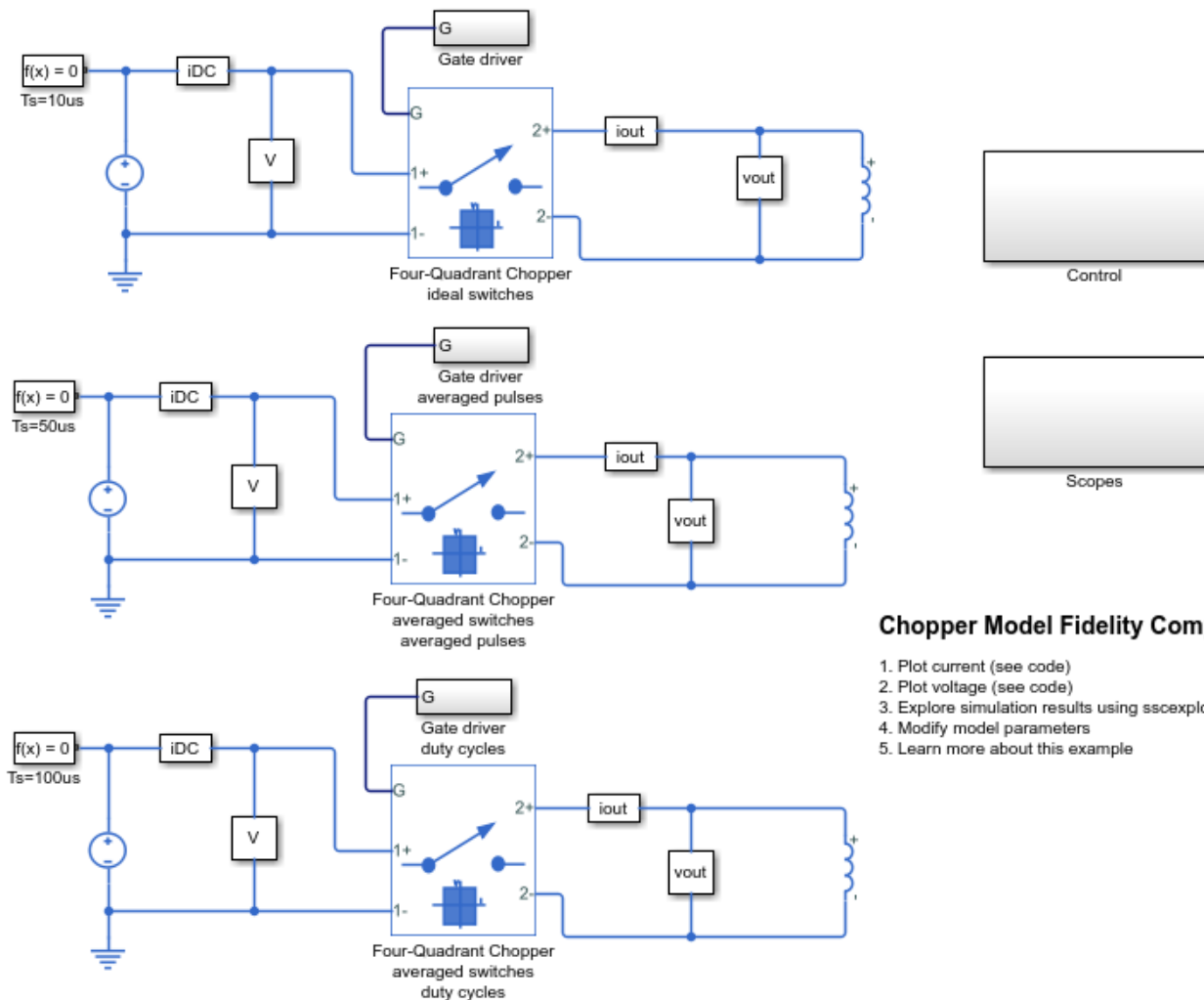
Model

Chopper Model Fidelity Comparison

1. Plot current (see code)
2. Plot voltage (see code)
3. Explore simulation results using sscexplore
4. Modify model parameters
5. Learn more about this example

Simulation Results from Simscape Logging

The plot below shows the comparison of the currents on both sides of the converter..
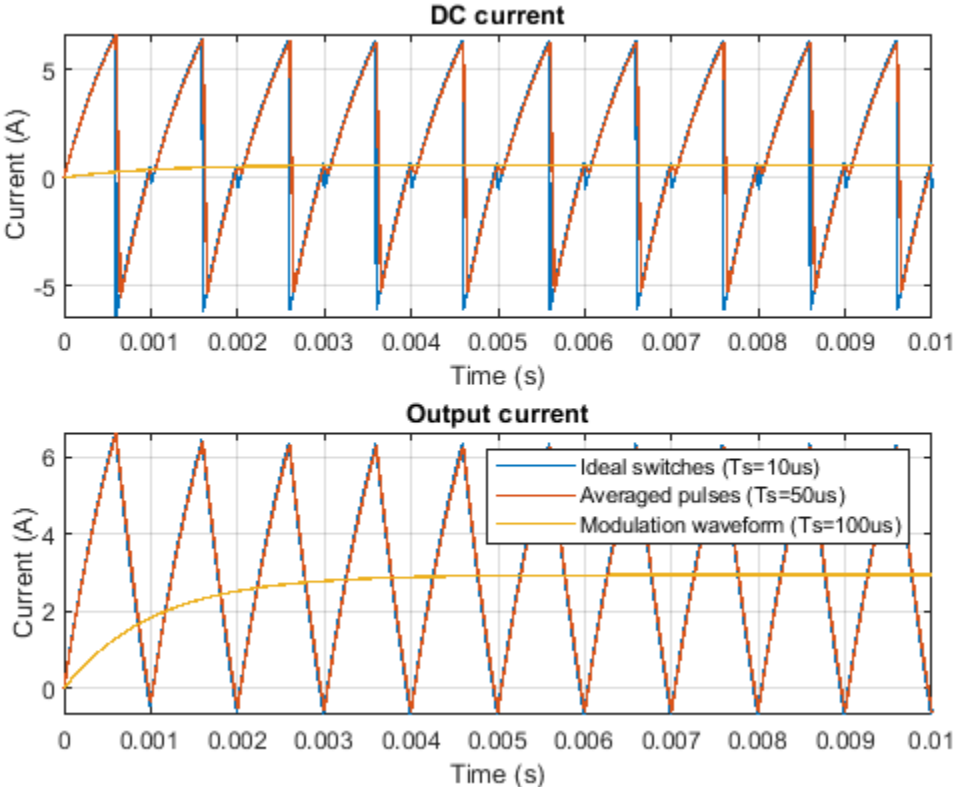
The plot below shows the comparison of the voltages on both sides of the converter.
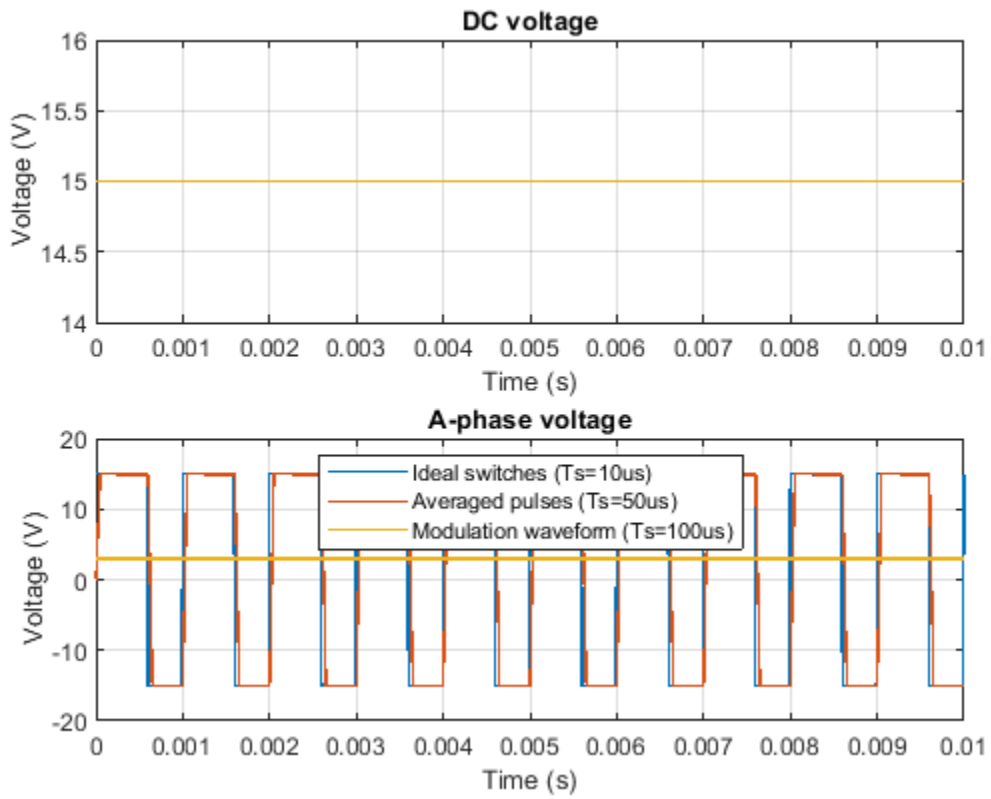
Single-Phase Grid-Connected Solar Photovoltaic System

This example shows how to model a rooftop single-phase grid-connected solar photovoltaic (PV) system. This example supports design decisions about the number of panels and the connection topology required to deliver the target power. The model represents a grid-connected rooftop solar PV that is implemented without an intermediate DC-DC converter. To parameterize the model, the example uses information from a solar panel manufacturer datasheet. Solar power is injected into the grid with unity power factor (UPF).

To track the maximum power point (MPP), the example uses these maximum power point tracking (MPPT) techniques:

- Incremental conductance
- Perturbation and observation

Three inverter options are available:

- Average
- Continuous
- Discrete

This example linearizes the system to generate an open-loop Bode plot from which phase and gain margin can be determined.

To open a script that provides information on the parameterization, features, and options in this example, at the MATLAB® command line, enter: edit 'ee_solar_gridconnected_singlephase_data'

PV System Model

```
*******************************************************************************************
****           For the given solar panel, estimated boostless PV plant parameters        ****
*******************************************************************************************
*** Power rating input from the user = 4.70 kW
*** Minimum number of panel required per string = 17
*** Maximum number of panel connected per string without reaching maximum system voltage = 27
*** Minimum power rating of the boost-less solar PV plant = 3.83 kW
*** Maximum power possible per string without reaching maximum DC voltage = 6.08 kW
*** Actual number of panel per string = 21
*** Number of strings connected in parallel = 1
*** Actual solar PV plant power = 4.73 kW
*******************************************************************************************
```
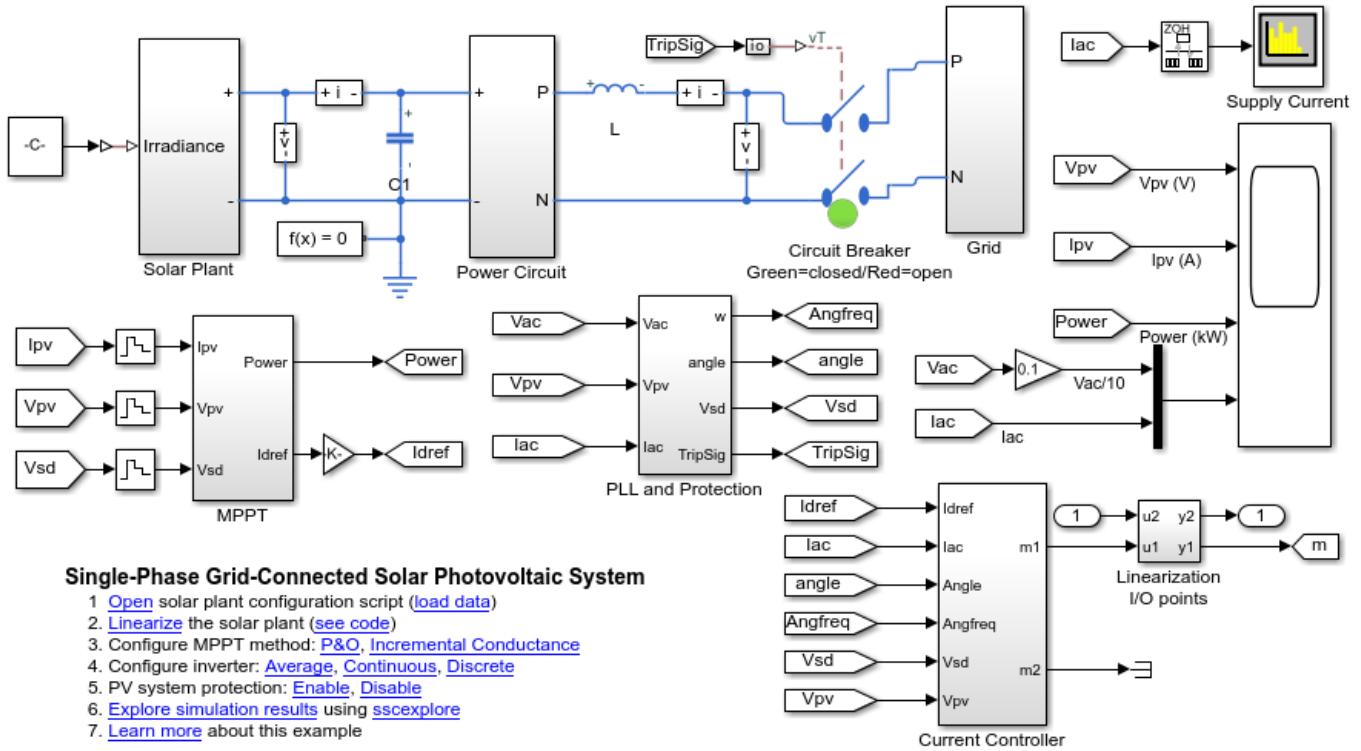
Single-Phase Grid-Connected Solar Photovoltaic System

1. Open solar plant configuration script (load data)
2. Linearize the solar plant (see code)
3. Configure MPPT method: P&O, Incremental Conductance
4. Configure inverter: Average, Continuous, Discrete
5. PV system protection: Enable, Disable
6. Explore simulation results using sscexplore
7. Learn more about this example

Solar Plant Subsystem

The solar plant subsystem models a solar plant that contains parallel-connected strings of solar panels. The solar panel is modeled using the Solar Cell block from the Simscape™ Electrical™ library. The number of series-connected solar panels in a string is estimated based on supply voltage, voltage drop across the line inductor, supply voltage fluctuation, open circuit voltage dependence on temperature and irradiance. The number of solar panel strings connected in parallel is estimated based on the plant power rating. Connecting multiple panels can slow the simulation because it

increases the number of elements in a model. By assuming uniform irradiance and temperature across all the solar panels, the number of solar elements can be reduced by using the controlled current and voltage sources as shown in the solar panel subsystem.

Maximum Power Point Tracking (MPPT)

Two MPPT techniques are implemented. By using the variant variable 'MPPT', you can choose incremental conductance MPPT or perturbation and observation MPPT. For perturbation and observation, set the variable MPPT to zero and MPPT to one for incremental conductance. The maximum power point tracking block influences the current that is supplied to the grid. To increase the voltage across the PV panel string, current supplied to the grid is reduced to inject more current into the DC bus capacitor.

Open Loop Bode Plot

Before linearizing the system, to disconnect the MPPT outer loop and break the current inner current loop, set the workspace variable 'closeLoop' to zero and use the average inverter model.

To use an average mode inverter, set the variant workspace variable 'powerCircuit' to zero.

Frequency Response (Grid-Connected Solar PV)

Three-Phase Grid-Connected Solar Photovoltaic System

This example shows how to model a three-phase grid-connected solar photovoltaic (PV) system. This example supports design decisions about the number of panels and the connection topology required to deliver the target power. The model represents a grid-connected rooftop solar PV that is implemented without an intermediate DC-DC converter. To parameterize the model, the example uses information from a solar panel manufacturer datasheet. Solar power is injected into the grid with unity power factor (UPF).

To track the maximum power point (MPP), the example uses these maximum power point tracking (MPPT) techniques:

- Incremental conductance
- Perturbation and observation

Three inverter options are available:

- Average
- Two-level
- Three-level

This example linearizes the system to generate an open-loop Bode plot from which phase and gain margin can be determined.

To open a script that provides information on the parameterization, features, and options in this example, at the MATLAB® command line, enter: edit 'ee_solar_gridconnected_threephase_data'

PV System Model

```
********************************************************************************************
****            For the given solar panel, estimated boostless PV plant parameters       ****
********************************************************************************************
*** Power rating input from the user = 35.00 kW
*** Minimum number of panel required per string = 33
*** Maximum number of panel connected per string without reaching maximum system voltage = 41
*** Minimum power rating of the boost-less solar PV plant = 7.43 kW
*** Maximum power possible per string without reaching maximum DC voltage = 9.23 kW
*** Actual number of panel per string = 39
*** Number of strings connected in parallel = 4
*** Actual solar PV plant power = 35.12 kW
********************************************************************************************
```
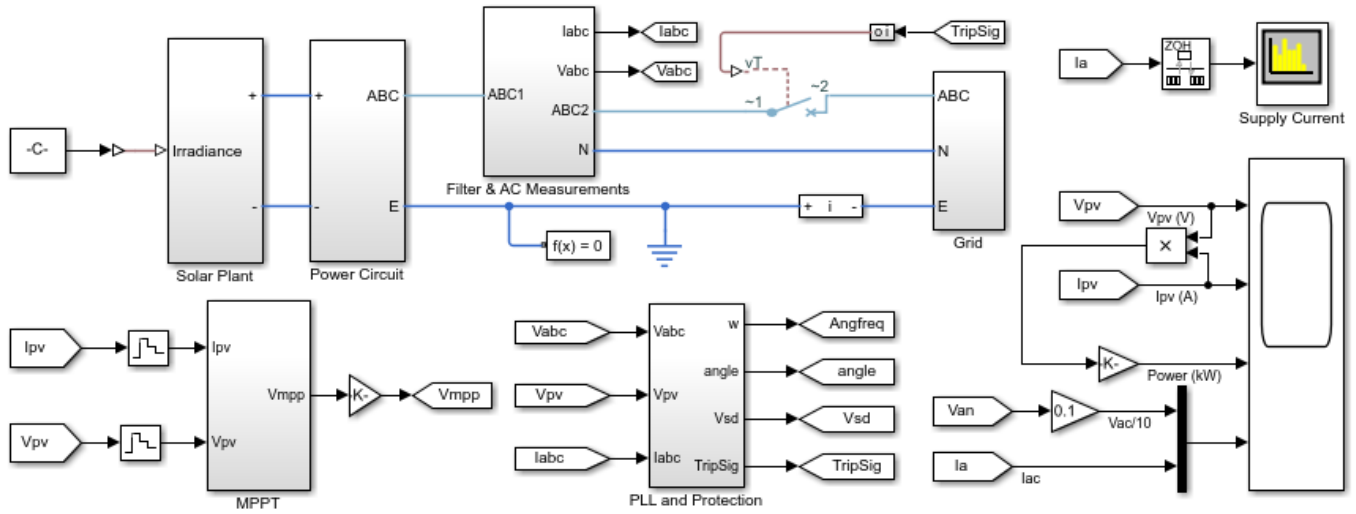
Three-Phase Grid-Connected Solar Photovoltaic System

1. Open solar plant configuration script (load data)
2. Linearize the solar plant (see code)
3. Configure MPPT method: P&O, Incremental Conductance
4. Configure inverter: Average, Two Level, Three Level
5. PV system protection Enable, Disable
6. Explore simulation results using sscexplore
7. Learn more about this example

Solar Plant Subsystem

The solar plant subsystem models a solar plant that contains parallel-connected strings of solar panels. The solar panel is modeled using the Solar Cell block from the Simscape™ Electrical™ library. The number of series-connected solar panels in a string is estimated based on supply voltage, voltage drop across the line inductor, supply voltage fluctuation, open circuit voltage dependence on temperature and irradiance. The number of solar panel strings connected in parallel is estimated based on the plant power rating. Connecting multiple panels can slow the simulation because it increases the number of elements in a model. By assuming uniform irradiance and temperature across all the solar panels, the number of solar elements can be reduced by using the controlled current and voltage sources as shown in the solar panel subsystem. Parasitic capacitance of solar panel is modelled using two lumped capacitors connected in both the terminal of the solar plant.
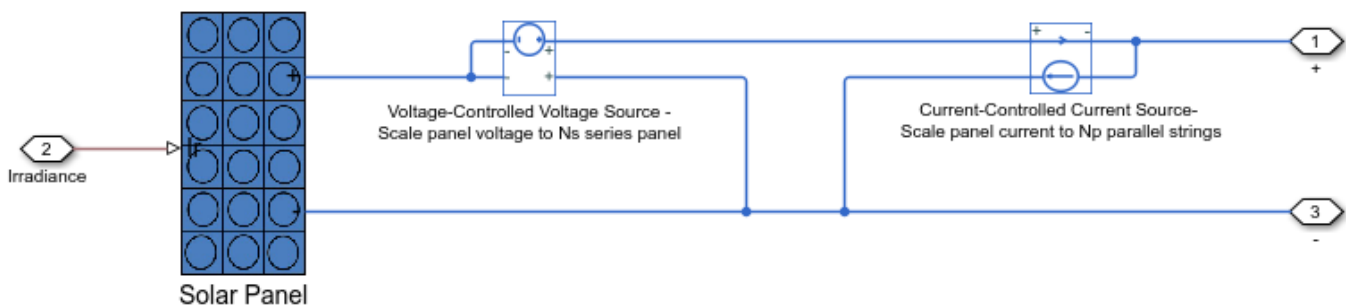
Maximum Power Point Tracking (MPPT)

Two MPPT techniques are implemented. By using the variant variable 'MPPT', you can choose incremental conductance MPPT or perturbation and observation MPPT. For perturbation and observation, set the variable MPPT to zero and MPPT to one for incremental conductance. The voltage controller that tracks the maximum power point influences the current that is supplied to the grid.

Open Loop Bode Plot

Before linearizing the system, to disconnect the MPPT outer loop and break the current inner current loop, set the workspace variable 'closeLoop' to zero and use the average inverter model.

To use an average mode inverter, set the variant workspace variable 'powerCircuit' to zero.

Frequency Response of Inner Current Loop of Grid-Connected Solar PV
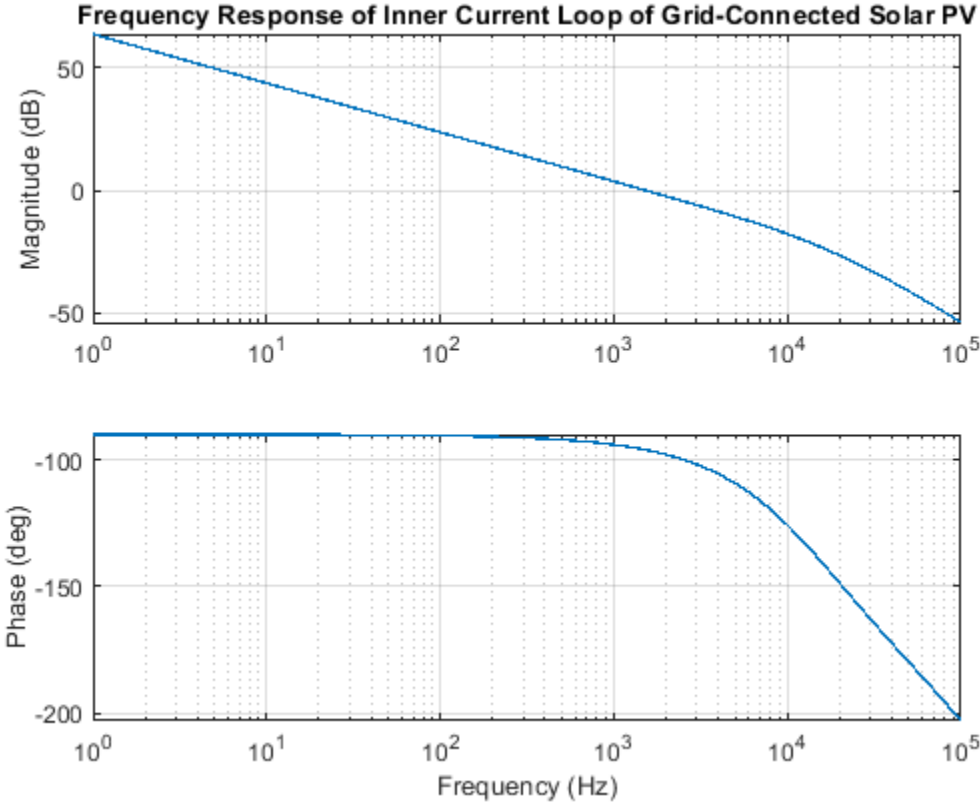
Solar PV System with MPPT Using Boost Converter

This example shows the design of a boost converter for controlling the power output of a solar PV system and helps you to:

- Determine how the panels should be arranged in terms of the number of series-connected strings and the number of panels per string to achieve the required power rating.
- Implement the MPPT algorithm using boost converter.
- Operate the solar PVsystem in the voltage control mode.
- Select a suitable `proportional` gain (K_v) and `phase-lead time constant` (T_v) for the PI controller, $\frac{k_v(sT_v+1)}{sT_v}$.

The DC load is connected across the boost converter output. The solar PV system operates in both maximum power point tracking and de-rated voltage control modes. To track the maximum power point (MPP) of the solar PV, you can choose between two maximum power point tracking (MPPT) techniques:

- Incremental conductance
- Perturbation and observation

You can specify the output DC bus voltage, solar PV system operating temperature and solar panel specification. Solar panel manufacturer data is used to determine number of PV panels required to deliver the specified generation capability.

Solar PV System with MPPT Using Boost Converter

To open a script that designs the Solar PV System with MPPT Using Boost Converter, at the MATLAB® command line, enter: edit 'ee_solar_boostconverter_maxpowerpoint_data'

The chosen solar PV plant parameters are:

```
**************************************************************************************************
****                 PV Plant Parameters for the Specified Solar Panel                  ****
**************************************************************************************************
*** Power rating input from the user  =  2.00 kW
*** Minimum number of panel required per string  =  8
*** Maximum number of panel connected per string without reaching maximum voltage  =  10
*** Minimum power rating of the solar PV plant  =  1.80 kW
*** Maximum power possible per string without reaching maximum DC voltage  =  2.25 kW
*** Actual number of panel per string  =  9
*** Number of strings connected in parallel  =  1
*** Actual solar PV plant power  =  2.03 kW
**************************************************************************************************
```
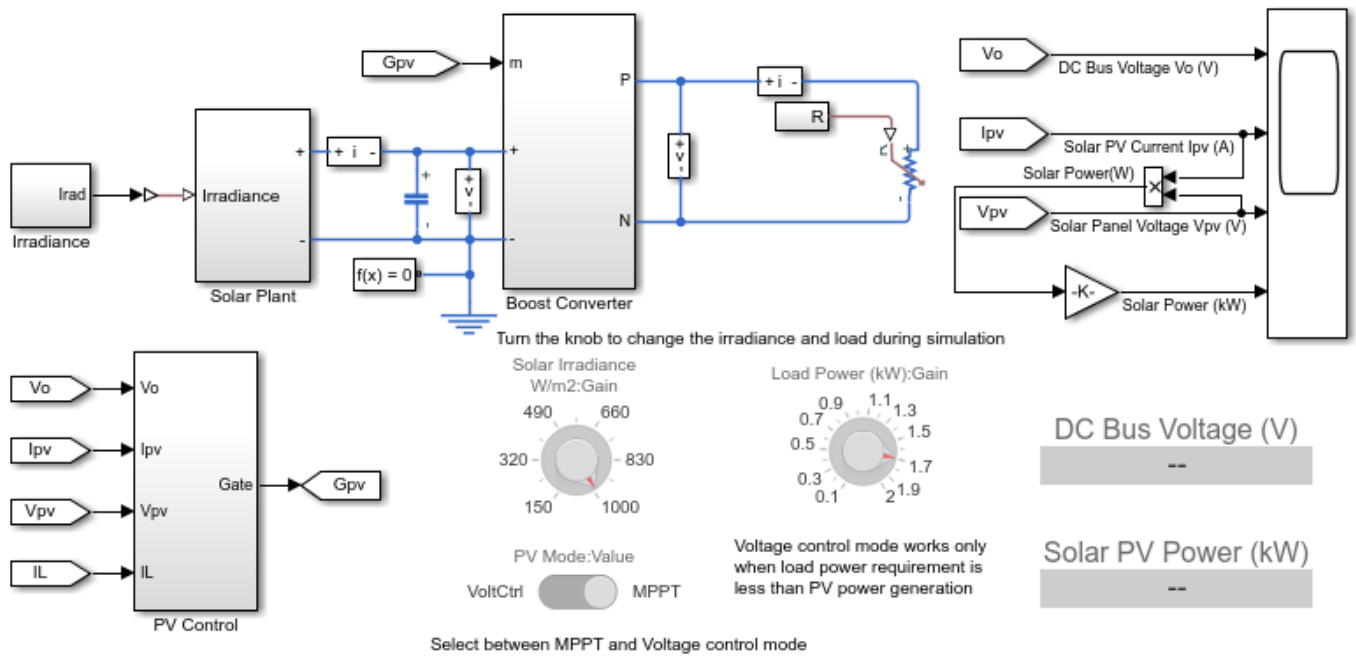
Solar PV System with MPPT Using Boost Converter

1 Open script that inputs values for solar plant (load data)
2. Configure MPPT P&O, Incremental Conductance
3. Explore simulation results using sscexplore
4. Learn more about this example

Solar Plant Subsystem

The solar plant subsystem models a solar plant that contains parallel-connected strings of solar panels. The solar panel is modeled using the Solar Cell block from the Simscape™ Electrical™ library. Given the specified DC bus voltage, solar cell characteristics and specified power rating, a calculation is made of the solar panel string length and number of parallel-connected strings. Connecting multiple panels can slow the simulation because it increases the number of elements in a model. By assuming uniform irradiance and temperature across all the solar panels, it is possible to reduce the number of solar elements by using the controlled current and voltage sources as shown in the solar panel subsystem.
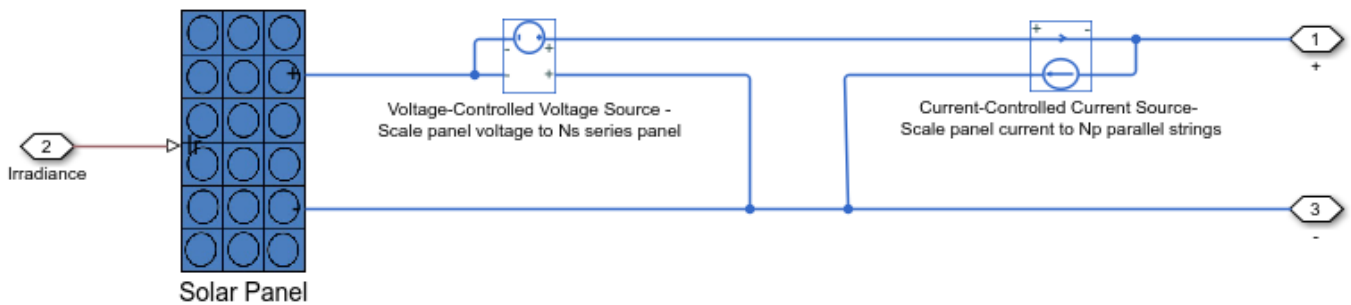
Maximum Power Point Tracking (MPPT)

Two MPPT techniques are implemented using the variant subsystem. Set the variant variable MPPT to 0 to choose the perturbation and observation MPPT method. Set the variable MPPT to 1 to choose the incremental conductance method.

Intermediate Boost DC-DC Converter

A boost DC-DC converter is used to control the solar PV power. The boost converter operates in both MPPT mode and voltage control mode. The voltage control mode is used only when load power is less than the maximum power generated by solar PV plant given the incident irradiance and panel temperature.
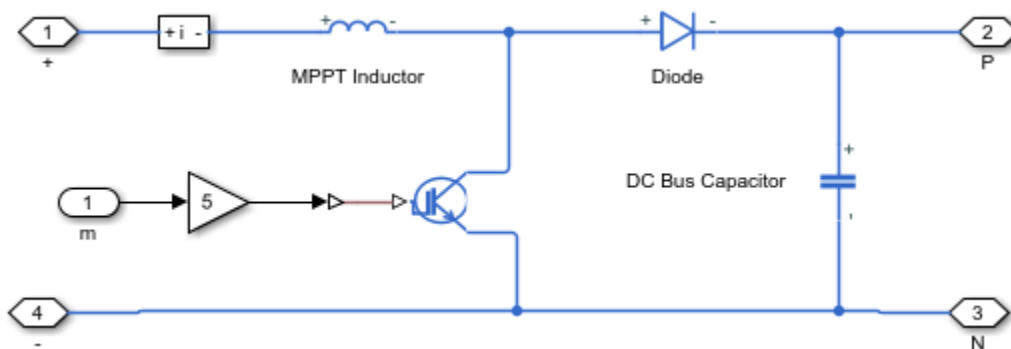
Simulation Output (MPPT Mode)
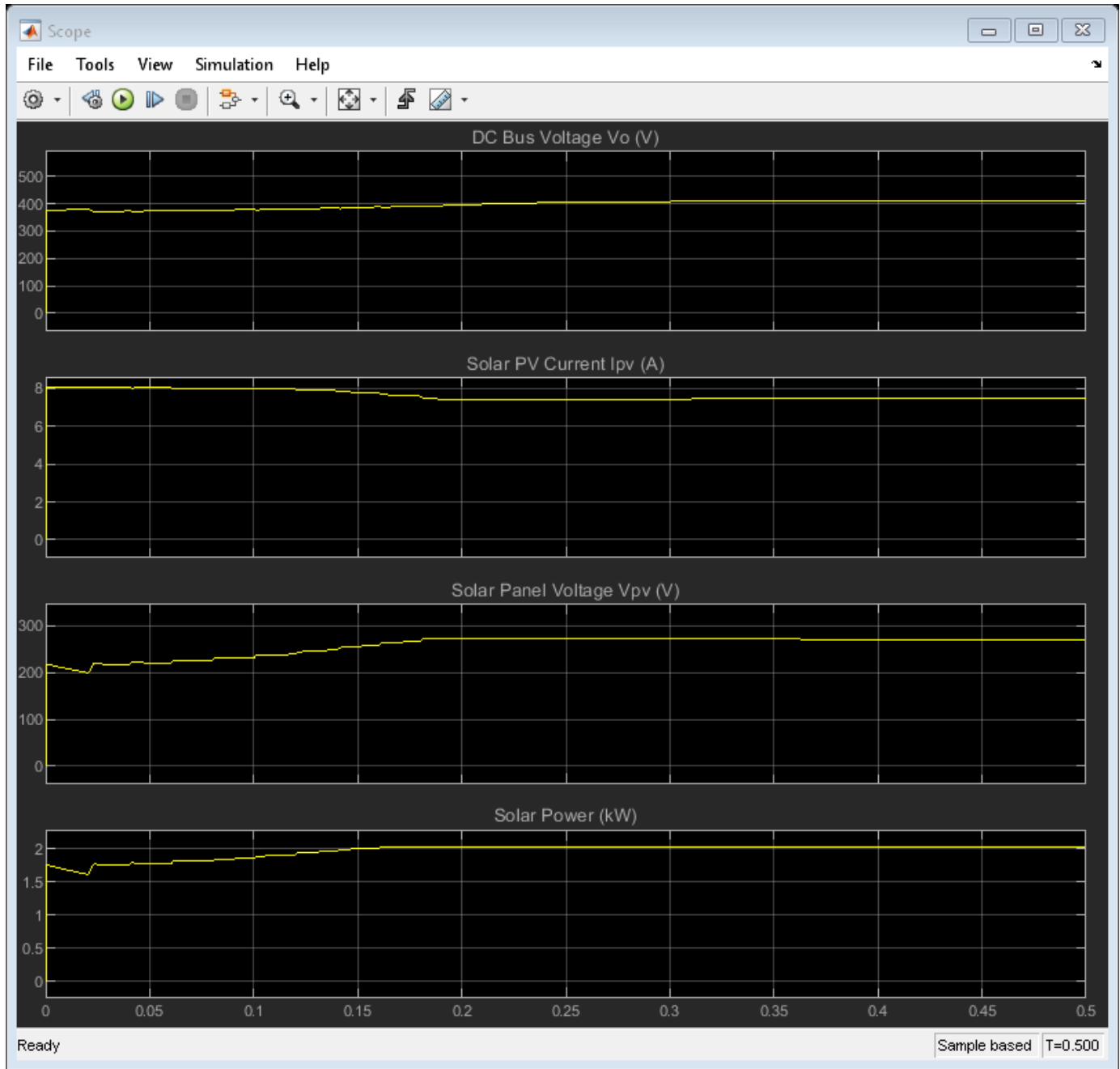
Stand-Alone Solar PV DC Power System with Battery Backup

This example shows the design of a stand-alone PV DC power system with battery backup and helps you to:

- Choose the necessary battery rating based on the connected load profile and available solar power.
- Determine how the panels should be arranged in terms of the number of series-connected strings and the number of panels per string.
- Select a proper PI controller proportional gain, (K_v), and phase-lead constant, (T_v).

Both solar PV and battery storage support stand-alone loads. The load is connected across the constant DC output. A solar PV system operates in both maximum power point tracking and de-rated voltage control modes. The battery management system uses bi-directional DC-DC converters.

A stand-alone PV system requires six normal operating modes based on the solar irradiance, generated solar power, connected load, state of charge of the battery, maximum battery charging and discharging current limits.

To track the maximum power point (MPP) of solar PV, you can choose between two maximum power point tracking (MPPT) techniques:

- Incremental conductance
- Perturbation and observation

You can specify the average daily connected load profile, region's daily available average solar energy (kWhr), solar PV system operating temperature, day of autonomy, battery recharge time, output DC voltage and solar panel specification. solar panel manufacturer data is used to determine number of PV panels required to deliver the specified generation capability.

PI controller of the form $\frac{K_v(sT_v+1)}{sT_v}$ is chosen to control the solar PV and battery management system (BMS).

This example uses:

- A MATLAB® live script to design the overall standalone PV system.
- Simulink® to design/simulate the control logic for the system.
- Simscape™ to simulate the power circuit.
- Stateflow™ to implement the supervisory control logic.

Stand-Alone PV DC Power System Model

To open a script that designs the standalone PV DC power system, at the Matlab® command line, enter: edit 'ee_solar_standalone_dcsystem_withbatterybackup_data'

The chosen battery and solar PV plant parameters are:

```
*********************************************************************************************
****          For the Given Stand-Alone PV System, Battery Sizing Parameters          ****
*********************************************************************************************
*** Calculated amphr of the battery  =  542.91 Ahr
*** Battery nominal voltage  =  78 V
```

```
*** Battery voltage at 80% discharge  =  70.20 V
*** Number of required battery cell  =  39.00
*** Average discharge current  =  4.28 A
********************************************************************************
********************************************************************************
****            For the Given Solar Panel, PV Plant Parameters        ****
********************************************************************************
*** Required PV Power rating  =  9.36 kW
*** Minimum number of panels required per string  =  8
*** Maximum number of panels connected per string without reaching maximum voltage  =  10
*** Minimum power rating of the solar PV plant  =  1.80 kW
*** Maximum power possible per string without reaching maximum DC voltage  =  2.25 kW
*** Actual number of panels per string  =  8
*** Number of strings connected in parallel  =  5
*** Actual solar PV plant power  =  9.01 kW
********************************************************************************
********************************************************************************
****                    Battery Charging/Discharging Parameters                    ****
********************************************************************************
Reference battery charging current = 45.24 A
Maximum battery charging current = 128.29 A
Maximum battery discharging current = 64.14 A
Maximum battery charging Power = 10.01 kW
Maximum battery discharging Power = 5.00 kW
********************************************************************************
```


Stand-Alone Solar PV DC Power System with Battery Backup
1 Open script that inputs values for solar plant (load data)
2. Configure MPPT P&O, Incremental Conductance
3. Explore simulation results using sscexplore
4. Learn more about this example

Stand-Alone Solar PV DC Power System Monitoring Panel

This example uses the Simulink® Dashboard feature to display all the real time system parameters. Turn the dashboard knob in the monitoring panel to modify the solar irradiance and the load during the simulation. By changing these parameters, you can observe how the PV system switches between the operating modes.

Solar Plant Subsystem

The solar plant subsystem models a solar plant that contains parallel-connected strings of solar panels. The solar panel is modeled using the Solar Cell block from the Simscape™ Electrical™ library. This example uses the output voltage from the DC bus, open circuit voltage depending on temperature and irradiance to estimate the number of solar panel strings connected in series, and the plant power rating to estimate the number of solar panel strings connected in parallel. Connecting multiple panels can slow the simulation because it increases the number of elements in a model. By assuming uniform irradiance and temperature across all the solar panels, it is possible to reduce the number of solar elements by using the controlled current and voltage sources as shown in the solar panel subsystem.

Solar Panel

Maximum Power Point Tracking (MPPT)

Two MPPT techniques are implemented using the variant subsystem. Set the variant variable MPPT to 0 to choose the perturbation and observation MPPT. Set the variable MPPT to 1 to choose incremental conductance.

Intermediate Boost DC-DC Converter

Boost DC-DC converter is used to control the solar PV power. When battery is not fully charged solar PV plant operated in maximum power point. When battery is fully charged and load is smaller than the PV power, solar PV is operated in constant output DC bus voltage control mode.

Battery Management System (BMS)

The battery management system uses a bi-directional DC-DC converter. The battery is charged by the buck converter configuration and it is discharged using the boost converter configuration. To improve battery performance and life cycle, systems with battery backup have limited maximum battery charging and discharging current. This example sets a limit on the maximum amount of power that a battery can supply to the load and absorb from the solar PV source. Here, the maximum charging power is equal to the solar plant capacity at the standard test condition. The chosen maximum charging power should be able to recharge the battery sooner than the battery recharge time specified by the user

Here, separate controller is used for charging and discharging operation. BMS controller have two loops, an outer voltage loop and inner current loop.

Bi-directional converter for energy management system (EMS)

Supervisory Control(Mode Control) Parameters

Stand-alone PV system in this example comprises seven operating modes. These modes are selected based on DC bus voltage, solar irradiance and state of charge of the battery. The DC bus voltage level is used as a measure to detect a load imbalance. If the DC bus voltage is greater than V_{dc-max}, the system is generating more power than what the load is requiring. If the DC bus voltage is less than V_{dc-min}, then the load is requiring more power than what the system is generating.

DC bus voltage level (V_{dc}), solar irradiance $(Irrad)$ and the battery state of charge (SoC) are used to decide the suitable operating mode.

Operating modes of the stand-alone PV DC System are:

- Mode-0 - Start mode (Default simulation starting mode)
- Mode-1 - PV in output voltage control, battery fully charged and isolated
- Mode-2 - PV in maximum power point, battery is charging
- Mode-3 - PV in maximum power point, battery is discharging
- Mode-4 - Night mode, PV shutdown, battery is discharging
- Mode-5 - Total system shutdown
- Mode-6 - PV in maximum power point, battery is charging, load is disconnected

Stateflow mode control diagram
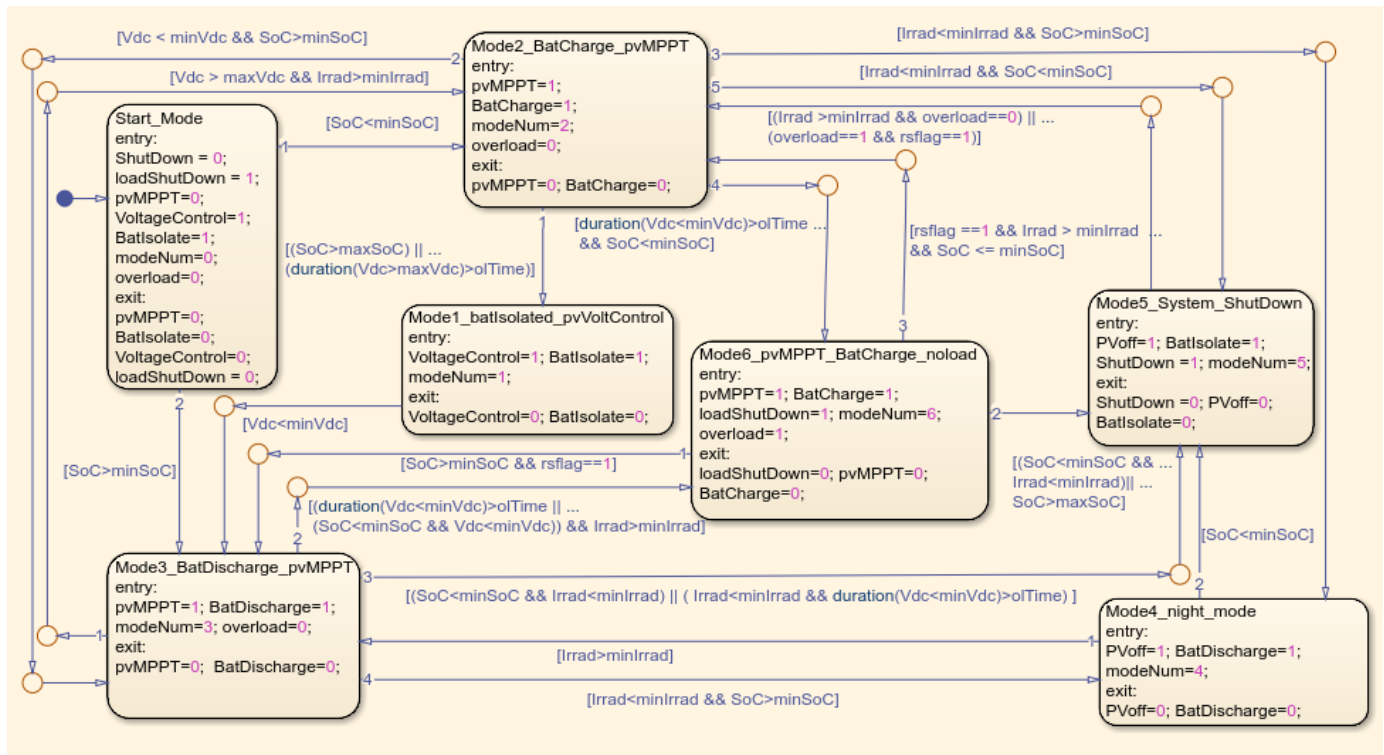
Simulation Output

Stand-Alone Solar PV AC Power System with Battery Backup

This example shows the design of a stand-alone PV AC power system with battery backup and helps you to:

- Choose the necessary battery rating based on the connected load profile and available solar power.
- Determine how the panels should be arranged in terms of the number of series-connected strings and the number of panels per string.
- Helps to design constant voltage single-phase AC supply.
- Select a suitable value for PI controller proportional gain, (K_v), and phase-lead constant, (T_v).

Both solar PV and battery storage support stand-alone loads. The load is connected across the constant voltage single-phase AC supply. A solar PV system operates in both maximum power point tracking and de-rated voltage control modes. The battery management system uses bi-directional DC-DC converters.

A stand-alone PV system requires six normal operating modes based on the solar irradiance, generated solar power, connected load, state of charge of the battery, maximum battery charging and discharging current limits.

To track the maximum power point (MPP) of solar PV, you can choose between two maximum power point tracking (MPPT) techniques:

- Incremental conductance
- Perturbation and observation

You can specify the average daily connected load profile, region's daily available average solar energy (kWhr), solar PV system operating temperature, day of autonomy, battery recharge time, ac supply and solar panel specification. solar panel manufacturer data is used to determine number of PV panels required to deliver the specified generation capability.

A $\frac{K_v(sT_v+1)}{sT_v}$ PI controller is chosen to control the solar PV and battery management system (BMS).

This example uses:

- A MATLAB® live script to design the overall standalone PV system.
- Simulink® to design/simulate the control logic for the system.
- Simscape™ to simulate the power circuit.
- Stateflow™ to implement the supervisory control logic.

Stand-Alone PV AC Power System Model

To open a script that designs the standalone PV AC power system, at the Matlab® command line, enter: edit 'ee_solar_standalone_acsystem_withbatterybackup_data'

The chosen battery and solar PV plant parameters are:

```
*****************************************************************************************
****           For the Given Stand-Alone PV System, Battery Sizing Parameters        ****
*****************************************************************************************
```

```
*** Calculated amphr of the battery  =  542.91 Ahr
*** Battery nominal voltage  =  78 V
*** Battery voltage at 80% discharge  =  70.20 V
*** Number of required battery cell  =  39.00
*** Average discharge current  =  4.28 A
********************************************************************************
********************************************************************************
****           For the Given Solar Panel, PV Plant Parameters       ****
********************************************************************************
*** Required PV Power rating  =  9.36 kW
*** Minimum number of panels required per string  =  8
*** Maximum number of panels connected per string without reaching maximum voltage  =  10
*** Minimum power rating of the solar PV plant  =  1.80 kW
*** Maximum power possible per string without reaching maximum DC voltage  =  2.25 kW
*** Actual number of panels per string  =  8
*** Number of strings connected in parallel  =  5
*** Actual solar PV plant power  =  9.01 kW
********************************************************************************
********************************************************************************
****                    Battery Charging/Discharging Parameters                ****
********************************************************************************
Reference battery charging current = 45.24 A
Maximum battery charging current = 128.29 A
Maximum battery discharging current = 64.14 A
Maximum battery charging Power = 10.01 kW
Maximum battery discharging Power = 5.00 kW
********************************************************************************
```


Stand-Alone Solar PV AC Power System with Battery Backup
1. Open script that inputs values for solar plant (load data)
2. Configure MPPT P&O, Incremental Conductance
3. Configure inverter: Average, Discrete
4. Explore simulation results using sscexplore
5. Learn more about this example

Stand-Alone Solar PV AC Power System Monitoring Panel

This example uses the Simulink® Dashboard feature to display all the real time system parameters. Turn the dashboard knob in the monitoring panel to modify the solar irradiance and the real and reactive power of the connected load during the simulation. By changing these parameters, you can observe how the PV system switches between the operating modes.

Solar Plant Subsystem

The solar plant subsystem models a solar plant that contains parallel-connected strings of solar panels. The solar panel is modeled using the Solar Cell block from the Simscape™ Electrical™ library. This example uses the output voltage from the DC bus, open circuit voltage depending on temperature and irradiance to estimate the number of solar panel strings connected in series, and the plant power rating to estimate the number of solar panel strings connected in parallel. Connecting multiple panels can slow the simulation because it increases the number of elements in a model. By assuming uniform irradiance and temperature across all the solar panels, it is possible to reduce the number of solar elements by using the controlled current and voltage sources as shown in the solar panel subsystem.

Solar Panel

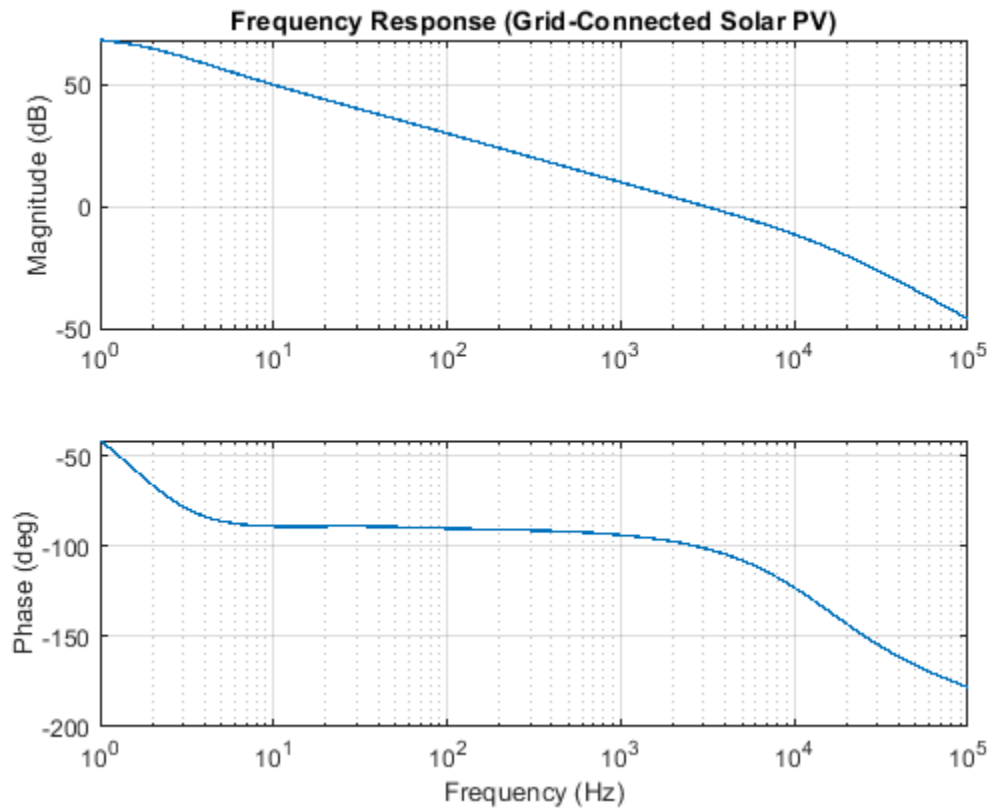## Maximum Power Point Tracking (MPPT)

Two MPPT techniques are implemented using the variant subsystem. Set the variant variable MPPT to 0 to choose the perturbation and observation MPPT. Set the variable MPPT to 1 to choose incremental conductance.

Intermediate Boost DC-DC Converter

Boost DC-DC converter is used to control the solar PV power. When battery is not fully charged solar PV plant operated in maximum power point. When battery is fully charged and load is smaller than the PV power, solar PV is operated in constant output DC bus voltage control mode.

Battery Management System (BMS)

The battery management system uses a bi-directional DC-DC converter. The battery is charged by the buck converter configuration and it is discharged using the boost converter configuration. To improve battery performance and life cycle, systems with battery backup have limited maximum battery charging and discharging current. This example sets a limit on the maximum amount of power that a battery can supply to the load and absorb from the solar PV source. Here, the maximum charging power is equal to the solar plant capacity at the standard test condition. The chosen maximum charging power should be able to recharge the battery sooner than the battery recharge time specified by the user.

Here, separate controller is used for charging and discharging operation. BMS controller have two loops, an outer voltage loop and inner current loop.

Bi-directional converter for energy management system (EMS)

Single-Phase Constant Voltage AC Power Supply

The single-phase constant voltage AC power supply provides a constant AC voltage to the connected complex loads. A single-phase inverter converts the output DC voltage from the boost converter to a constant single AC voltage supply. Choose a suitable PI controller to control the output voltage of the single-pahse inverter. To provide a smooth AC supply to the load, this model uses a LC filter.

Supervisory Control(Mode Control) Parameters

Stand-alone PV system in this example comprises seven operating modes. These modes are selected based on DC bus voltage, solar irradiance and state of charge of the battery. The DC bus voltage level is used as a measure to detect a load imbalance. If the DC bus voltage is greater than V_{dc-max}, the system is generating more power than what the load is requiring. If the DC bus voltage is less than V_{dc-min}, then the load is requiring more power than what the system is generating.

DC bus voltage level (V_{dc}), solar irradiance $(Irrad)$ and the battery state of charge (SoC) are used to decide the suitable operating mode.

Operating modes of the stand-alone PV AC System are:

- Mode-0 - Start mode (Default simulation starting mode)

- Mode-1 - PV in output voltage control, battery fully charged and isolated
- Mode-2 - PV in maximum power point, battery is charging
- Mode-3 - PV in maximum power point, battery is discharging
- Mode-4 - Night mode, PV shutdown, battery is discharging
- Mode-5 - Total system shutdown
- Mode-6 - PV in maximum power point, battery is charging, load is disconnected

Stateflow mode control diagram
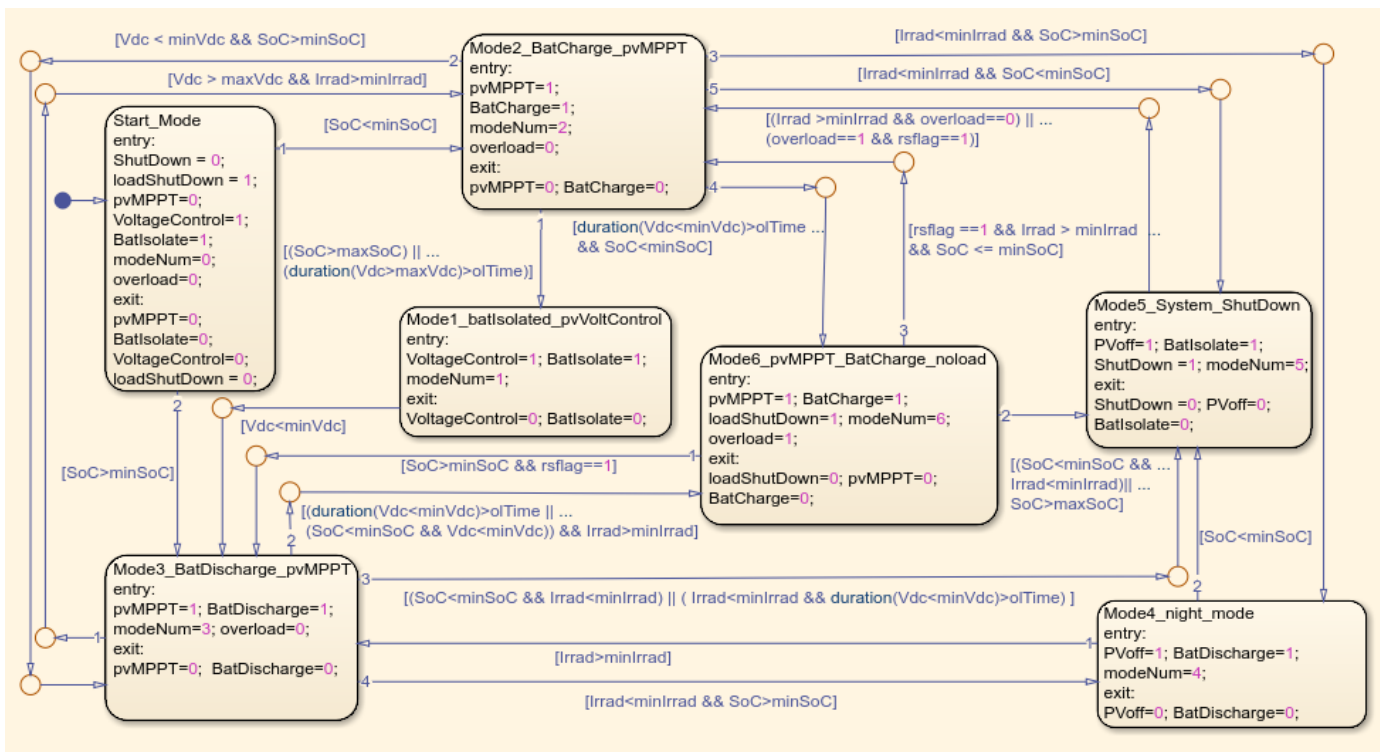
Simulation Output
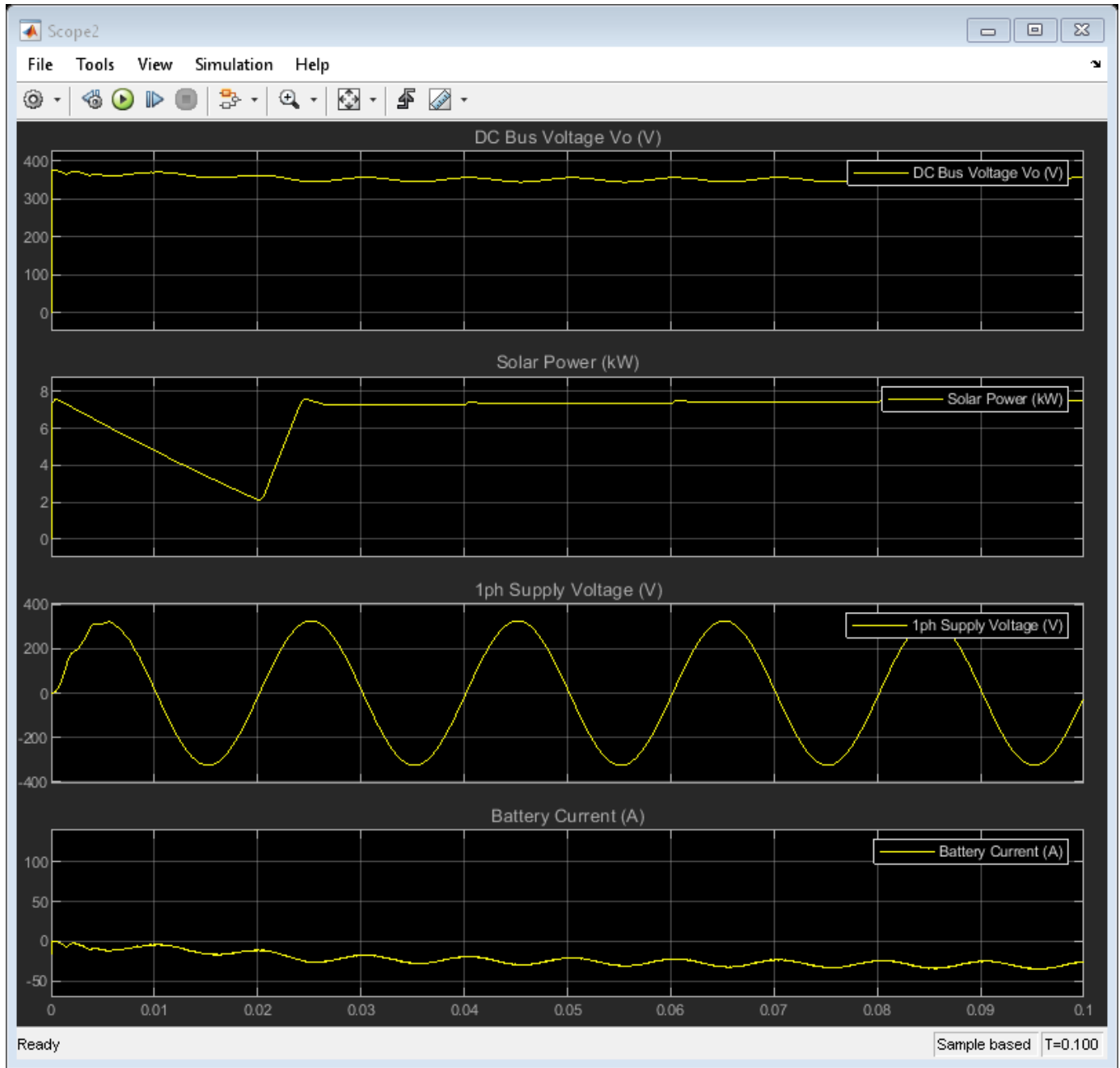
Pre-Parameterized Stepper Motor Block Validation

This example demonstrates and validates a set of predefined parameterizations that are supplied with the Simscape™ Electrical™ Stepper Motor block. These predefined parameterizations are determined from manufacturer datasheets.

Test Harness

First the particular stepper motor to be modeled must be selected. To do this, open the stepper motor block dialog and click on Select a predefined parameterization. This opens the Block Parameterization Manager from which you select the stepper part and apply it to the block. Based on the part you select, this script then loads the corresponding data that was used to parameterize the part, including pull-in curves from the original datasheet. The test harness works by gradually increasing the load until slip occurs for each step rate demand tested. Slip detection is implemented in the Slip Detection subsystem and uses a Simulink® assertion block that checks the difference between expected and actual rotor angle.
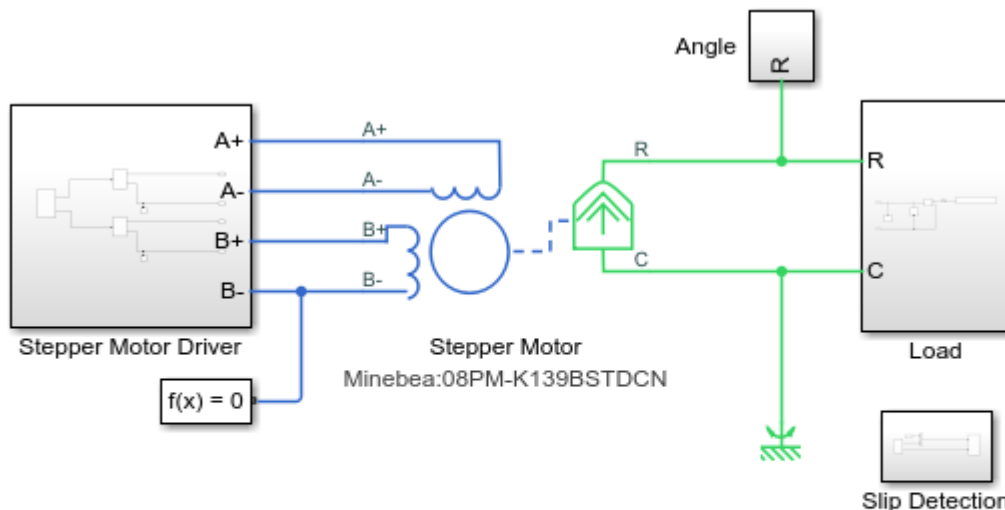
Pre-Parameterized Stepper Motor Block Validation
1. Open script to view the manufacturer data and define load torque
2. Plot pull-in characteristics of the selected stepper motor (see code)
3. Regenerate the pull-in characteristics for given step rate (computationally intensive, see code)
4. Explore simulation results using sscexplore
5. Learn more about this example

Simulated Results and Datasheet Pull-in Torque Characteristics

The plot below shows the simulated pull-in characteristics of one of the parameterized stepper motor options, along with the curve given in the manufacturer datasheet. Achieving an exact match for the pull-in characteristics can be challenging, a key factor being that most datasheets do not specify the test conditions used. Engineering judgement and simplifying assumptions are used to fill in for missing data. As a result deviations between simulated and actual physical behavior are to be expected. To ensure requisite accuracy, following initial parameterization from a test harness like this, the next step is to validate simulated behavior against experimental data and to refine component models as necessary.
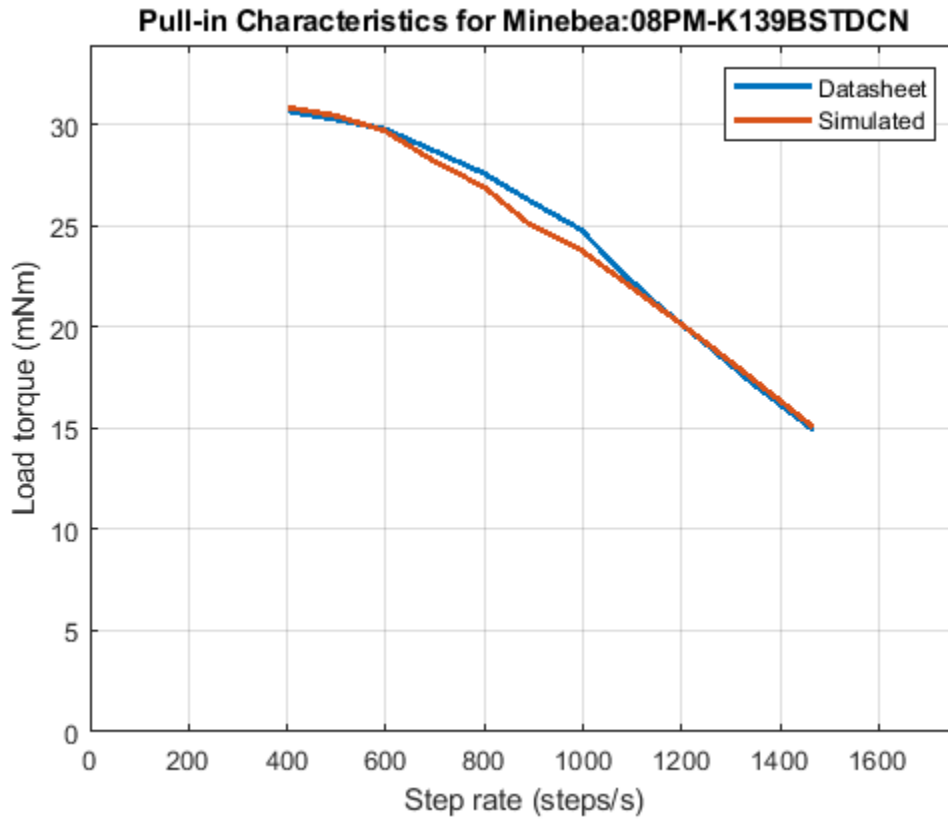
Pull-in Characteristics for Minebea:08PM-K139BSTDCN

For any given stepper motor, the set of model parameter values that matches the pull-in torque characteristics is often not unique. To ensure the parameterization is representative, it is good practice to also generate the pullout curve and compare it with the curve given in the datasheet.

To learn more about parameterizing a stepper motor refer to example ee_motor_stepper_pullin_characteristics.

IEEE 9-Bus Loadflow

This example shows a model of a 9-bus three-phase power system network. This example is based on an IEEE benchmark test case, further details of which can be found in "Power System Control and Stability" by P. M. Anderson and A. A. Fouad (IEEE Press, 2003). Simscape™ initializes two of the generators to the specified powers and terminal voltages, and initializes the remaining swing bus generator to meet just the specified voltage. The resulting load flow solution is appended to each of the busbars post-simulation. The four rows correspond to per-unit voltage, phase, active power, and reactive power respectively. Looking at Bus 1, it can be seen from the annotation that the swing generator delivers 76.4MW of active power and 27.5MVAr or reactive power to the network. Differences to the original benchmark are due to the transmission line models and transformer configurations used.

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Model
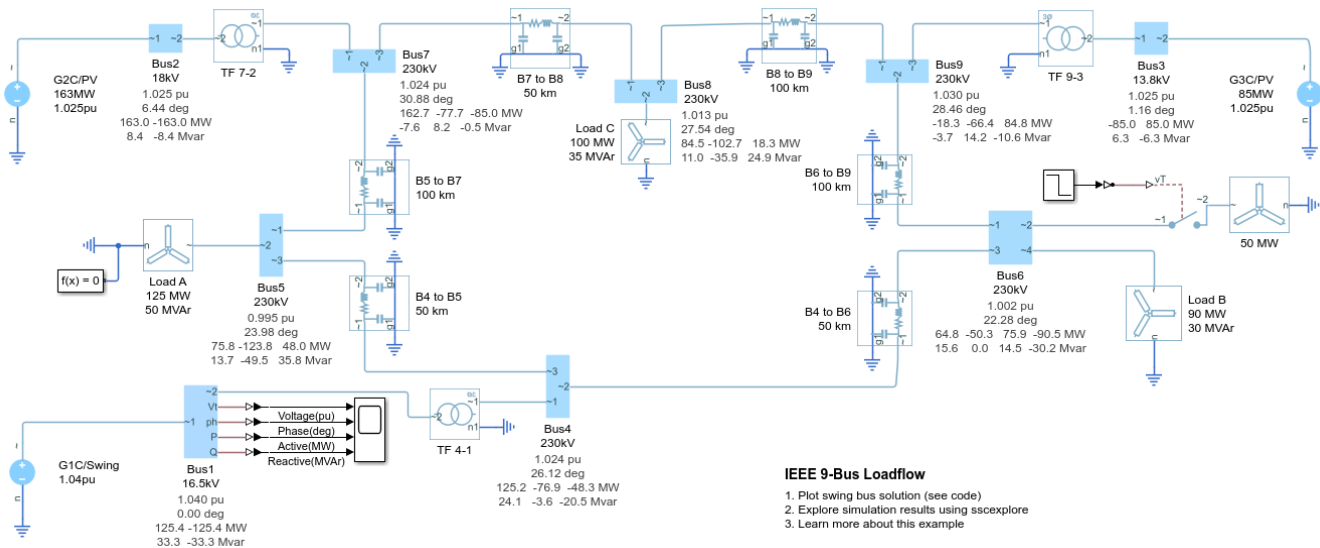
Simulation Results from Simscape Logging

The plot below shows the dynamic response due to adding a 50MW load to Bus 6 half-way through the simulation. The additional active and reactive powers that the swing bus must deliver can be seen.

Swing bus results
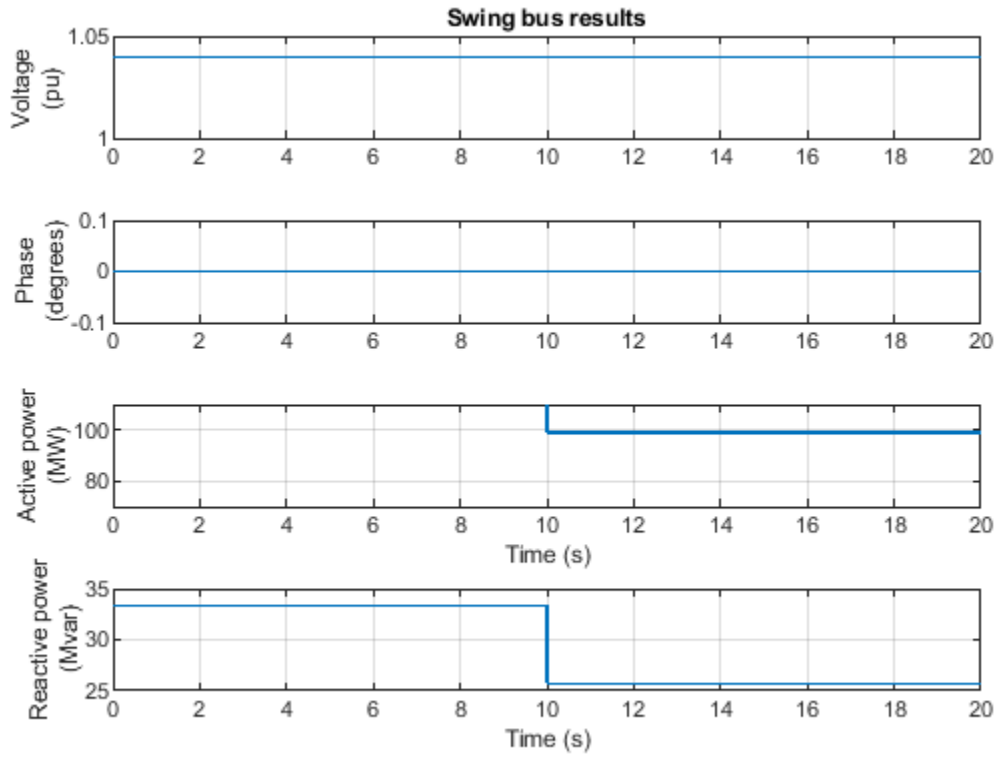
2-Bus Loadflow

This example shows a model of a two-bus three-phase power system network. The model uses three instances of the Load Flow Source block from Simscape™ Electrical™, one configured to be the swing bus, one configured to be the PV bus, and one configured to be the PQ load. The PV bus regulates its output to be at a voltage of 1.025 times rated voltage and to deliver 80MW active power to the network. The Swing bus regulates voltage at the other end of the transmission line to be one times rated voltage, and it delivers the requisite power to the network so that overall active and reactive powers balance. The Simscape initialization solver determines the required internal initial voltage amplitudes and phases in both the PV bus and the Swing bus so as to start in steady state.

The two busbar blocks are tagged with a summary of the load flow initialization results. These tags are updated immediately following simulation of the model. From the first row of the tags it can be seen that the requested voltages at the terminals of the two sources have been honored. The second rows of the tags show that Busbar2 leads Busbar1 by 2.51 degrees, this reflecting that there is a net power flow from Busbar2 to Busbar1. The third and fourth rows show the active and reactive powers respectively. For example, the third row of the Busbar2 tag indicates that 60MW of active power flows out of the busbar to the transmission line, 80MW of active power is supplied by the PV bus, and that 20MW of active power is delivered to the PQ load.

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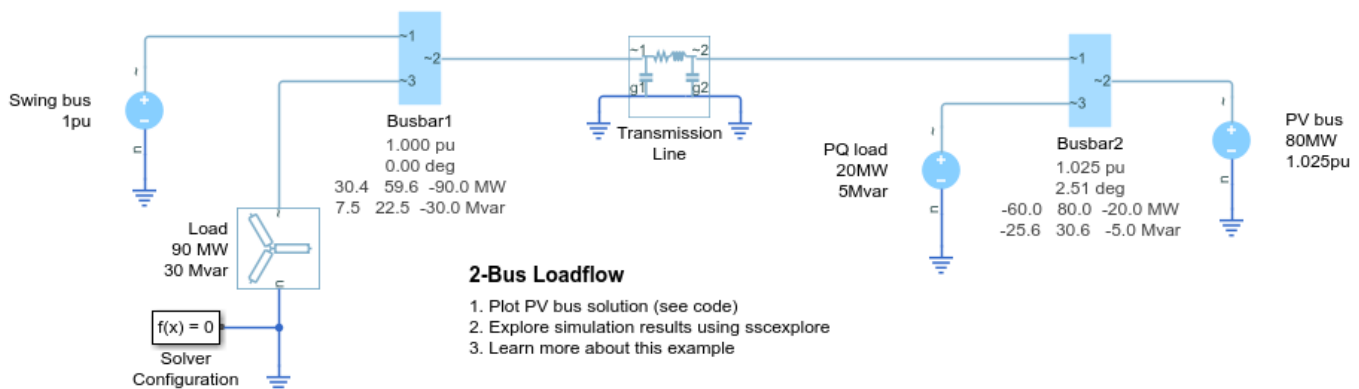
Model

Simulation Results from Simscape Logging

The plot below shows the simulation results following the steady state initialization to meet load flow targets. The Equation formulation option on the Solver Configuration block is set to Frequency and time for fast simulation. This model can also be run using the Time option provided that the Start simulation from steady state option is checked on the Solver Configuration block. However, simulating using the Time option does not update the Busbar blocks with loadflow results.
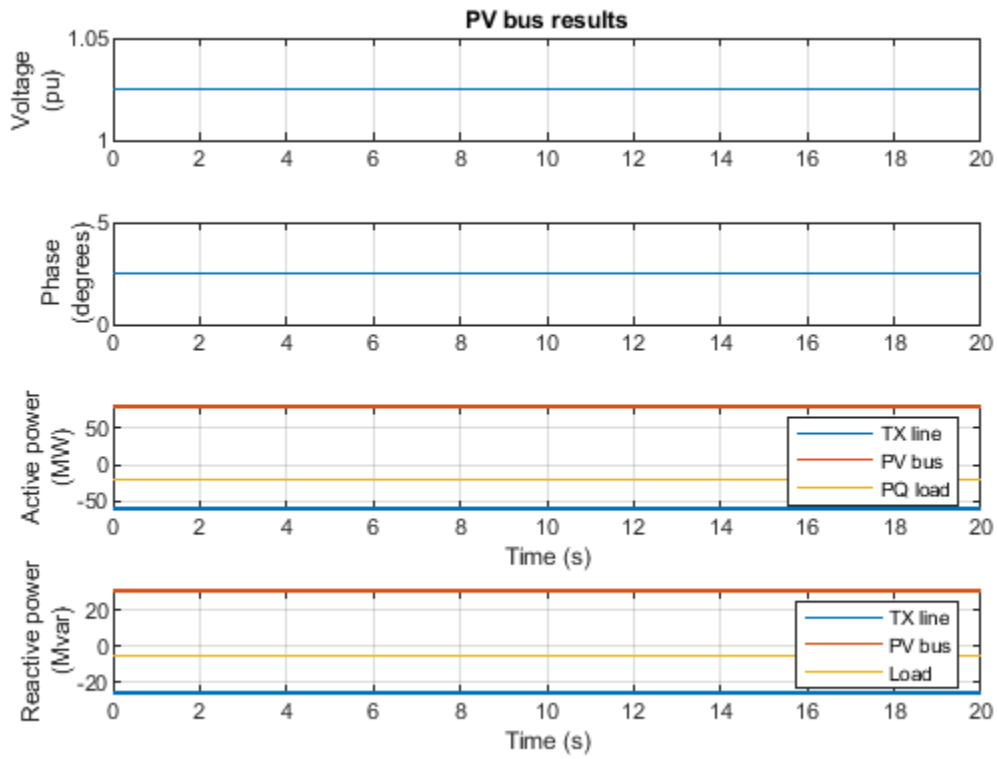
Induction Motor Initialization with Loadflow

This example shows how to initialize a three-phase induction motor as part of a load flow analysis. When initializing an induction machine that is directly connected to an AC network, in steady state there is one degree of freedom which can be set by any one of shaft torque, shaft power, motor speed or electrical power.

Here the model is configured to initialize delivering 8kW of mechanical power to a load. Hence the mechanical power consumed variable on the induction motor variables tab is set to -8kW with high priority. The slip and real power generated variables are left at priority none. Similarly, because the inertia initial speed is equivalent to slip, the rotational velocity is set to priority none. To help the intialisation converge, the beginning value is set to synchronous speed rather than left at zero.

The shaft torque is set to -8kW divided by the synchronous mechanical speed. This is an approximation as some negative slip is expected when generating. The error will result in an initial acceleration of the inertia. If increased accuracy is required, the measured slip from the first simulation can be used to correct the shaft torque applied, and the simulation run a second time. However, it is important to note that this error in the shaft torque will not change load flow results for the rest of the electrical network.

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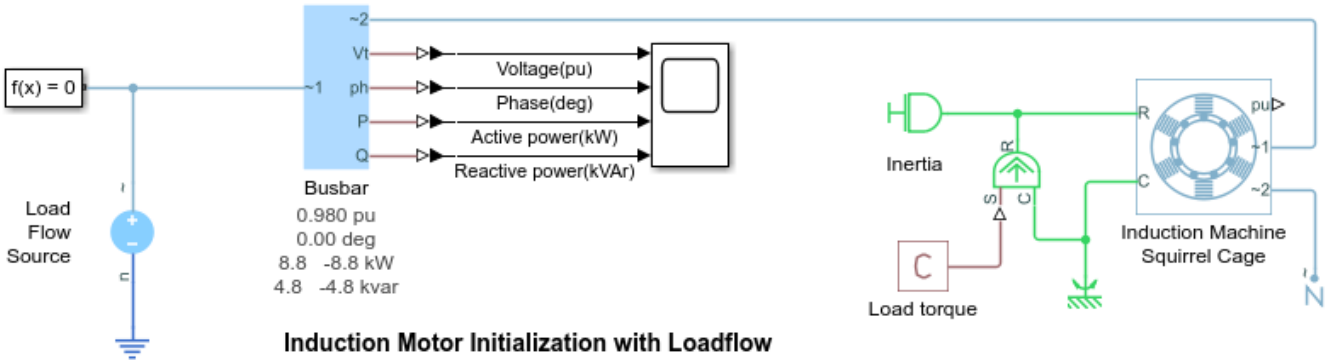
Model

Induction Motor Initialization with Loadflow

1. Plot results at busbar (see code)
2. Explore simulation results using sscexplore
3. Learn more about this example

Simulation Results from Simscape Logging

The plot below shows the simulation results following the steady state initialization to meet load flow targets. It can be seen that there is no transient, indicating that the induction motor has been correctly initialized.
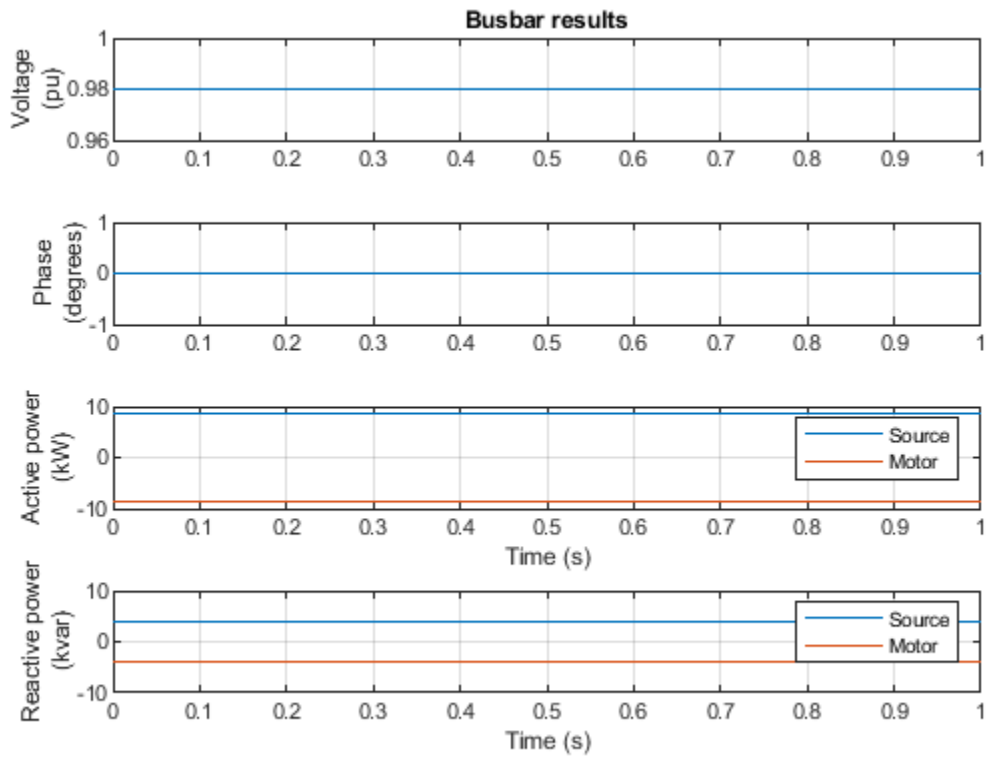
Synchronous Machine Initialization with Loadflow

This example shows how to initialize synchronous machine as part of a load flow analysis. When initializing a synchronous machine there are two degrees of freedom which can be set by any two of rotor angle, active power, reactive power and terminal voltage. The pair of variables that are constrained is set by the source type drop-down menu, this having options of Swing bus, PV bus and PQ bus. Here the machine is configured for a swing bus with a 1.02 per-unit voltage and zero degrees phase.

The shaft torque is set to 30MW divided by the synchronous mechanical speed. This is only an approximate value, but any difference to the value required for steady state will just result in a non-zero initial acceleration for the machine inertia. This will not change load flow results for the rest of the electrical network. Similarly the AVR is initialized close to the expected steady state field voltage, but any difference to the required value only creates an initial transient for field winding flux linkage. Again, this does not impact load flow results for the rest of the electrical network.

Exact values for the AVR and shaft torque for steady state can be determined after the initial load flow solution is found. This is done by setting the initialization option to Set real power, reactive power, terminal voltage, and terminal phase. The load flow solution can then be copied to the four corresponding parameters. Right-click the block and select Electrical->Display Associated Initial Conditions. This then prints the initial mechanical torque and field circuit voltage to the command prompt.

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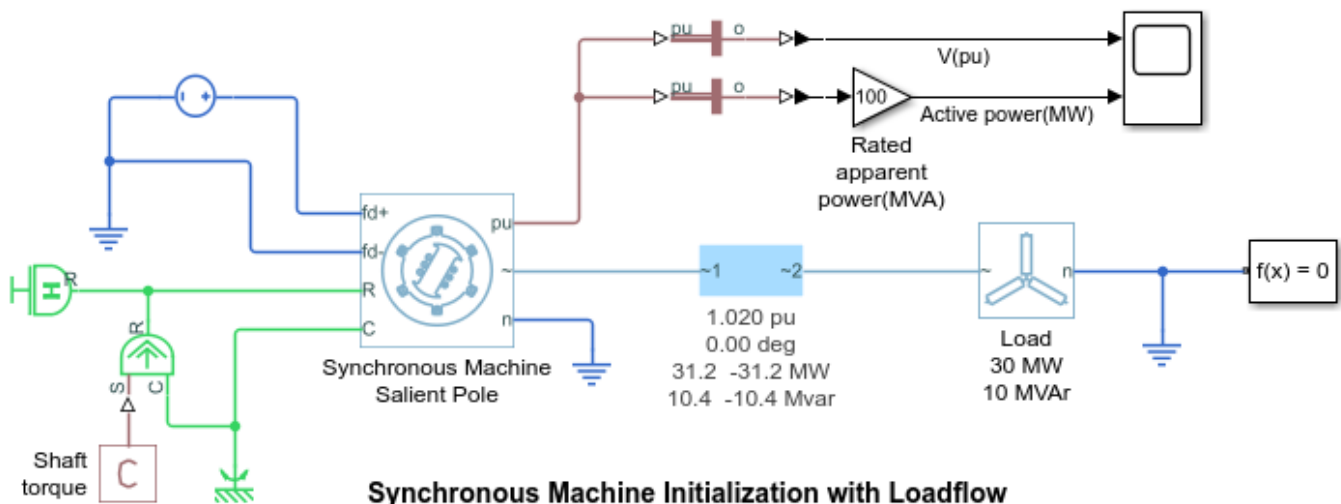
Model

Synchronous Machine Initialization with Loadflow

1. Plot results at busbar (see code)
2. Explore simulation results using sscexplore
3. Learn more about this example

Simulation Results from Simscape Logging

The plot below shows the simulation results following the steady state initialization to meet load flow targets. The ramp away from the steady state solution reflects that the governor and AVR are not modeled here. However, the initial values show that the loadflow targets are met.
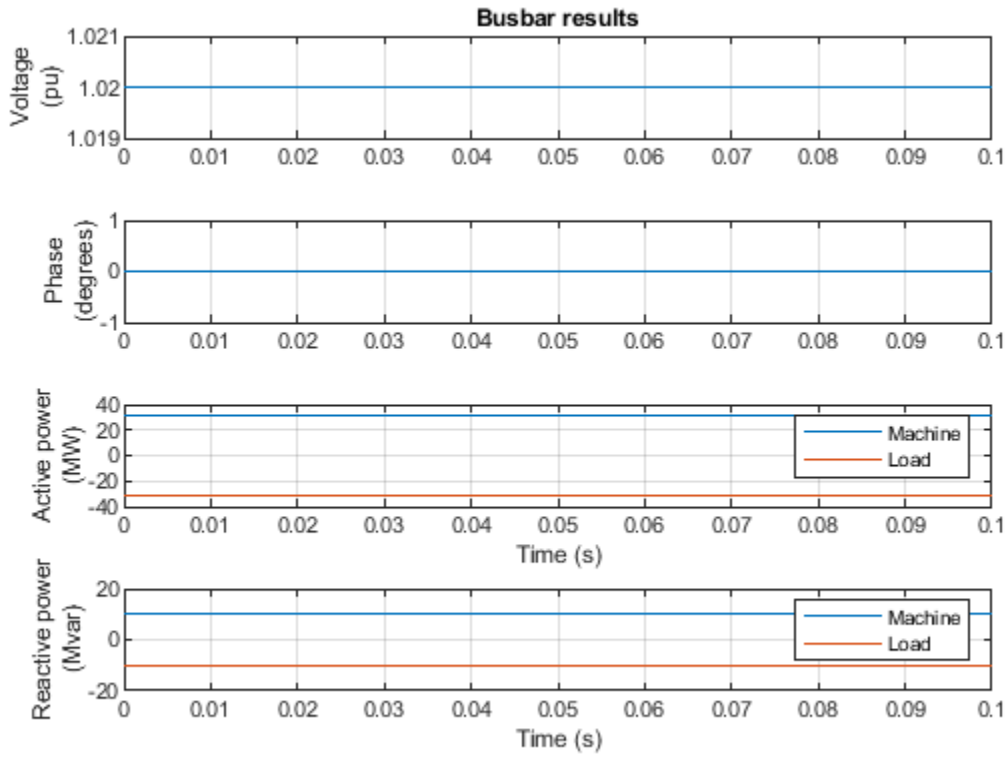
Stepper Motor Pull-in Characteristics

This example shows how to parameterize and tune a stepper motor using manufacturer datasheet information and a test harness. The model is parameterized using numerical data extracted from a datasheet. The simulation generates pull-in torque characteristics that you can compare to a manufacturer-provided pull-in curve. To tune the stepper motor model, the example uses a test harness that varies the drive type and load parameters.

Test Harness

The stepper is modeled using the Stepper Motor block from the Simscape™ Electrical™ library. The stepper motor driver is modelled by a current source and a first order filter. The motor is placed in a test harness. The test harness gradually increases the load until slip occurs for each step rate demand tested. Slip detection is implemented in the Slip Detection subsystem. The subsystem contains a Simulink® assertion block that determines the difference between expected and actual rotor angle.

Stepper Motor Pull-in Characteristics

1. Open script to define the manufacturer data
2. Plot pull-in characteristics (see code)
3. Regenerate the pull-in characteristics (computationally intensive, see code)
4. Explore simulation results using sscexplore
5. Learn more about this example
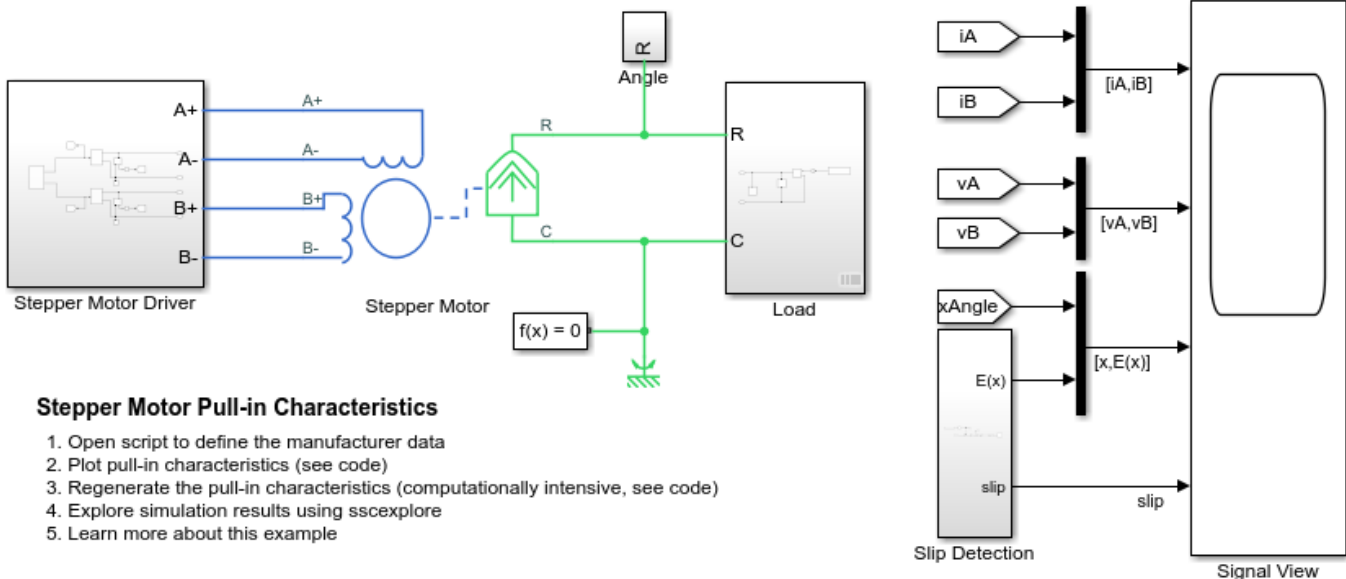
Pull-in Torque Characteristics

The plot shows the pull-in characteristics generated by the test harness simulation. Results are superimposed on the pull-in curve from the manufacturer datasheet. Achieving an exact match for the pull-in characteristics can be challenging because most datasheets do not specify the test conditions. Furthermore, some datasheets do not provide all the numerical values required to model the stepper motor. In this case, the pull-in curve from the simulation is used to determine for representative values that yield an acceptable pull-in curve match.

Pull-in Characteristics of a Two-Phase Stepper Motor
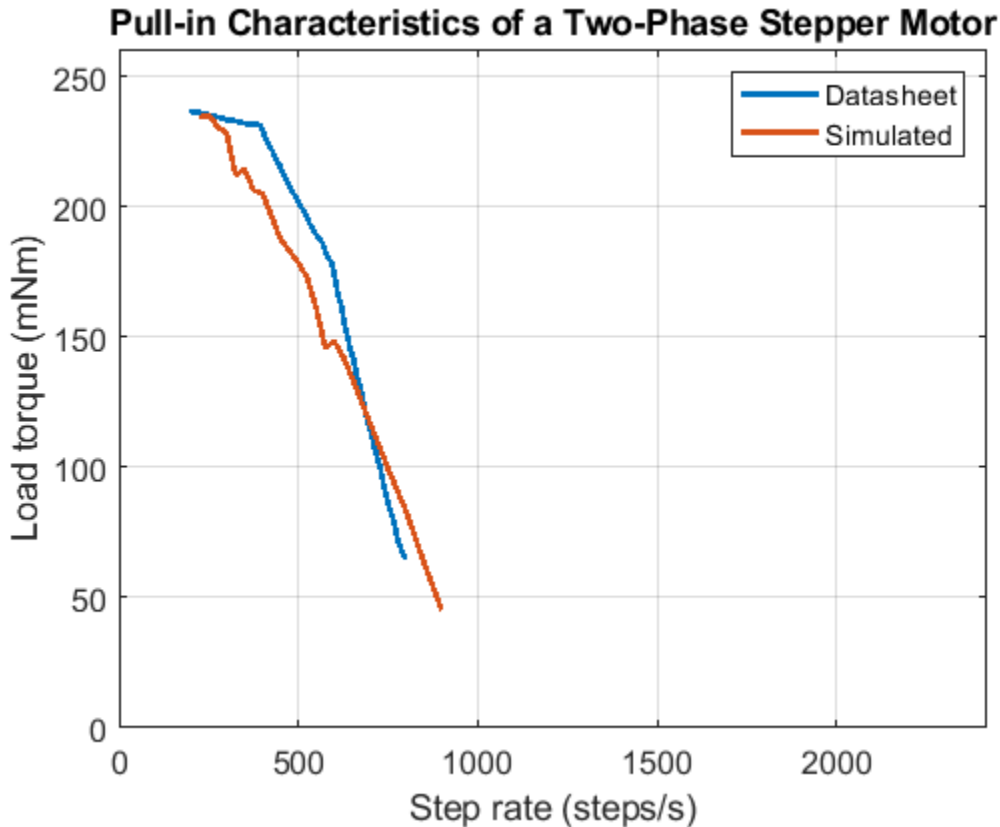
Pull-in Characteristics Sensitivity to Drive and Load Parameters

In pull-in mode, a stepper motor must start and stop without losing synchronization. Due to the dynamic nature of stepper motors, pull-in torque-speed performance is highly sensitive to stepper motor drive configuration and load parameters.

When modeling or tuning a stepper motor, consider that:

- Increasing the load inertia reduces the stepper motor pull-in torque at higher step rates. Generally, for high step-rate operation, load inertia is less than three times that of rotor inertia. It is a generally-accepted practice to limit the load inertia to less than ten times that of rotor inertia.

- Loads with higher damping component results in better performance at lower step rates because damping helps to overcome the effects of stepper motor resonance. Similarly, internal motor rotor damping can also help to improve the performance at low step rates.

- Stepper motors with low winding resistance are often driven using a constant current drive. To reduce the current rise time, higher than the rated voltage is applied to the motor. The higher supply voltage yields higher pull-in torque capability at higher step rates.

- Pull-in torque curve provided on a manufacturer datasheet is typically given for a particular driver and load configuration (load type, load inertia and load damping). Manufacturers tend to test stepper motors either with a dyno setup or by applying friction to the rotor wheel. The test method is rarely included on the data sheet. Therefore, it is always important to simulate the stepper motor to generate the curve for parameterization validation purposes.

The plot shows how the pull-in torque is sensitive to rotor damping, drive voltage, load inertia, and load type.

Stepper Motor Pull-in Characteristics
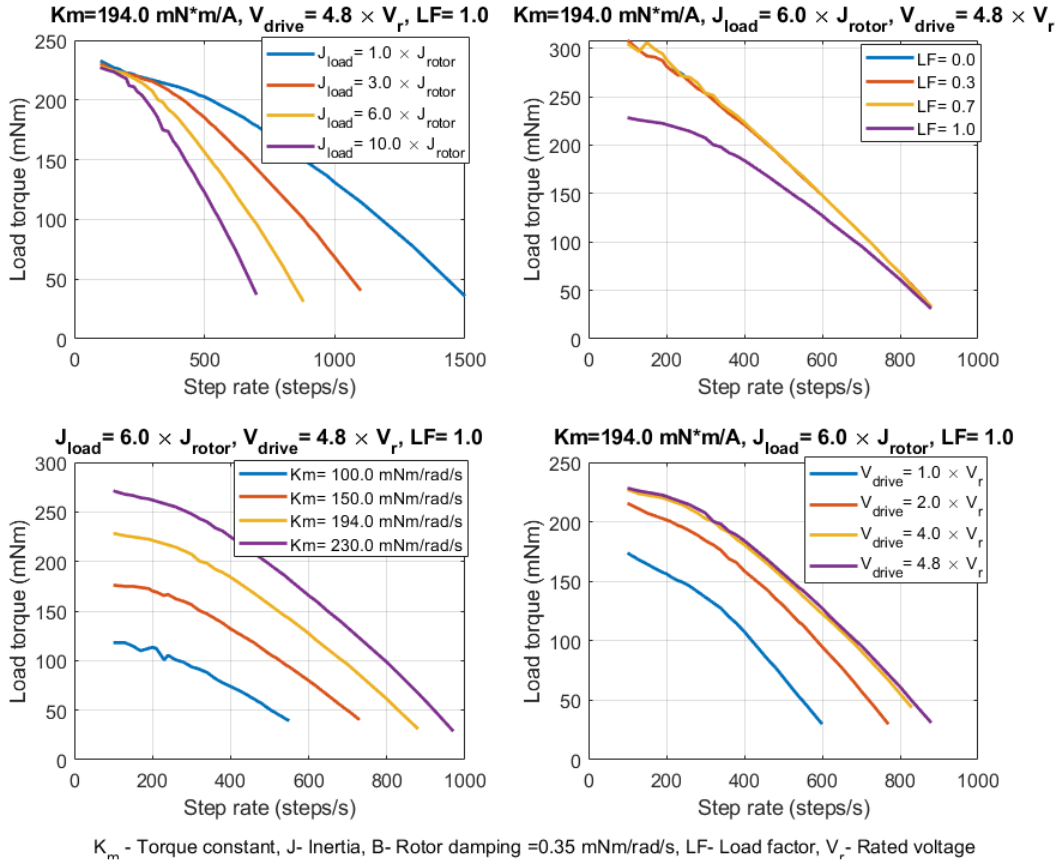
For any given stepper motor, the set of model parameter values that matches the pull-in torque characteristics is not typically unique. To ensure the parameterization is representative, it is good practice to also generate and compare the pull-out curve with the datasheet.

10-93

Three-Phase Matrix Converter

This example shows a three-phase matrix converter that drives a static load and draws unity power factor at the source. The Scopes subsystem contains scopes that allow you to see the simulation results.

Model
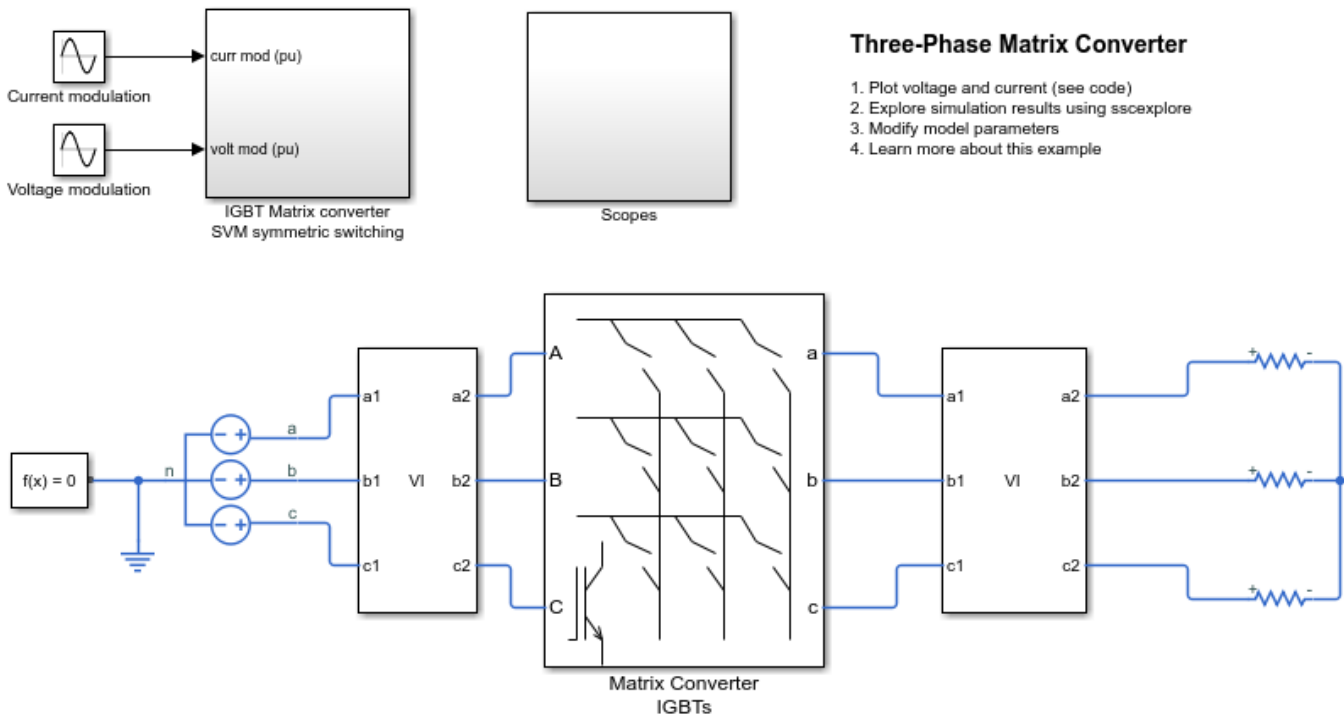
Simulation Results from Simscape Logging

The plot below shows the voltages and currents for the source and load.
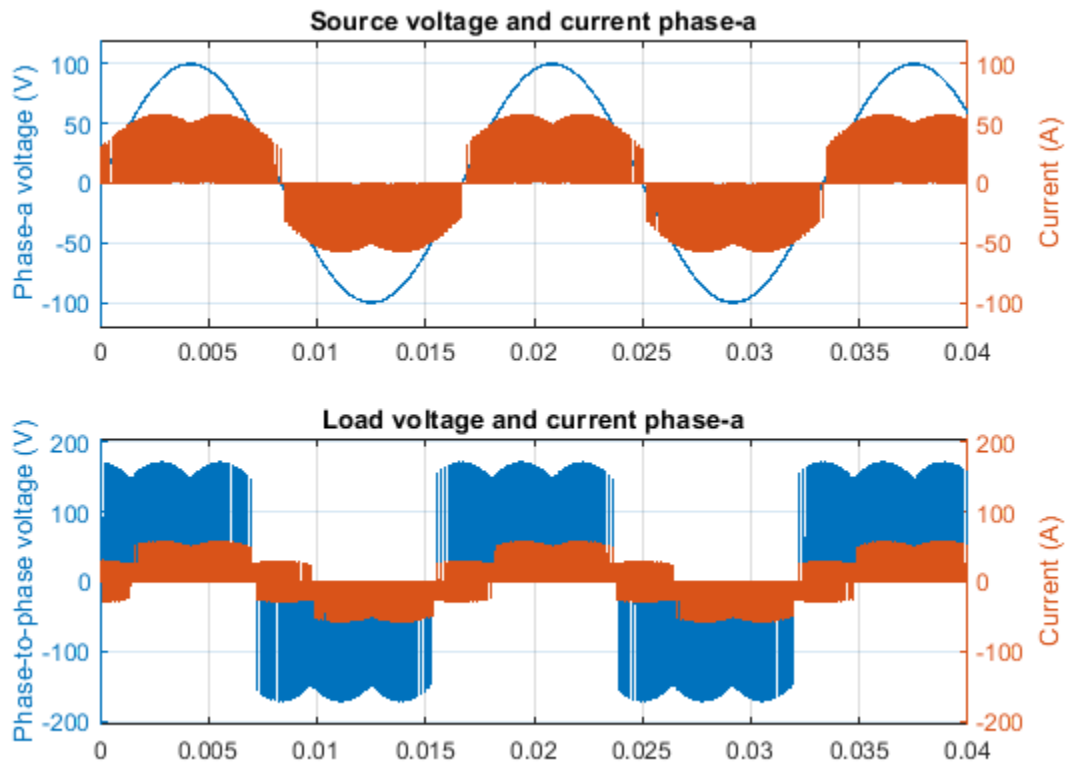
Battery Parameter Extraction from Data

This example shows optimization of the Battery block's parameters to fit data defined over different temperatures. It uses the MATLAB® optimization function `fminsearch`. Other products available for performing this type of parameter fitting with Simscape™ Electrical™ models are the Optimization Toolbox™ and Simulink® Design Optimization™. These products provide predefined functions to manipulate and analyze blocks using GUIs or a command line approach.

Strategy

Fit output voltage curves for a Battery to data using a 4 step procedure:

1 Optimize parameters in the Battery Main dialog tab.
2 Optimize parameters in the Battery Dynamics dialog tab.
3 Optimize nominal voltage and internal resistance in the Battery Temperature Dependence dialog tab.
4 Optimize temperature dependent charge dynamics parameters in the Battery Temperature Dependence dialog tab.

Data and Block Setup

The MATLAB data file, *ee_battery_data.mat*, stores Battery data as an array of structures. Each structure contains 5 fields: *v* (voltage) , *i* (current), *t* (time), *SOC0* (initial state of charge) and *T* (temperature). Scope save the output voltage as structure data, out.Vo.signals.values.

```
% Load Battery data
load ee_battery_data.mat
assignin('base','T1',battery_data(find([battery_data(1:2).T]==25)).T);
assignin('base','T2',battery_data(find([battery_data(1:2).T]~=25)).T);
% Display the Battery model
Model = 'ee_battery';
open_system(Model)
```
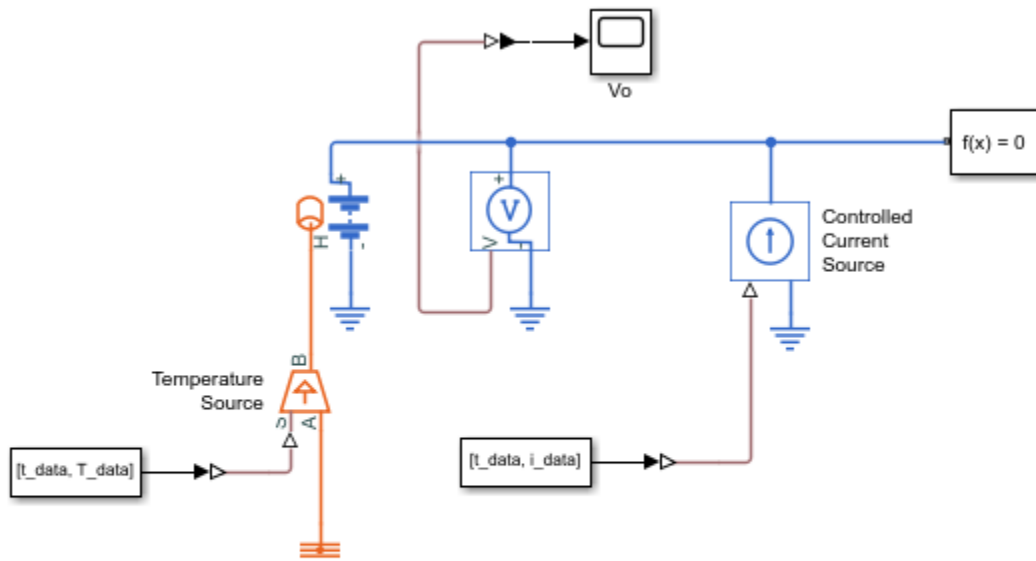
Battery Parameter Tuning

This test model is used by the Battery Parameter Extraction From Data example.
It is invoked by the parameter optimization when tuning parameters to fit the
measured voltage curve data.

```
close_system(Model, 0);
```

Initial Parameter Specification

Starting values for `fminsearch` can be estimated using a combination of Battery block defaults and data sheet values

List of parameters and initial values prior to optimization

```
ParsListMain = {'Vnom', 'R1', 'AH', 'V1', 'AH1'};
InitGuessMain = [3.6, 0.045, 2.7, 3.4, 1.4];
ParsListDyn = {'Rp1', 'tau1'};
InitGuessDyn = [0.006, 200];
ParsListTemp = {'Vnom_T2', 'R1_T2', 'V1_T2','Rp1_T2','tau1_T2'};
InitGuessTemp = [3.8, 0.055, 3.6, 0.006, 200 ];

Pars0 = reshape([[ParsListMain ParsListDyn ParsListTemp]; cellstr(num2str([InitGuessMain InitGu
fprintf('\t%5s = %s\n', Pars0{:});
clear Pars0
```

```
    Vnom =    3.6
      R1 = 0.045
      AH =    2.7
      V1 =    3.4
     AH1 =    1.4
     Rp1 = 0.006
    tau1 =    200
 Vnom_T2 =    3.8
   R1_T2 = 0.055
   V1_T2 =    3.6
```

```
     Rp1_T2 = 0.006
     tau1_T2 =   200
```

Since `fminsearch` is an unconstrained nonlinear optimizer that locates a local minimum of a function, varying the initial estimate will result in a different solution set.

Plot Data Versus Battery Output Using Initial Parameters

Load single cell Battery model and set parameters

```
load_system(Model);
% Enable Fast Restart to speedup the simulation
set_param(Model,'FastRestart','on')

Pars = reshape([ParsListMain; cellstr(num2str(InitGuessMain'))'],1,[]);
for k=1:2:length(Pars)
    evalin('base',[Pars{k} '=' Pars{k+1} ';'])
end

Pars = reshape([ParsListDyn; cellstr(num2str(InitGuessDyn'))'],1,[]);
for k=1:2:length(Pars)
    evalin('base',[Pars{k} '=' Pars{k+1} ';'])
end

Pars = reshape([ParsListTemp; cellstr(num2str(InitGuessTemp'))'],1,[]);
for k=1:2:length(Pars)
    evalin('base',[Pars{k} '=' Pars{k+1} ';'])
end

% Generate preliminary model curves and plot against data
num_lines = length(battery_data)-1;
v_model = cell(1, num_lines);
t_model = cell(1, num_lines);
legend_info_data  = cell(1, num_lines);
legend_info_model = cell(1, num_lines);

for idx_data = 1:num_lines
    assignin('base','t_data',battery_data(idx_data).t);
    assignin('base','i_data',battery_data(idx_data).i);
    assignin('base','T_data',battery_data(idx_data).T*ones(length(t_data),1));
    assignin('base','T0',battery_data(idx_data).T);
    assignin('base','Ts',t_data(2)-t_data(1));
    assignin('base','AH0',AH*battery_data(idx_data).SOC0);

    out = sim(Model);
    v_model{idx_data} = out.Vo.signals.values;
    t_model{idx_data} = out.Vo.time;
    legend_info_data{idx_data}  = [ 'Temp = '                       ...
        num2str(battery_data(idx_data).T) '\circC, Data'];
    legend_info_model{idx_data} = [ 'Temp = '                       ...
        num2str(battery_data(idx_data).T) '\circC, Model'];
end
plot([battery_data(1:num_lines).t]/3600, [battery_data(1:num_lines).v], 'o', [t_model{:}]/3600
xlabel('Time (hours)');
ylabel('Battery voltage (V)');
legend([legend_info_data legend_info_model], 'Location', 'Best');
title('Model with Initial Parameter Values');
```

Model with Initial Parameter Values
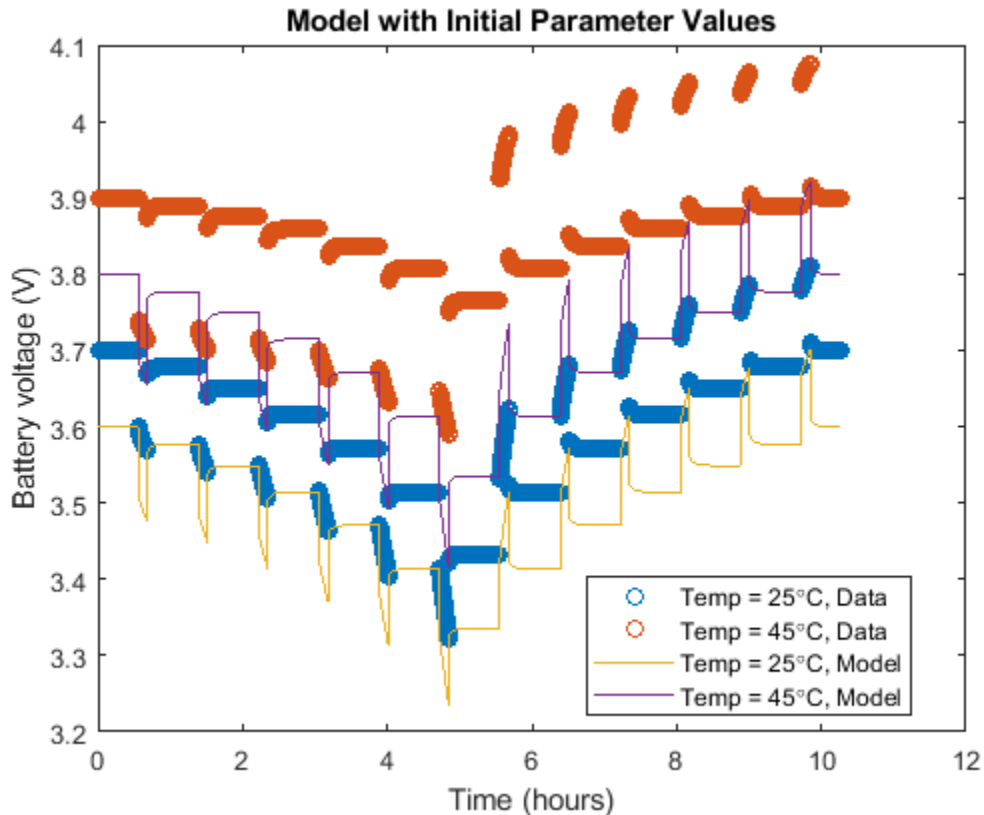
Sum of Squares of Error Calculation

`ee_battery_lse` is the function to be minimized by `fminsearch`. This function returns a sum of squares of error for the difference between the Battery output voltage and the data. If an invalid parameter value is supplied by `fminsearch`, the `catch` statement returns a large value for the error.

Optimize Main Tab Dialog Parameters Without Charge Dynamics (Step 1)

```
% Find ambient temperature data index
idx_data = find([battery_data(1:num_lines).T]==25);
assignin('base','t_data',battery_data(idx_data).t);
assignin('base','i_data',battery_data(idx_data).i);
assignin('base','T_data',battery_data(idx_data).T*ones(length(t_data),1));
assignin('base','T0',battery_data(idx_data).T);
assignin('base','Ts',t_data(2)-t_data(1));
assignin('base','v_data',battery_data(idx_data).v);

% Optimize parameters in main dialog tab of Battery
assignin('base','ParsList',ParsListMain(1:4));
InitGuess = InitGuessMain(1:4);
OptPars = fminsearch(@ee_battery_lse, InitGuess,            ...
    optimset('TolX', 1e-3));

OptParsMain = [OptPars(1:4) InitGuessMain(5)];

% Update Battery block with optimized parameters
Pars = reshape([ParsListMain; cellstr(num2str(OptParsMain'))'],1,[]);
```

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```
    for k=1:2:length(Pars)
        evalin('base',[Pars{k} '=' Pars{k+1} ';'])
    end

    % Display optimized parameters
    fprintf(['Optimized parameters for the battery main '       ...
      'dialog tab are:\n']);
    fprintf('\t%5s = %s\n', Pars{:});

    clear i_data v_data t_data T_data Ts
    clear k InitGuess
```

```
Optimized parameters for the battery main dialog tab are:
     Vnom =   3.6999
       R1 = 0.050299
       AH =   2.6033
       V1 =   3.5265
      AH1 =      1.4
```

Optimize Charge Dynamics Parameters (Step 2)

```
    % Use only one current pulse for optimizing the charge dynamics
    i_pos=battery_data(1).i.*(battery_data(1).i>=0);
    a=find(diff(i_pos)>0,2);
    b = find(diff(battery_data(1).i));
    c = fix((b(find(b<a(1),1,'last'))+a(1))/2);
    assignin('base','i_data',battery_data(idx_data).i(c+1:a(2)));
    assignin('base','v_data',battery_data(1).v(c+1:a(2)));
    assignin('base','t_data',battery_data(idx_data).t(1:length(i_data)));
    assignin('base','T_data',battery_data(idx_data).T*ones(length(t_data),1));
    assignin('base','T0',battery_data(idx_data).T);
    assignin('base','Ts',t_data(2)-t_data(1));

    % Find Battery initial charge before optimizing charge dynamics parameters
    assignin('base','ParsList',{'charge'});
    InitGuessCharge = OptParsMain(3);
    OptCharge = fminsearch(@ee_battery_lse, InitGuessCharge,           ...
        optimset('TolX', 1e-3));
    assignin('base','AH0',OptCharge);
    % Optimize Battery charge dynamics parameters
    assignin('base','ParsList',[ParsListMain(2) ParsListDyn]);
    InitGuessDyn = [OptPars(2) InitGuessDyn];
    OptParsDyn = fminsearch(@ee_battery_lse, InitGuessDyn,             ...
        optimset('TolX', 1e-3));

    % Update Battery block with optimized charge dynamics parameters
    ParsListMainDyn = [ParsListMain ParsListDyn];
    OptParsMainDyn = [OptPars(1) OptParsDyn(1) OptPars(3:4) InitGuessMain(5) OptParsDyn(2:3)];
    Pars = reshape([ParsListMainDyn; cellstr(num2str(OptParsMainDyn'))'],1,[]);
    for k=1:2:length(Pars)
        evalin('base',[Pars{k} '=' Pars{k+1} ';'])
    end
    assignin('base','AH0',AH*battery_data(idx_data).SOC0);

    % Display optimized parameters
    fprintf(['Optimized parameters for the Battery, '          ...
        'including charge dynamics, are:\n']);
    fprintf('\t%5s = %s\n', Pars{:});
```

```matlab
clear i_data v_data t_data T_data Ts
clear i_pos a b c
clear k
clear OptPars OptParsDyn ParsListMainDyn InitGuessMain InitGuessDyn ParsListDyn ParsListMain
```

```
Optimized parameters for the Battery, including charge dynamics, are:
    Vnom =    3.699931
      R1 =  0.05019736
      AH =    2.603326
      V1 =    3.526493
     AH1 =         1.4
     Rp1 = 0.005029392
    tau1 =     109.691
```

Optimize Temperature Dependent Parameters (Step 3)

```matlab
idx_data = find([battery_data(1:num_lines).T]~=25);
assignin('base','t_data',battery_data(idx_data).t);
assignin('base','i_data',battery_data(idx_data).i);
assignin('base','T_data',battery_data(idx_data).T*ones(length(t_data),1));
assignin('base','T0',battery_data(idx_data).T);
assignin('base','Ts',t_data(2)-t_data(1))
assignin('base','v_data', battery_data(2).v);

% Use parameters for T1 as initial guess for T2 parameters
InitGuessTemp = [OptParsMainDyn(1:2) OptParsMainDyn(4) OptParsMainDyn(6:7)];
Pars = reshape([ParsListTemp; cellstr(num2str(InitGuessTemp'))'],1,[]);
for k=1:2:length(Pars)
    evalin('base',[Pars{k} '=' Pars{k+1} ';'])
end

% Optimize Battery temperature dependent parameters
assignin('base','ParsList',ParsListTemp(1:3));
OptParsTemp = fminsearch(@ee_battery_lse, InitGuessTemp(1:3),                 ...
    optimset('TolX', 1e-3));

% Update Battery block with optimized parameters
Pars = reshape([ParsListTemp(1:3); cellstr(num2str(OptParsTemp'))'],1,[]);
for k=1:2:length(Pars)
    evalin('base',[Pars{k} '=' Pars{k+1} ';'])
end
assignin('base','AH0',AH*battery_data(idx_data).SOC0);

% Display optimized parameters
fprintf(['Optimized temperature dependent parameters for the Battery '       ...
    'are:\n']);
fprintf('\t%5s = %s\n', Pars{:});

clear i_data v_data t_data T_data Ts
clear k
clear OptParsMainDyn
```

```
Optimized temperature dependent parameters for the Battery are:
    Vnom_T2 =   3.9003
    R1_T2 = 0.081404
    V1_T2 =   3.8133
```

Optimize Charge Dynamics Parameters for Second Temperature (Step 4)

```matlab
% Find index into data for non-room temperatures

% Use only one current pulse for optimizing the charge dynamics
i_pos=battery_data(idx_data).i.*(battery_data(idx_data).i>=0);
a=find(diff(i_pos)>0,2);
b = find(diff(battery_data(idx_data).i));
c = fix((b(find(b<a(1),1,'last'))+a(1))/2);
assignin('base','i_data',battery_data(idx_data).i(c+1:a(2)));
assignin('base','v_data',battery_data(idx_data).v(c+1:a(2)));
assignin('base','t_data',battery_data(idx_data).t(1:length(i_data)));
assignin('base','T_data',battery_data(idx_data).T*ones(length(t_data),1));
assignin('base','T0',battery_data(idx_data).T);
assignin('base','Ts',t_data(2)-t_data(1))

% Find Battery initial charge before optimizing charge dynamics parameters
assignin('base','ParsList',{'charge'});
InitGuessCharge = OptParsMain(3);
OptCharge = fminsearch(@ee_battery_lse, InitGuessCharge,               ...
    optimset('TolX', 1e-3));
assignin('base','AH0',OptCharge);

% Optimize Battery charge dynamics parameters
assignin('base','ParsList', [ParsListTemp(2) ParsListTemp(4:5)]);
InitGuessTempDyn = [OptParsTemp(2) InitGuessTemp(4:5)];
OptParsTempDyn = fminsearch(@ee_battery_lse, InitGuessTempDyn,          ...
    optimset('TolX', 1e-3));

% Update Battery block with optimized parameters
OptParsTempDyn = [OptParsTemp(1) OptParsTempDyn(1) OptParsTemp(3) OptParsTempDyn(2:3)];
Pars = reshape([ParsListTemp; cellstr(num2str(OptParsTempDyn'))'],1,[]);
for k=1:2:length(Pars)
    evalin('base',[Pars{k} '=' Pars{k+1} ';'])
end
assignin('base','AH0',AH*battery_data(idx_data).SOC0);

% Display optimized parameters
fprintf(['Optimized temperature dependent parameters for the Battery, '       ...
    'including charge dynamics, are:\n']);
fprintf('\t%5s = %s\n', Pars{:});

clear i_data v_data t_data T_data Ts
clear i_pos a b c
clear k
clear OptCharge OptParsMain OptParsTemp OptParsTempDyn
clear Pars ParsList ParsListTemp InitGuessCharge InitGuessTemp InitGuessTempDyn
```

```
Optimized temperature dependent parameters for the Battery, including charge dynamics, are:
    Vnom_T2 =    3.900266
    R1_T2 =  0.07979468
    V1_T2 =    3.813256
    Rp1_T2 = 0.007920818
    tau1_T2 =    160.2999
```

Display Optimized Curves

```
for idx_data = 1:num_lines
    assignin('base','t_data',battery_data(idx_data).t);
    assignin('base','i_data',battery_data(idx_data).i);
    assignin('base','T_data',battery_data(idx_data).T*ones(length(t_data),1));
    assignin('base','T0',battery_data(idx_data).T);
    assignin('base','Ts',t_data(2)-t_data(1));

    out = sim(Model);
    v_model{idx_data} = out.Vo.signals.values;
    t_model{idx_data} = out.Vo.time;
end
plot([battery_data(1:num_lines).t]/3600, [battery_data(1:num_lines).v], 'o', [t_model{:}]/3600
xlabel('Time (hours)');
ylabel('Battery voltage (V)');
legend([legend_info_data legend_info_model], 'Location', 'Best');
title('Model with Optimized Parameter Values');
```
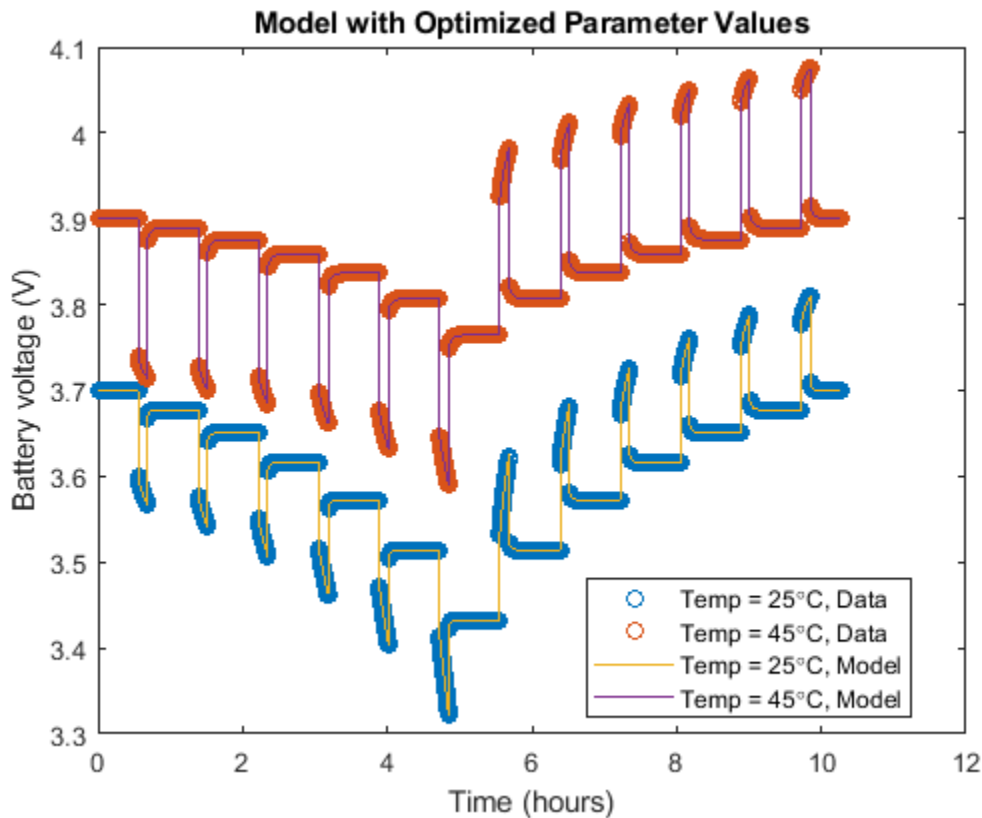

Validation using a dynamic current cycle

```
idx_data = 3;
assignin('base','t_data',battery_data(idx_data).t);
assignin('base','i_data',battery_data(idx_data).i);
assignin('base','T_data',battery_data(idx_data).T);
assignin('base','T0',battery_data(idx_data).T(1));
assignin('base','Ts',t_data(2)-t_data(1));
assignin('base','AH0',AH*battery_data(idx_data).SOC0)
```

```
out = sim(Model);
subplot(3,1,1)
plot(t_data, battery_data(3).v, 'o', out.Vo.time, out.Vo.signals.values)
xlabel('Time (s)');
ylabel('Battery voltage (V)');
legend('Data','Model', 'Location', 'Best');
subplot(3,1,2)
plot(t_data,i_data)
xlabel('Time (s)');
ylabel('Current requirement (A)');
subplot(3,1,3)
plot(t_data,T_data)
xlabel('Time (s)');
ylabel('Temperature (^oC)');
title('Model validation');
```
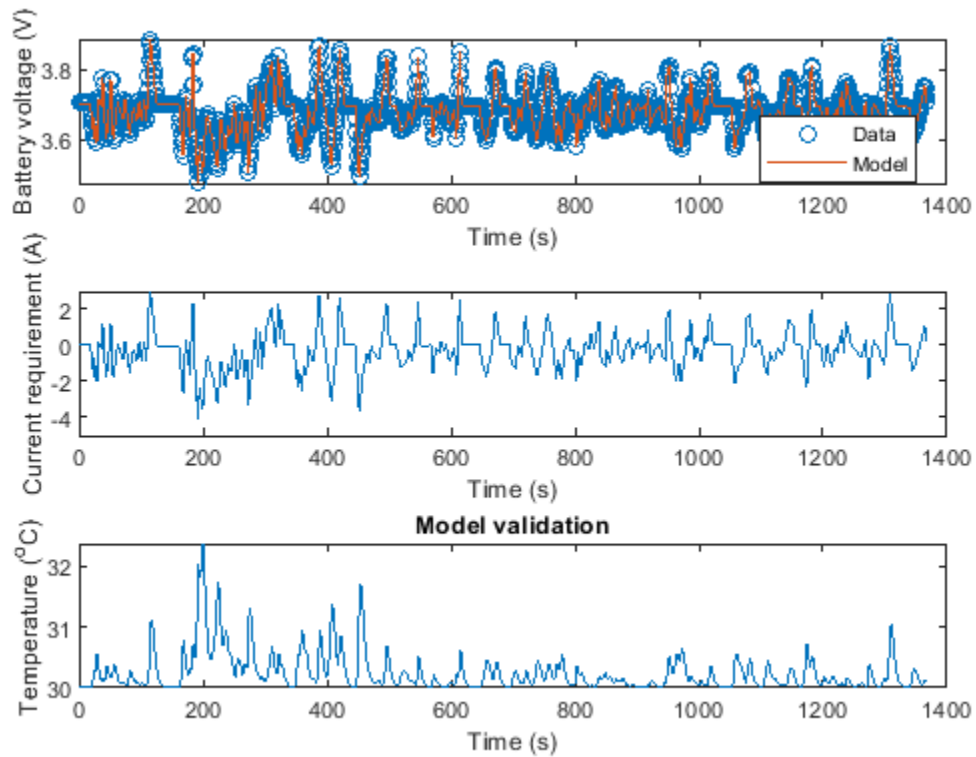


```
bdclose(Model)
clear num_lines legend_info_data legend_info_model out v_model t_model
clear battery_data idx_data Ts Model
```

Power Converter Model Fidelity Comparison

This example shows how to use different levels of fidelity in power converters. The system contains three converters. The top converter uses ideal switches and protection diodes at a 10 us sample time. To yield accurate results even though the model is under sampled at 50 us sample time, the middle converter uses averaged switches with averaged pulses. To further increase the sample rate and to operate as an ideal averaged converter, the bottom converter uses averaged switches and modulation waveforms instead of gate pulses. The Control subsystem contains a three-phase, two-level PWM waveform generator. The Scopes subsystem contains Scope blocks that allow you to see the simulation results.

Model
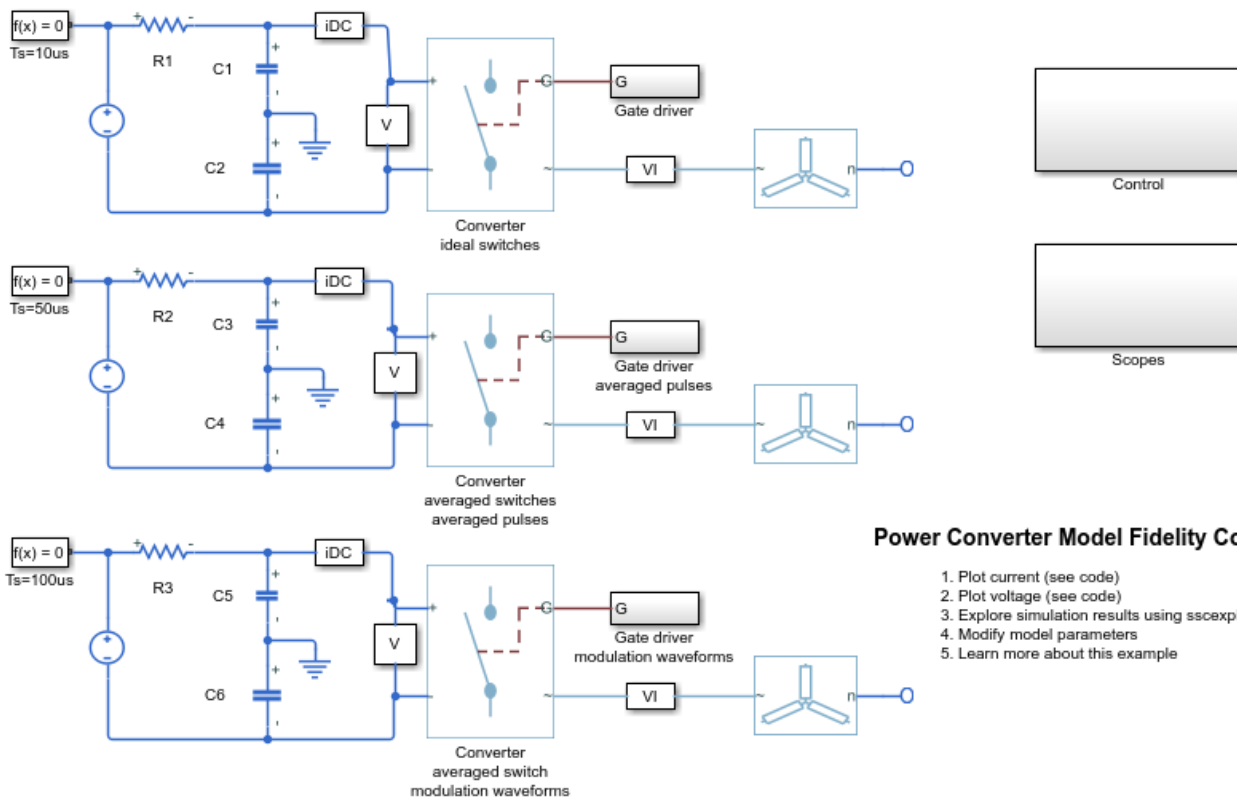
Simulation Results from Simscape Logging

The plot below shows the comparison of the DC currents, as well as the AC a-phase currents.
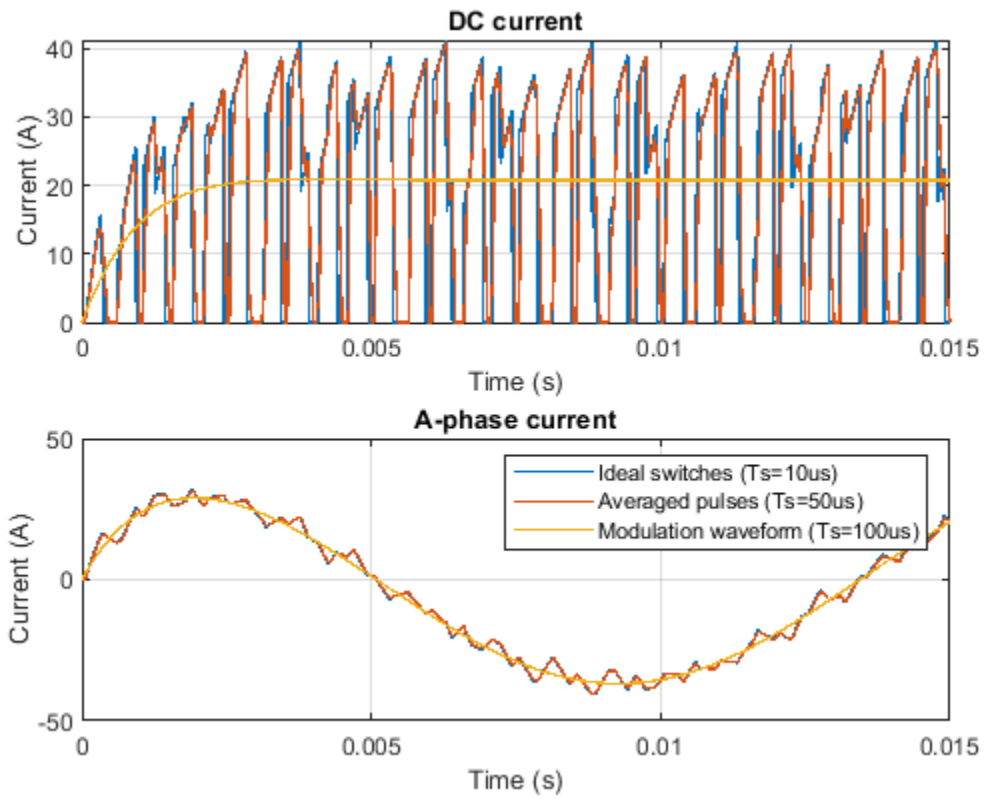
The plot below shows the comparison of the DC voltage, as well as the AC a-phase voltage.
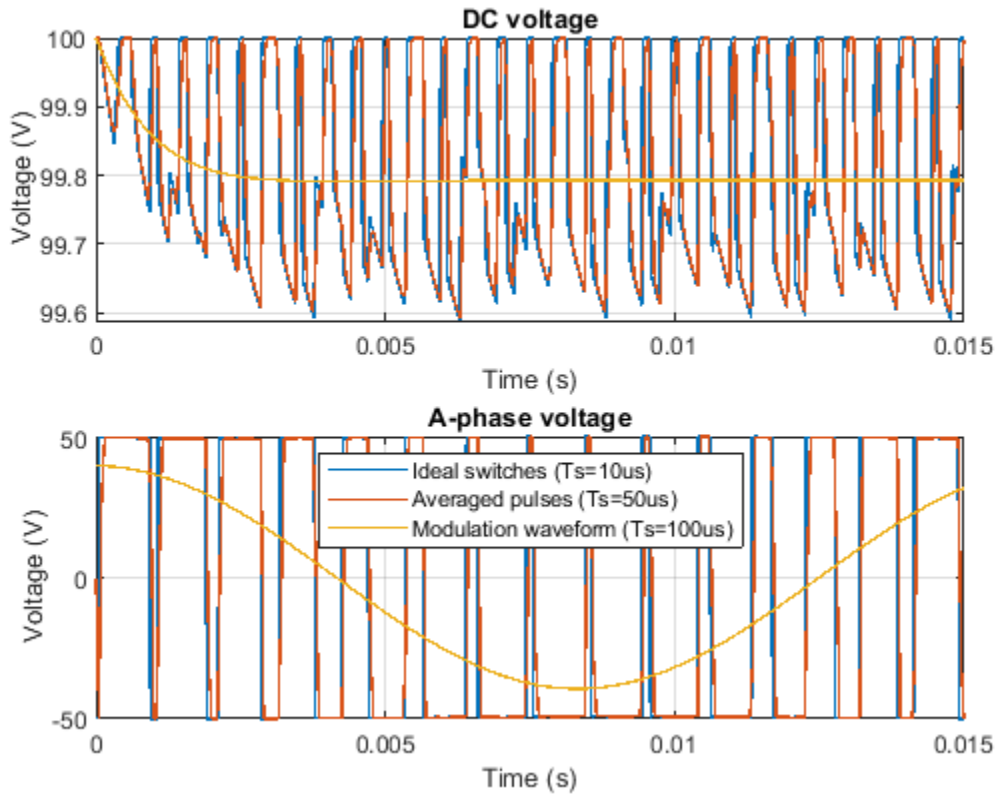
Earthing Effects with Unbalanced Load

This example shows the effects of three different types of earthing connections on network voltages and currents.

In each three-phase network, an AC voltage source drives a resistive load. The load is modeled as subsystem that contains two Variable Resistor blocks from the Simscape™ Electrical™ Passive library.

The earthing connection in each network is modeled using one of these blocks from the Simscape Electrical Connections library: * Grounded Neutral * Floating Neutral * Open Circuit

Initially the load is balanced. At 0.05 s, the resistance on phase-a increases from 1 to 3 Ohms. At 0.07 s, the resistance on phase-c increases from 1 to 1.5 Ohms. The resistance on phase-b does not change during the simulation.

Model
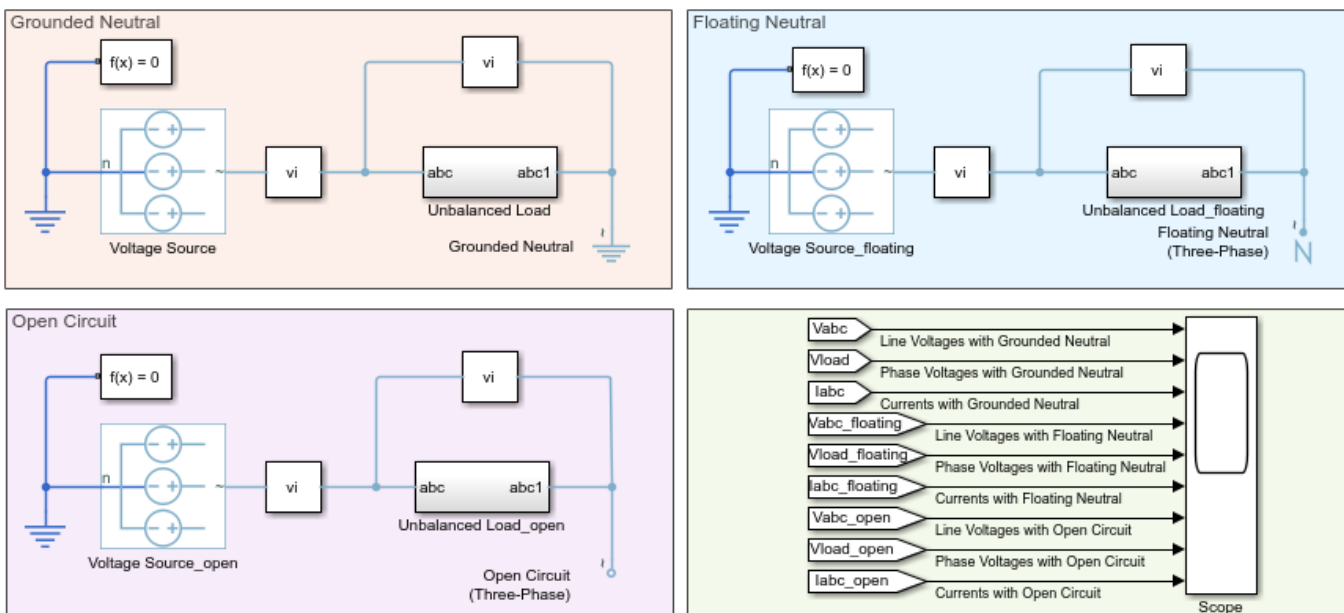
Earthing Effects with Unbalanced Load

1. Plot a comparison of phase voltages and currents between grounded and floating neutral (see code)
2. Explore simulation results using sscexplore
3. Learn more about this example

Simulation Results from Simscape Logging

The plots below show the line voltage, phase voltages, and phase currents flowing through an unbalanced three-phase resistive load when it is connected to different earthing options.

Notice that until 0.05 seconds there is no difference between grounded neutral and floating neutral. At 0.05 seconds, the load becomes unbalanced and the currents flowing through it change in both the scenarios. Due to the load being effectively connected to ground, the phase voltage with a grounded neutral remains unaffected throughout the whole simulation.

In the floating neutral scenario, the phase voltage changes significantly as the load becomes unbalanced, accordingly to the different resistance values in each phase. In this case, if a system

operator only monitors line-voltages, undesirable overvoltages could occur on individual phases without being observed. This could lead to overheating, accelerated breakdown of insulation, or other system problems.

Comparison of phase voltages with grounded and floating neutral
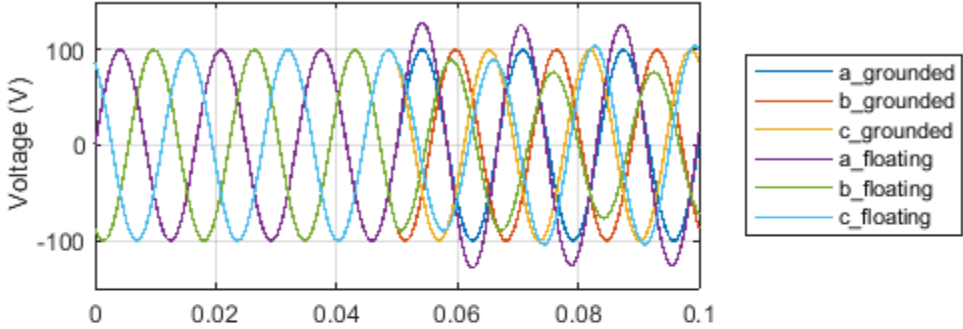
Comparison of currents with grounded and floating neutral
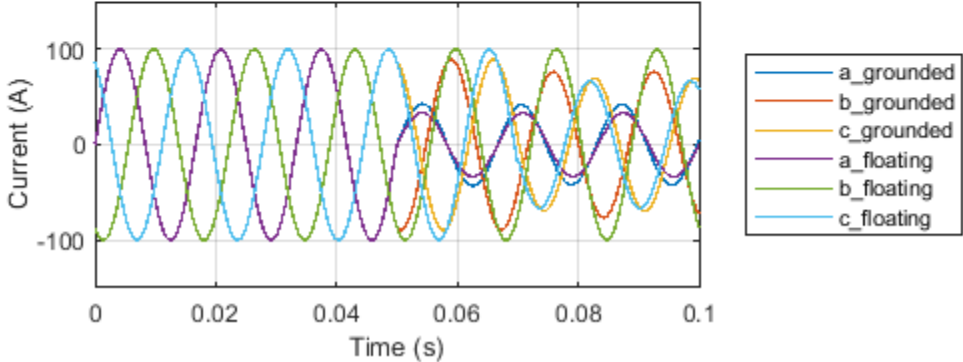
Clocked Set-Dominant SR-Latch

This example shows how to model a Set-dominant SR-Latch from Simscape™ Electrical™ logic components. Initial conditions are passed to the relevant AND gates via the initialization commands of the switches.

Model
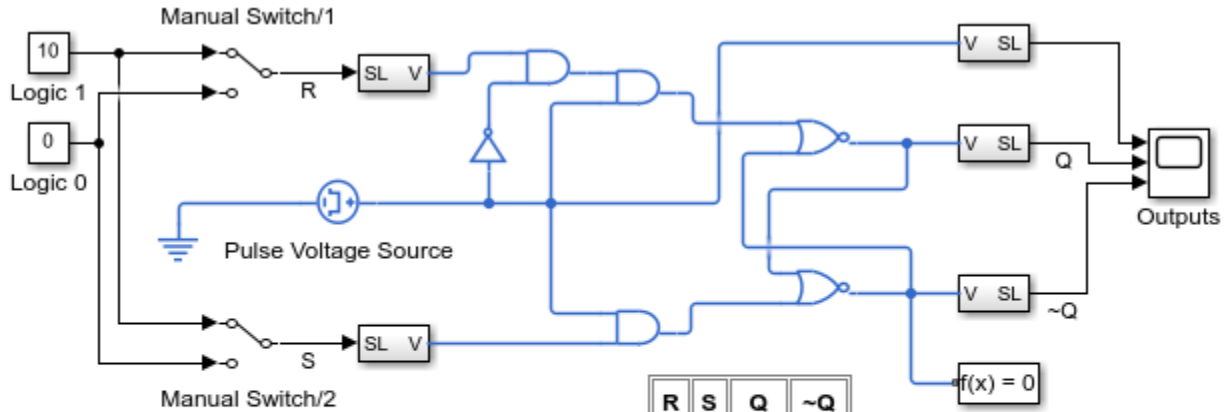
Clocked Set-Dominant SR-Latch

1. Plot inputs and outputs for flip-flop (see code)
2. Explore simulation results using sscexplore
3. Learn more about this example

R	S	Q	~Q
0	0	Memory	
0	1	1	0
1	0	0	1
1	1	Set	

Simulation Results from Simscape Logging

The plots below show the inputs and outputs for the Set-Dominant SR-Latch. Both inputs to the SR-Latch are set high, so its output state is set high for Q, and low for ~Q.

Set-Dominant SR-Latch Inputs

Set-Dominant SR-Latch Outputs
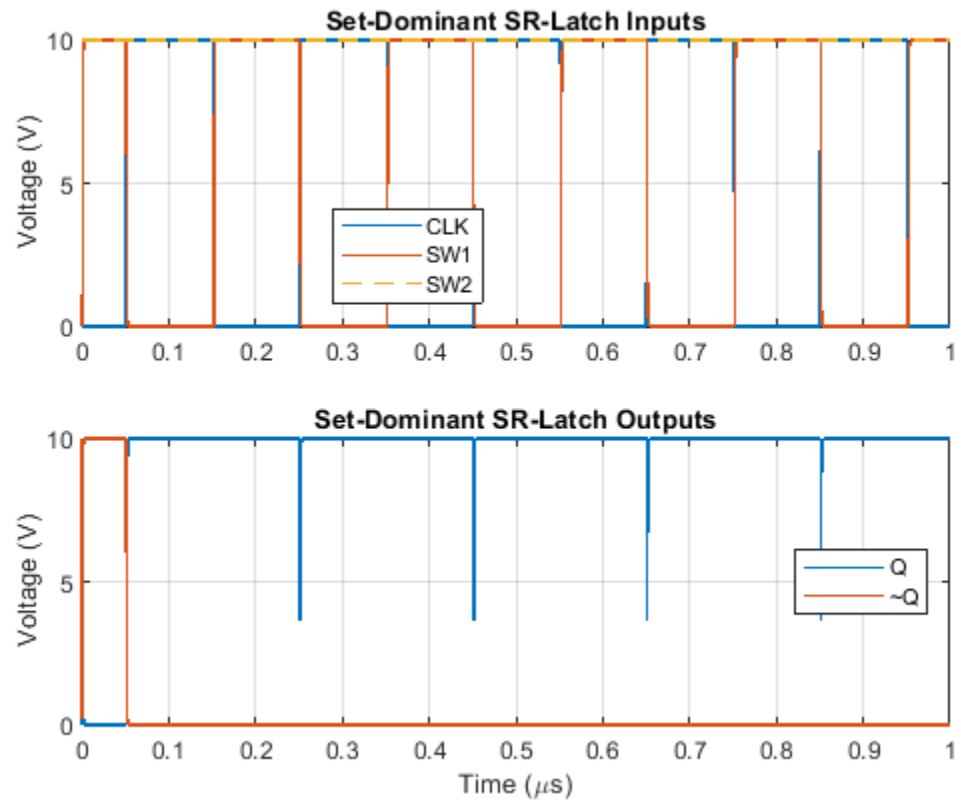
Clocked Reset-Dominant SR-Latch

This example shows how to model a Reset-dominant SR-Latch from Simscape™ Electrical™ logic components. Initial conditions are passed to the relevant AND gates via the initialization commands of the switches.

Model
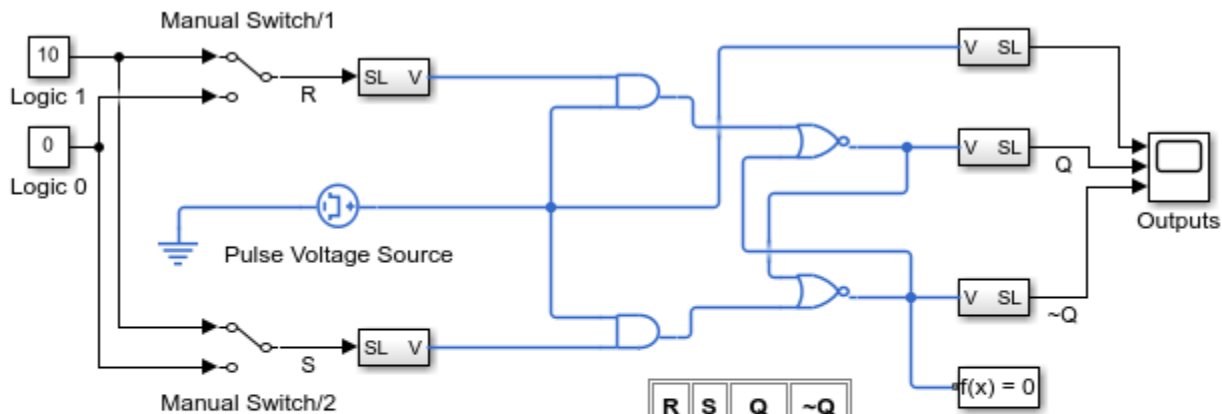
Clocked Reset-Dominant SR-Latch

1. Plot inputs and outputs for flip-flop (see code)
2. Explore simulation results using sscexplore
3. Learn more about this example

R	S	Q	~Q
0	0	Memory	
0	1	1	0
1	0	0	1
1	1	Reset	

Simulation Results from Simscape Logging

The plots below show the inputs and outputs for the Reset-Dominant SR-Latch. Both inputs to the SR-Latch are set high, and Q follows the opposite to the pulse signal. Output Q changes everytime the pulse is modified. The output ~Q stays low.
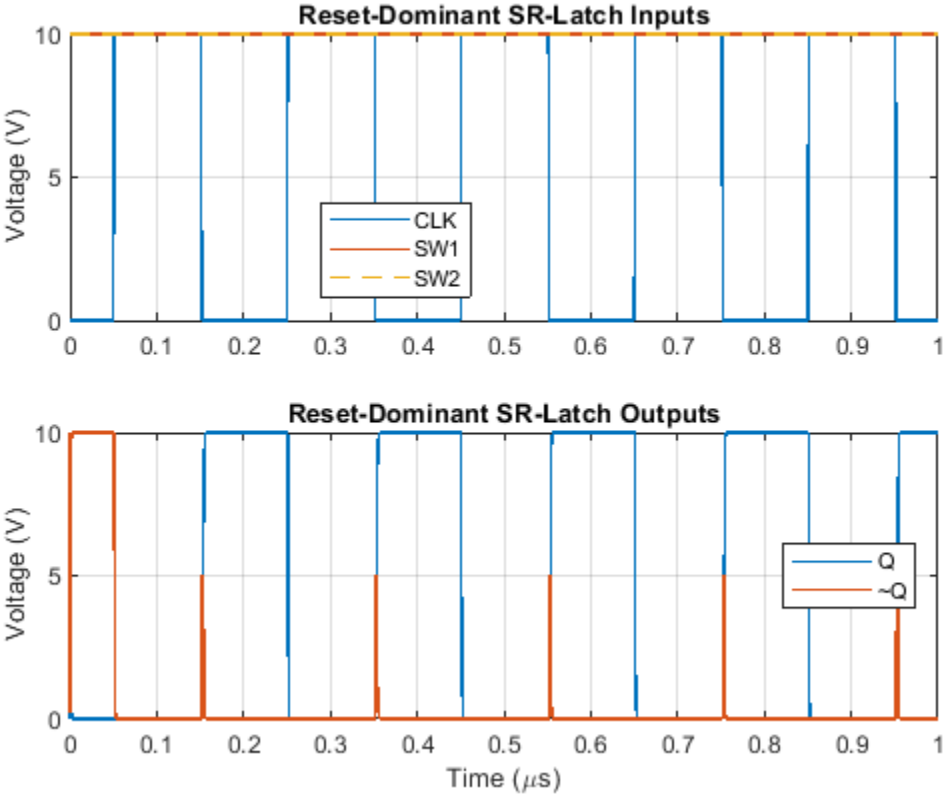
Frequency-Dependent Transmission Line

This example shows a custom frequency-dependent transmission line model. The characteristic admittance and propagation function are first derived from the frequency-dependent resistance, reactance, and susceptance. The derived values are fitted using `RF Toolbox`™. The Universal Line Model (ULM) [1] is then implemented in `Simscape`™ based on the fitted parameters. The results from the frequency-dependent transmission line model and the classic pi-section transmission line model are compared.

Model
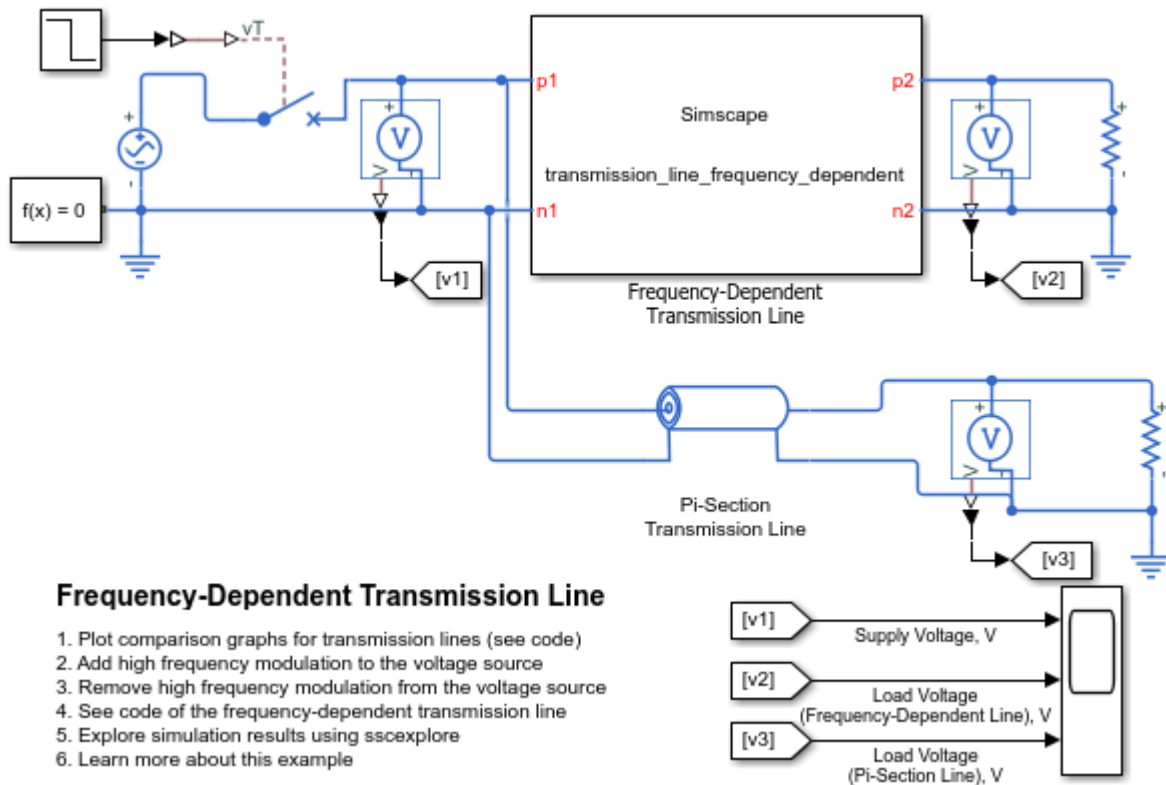
Specification of Parameters

Import the frequency-dependent parameters of the transmission line. These parameters are computed for an overhead line that is 20 m above the ground [2]. The ground resistivity and the skin effect of the conductor are considered negligible. The following parameters are pre-computed for the simulation:

- Frequency-dependent series resistance per unit length, R
- Frequency-dependent series reactance per unit length, X
- Frequency-dependent shunt susceptance per unit length, B
- Corresponding frequency, $freq$
- Length of the transmission line, len

```
Name          Size              Bytes  Class     Attributes

B            1000x1             8000  double
R            1000x1             8000  double
X            1000x1             8000  double
freq         1000x1             8000  double
len             1x1                8  double
```

The frequency-dependent R, L and C are shown in these figures:
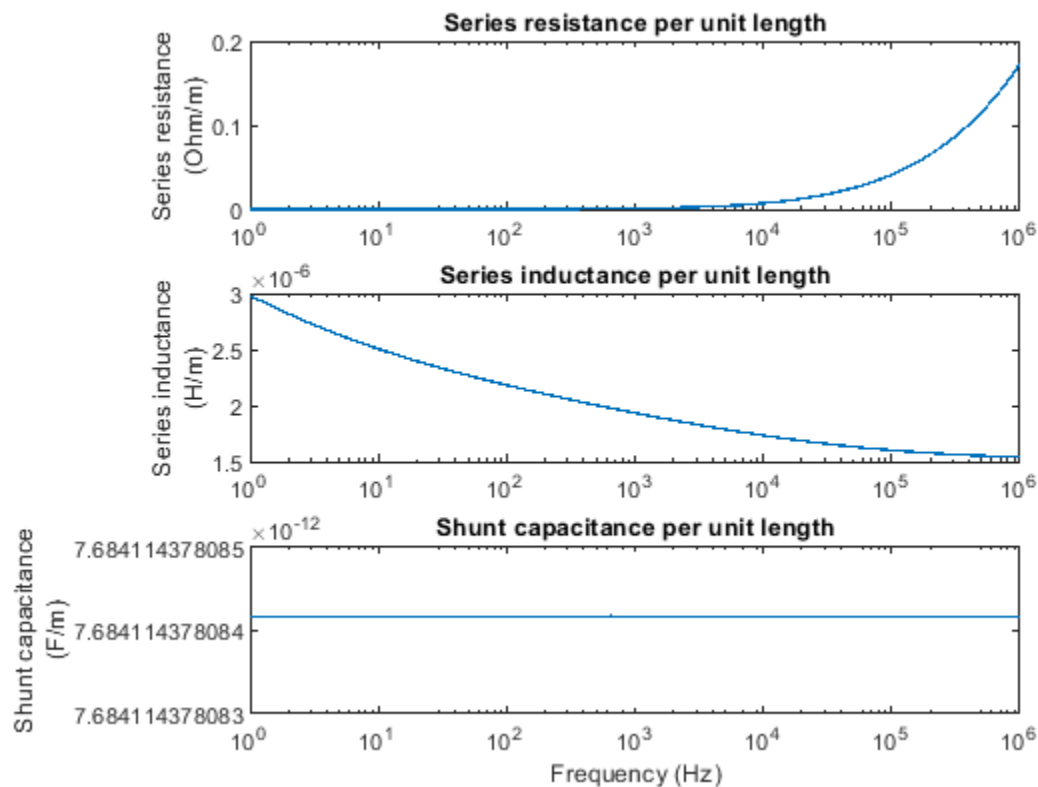
Characteristic Admittance and Propagation Function

The characteristic admittance is expressed as $Y_c = \sqrt{Y/Z}$, where Z and Y are the frequency-dependent series impedance and shunt admittance per unit length.

The propagation velocity is expressed as: $V = \omega/Imag(\Gamma)$, where $\Gamma = \sqrt{YZ}$ is the propagation constant, and ω is the corresponding angular velocity.

The propagation function, H, is then expressed as $H = e^{-\Gamma len}$.

Rational Fitting for Characteristic Admittance

To convert the characteristic admittance to the rational form, use the rational fit function `rationalfit` from RF Toolbox.

$$Y_c = \sum_{i=1}^{n} \frac{YcResidues_i}{s - YcPoles_i}$$

where:

- n is the number of poles (order of the fit).
- $YcPoles_i$ is the i_{th} pole of Y_c.
- $YcResidues_i$ is the i_{th} residue of Y_c.

In this case, an eighth order fit is performed.

These figures show the comparison between the characteristic admittance before and after rational fitting.
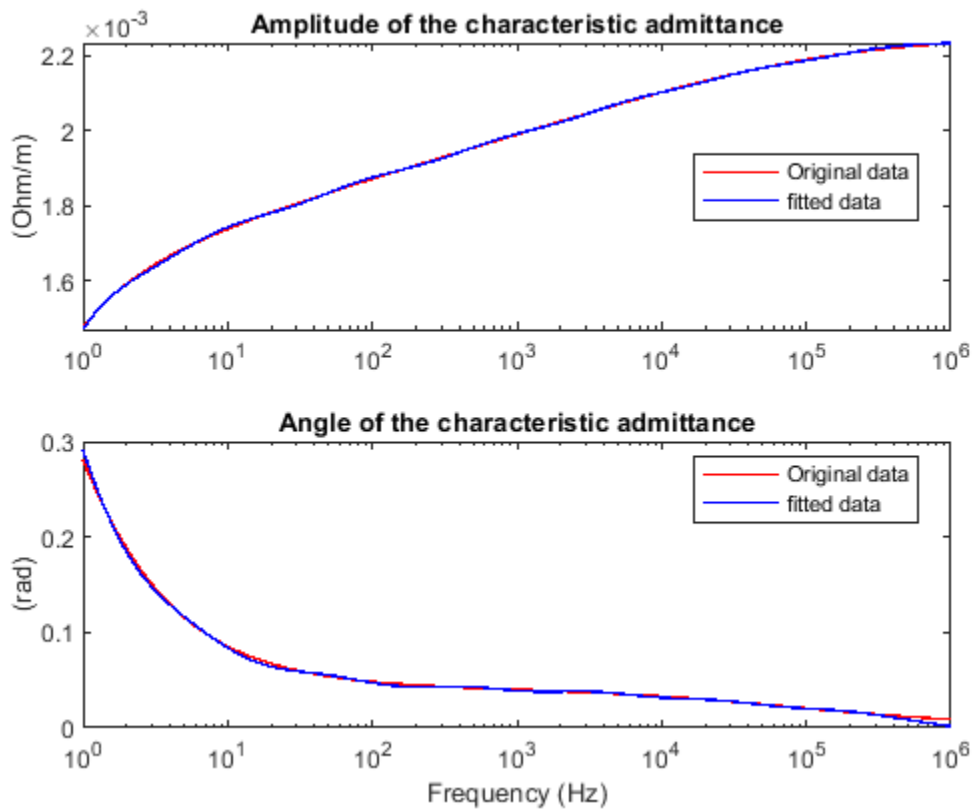
Rational Fitting for Propagation Function

The time delay of the propagation function is first removed to help reduce the order of rational fitting, where τ is the propagation time delay and H_k is the propagation function without time delay. The time delay is represented by a delay unit in the model.

To convert the propagation function without time delay to the rational form, use the `rationalfit` function from RF Toolbox.

$$H_k = \sum_{i=1}^{n} \frac{HkResidues_i}{s - HkPoles_i}$$

where:

- n is the number of poles (order of the fit).
- $HkPoles_i$ is the i_{th} pole of H_k.
- $HkResidues_i$ is the i_{th} residue of H_k.

In this case, an eighth order fit is performed.

These figures show that the propagation function H (with time delay) before and after rational fitting agree.
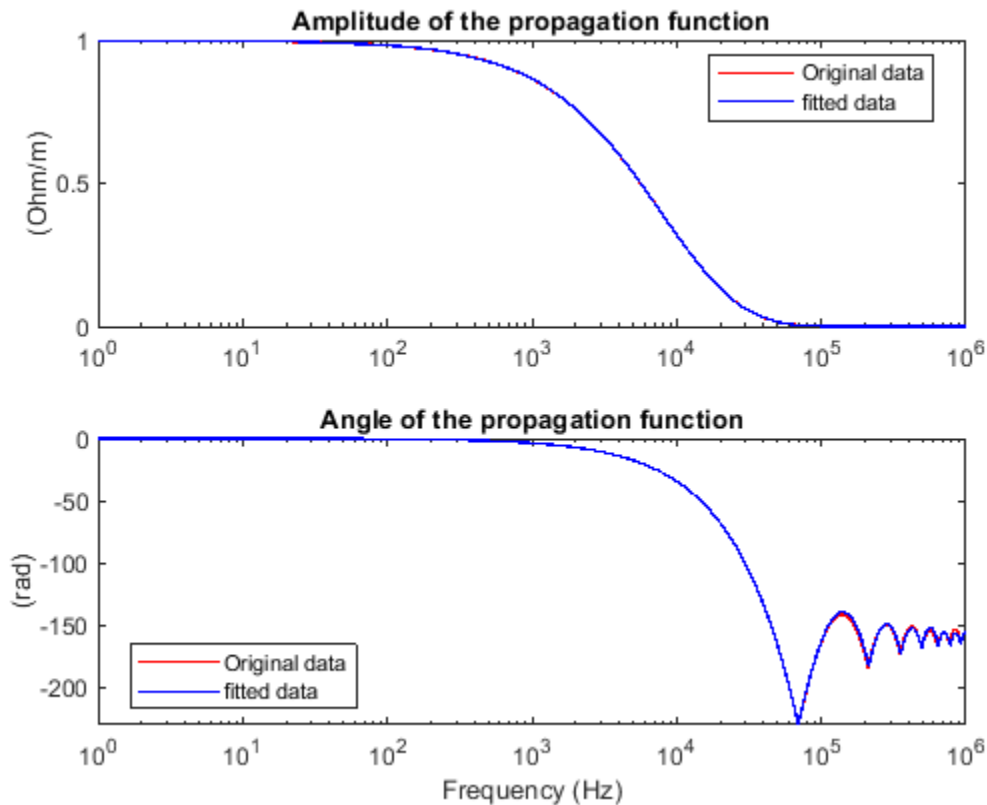
Universal Line Model Implemented in Simscape

In this example, only a single conductor and the ground return is considered. The equivalent circuit of the line in Laplace domain can be deduced from the Universal Line Model (ULM) [1]. Key variables are:

- V_j is the voltage at terminal j.
- I_j is the current at terminal j.
- $I_{sh,j}$ is the shunt current at terminal j.

- $I_{rfl,j}$ is the reflected current from terminal j.
- $I_{aux,j}$ is the auxiliary current from terminal j.
- H is the propagation function.
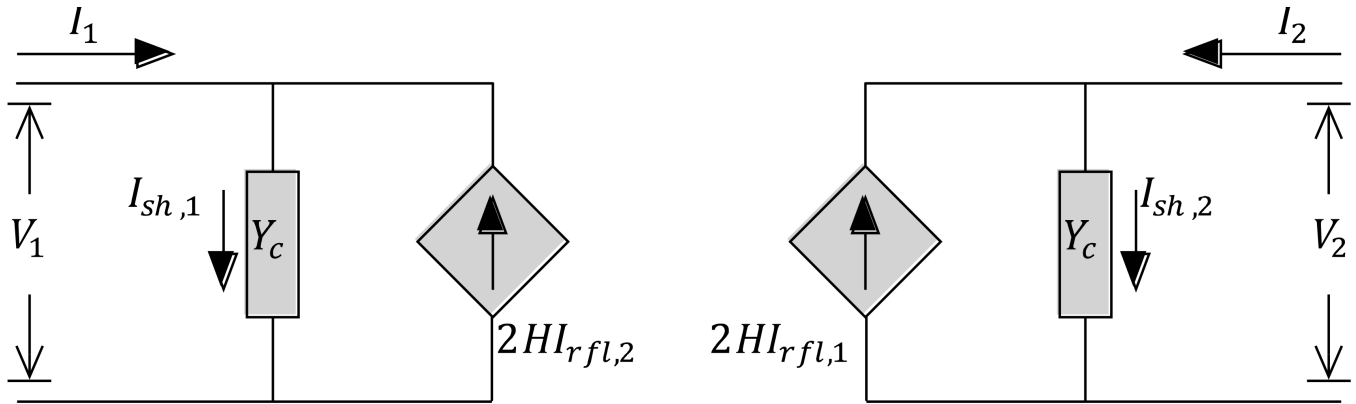
From this equivalent circuit, the system of equations can be written as:

- $I_1 = I_{sh,1} - I_{aux,2}$
- $I_2 = I_{sh,2} - I_{aux,1}$

where:

- $I_{aux,1} = 2HI_{rfl,1}$
- $I_{aux,2} = 2HI_{rfl,2}$
- $I_{rfl,1} = (I_1 + I_{sh,1})/2$
- $I_{rfl,2} = (I_2 + I_{sh,2})/2$
- $I_{sh,1} = Y_c V_1$
- $I_{sh,2} = Y_c V_2$

Considering the rational form of the characteristic admittance, the shunt current on terminal one is:

- $$I_{sh,1} = Y_c V_1 = \sum_{i=1}^{n} W_i$$

- $W_i = \frac{YcResidues_i V_i}{s - YcPoles_i}$

To transform these equations from Laplace domain to time domain, an inverse Laplace transform is performed. This transformation leads to:

- $$i_{sh,1} = Y_c v_1 = \sum_{i=1}^{n} w_i$$

- $\frac{dw_i}{dt} = YcPoles_i w_i + YcResidues_i v_1$

where w_i, v_1 and $i_{sh,1}$ are the time domain representation of W_i, V_1 and $I_{sh,1}$.

Similarly, considering the rational form of the propagation function, the auxiliary current on terminal one is:

- $$I_{aux,1} = 2HI_{rfl,1} = 2\sum_{i=1}^{n} X_i$$

- $$X_i = \frac{HkResidues_i}{s - HkPoles_i} e^{-s\tau} I_{rfl,1}$$

To transform these equations from Laplace domain to time domain, an inverse Laplace transform is performed. This transformation leads to:

- $$i_{aux,1} = 2\sum_{i=1}^{n} x_i$$

- $$\frac{dx_i}{dt} = HkPoles_i x_i + HkResidues_i i_{rfl,1}(t - \tau)$$

Currents on terminal two can be deduced using the same procedure. Time domain equations are then implemented in `Simscape` using Simscape language.

Simulation Results from Simscape Logging

For the first simulation case, the voltage source is generating a 60 Hz sine wave. The Pi-Section Transmission Line uses an RLC parameterized assuming a 60 Hz input, which matches the frequency of the voltage source. The plot shows the input and output terminal voltages of the transmission line. The two models show good agreement at steady state.
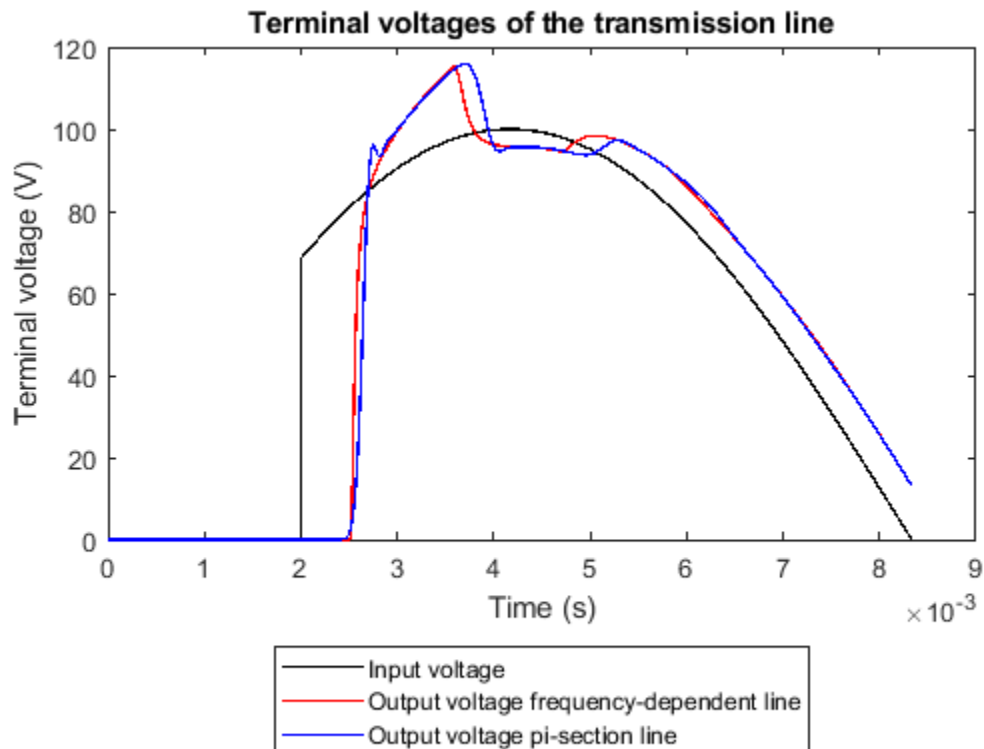
For the second simulation case, the voltage source is generating a 60 Hz sine wave with a 10 kHz modulation. The Pi-Section Transmission Line still uses an RLC parameterized assuming a 60 Hz input. It is clear that the custom frequency-dependent transmission line model is suitable for broader band signals whereas the pi-section model is only applicable for extremely narrow band signals.
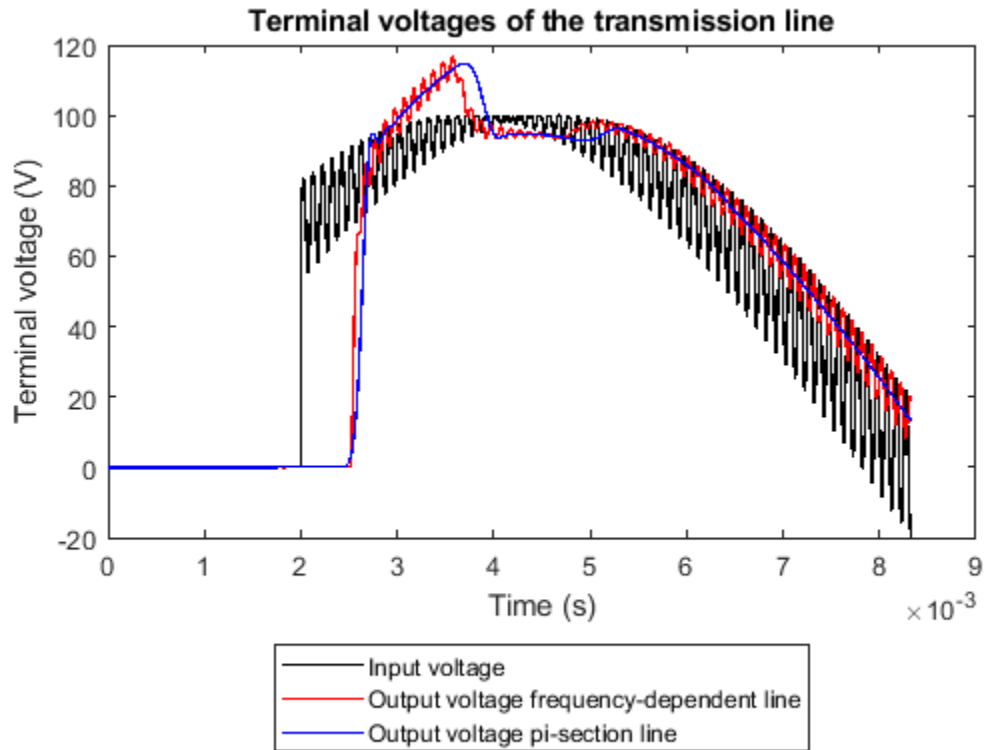
References

[1] Morched, Atef, Bjorn Gustavsen, and Manoocher Tartibi. "A universal model for accurate calculation of electromagnetic transients on overhead lines and underground cables." IEEE Transactions on Power Delivery 14.3 (1999): 1032-1038.

[2] Dommel, Herman W. "Overhead line parameters from handbook formulas and computer programs." IEEE Transactions on Power Apparatus and Systems 2 (1985): 366-372.

Load Side Converter Control

This example shows how to control the RMS voltage in a load-side converter. The load is provided by a three-phase series RL element. The Control subsystem uses a PI-based cascade control structure with two control loops, an outer voltage control loop and an inner current control loop. The simulation uses step references. The Scopes subsystem contains scopes that allow you to see the simulation results.

Model

Load Side Converter Control

1. Plot voltage and current (see code)
2. Explore simulation results using sscexplore
3. Modify model parameters
4. Learn more about this example
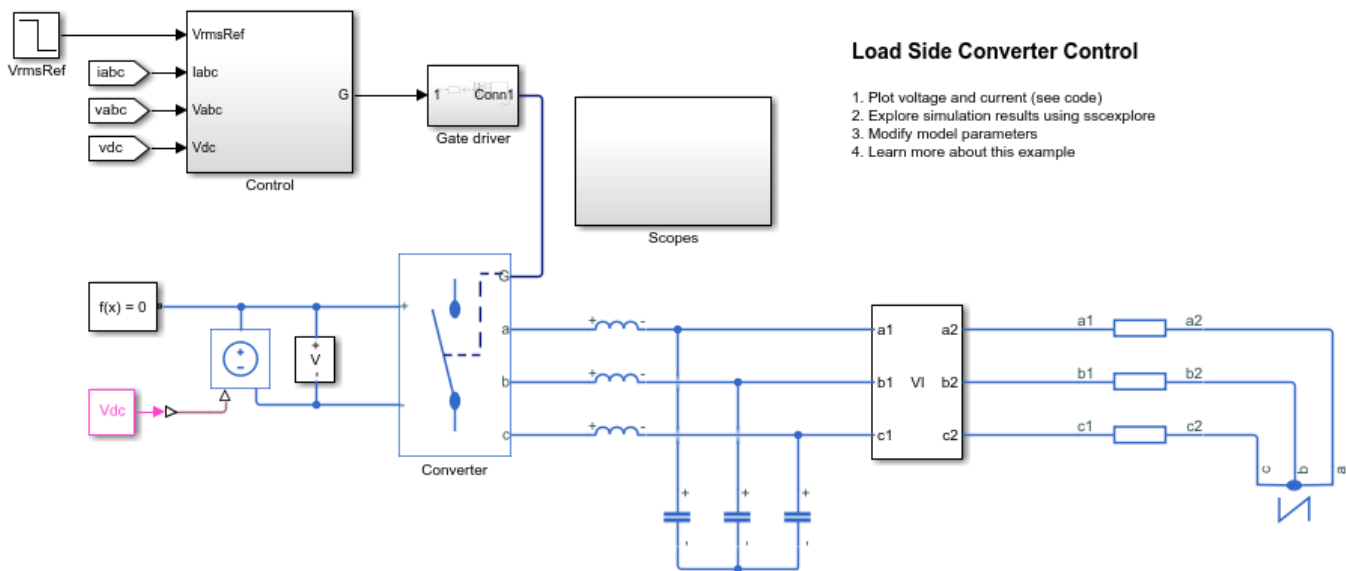
Simulation Results from Simscape Logging

The plot below shows the output RMS voltage and the AC phase currents.
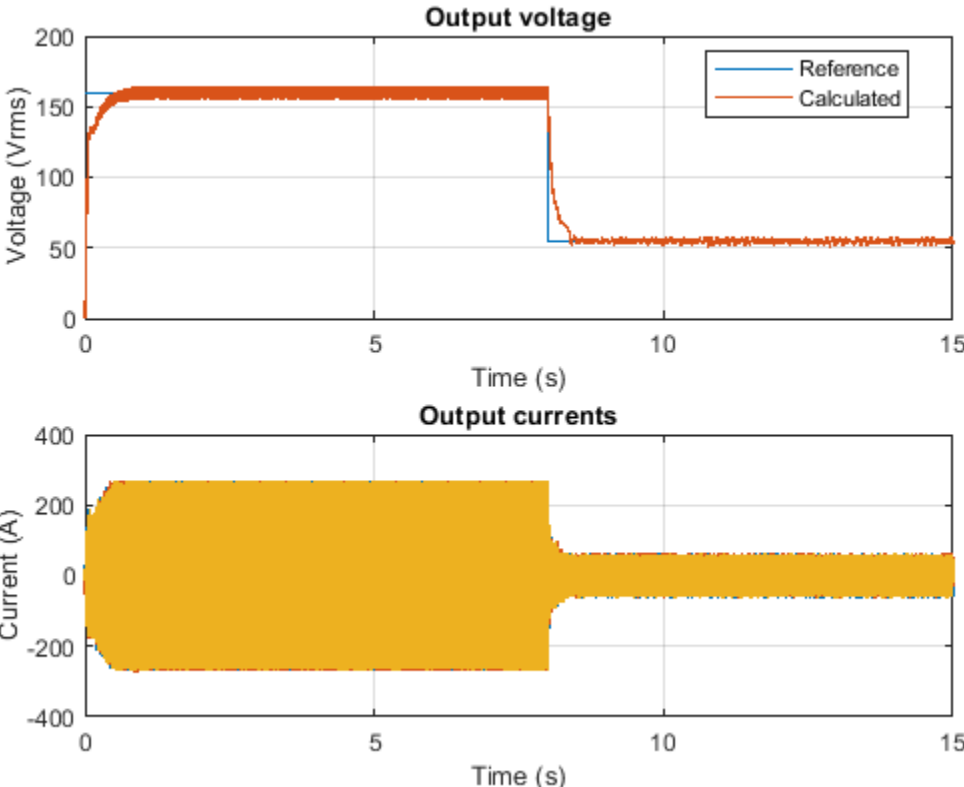
PMSM Position Control

This example shows how to control the rotor position in a PMSM based electrical drive. An ideal torque source provides the load. The Control subsystem uses a cascade control structure with two control loops, an outer loop for position and speed control and an inner loop for current control. An optimal state-feedback linear quadratic regulator controls the position and speed. A Luenberger observer estimates the load. The inner current-control loop is implemented using PI controllers. The PMSM is fed by a controlled three-phase inverter. The simulation uses step references. The Scopes subsystem contains scopes that allow you to see the simulation results.

Model
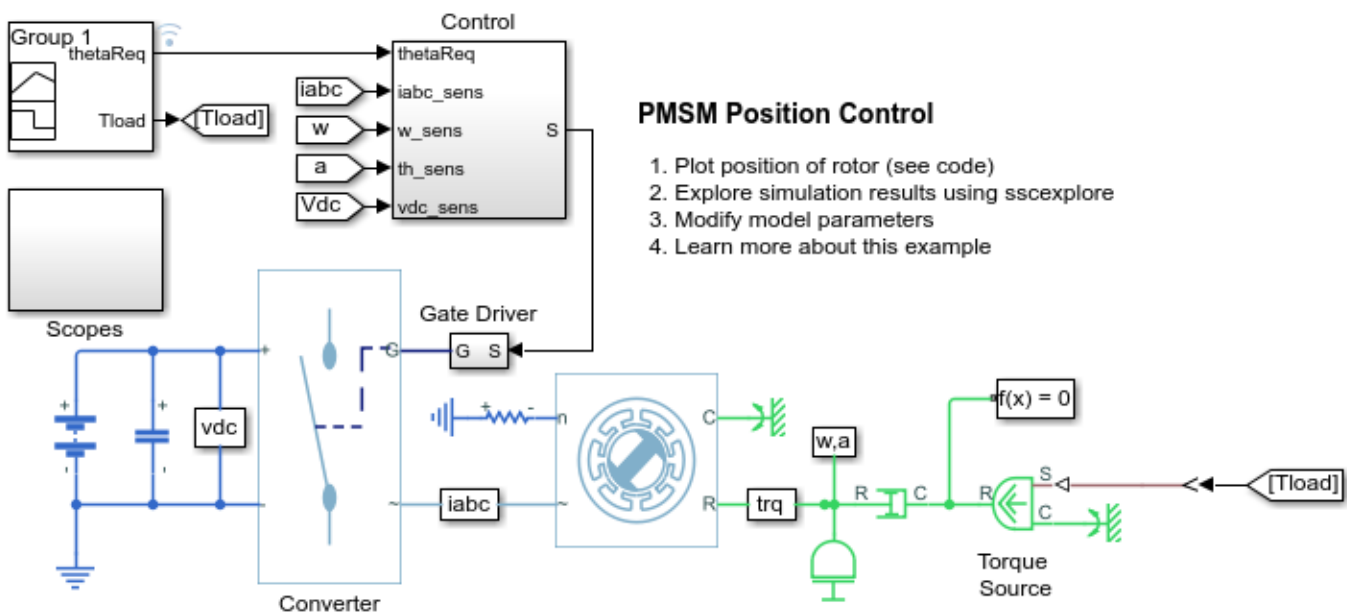
Simulation Results from Simscape Logging

The plot below shows the requested and measured angle for the test and the rotor speed in the electric drive.
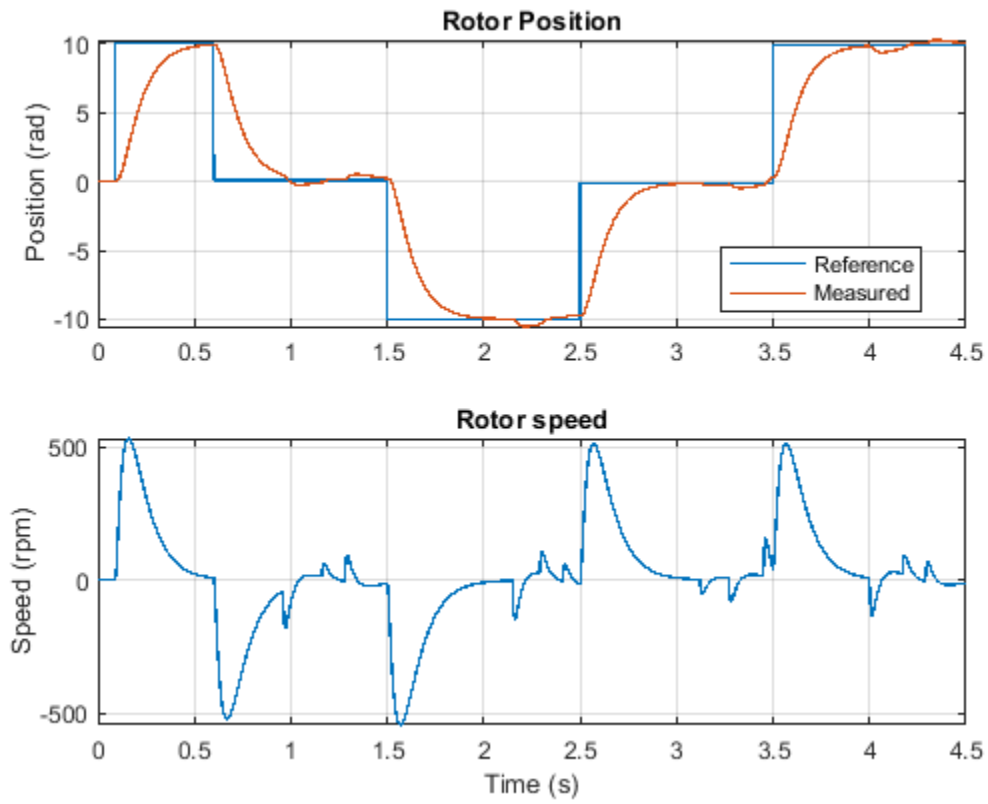
Power Factor Correction for CCM Boost Converter

This example shows how to correct the power factor using a PFC pre-converter. This technique is useful when non-linear impedances, such as Switch Mode Power Supplies, are connected to an AC grid. As the current flowing through the inductor is never zero during the switching cycle, the boost converter operates in Continuous Conduction Mode (CCM). The inductor current and the output voltage profiles are controlled using simple integral control. During start up, the reference output voltage is ramped up to the desired voltage.

Model
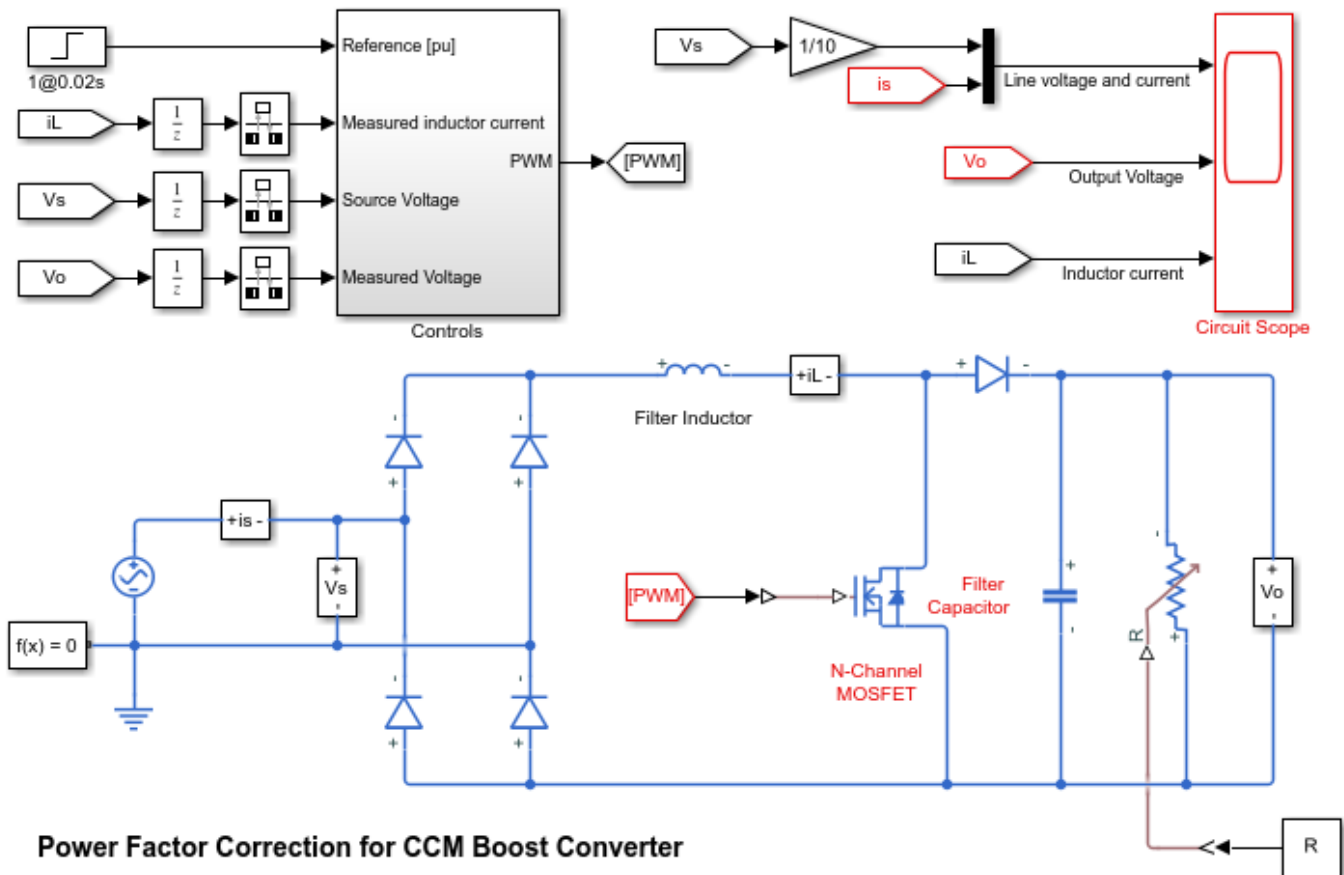
Power Factor Correction for CCM Boost Converter

1. Plot voltage and current (see code)
2. Explore simulation results using sscexplore
3. Modify model parameters
4. Learn more about this example

Simulation Results from Simscape Logging

The plot below shows the line voltage and current and the the output voltage.
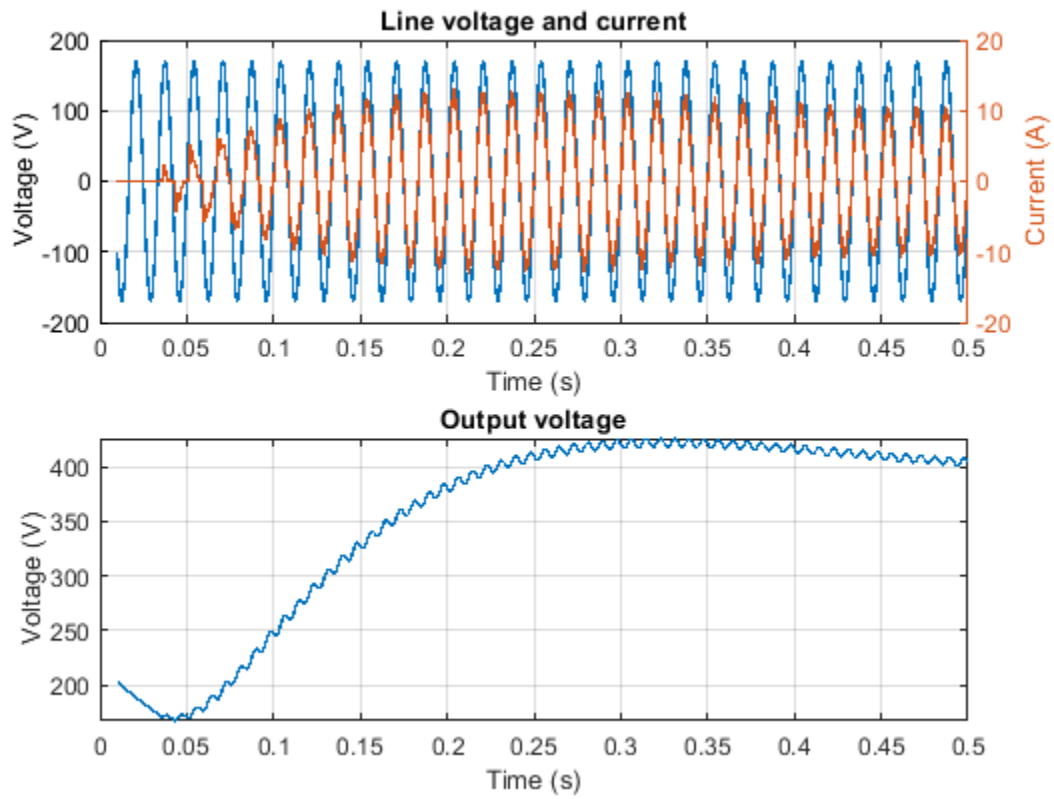
PWM-Controlled DC Motor

This model shows how to use the Controlled PWM Voltage and H-Bridge blocks to control a motor. The DC Motor block uses manufacturer datasheet parameters, which specify the motor as delivering 10W mechanical power at 2500 rpm and no-load speed as 4000 rpm when run from a 12V DC supply. Hence if the PWM reference voltage is set to its maximum value of +5V, then the motor should run at 4000 rpm. If it is set to +2.5V, then it should run at approximately 2000 rpm. The Simulation model parameter is set to Averaged for both the Controlled PWM Voltage and H-Bridge blocks, resulting in fast simulation. To validate the averaged behavior, change the Simulation mode parameter to PWM in both blocks.

Model
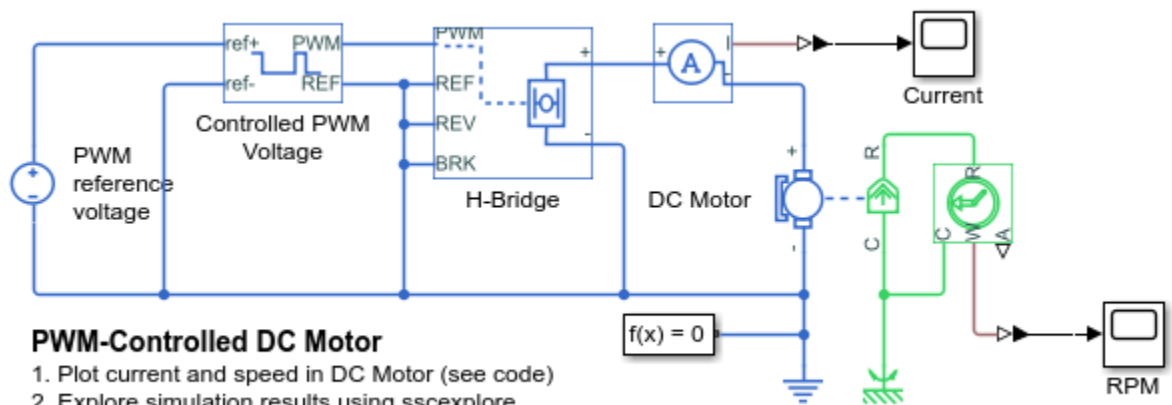
PWM-Controlled DC Motor
1. Plot current and speed in DC Motor (see code)
2. Explore simulation results using sscexplore
3. Learn more about this example

Simulation Results from Simscape Logging

The plot below shows the current passing through the motor and the speed of the motor shaft.
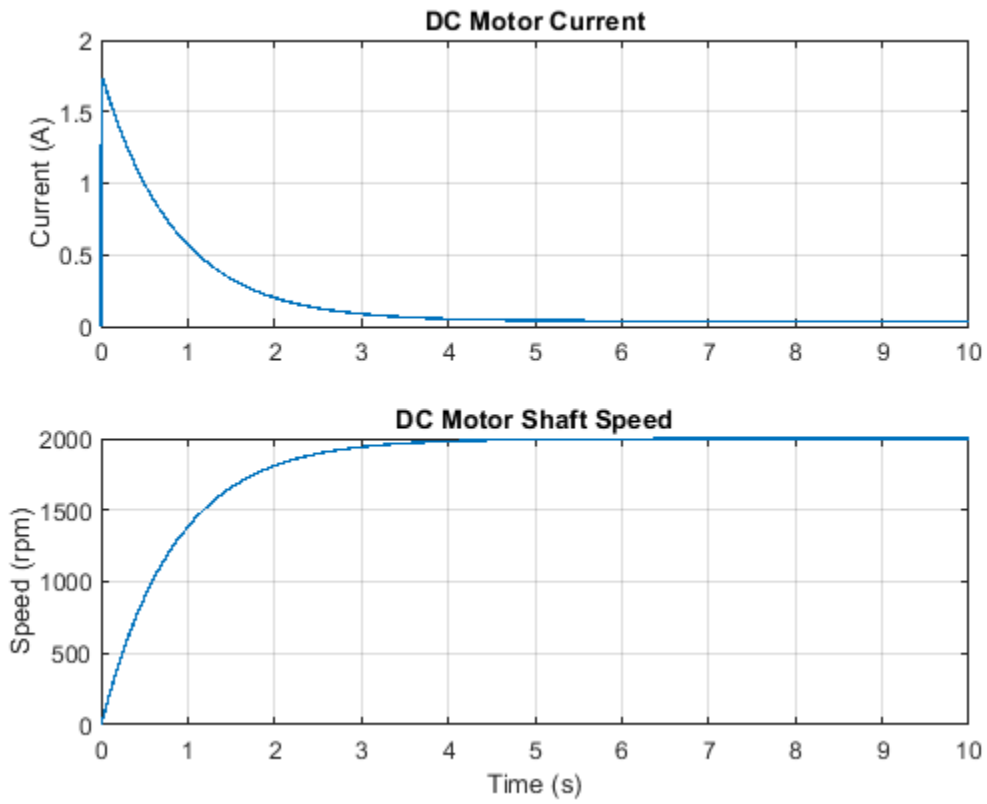
Microcontroller with GPIO, ADC and DAC Connections

This example shows how to model the interface between a microcontroller unit (MCU) and a physical system. Here the microcontroller's GPIO, ADC and DAC connections are used to control a DC motor and connected load with limited angle travel. Load angle measurement is via a potentiometer sensor. This measurement is calibrated by initially ramping the rotor position until the photodiode detects the zero-angle light pulse from the LED. Once calibrated the MCU commands a 0.1Hz 45 degree amplitude sinusoid.

Model
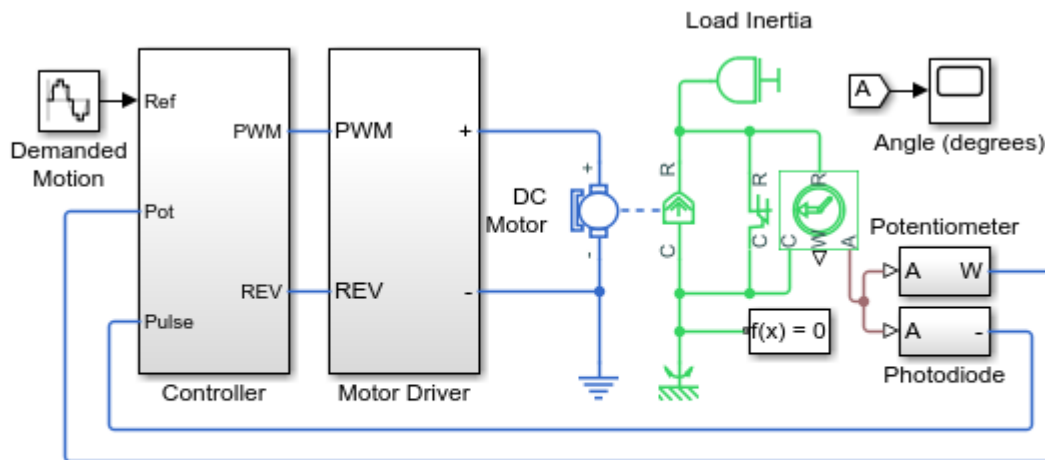
Microcontroller with GPIO, ADC and DAC Connections
1. Plot angle and control mode of motor (see code)
2. Explore simulation results using sscexplore
3. Learn more about this example

Controller Subsystem
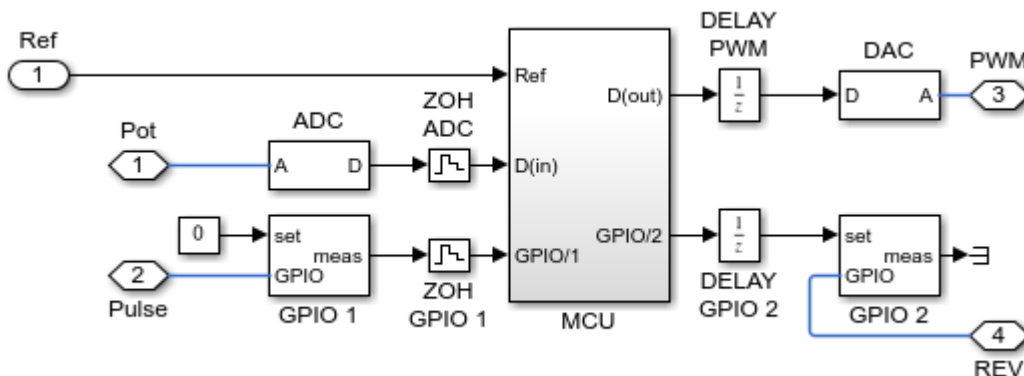
GPIO 1 Subsystem
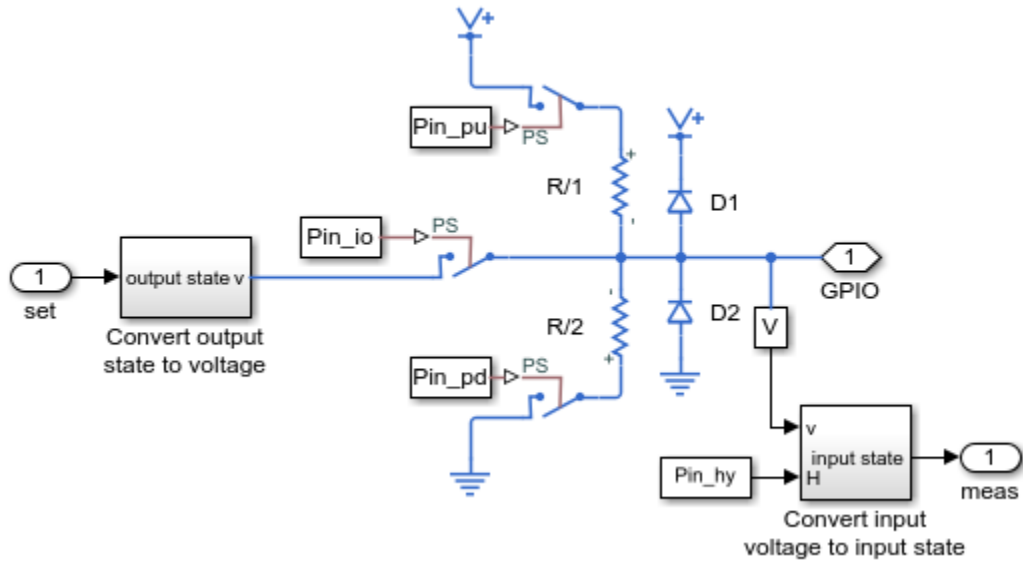
Motor Driver Subsystem
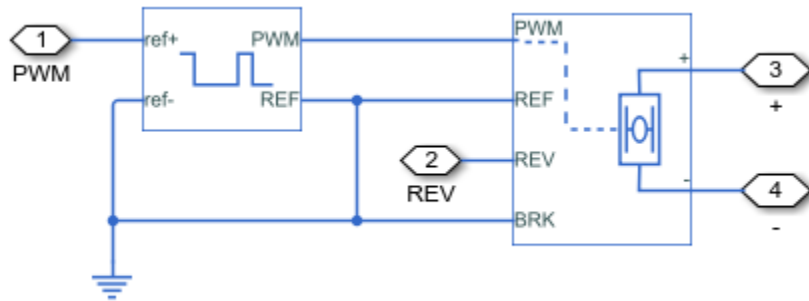
Photodiode Subsystem

Potentiometer Subsystem

Simulation Results from Simscape Logging

The plot below shows the performance of the motor control system. At the start of simulation, the controller spins the motor until it finds a known position of the motor shaft (0 degrees). After that, the controller tracks the reference signal.

Motor Torque-Speed Curves

This example shows a comparison of the torque-speed characteristics for five different motor types. To select the motor type, right-click on the Electric Motor block, select Variant->Override using and then the desired motor. All motors have been sized for roughly the same mechanical power rating.

To run and compare all five motors, click on the hyperlink to plot characteristic curves. Understanding the torque-speed and power-speed behavior of a motor is critical when choosing a motor for a given application. Modify this model and its supporting library to compare specific motors based on their datasheet values. Critical parameters are typically starting torque (torque at zero RPM), and peak/maximum power and the corresponding RPM.

The DC Motor has a linear torque-speed characteristic, and power peaks at about half the maximum RPM. The Shunt Motor also has a linear torque-speed characteristic and similarly-shaped power curve. In practice a shunt motor cannot run at its peak power or torque as the currents involved are too large. This particular motor is designed to deliver a maximum 50 Watts of mechanical power at 4000 rpm.

The Universal Motor torque-speed characteristic follows $1/x^2$ relationship. In this case, the relatively large starting torque is achieved with realistic current values. The power is just peaking at around 6000rpm. The Induction Motor torque-speed curve is typical of this type of motor having a low starting torque, and a peak torque just below synchronous speed. Note that both motoring and generating regions are plotted.

The Servomotor is a controlled servomotor with a maximum torque of 0.25 Nm. The torque demand is set to 0.2 Nm, which is achievable up to 4350rpm above which envelope limiting occurs. To see the permissible torque-envelope, set the Electric Motor variant choice to 'Servomotor', open the Electric Motor block and double click on the Servomotor block. The envelope is defined by the first two block parameters.

Model

Motor Torque-Speed Curves
1. Plot characteristic curves for all motors (see code)
2. Explore simulation results using sscexplore
3. Learn more about this example

Simulation Results from Simscape Logging

The plots below show characteristic curves for different types of motors.

Linear Electric Actuator (Motor Model)

This model shows how to develop a model of an uncontrolled linear actuator using datasheet parameter values. The actuator consists of a DC motor driving a 6.25:1 worm gear which in turn drives a 3mm lead screw to produce linear motion. Manufacturer data for the actuator defines the no-load linear speed (26mm/s), rated load (1000N), rated-load linear speed (19mm/s), and maximum current (5A). The maximum static force is 4000N and the rated voltage is 24V DC.

If friction and rotor damping are neglected, the DC Motor mask parameters can be calculated as follows. A no-load speed of 26mm/s is equivalent to (26/3)*6.25*60=3250rpm. The rated motor speed is (19/3)*6.25*60=2375rpm, and the rated power is 1000N*19e-3m/s=19W. To run the model based on these parameters, set the DC Motor Model parameterization parameter to By rated power, rated speed & no-load speed and comment out the Friction block. The results validate the speeds for zero and 1000N load, but under-predict the maximum current and maximum static force.

For a more accurate approximation of the motor, friction effects must be included. The no-load and rated-load speed information depends on the unknown friction levels, so instead we parameterize the motor in terms of the maximum current which occurs when the rotor is locked (i.e. no back emf). Then the winding resistance is given by rated voltage divided by maximum current i.e. 24V/5A = 4.8 ohms. The no-load current, measured to be 0.35A, indirectly provides information about the friction. Given this value, the torque constant is given by maximum static torque divided by the net useful current i.e. (4000N*3e-3m/s/(2*pi*6.25))/(5A-0.35A) = 0.066Nm/A. To run the model based on these parameters, set the DC Motor Model Parameterization parameter to By equivalent circuit parameters and reinstate the Friction block. The torsional friction must now be determined such that the no-load speed is 26mm/s. A value of 0.022Nm is required to achieve this. The model also confirms maximum current is 5A and maximum linear force is 4000N.

Note that a limitation of this model is that the load can back drive the motor through the worm gear. A more detailed friction model would be required to prevent this.

Model
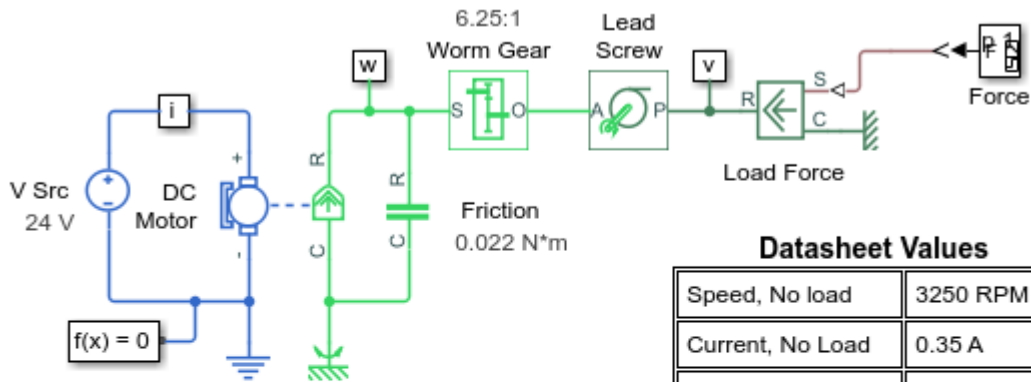
Linear Electric Actuator (Motor Model)
1. Plot results of actuator test (see code)
2. Explore simulation results using sscexplore
3. Learn more about this example
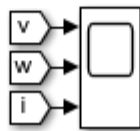
Datasheet Values

Speed, No load	3250 RPM
Current, No Load	0.35 A
Speed, Rated Load	2375 RPM
Rated Load	1000N
Rated Voltage	24 V
Max Static Force	4000N
Max Current	5A
Max Load	4000N

Simulation Results from Simscape Logging

The plot below shows the performance of the motor compared to values found on the datasheet for a linear actuator. The test includes no load, rated load, and max load. The motor speed and currents match the specification closely, indicating that we have assigned parameter values that match the datasheet.
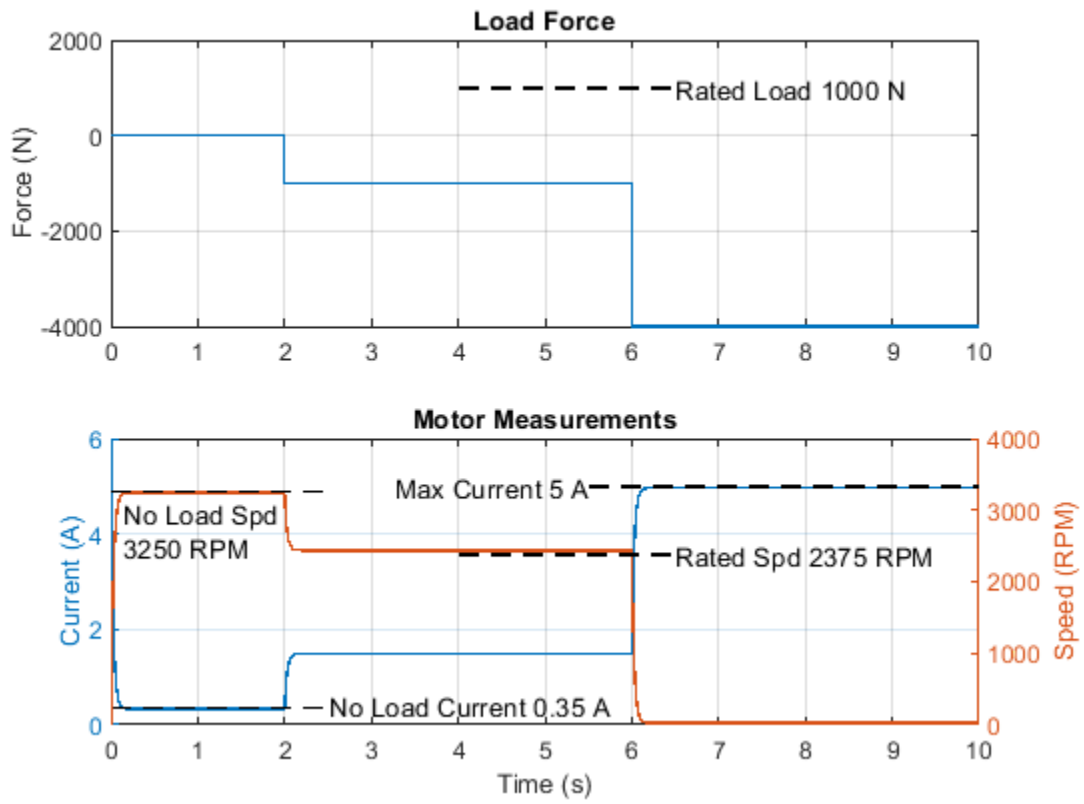
Linear Electric Actuator with Control

This example shows a detailed implementation model of a controlled linear actuator. The actuator consists of a DC motor driving a worm gear which in turn drives a lead screw to produce linear motion. The model includes quantization effects of the Hall-effect sensor and the implementation of the control in analog electronics. There are multiple variant subsystems in this model that have models at varying levels of fidelity.

The speed control and current control models are implemented using pure Simulink® blocks. This permits us to easily specify the control algorithm and identify the requirements for an analog circuit implementation, which is shown in another variant.

The motor drive circuit model is implemented in an abstract circuit which can simulate in two different simulation modes. In average mode, the voltage applied to the motor varies continuously between the maximum and minimum values depending upon the voltages at its input pins. In PWM mode, a PWM voltage is applied to the motor and its duty cycle depends on the voltage applied at its input pins. The motor circuit is also implemented using semiconductor devices. This enables exploration of individual switching events.

Model
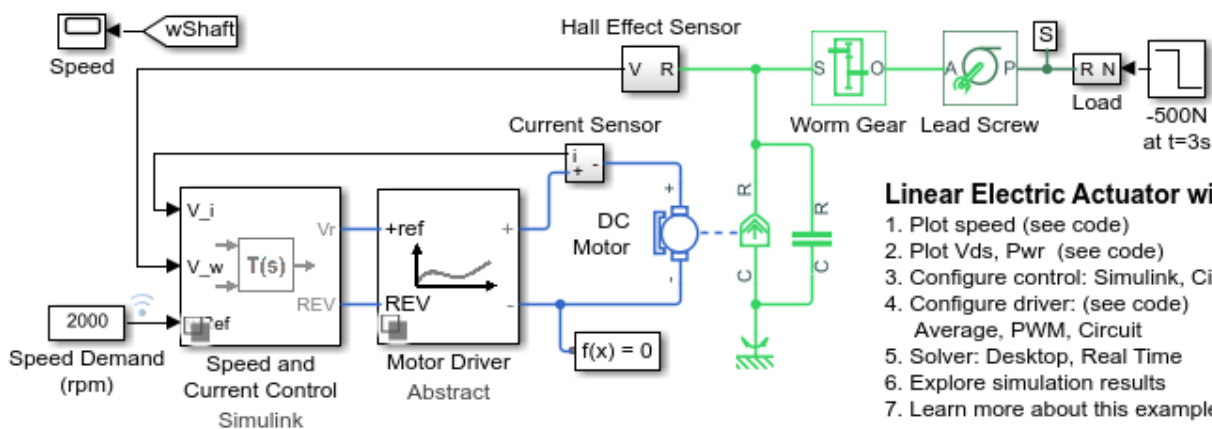
Speed Controller Subsystem

The circuit shown below implements a PI controller with a filter using Simscape™ Electrical™ components. This is enabled when the Circuit variant is selected within the Speed and Current Control subsystem.
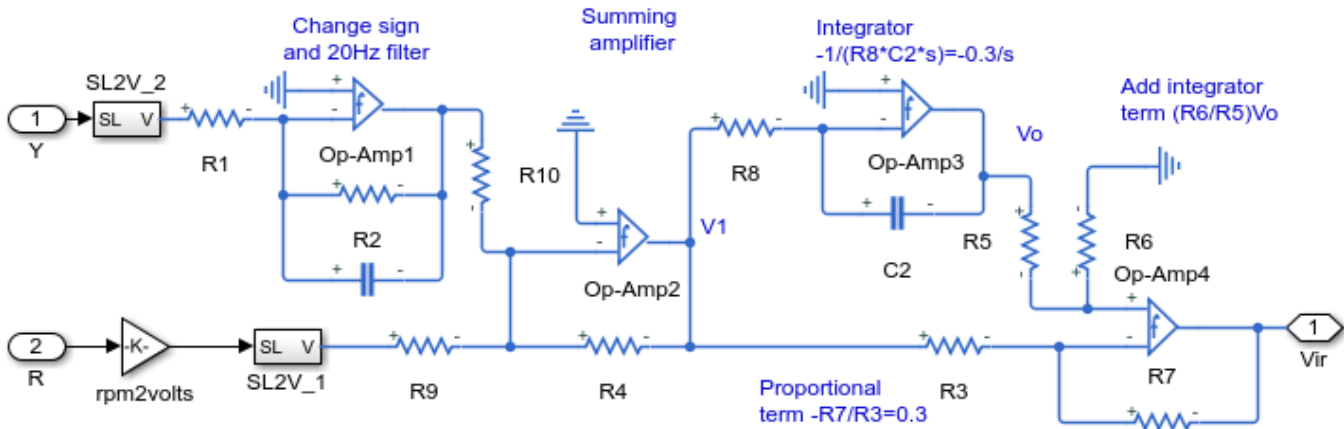
Abstract Motor Driver Circuit Subsystem

The blocks below are an abstract model of a motor driver circuit. The Controlled PWM Voltage block generates a PWM signal based on the voltages at its +ref and -ref ports. The H-Bridge block represents an H-bridge circuit commonly used to drive electric motors. These blocks can be configured to run in average mode or PWM mode.

Simulation Mode: Averaged, PWM
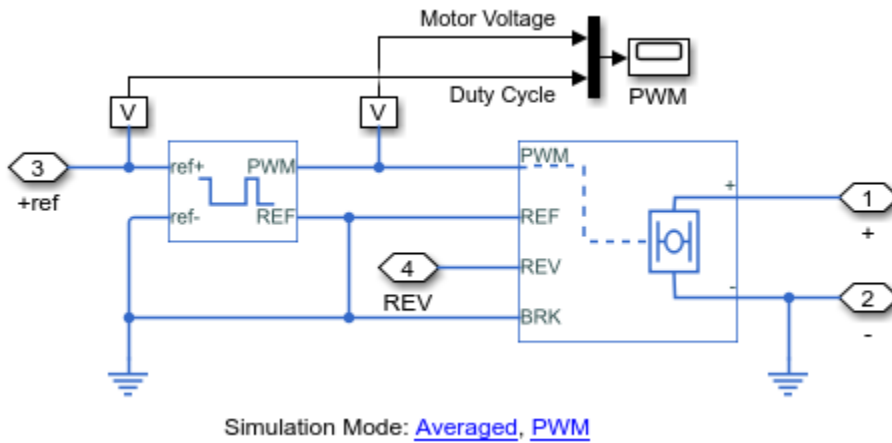
Simulation Results from Simscape Logging - Abstract Model

The plot below shows how the motor speed tracks a reference input. The actual speed of the shaft and the speed determined by the shaft encoder are shown, indicating that the signal that the control system sees is not a perfect measurement of the shaft speed. The system response to a disturbance torque at 3 seconds is shown.
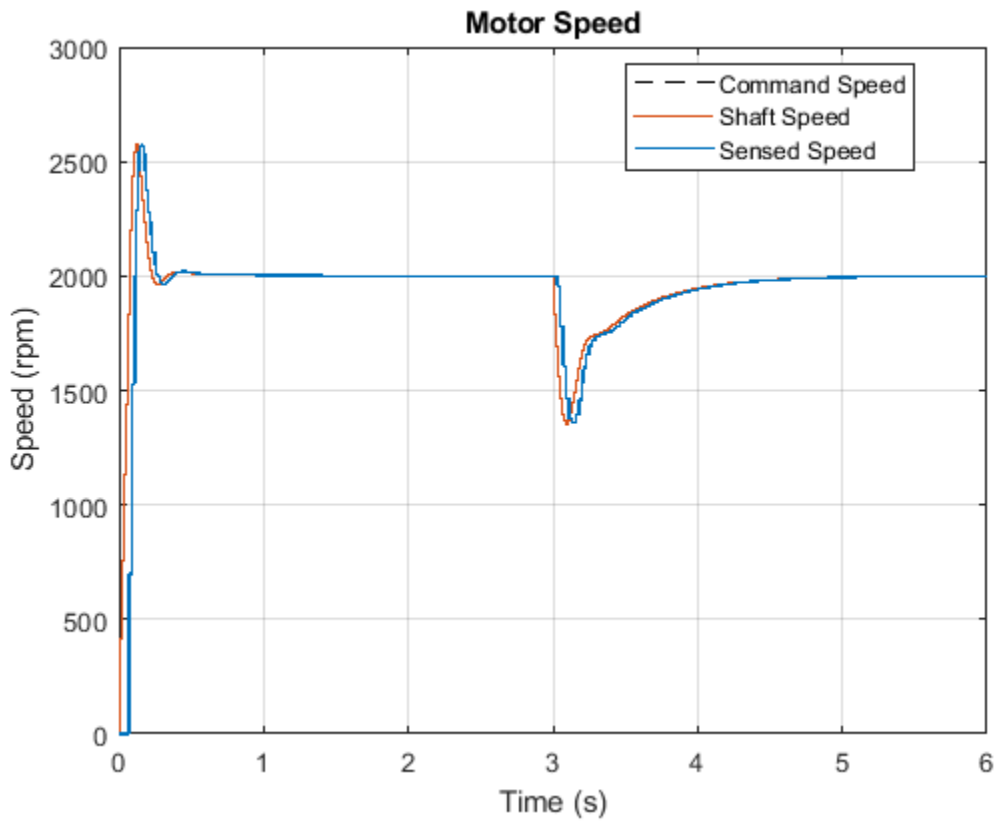
Circuit Subsystem

The circuit shown below is an H-bridge circuit implemented using Simscape Electrical components. It is enabled when the Circuit variant of the Motor Driver variant subsystem is selected.
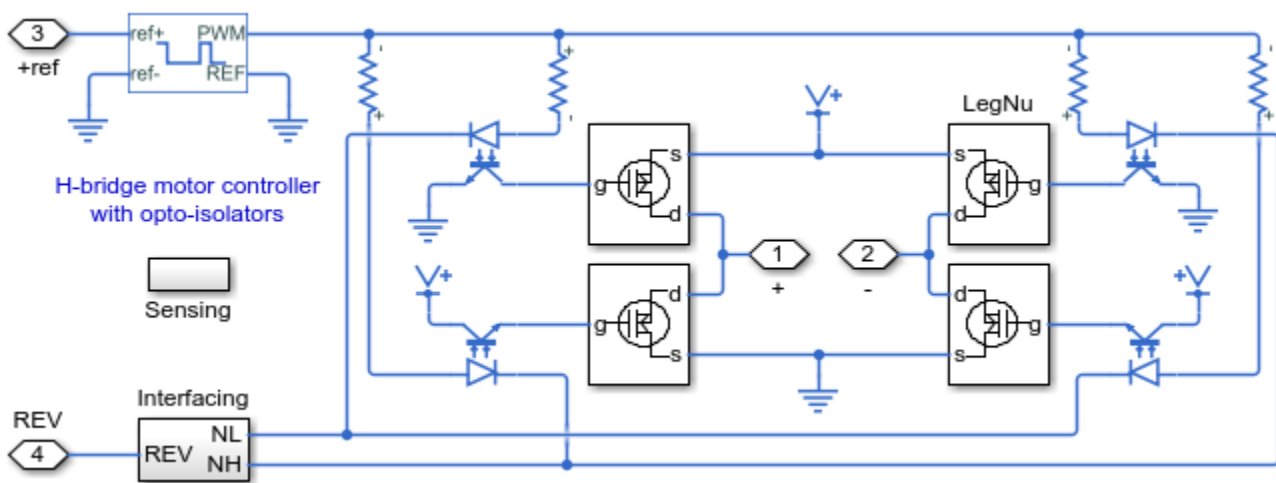
MOSFET Subsystem

The circuit below is one of the four legs of the H-bridge. The MOSFET semiconductor and other components control the flow of current through the leg of the H-bridge.
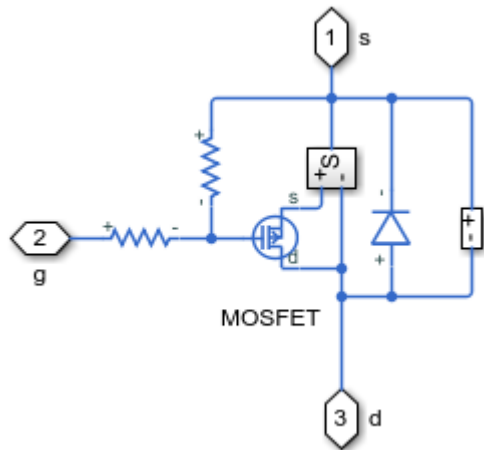
Simulation Results from Simscape Logging - Detailed Model

The plot below shows a single switching event during a simulation using the detailed model of the H-bridge. The spike in MOSFET power is due to the fact that for a brief period of time, both MOSFETs on the same side of the motor are conducting, creating a short circuit.
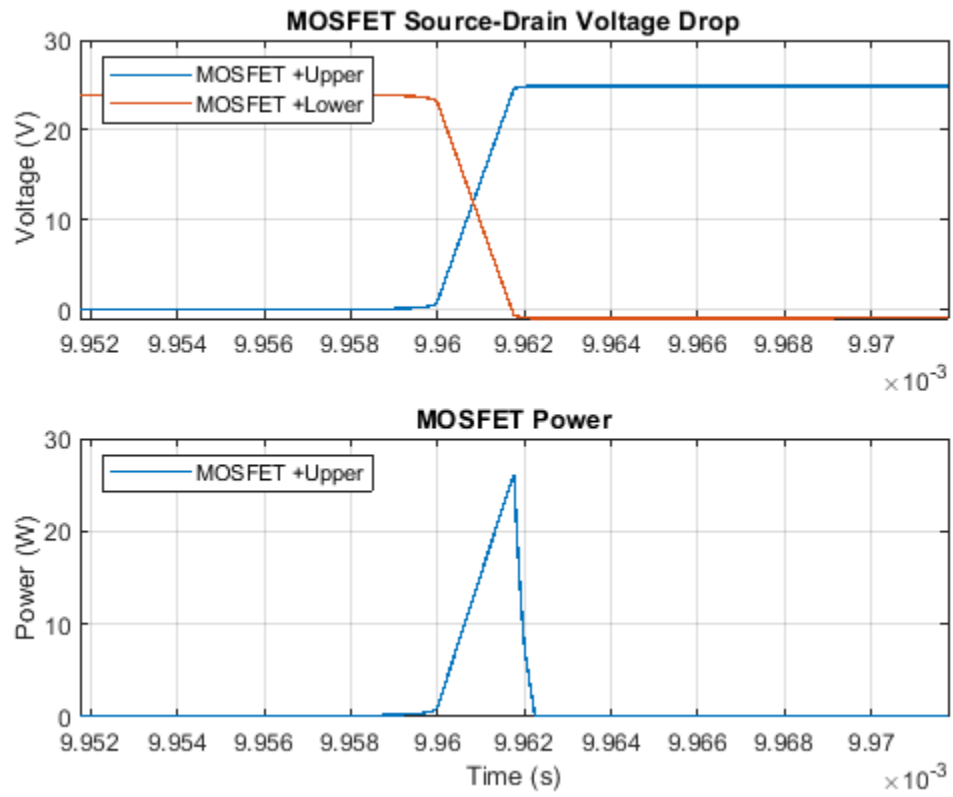
Brushless DC Motor

This example shows how a system-level model of a brushless DC motor (i.e. a servomotor) can be constructed and parameterized based on datasheet information. The motor and driver are modeled as a single masked subsystem. If viewing the model in Simulink®, select the Motor and driver block, and type Ctrl+U to look under the mask and see the model structure.

This model of a brushless DC motor uses a standard configuration. An inner feedback loop controls current and an outer feedback loop controls motor speed. Speed demand is set by the voltage presented at the Vref pin, and motor direction by the voltage presented at the Vdir pin. If the voltage at the Vbrk pin goes high, then Vref is overridden and speed demand set to zero to implement a braking action.

In this model, the speed demand is set to 2V which corresponds to 40,000rpm. After one second, the Vdir pin is set high, and the motor reverses to -40,000rpm. At 2 seconds, the braking pin Vbrk is set high, and the motor slows to zero rpm. The efficiency of the brushless DC motor is calculated as the ratio of mechanical power out to electrical power in. Hence it can be transiently outside of the 0 to 100 percent range due to rotor inertia.

The manufacturer datasheet for the brushless dc motor gives the stall torque as 0.44mNm, the maximum permissible speed as 100,000rpm, mechanical time constant as 5ms, rotor inertia as 0.005gcm^2, efficiency as 41 percent at 0.23mNm and 40,000rpm, no-load current as 22mA and nominal voltage as 12V. The time constant is based on using the manufacturer motor driver which uses block commutation. The motor driver can be configured so that when the speed reference voltage is as at its maximum of +5V, the commanded speed is 100,000rpm.

The Servomotor block in the Motor and driver subsystem is used to model the inner current feedback loop, plus balance mechanical and electrical powers. For system design, it is not usually necessary to model the current switching controlled by the motor driver, whereas ensuring the correct torque-speed characteristics and current drawn from the DC supply is. The vector of maximum torque values is in practice determined by the maximum driver current. Motor drivers normally have a maximum current setting which should be matched to the maximum rated motor torque, or the maximum torque that is to be applied to the load if the motor is over-specified. Here the vector of maximum torque values is set to be the motor stall torque up to and beyond the maximum speed of 100,000rpm. The assumption is that system in which the motor and driver are used will take care of ensuring that the motor does not overheat by operating too long at high torque and speed combinations.

Motor electrical losses are assumed to consist of two terms. The first is a fixed loss that is independent of load, and this is calculated as Vcc*I0 where Vcc is the nominal supply voltage and I0 is the no-load DC current drawn from the driver power supply. Note that if block commutation is used, as for the driver this example is based on, I0 will be twice the current in an energized phase winding. The second loss term is proportional to the square of instantaneous motor winding current. This can be approximated as a term that is proportional to the square of the average torque. The two loss terms are implemented by the Servomotor block.

There are three Motor and driver mask parameters which have to be tuned to match datasheet values. These are the proportional and integral gains for the speed feedback controller, and the time constant for the inner-loop current controller. Here, the datasheet gives the no-load time constant as 5ms. A typical rule of thumb is that an inner control loop should be at least ten times faster than the outer loop. This means a time constant of 0.5ms for the current controller. With this value set, the proportional term is then increased until the speed time constant is approximately 5ms. The integral gain should then be set when performing a speed step under load, and increased until steady-state

error is removed in the order of 5ms. Some fine tuning of the two gains is then needed to recover the 5ms rise time under no load.

Model

Brushless DC Motor
1. Plot speed of motor (see code)
2. Explore simulation results using sscexplore
3. Learn more about this example
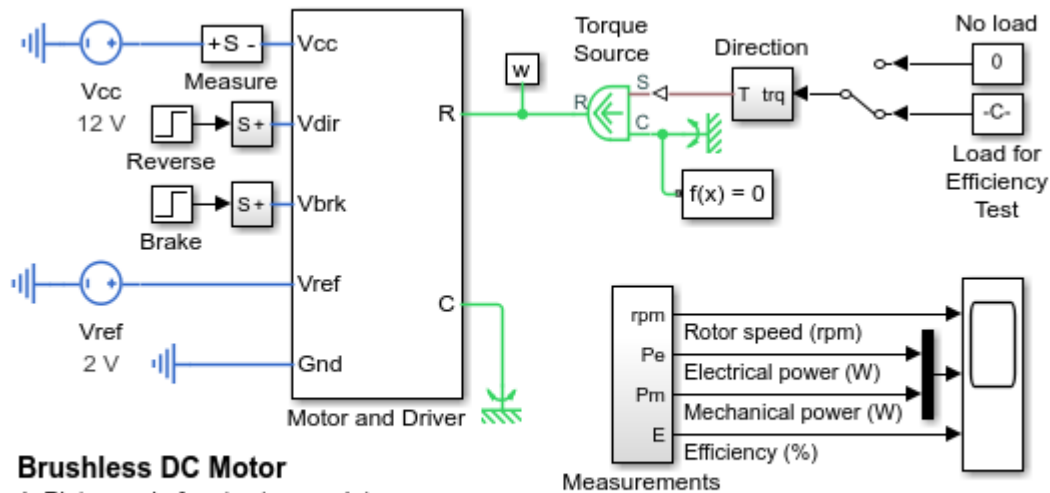
Motor and Driver Subsystem
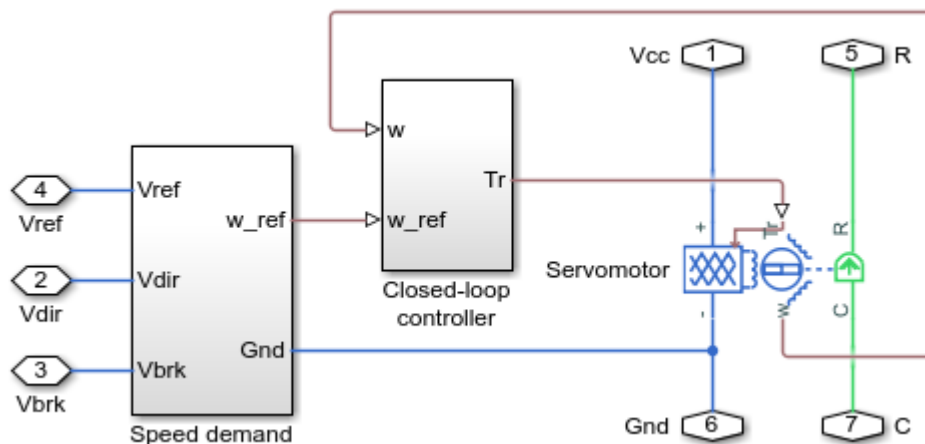
Simulation Results from Simscape Logging

The plot below shows the speed of the brushless DC motor under varying conditions. The load torque is a constant value always opposed to the rotation of the shaft. Commands to reverse direction and brake are applied.
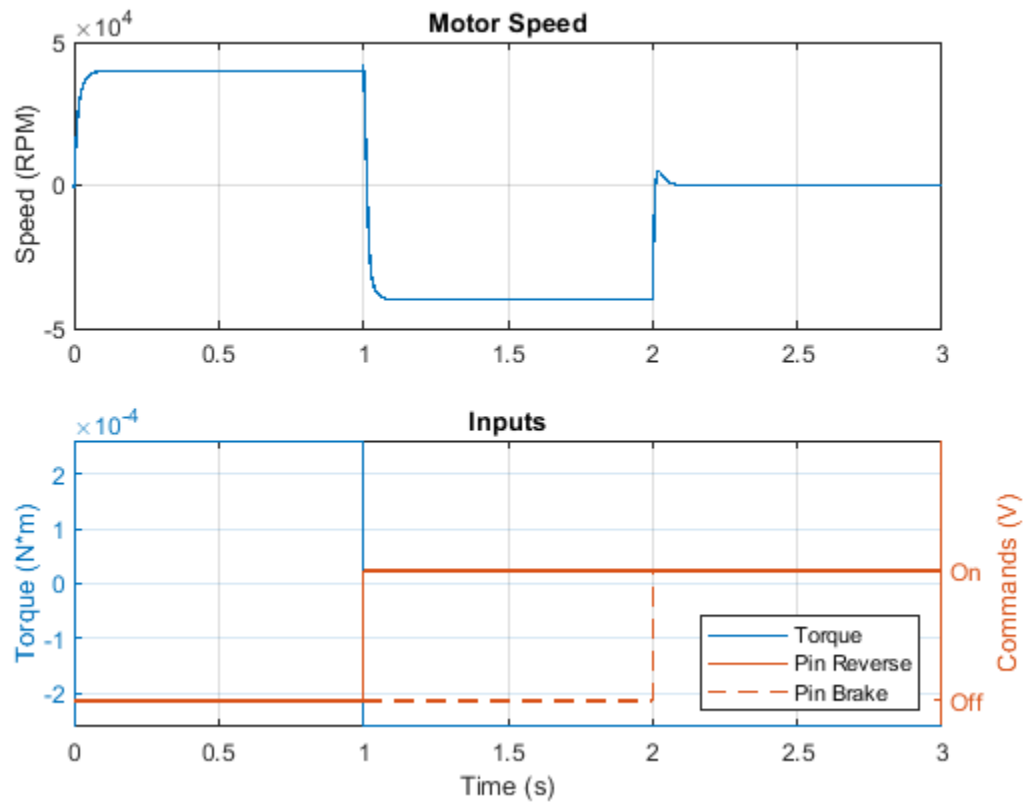
Import IPMSM Flux Linkage Data from ANSYS Maxwell

This example shows how you can import a motor design from ANSYS® Maxwell® into a Simscape™ simulation.

Flux Linkage Data

The data file defining the motor flux linkage is ee_ece_data.txt, and is reproduced with permission of ANSYS, Inc. To view the data, open the model from MATLAB® and click on the "From ANSYS Maxwell" link.

To read this data into MATLAB, the text file requires some minor editing to make it executable from the MATLAB command line. The resultant script is ee_ece_data.m and can be viewed by clicking on the "MATLAB script" link from the model. To see a summary of the edits, click on the "Compare" link.

Model
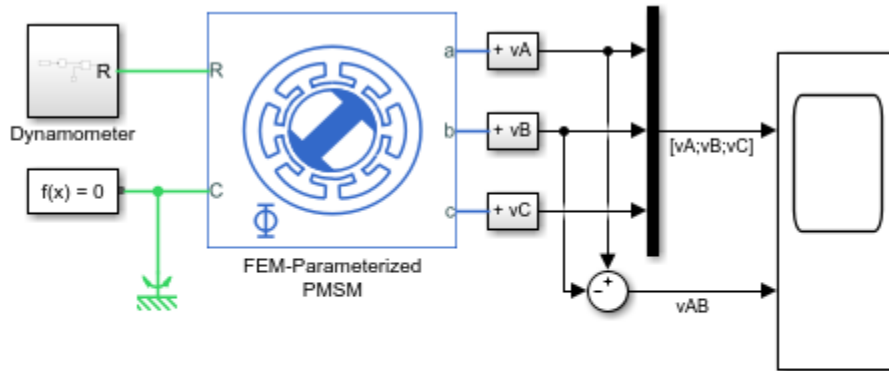
Import IPMSM Flux Linkage Data from ANSYS Maxwell

1. Motor data: From ANSYS Maxwell, MATLAB script, Compare
2. Plot voltages in motor windings (see code)
3. Explore simulation results using sscexplore
4. Learn more about this example

Simulation Results from Simscape Logging

The plot below shows the simulated open-circuit voltages. Notice the nonlinear back EMF profile that would not be captured if using a simplified PMSM model with fixed back EMF constant.
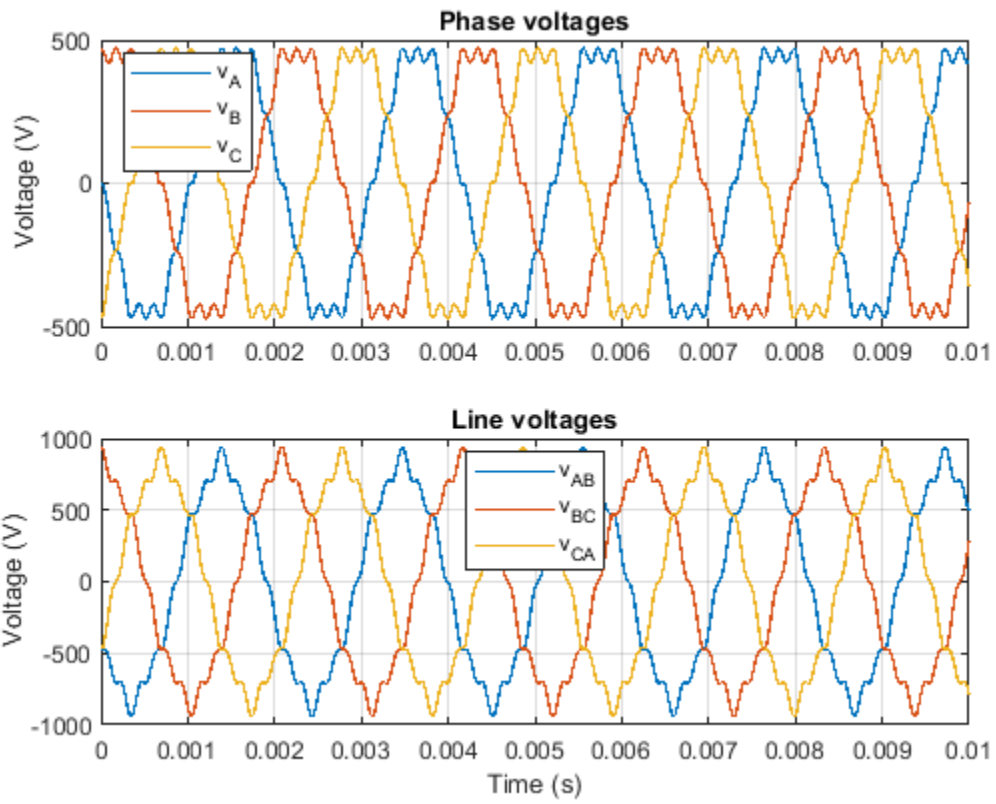
Switched Reluctance Motor Parameterized with FEM Data

This example shows how to build a model of a Switched Reluctance Motor (SRM). A custom Simscape™ composite component is used to construct the three stator pole pairs using the Simscape Electrical™ FEM-Parameterized Rotary Actuator as a base component. The states of the input pins (reverse, on/off) control the torque applied to the motor shaft.

Model
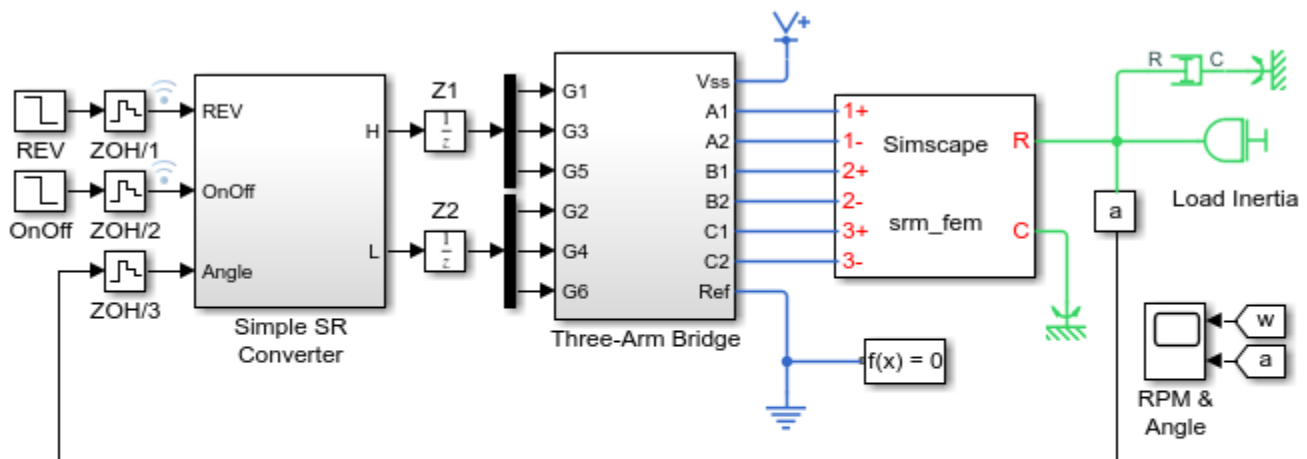
Switched Reluctance Motor Parameterized with FEM Data

1. Plot speed of motor (see code)
2. Plot torque applied by each winding (see code)
3. Plot motor data used in parameterization (see code)
4. Open custom motor library created using Simscape language
5. Explore simulation results using sscexplore
6. Learn more about this example

Three-Arm Bridge Subsystem
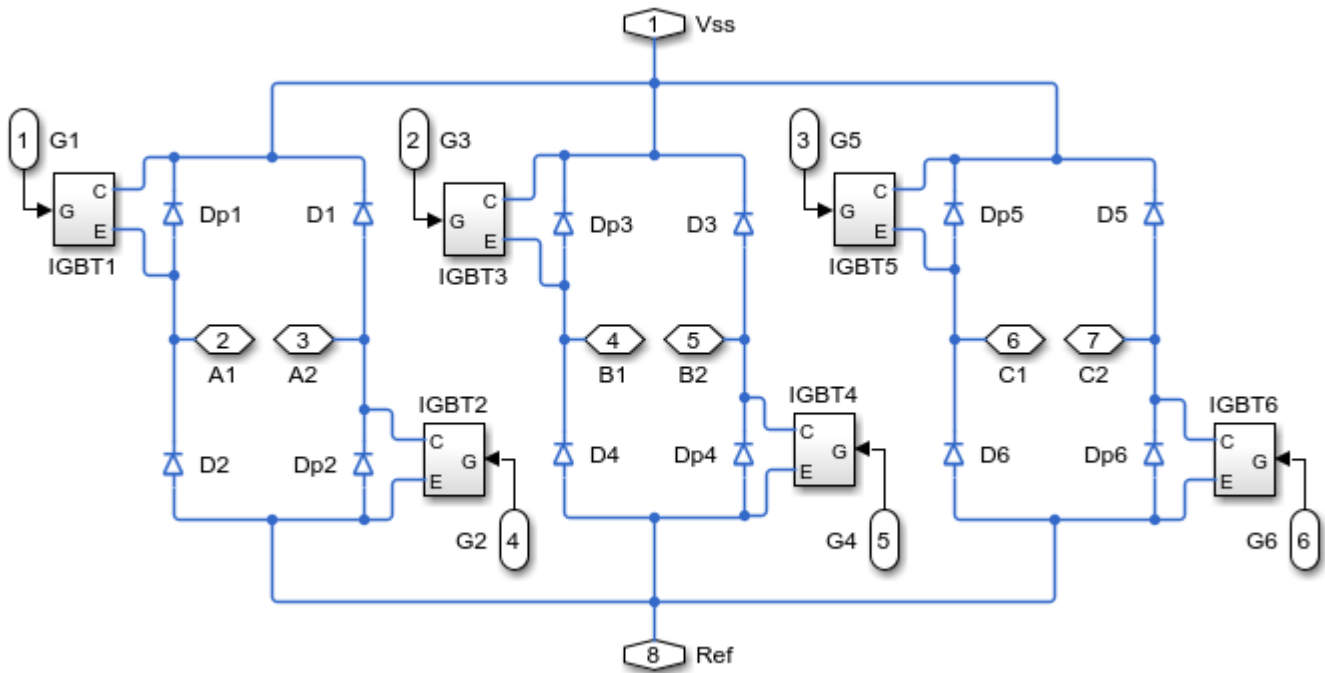
Simulation Results from Simscape Logging

The plot below shows the behavior of the motor as control signals at the input pins of the converter are varied. The sign of the torque the motor applies to its shaft is controlled by the reverse signal. If the On/Off signal is set to 0, the motor applies no torque to the motor shaft. In this test, after the On/Off signal is set to zero, the motor shaft slows down due to viscous friction applied to the shaft.
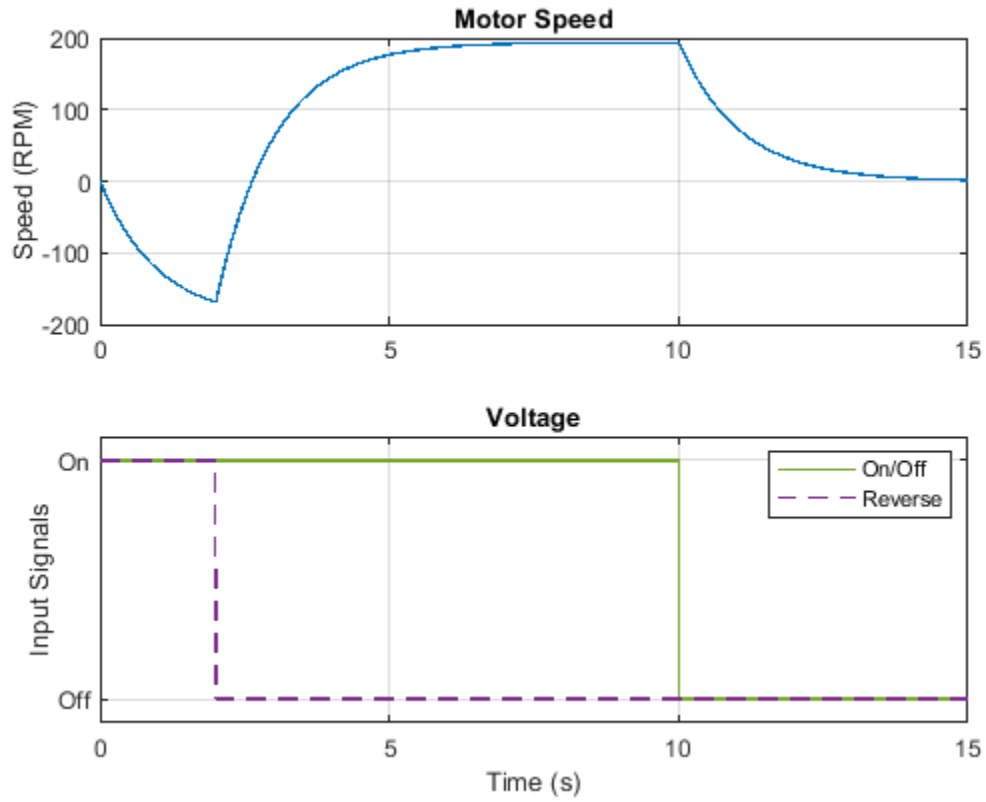
The plot below shows the behavior of the motor over a very short period of time so that the effect of the torque applied by each winding can be observed. A very small ripple in motor speed can be observed as the individual windings are energized and de-energized.
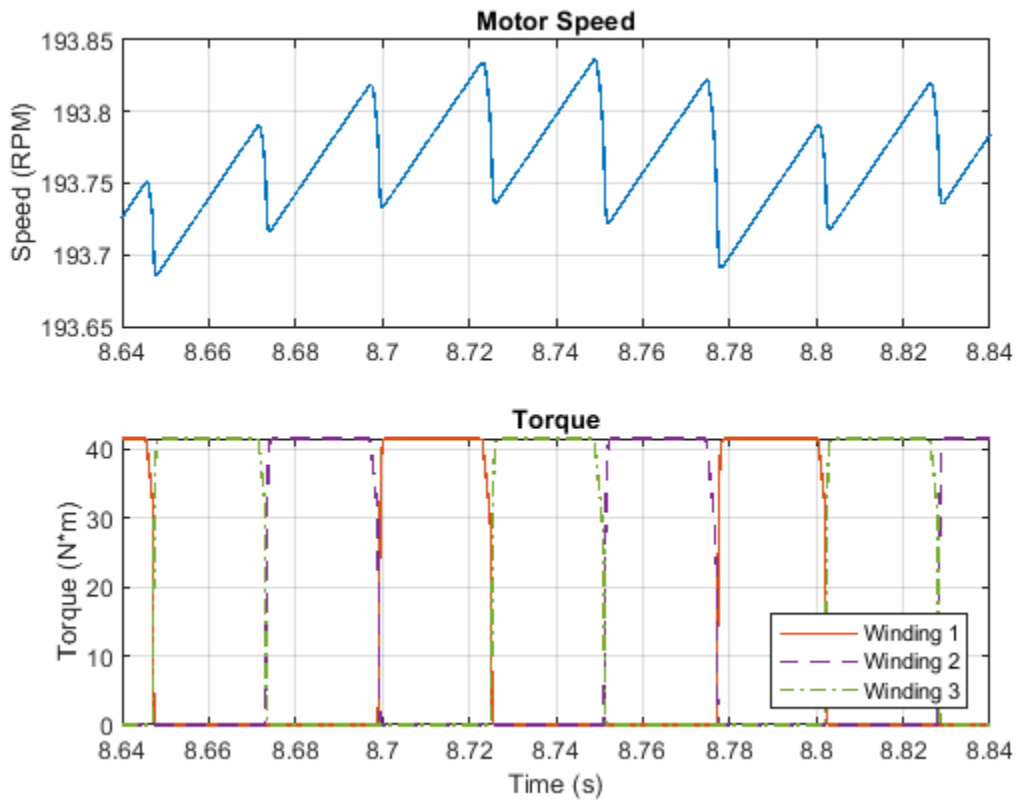
The plots below show the data used to parameterize the motor. Lookup tables for both flux linkage and motor torque dependent upon angle and current are plotted.

Flux Linkage vs Angle and Current

Motor Torque vs Angle and Current
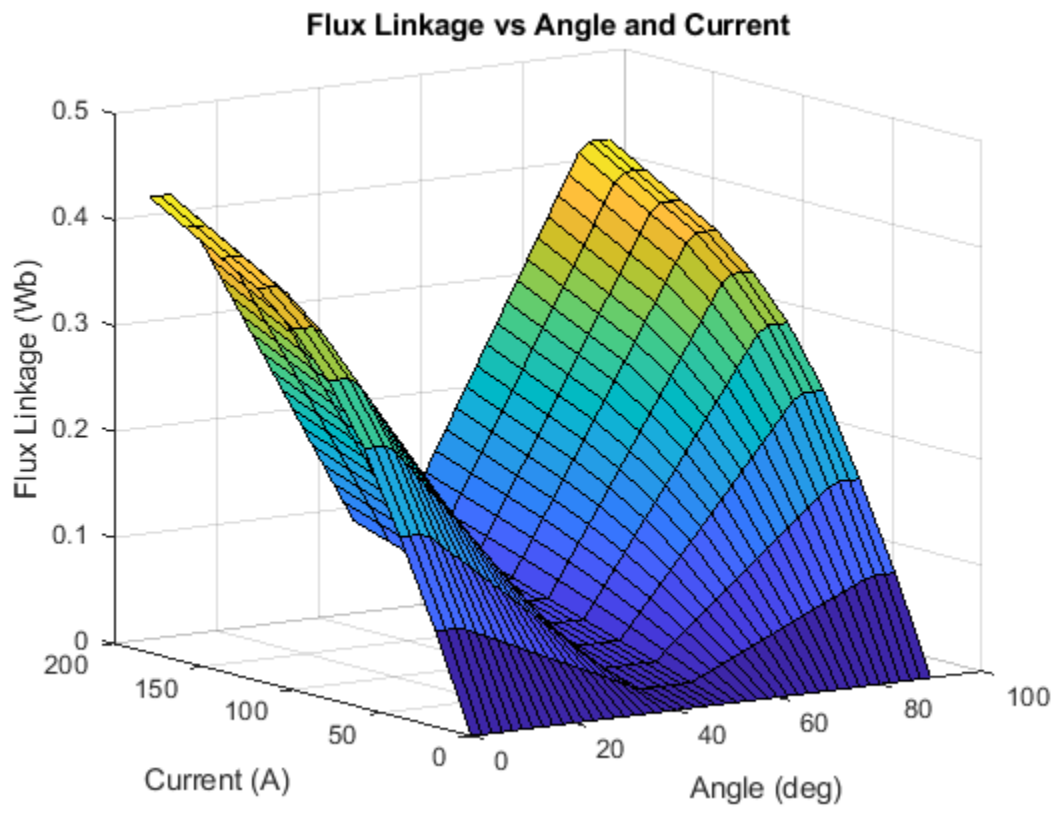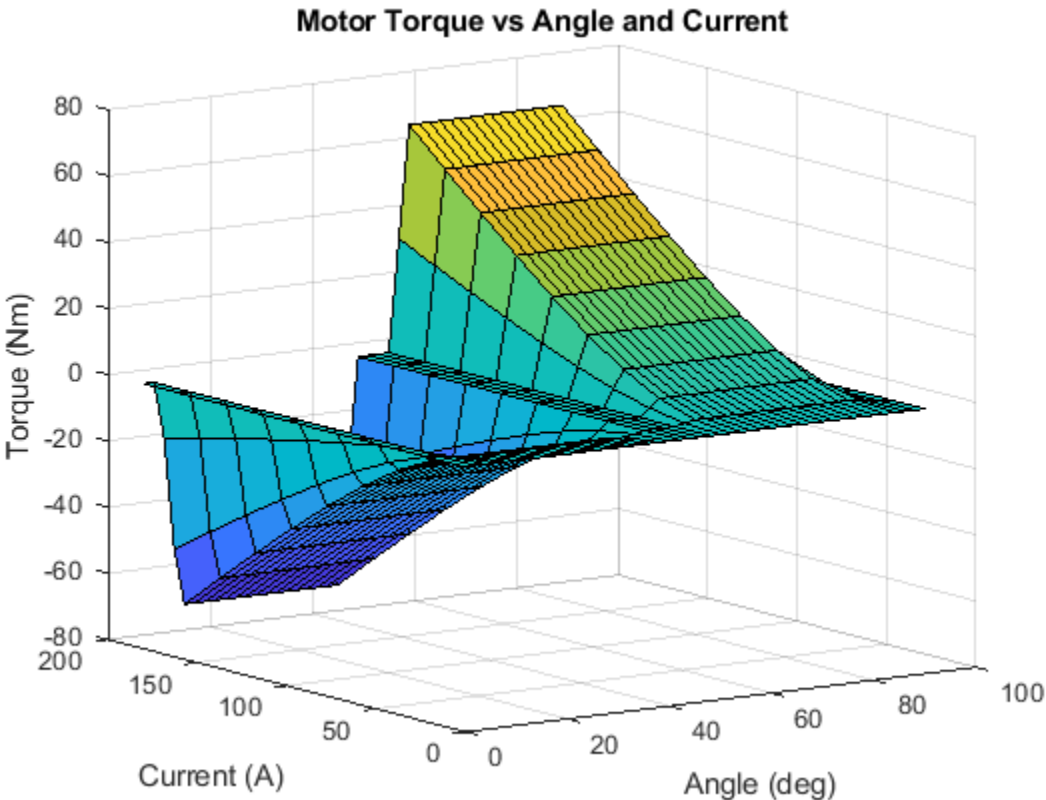
HEV PMSM Drive Test Harness

This example shows a test harness for a Permanent Magnet Synchronous Motor (PMSM) drive sized for use in a typical hybrid vehicle. The test harness can be used to determine overall drive losses when operating at a given speed and torque. Tabulated losses information from this test harness can then be used by the Simscape™ Electrical™ Motor & Drive (System Level) block for rapid simulation of complete drive cycles whilst still accurately predicting overall system efficiency.

Model
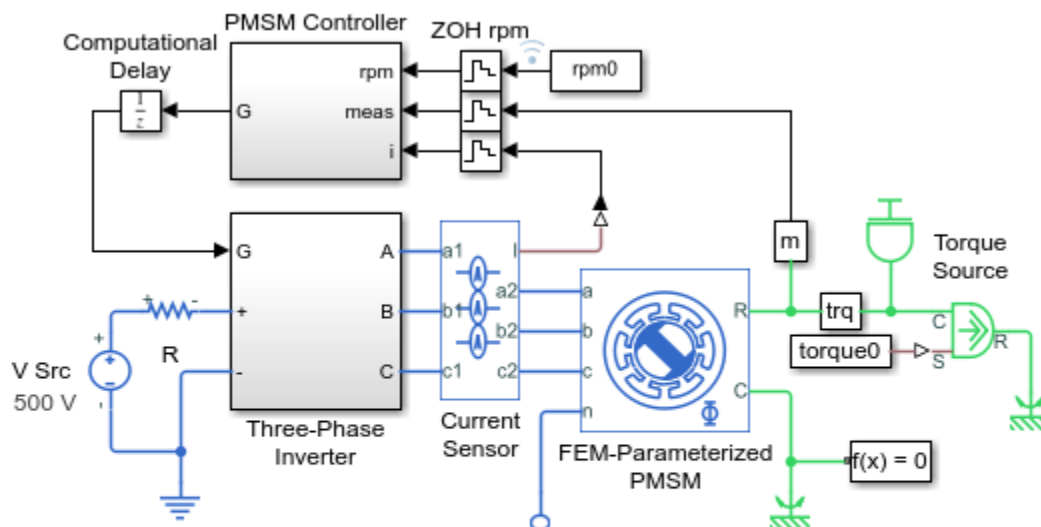
HEV PMSM Drive Test Harness
1. Plot currents in motor windings (see code)
2. Calculate losses in circuit (see code)
3. Calculate parameters for PMSM flux linkage (see code)
4. Explore simulation results using sscexplore
5. Learn more about this example

Three-Phase Inverter Subsystem
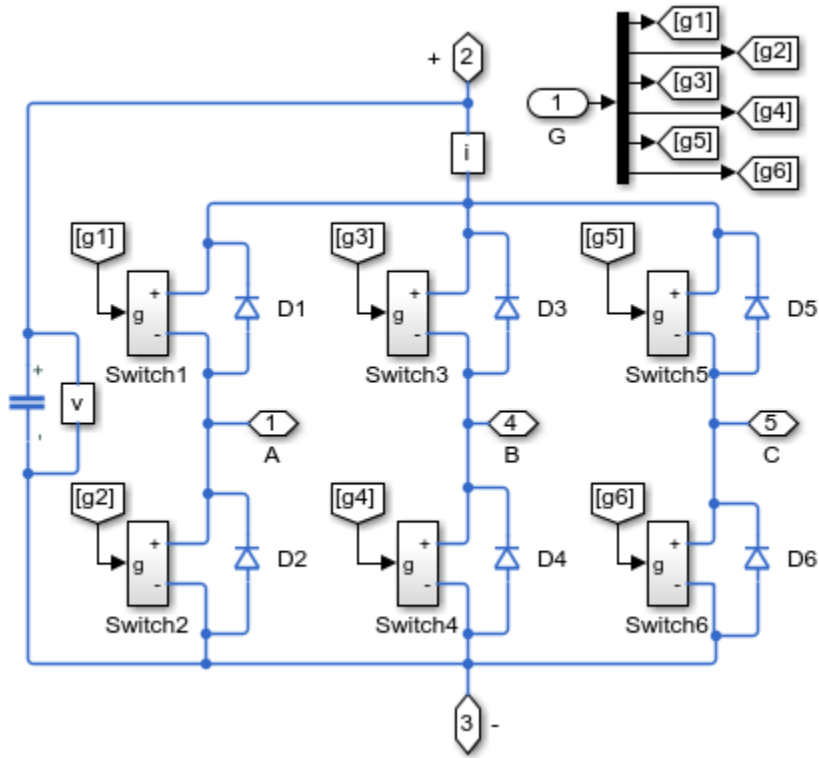
Simulation Results from Scopes and Simscape Logging

The first plot below shows the result from Spectrum Analyzer. Second plot shows measured and commanded rotor speed. The winding currents are also plotted, which explains the high frequency ripple in the motor speed.
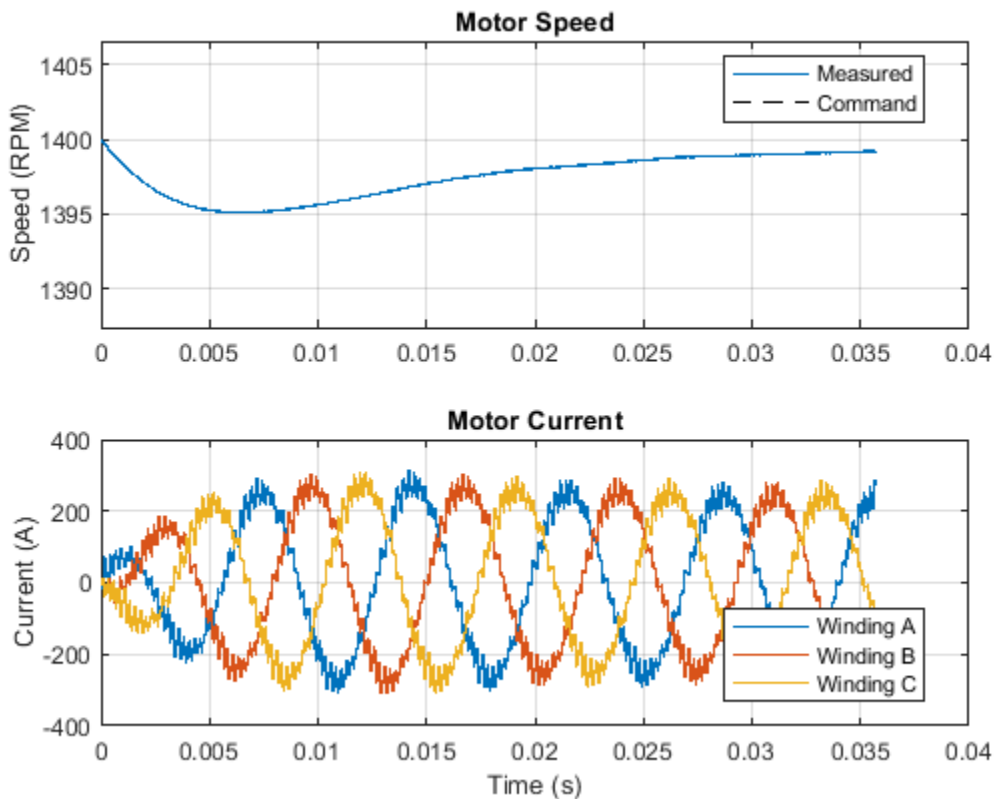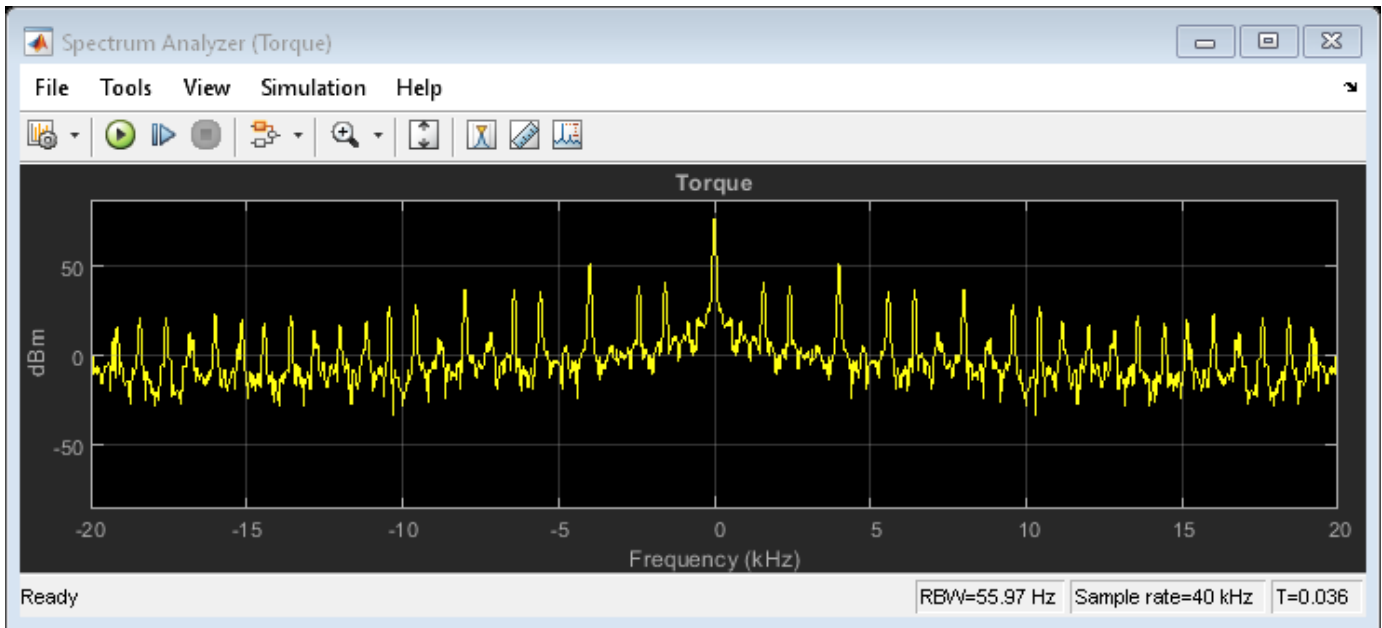
The table below shows the power dissipated by individual components in the ee_pmsm_drive model. These totals were calculated from simulation results using logged Simscape variables and the losses calculation utility ee_getPowerLossSummary. The total output power in the load, as well as the losses are shown.

```
Running the PMSM Drive Test Harness model to generate simulation data
Efficiency = 86.3937% when speed = 1400rpm and torque = 200Nm
Losses in watts by component are as follows:
                                  LoggingNode                            Power

    _____    _____

    {'ee_motor_pmsm_drive.FEM_Parameterized_PMSM'                   }    3541.4
    {'ee_motor_pmsm_drive.Three_Phase_Inverter.Switch1.IGBT.transistor'}  162.3
    {'ee_motor_pmsm_drive.Three_Phase_Inverter.Switch4.IGBT.transistor'}  152.4
    {'ee_motor_pmsm_drive.Three_Phase_Inverter.Switch5.IGBT.transistor'}  149.9
    {'ee_motor_pmsm_drive.Three_Phase_Inverter.Switch2.IGBT.transistor'}  147.8
    {'ee_motor_pmsm_drive.Three_Phase_Inverter.Switch6.IGBT.transistor'}  147.8
    {'ee_motor_pmsm_drive.Three_Phase_Inverter.Switch3.IGBT.transistor'}  147.6
    {'ee_motor_pmsm_drive.Three_Phase_Inverter.D3'                  }      20.6
    {'ee_motor_pmsm_drive.Three_Phase_Inverter.D6'                  }      20.3
    {'ee_motor_pmsm_drive.Three_Phase_Inverter.D4'                  }      20.1
    {'ee_motor_pmsm_drive.Three_Phase_Inverter.D5'                  }      20.1
    {'ee_motor_pmsm_drive.Three_Phase_Inverter.D1'                  }        20
    {'ee_motor_pmsm_drive.Three_Phase_Inverter.D2'                  }      19.5
    {'ee_motor_pmsm_drive.Three_Phase_Inverter.C0'                  }      10.7
    {'ee_motor_pmsm_drive.Three_Phase_Inverter.Switch1.Gate_Driver' }       0.9
    {'ee_motor_pmsm_drive.Three_Phase_Inverter.Switch4.Gate_Driver' }       0.8
    {'ee_motor_pmsm_drive.Three_Phase_Inverter.Switch6.Gate_Driver' }       0.7
    {'ee_motor_pmsm_drive.Three_Phase_Inverter.Switch5.Gate_Driver' }       0.7
    {'ee_motor_pmsm_drive.Three_Phase_Inverter.Switch2.Gate_Driver' }       0.6
    {'ee_motor_pmsm_drive.Three_Phase_Inverter.Switch3.Gate_Driver' }       0.5
```
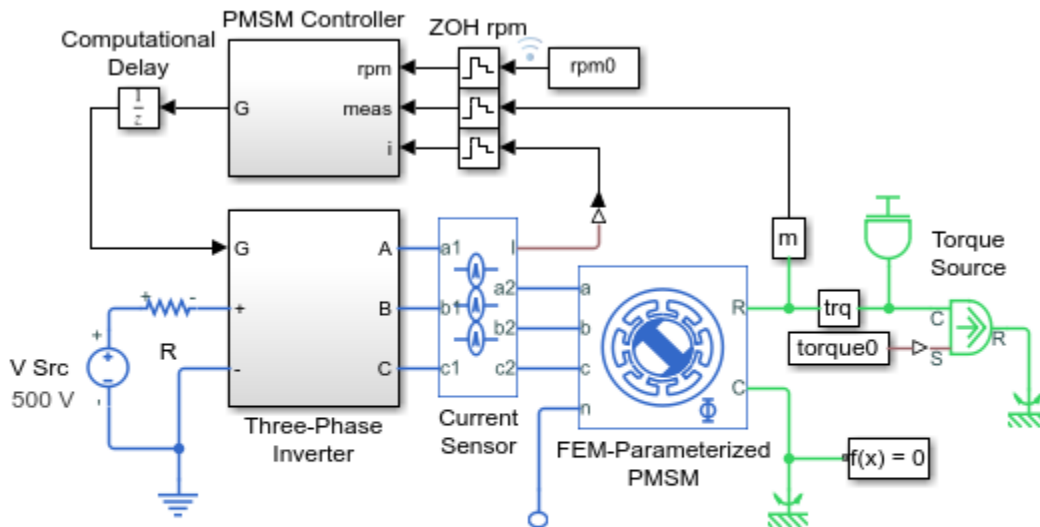

HEV PMSM Drive Test Harness
1. Plot currents in motor windings (see code)
2. Calculate losses in circuit (see code)
3. Calculate parameters for PMSM flux linkage (see code)
4. Explore simulation results using sscexplore
5. Learn more about this example
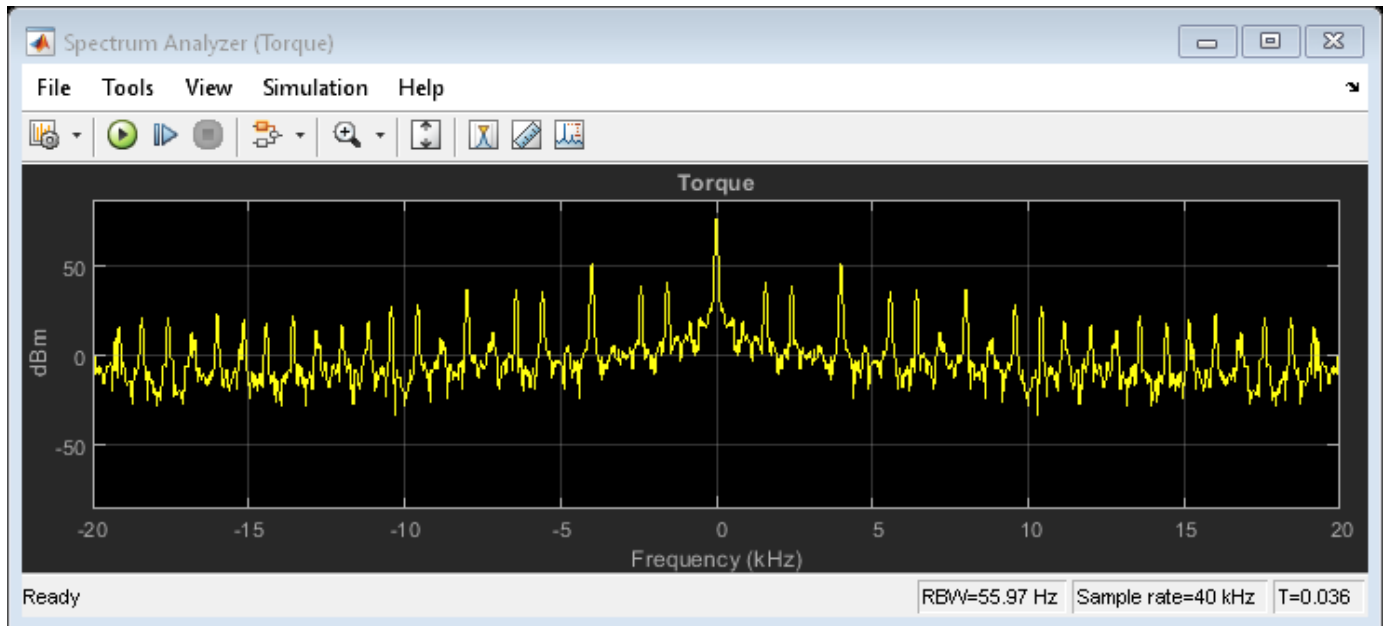
PMSM Iron Losses

This example shows a test harness for a Permanent Magnet Synchronous Motor (PMSM) that validates that the iron losses are as expected. The open-circuit test validates the main flux path losses which are defined to be 500W due to hysteresis and 1500W due to eddy current losses. The short-circuit test validates the cross-tooth flux path losses which are defined to be 200W due to hysteresis and 600W due to eddy current losses.

Model
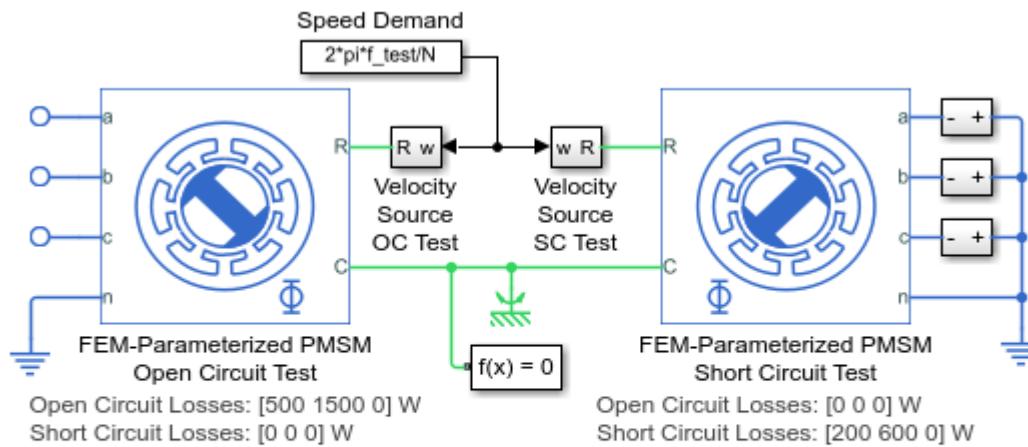
PMSM Iron Losses
1. Plot iron losses for both tests (see code)
2. Calculate parameters for PMSM flux linkage (see code)
3. Explore simulation results using sscexplore
4. Learn more about this example

Simulation Results from Simscape Logging

The plot below validates that the iron loss parameters entered into the FEM-Parameterized PMSM block provided the expected behavior in simulation. The sum of the parameter values relevant for the respective test match the transient simulation results.
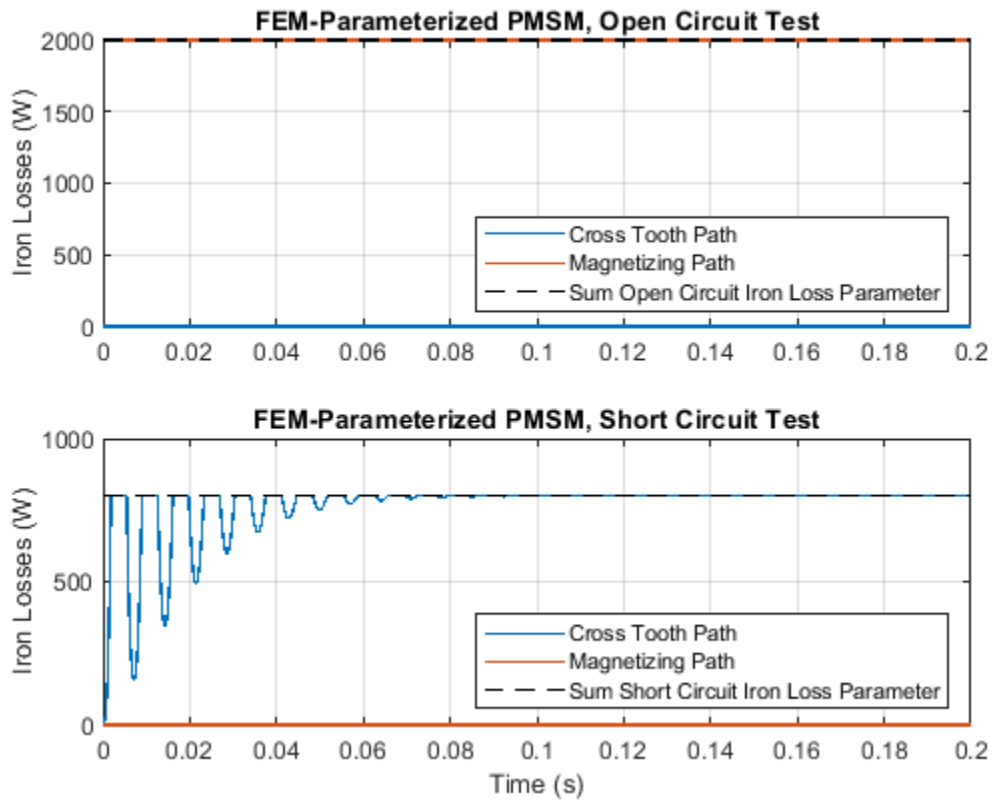
PMSM with Thermal Model

This example shows a nonlinear model of a PMSM with thermal dependency. The PMSM behavior is defined by tabulated nonlinear flux linkage data. Motor losses are turned into heat in the stator winding and rotor thermal ports.

Model

PMSM with Thermal Model
1. Plot speed of motor and winding currents (see code)
2. Plot temperature of windings and rotor (see code)
3. Set parameters for model (see code)
4. Explore simulation results using sscexplore
5. Learn more about this example

Thermal Model Subsystem
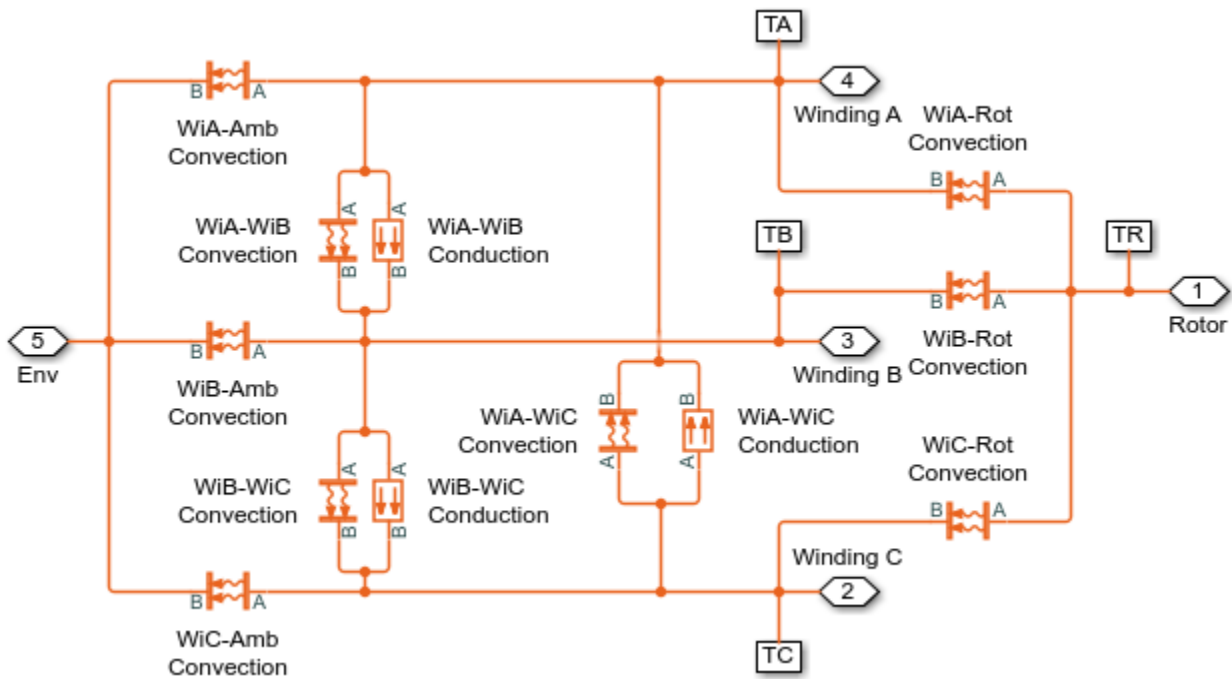
Simulation Results from Simscape Logging

The plot below shows motor speed and winding currents as the control system attempts to track a reference signal. A load torque is applied to the shaft, and the effect on the system is shown in the winding currents.
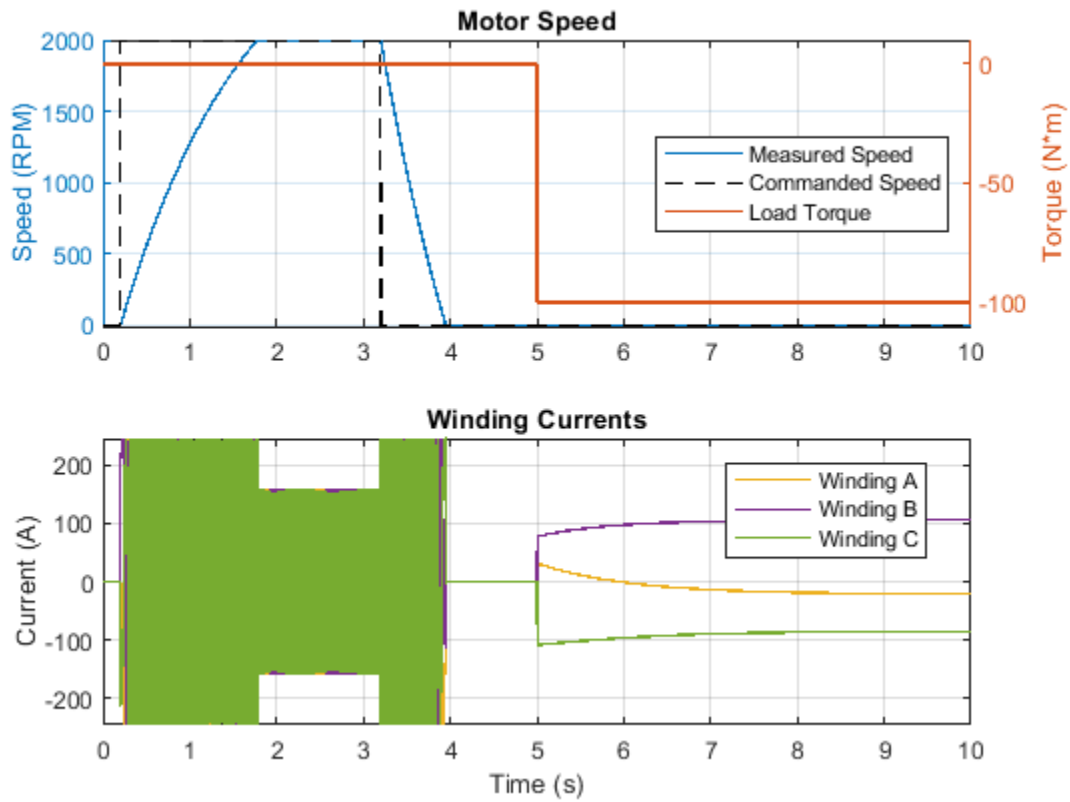
The plot below shows motor speed, torque, and temperature of motor components. The thermal model of the motor models heat transfer between the three windings and the rotor.
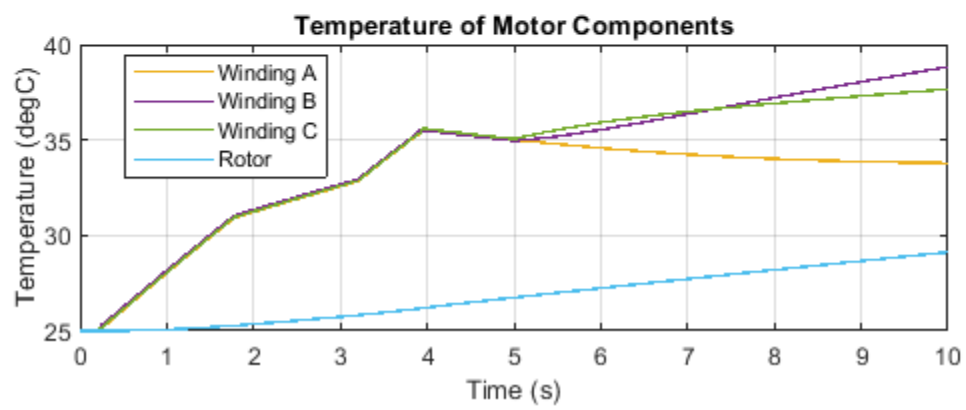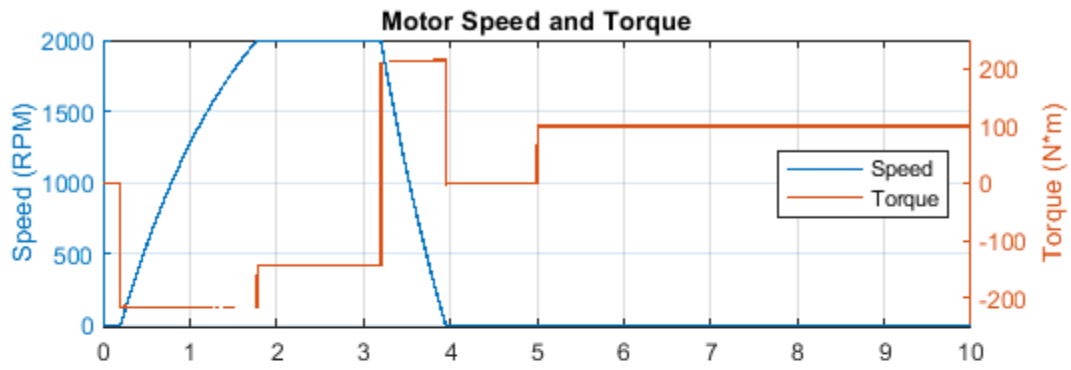
Stepper Motor with Control

This model shows how to use the Stepper Motor Driver and Stepper Motor blocks together to implement a controlled permanent magnet stepper motor. The model provides two controller options: one to control position and one to control speed. To change the controller type, right-click on the Controller block, select Variant->Override using-> and select Position or Speed.

The stepper has a full step size of 1.8 degrees. In position control model, the input Ref is the desired number of steps. In speed control mode, the input Ref is the desired number of steps per second.

This model is a system-level model suitable for studying the dynamics of the stepper and whether step angle will slip when driving a given load. It can also be used to tune the stepper controller to improve stepping performance. Often the controller is either partly or fully implemented on an off-the-shelf stepper controller module.

The alternative of implementing the algorithm on a microprocessor (such as a PIC) gives greater flexibility, and the microprocessor may also be used to control other parts of the overall system. In this case parts of the Stepper Motor Driver block may also be implemented in on the microprocessor, leaving only the power amplifier stage in analog electronics.

Model

Stepper Motor with Control
1. Plot angular speed of motor shaft (see code)
2. Plot voltages of stepper motor driver pins (see code)
3. Configure stepper controller: Speed, Position
4. Explore simulation results using sscexplore
5. Learn more about this example

Simulation Results from Simscape Logging

Position Control Test

The motor shaft angle as compared to the demand signal. A position control algorithm accepts a position command as a number of steps and converts it to a pulse train that controls the stepper motor driver. The spikes on the angular velocity plot occur as the shaft settles into its commanded position.

Speed Control Test

The plots below show the motor shaft angle as compared to the demand signal. A speed control algorithm accepts a speed command as a number of steps per second and converts it to a pulse train that controls the stepper motor driver. The spikes on the angular velocity plot occur as the shaft settles into the current step.

The plot below shows how the states of the pins on the stepper driver affect the motion of the stepper motor. The driver initiates a step each time the ENA signal rises above the Enable threshold voltage.
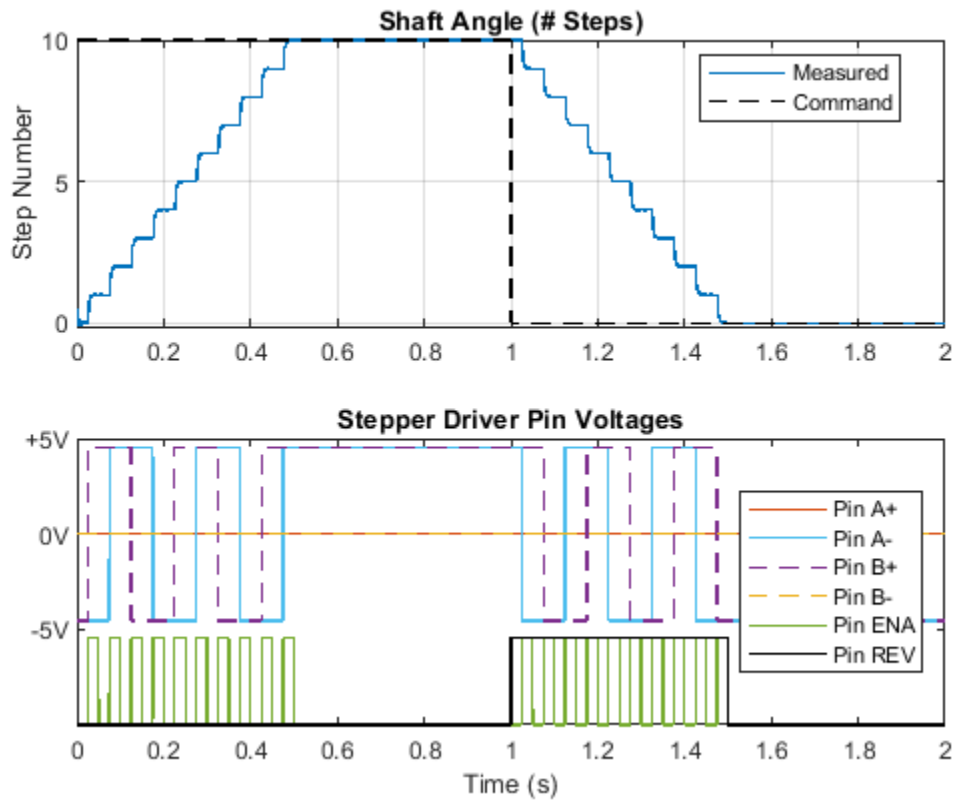
Stepper Motor Averaged Mode

This model shows the Stepper Motor simulating in Stepping and Averaged simulation modes. The purpose of Averaged mode is faster simulation for any loads that do not cause slip. To avoid incorrect interpretation of results, the stepper motor has an approximate detection of slip which can be set to generate a warning or an error.

The nominal inertial load for the stepper motor is 1e-3 kg*m^2. If you increase load inertia (workspace parameter J) to 0.05 kg*m^2 or load damping (workspace parameter L) to 2 N*m/s, both simulation modes will indicate slip. Once slip occurs, results will diverge as the Averaged mode assumes the stepper speed controller detects slip and adjusts the demand to maintain synchronous operation.

Model

Stepper Motor Averaged Mode
1. Plot angle and speed of shaft (see code)
2. Configure simulation mode: Average, Stepped (see code)
3. Set inertial load: Overload, Nominal
4. Compare simulation modes (see code)
5. Explore simulation results using sscexplore
6. Learn more about this example

Simulation Results from Simscape Logging

The plot below shows the shaft angle and shaft speed of the stepper motor in full stepping mode. Oscillations in the shaft speed occur as the shaft settles into the current step. In averaged mode, the angle and speed are smooth as individual steps are not simulated.

Shaft Angle

Shaft Speed

The plot below compares the shaft angle and shaft speed of the stepper motor in stepped and averaged mode. Oscillations in the shaft speed are seen in stepped mode as the individual steps are simulated.

Unipolar Stepper Motor with Control

This model shows how to use the Unipolar Stepper Motor Driver and Unipolar Stepper Motor blocks together to implement a controlled permanent magnet stepper motor. The model provides two controller options: one to control position and one to control speed. To change the controller type, right-click on the Controller block, select Variant->Override using-> and select Position or Speed.

The stepper has a full step size of 1.8 degrees. In position control mode, the input Ref is the desired number of steps. In speed control mode, the input Ref is the desired number of steps per second.

This model is a system-level model suitable for studying the dynamics of the stepper and whether step angle will slip when driving a given load. It can also be used to tune the stepper controller to improve stepping performance. Often the controller is either partly or fully implemented on an off-the-shelf stepper controller module.

The alternative of implementing the algorithm on a microprocessor (such as a PIC) gives greater flexibility, and the microprocessor may also be used to control other parts of the overall system. In this case parts of the Stepper Motor Driver block may also be implemented in on the microprocessor, leaving only the power amplifier stage in analog electronics.

Model

Unipolar Stepper Motor with Control

1. Plot angular speed of motor shaft (see code)
2. Plot voltages of stepper motor driver pins (see code)
3. Configure stepper controller: Speed, Position
4. Explore simulation results using sscexplore
5. Learn more about this example

Simulation Results from Simscape Logging

Position Control Test

The motor shaft angle as compared to the demand signal. A position control algorithm accepts a position command as a number of steps and converts it to a pulse train that controls the stepper motor driver. The spikes on the angular velocity plot occur as the shaft settles into its commanded position.

Speed Control Test

The plots below show the motor shaft angle as compared to the demand signal. A speed control algorithm accepts a speed command as a number of steps per second and converts it to a pulse train that controls the stepper motor driver. The spikes on the angular velocity plot occur as the shaft settles into the current step.

The plot below shows how the states of the pins on the unipolar stepper driver affect the motion of the unipolar stepper motor. The driver initiates a step each time the ENA signal rises above the Enable threshold voltage. The driver sets the common connections A0 and B0 to always be high, and grounds the A+, A-, B+ and B- as required.
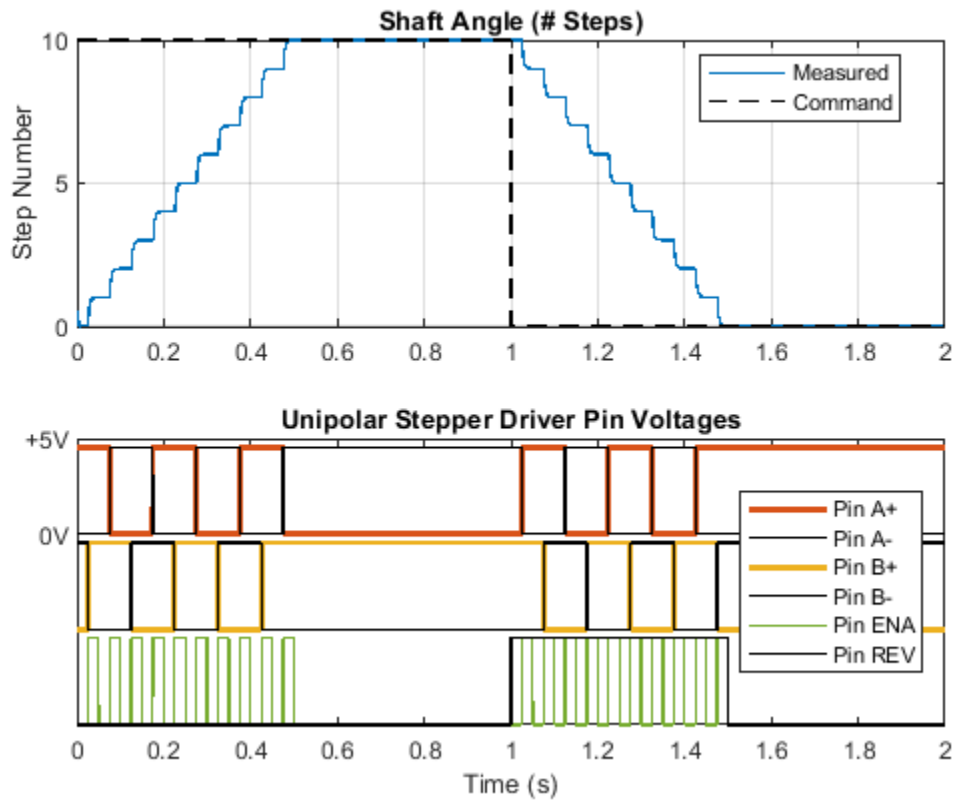
Unipolar Stepper Motor Averaged Mode

This model shows the Unipolar Stepper Motor simulating in Stepping and Averaged simulation modes. The purpose of Averaged mode is faster simulation for any loads that do not cause slip. To avoid incorrect interpretation of results, the stepper motor has an approximate detection of slip which can be set to generate a warning or an error.

Use the two callback blocks to switch between simulation modes to verify results. If you increase load inertia (workspace parameter J) to 0.05 kg*m^2 or load damping (workspace parameter L) to 2 N*m/s, both simulation modes will indicate slip. Once slip occurs, results will diverge as the Averaged mode assumes the stepper speed controller detects slip and adjusts the demand to maintain synchronous operation.

Model

Unipolar Stepper Motor Averaged Mode
1. Plot angle and speed of shaft (see code)
2. Configure simulation mode: Average, Stepped (see code)
3. Set inertial load: Overload, Nominal
4. Compare simulation modes (see code)
5. Explore simulation results using sscexplore
6. Learn more about this example

Simulation Results from Simscape Logging

The plot below shows the shaft angle and shaft speed of the stepper motor in full stepping mode. Oscillations in the shaft speed occur as the shaft settles into the current step. In averaged mode, the angle and speed are smooth as individual steps are not simulated.

The plot below compares the shaft angle and shaft speed of the stepper motor in stepped and averaged mode. Oscillations in the shaft speed are seen in stepped mode as the individual steps are simulated.
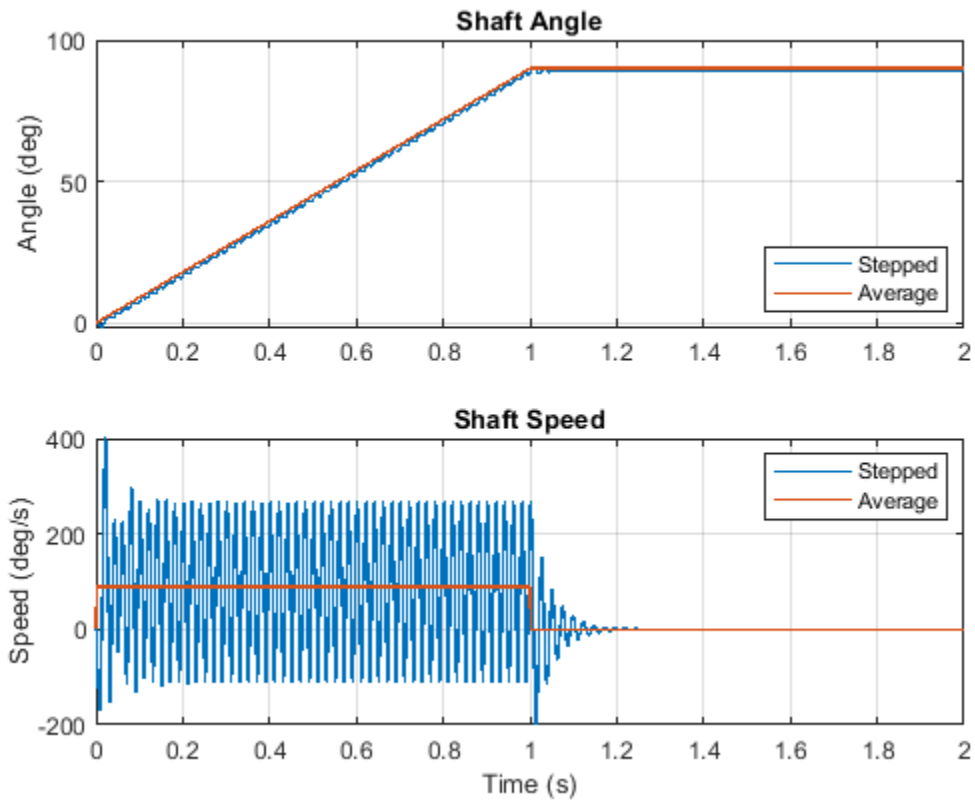
Hybrid Linear Actuator

This example shows a hybrid actuator consisting of a DC motor plus lead screw in series with a piezoelectric stack. The DC motor and lead screw combination supports large displacements (tens of millimeters), but is dynamically slow when tracking the reference demand x_ref. Conversely the piezoelectric stack only supports a maximum displacement of +-0.1mm, but has a very fast dynamic response. Combining the two actuator technologies creates a large stroke actuator with highly precise positioning.

An example application is antenna pointing control when tracking a satellite - large amplitude motion with time constants measured in a few seconds is required to move between satellites, whereas fine control is required to track a given non-geostationary satellite.

Setting the step input to 0.05mm shows system performance for fine control. The initial motion is provided by the piezoelectric stack, and this is then washed out as the DC Motor rotor angle moves to the new operating point. Setting the input to 10mm shows system performance for a larger input.

This model can be used to obtain the frequency response of the system. MATLAB® command linmod can be used to linearize the model. If you have Simulink® Control Design™, then a good way to understand the overall actuator performance is to plot a Bode plot. Open the model ee_actuator_dc_piezo. On the Apps tab, under Control Systems, click Model Linearizer. In the Model Linearizer, on the Linear Analysis tab, in the Linearize section, click Bode. Note that the initial state on the current controller integrator has been set to a small non-zero number to avoid linearizing the Abs block around zero. You can disable the piezoelectric stack to generate a Bode plot for the DC Motor by itself.

Model

Hybrid Linear Actuator
1. Plot extension of actuator (see code)
2. Linearize circuit to view frequency response (see code)
3. Step input: 0.05 mm, 10 mm
4. Piezo Stack: On, Off
5. Driver Circuit Mode: Average, PWM
6. Explore simulation results using sscexplore
7. Learn more about this example
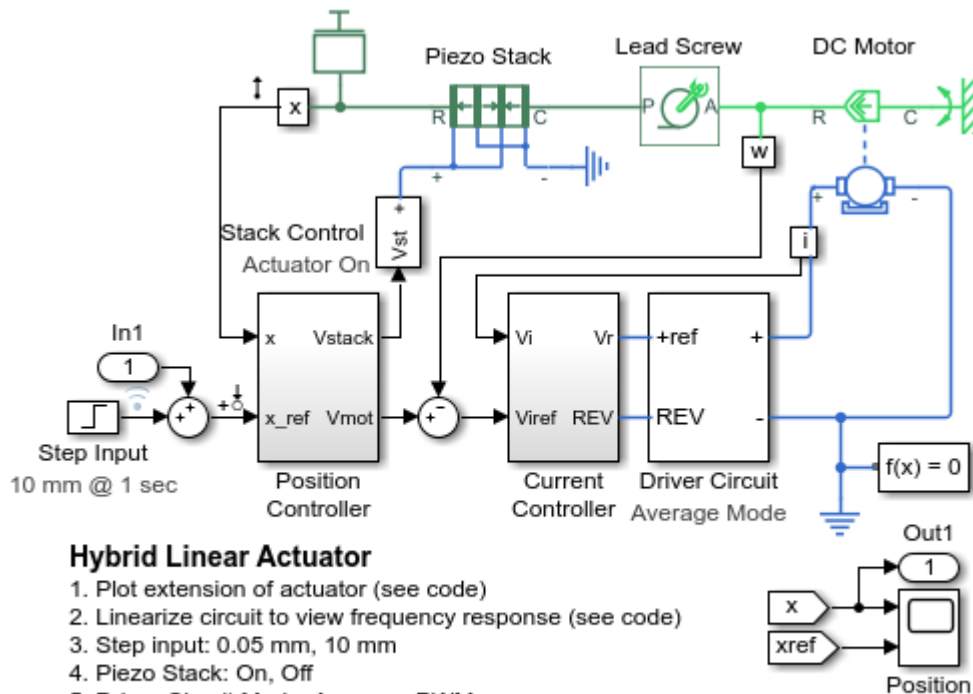
Simulation Results from Simscape Logging

The plot below shows the extension of the actuator as the control system attempts to track a reference input. The Piezo Stack can react much faster than the DC motor but has a limited range. The control system uses them together to track the reference signal.

Test with step input of 10 mm
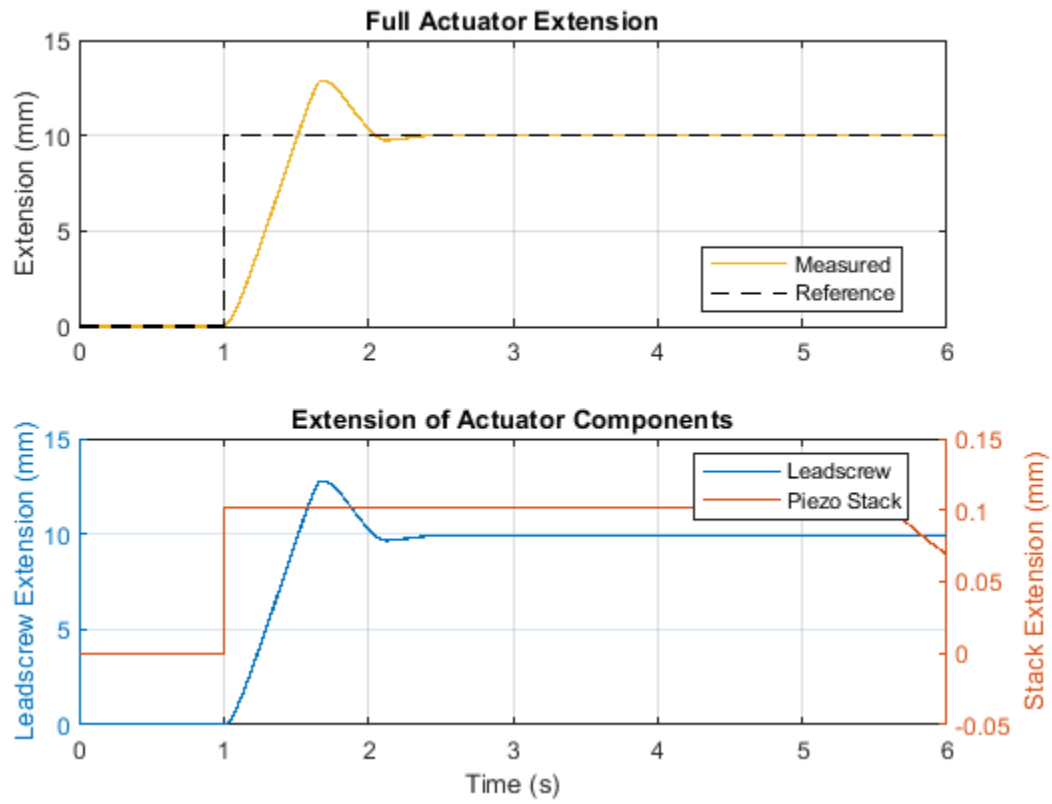
Test with step input of 0.05 mm
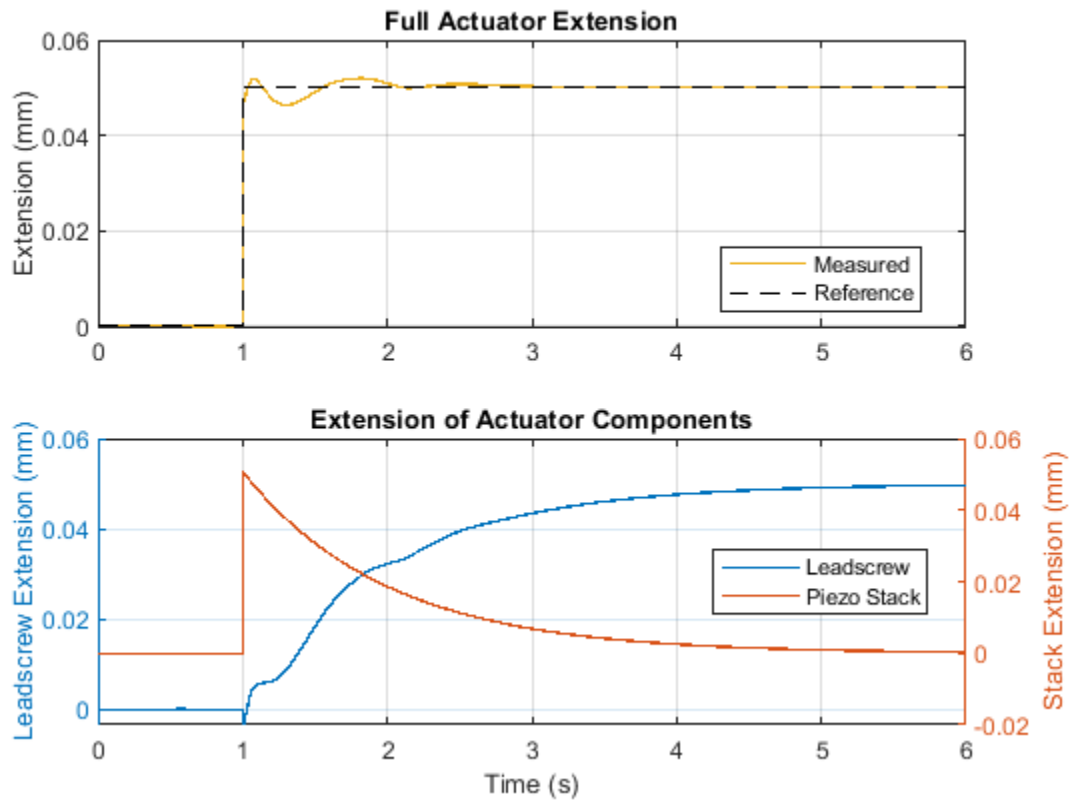
Frequency Response

Frequency response of full hybrid actuator
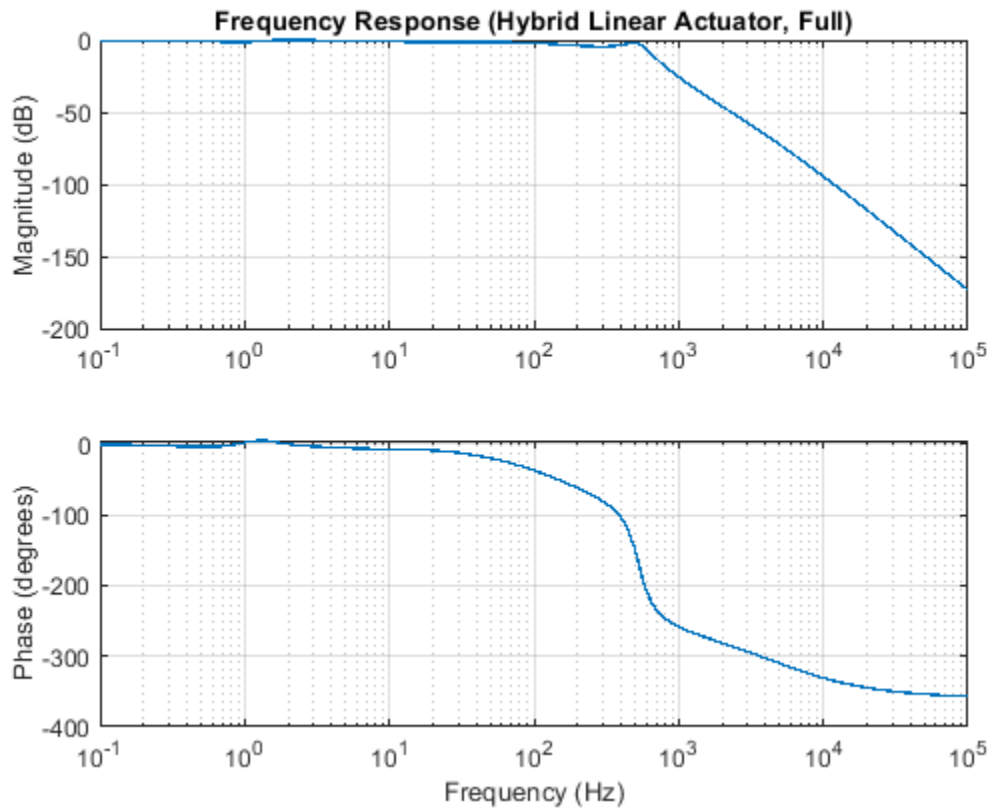
Frequency response of DC motor only
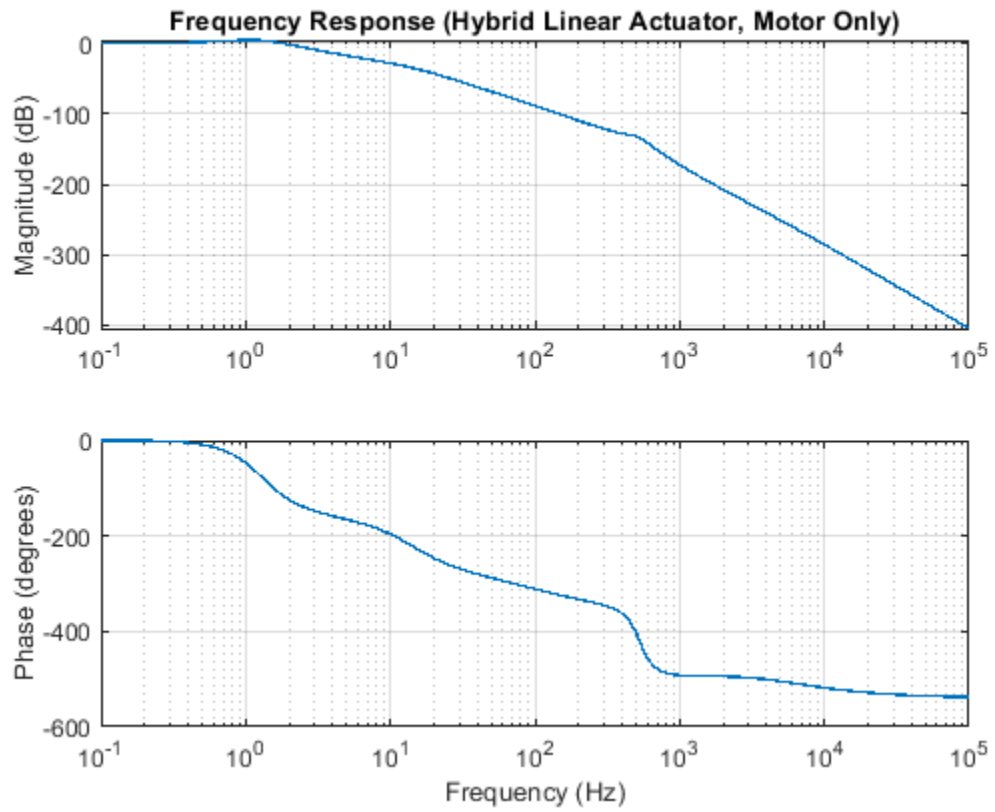
Frequency Response (Hybrid Linear Actuator, Motor Only)

Solenoid Parameterized with FEM Data

This example shows a limited travel solenoid with return spring. When unpowered, the spring holds the plunger at a distance of 0.1mm from the fully energized position. At 0.1 seconds, the solenoid is powered on and the displacement goes to zero. At 0.06s a force higher than the holding force is applied, and the plunger moves to its maximum travel of 0.2mm. The solenoid force and back emf characteristics are defined by the FEM-Parameterized Linear Actuator block. This block takes data in the format typically provided by a finite element magnetic field modeling tool. There are two parameterization options, one which works directly with flux data, and one which uses partial derivatives of flux with respect to current and displacement. The latter option is usually the better choice, it giving more accurate results for a given density of current and position data points. However, it requires more data pre-processing.

A key difference between this approach to modeling a solenoid and the approach used by the Simscape™ solenoid example 'ssc_solenoid' is that it can capture magnetic saturation effects. Conversely the Simscape example assumes a linear relationship between current and magnetic force. This linearity assumption is also made by the Simscape Electrical™ Solenoid component. In this model, the subsystem 'Simplified solenoid with no saturation effects' is parameterized with solenoid inductance data for a current of 0.1A. The inductance is the partial derivative of the flux with respect to current, and is also calculated by the initialization file. The difference in results compared to the linear model of the solenoid illustrates the effects of flux saturation. If you reduce the supply voltage from 12V to 1.4V, then the steady-state current is approximately 0.1A and the results then match once the current has reached this value.

Model
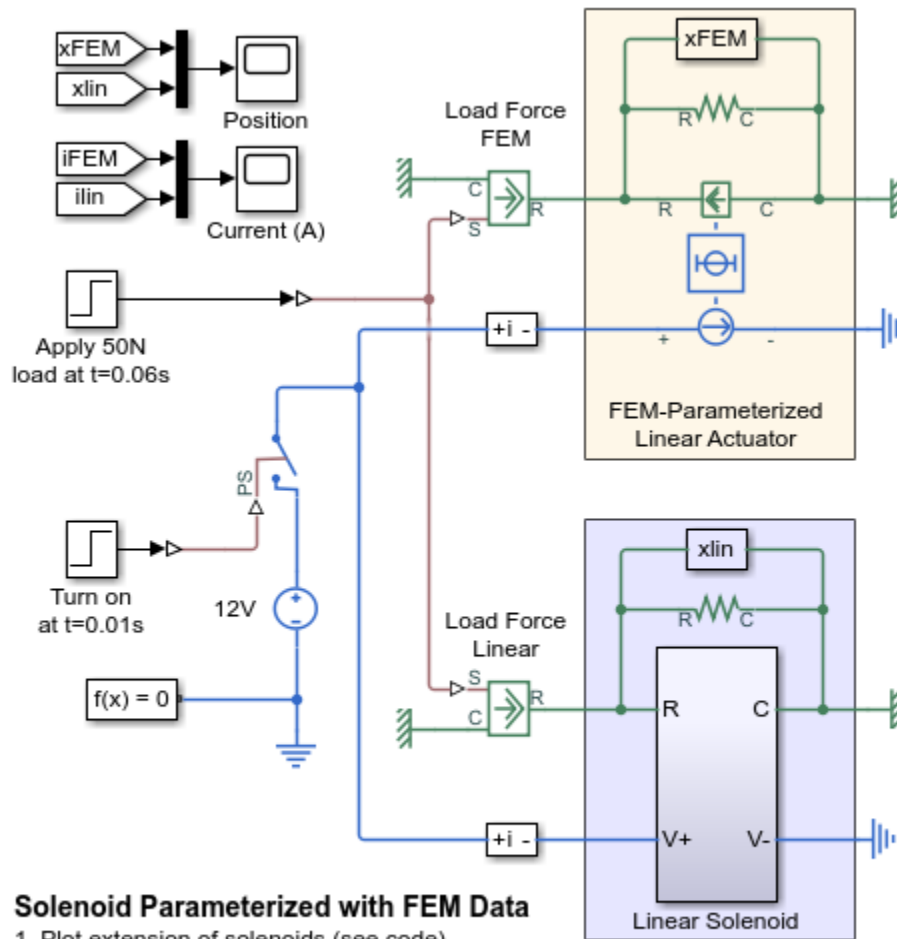
Solenoid Parameterized with FEM Data
1. Plot extension of solenoids (see code)
2. Calculate and plot flux data (see code)
3. Explore simulation results using sscexplore
4. Learn more about this example

Simulation Results from Simscape Logging

The plot below shows the difference in behavior between a linear solenoid model and a solenoid model parameterized with data provided from a finite element magnetic field modeling tool.
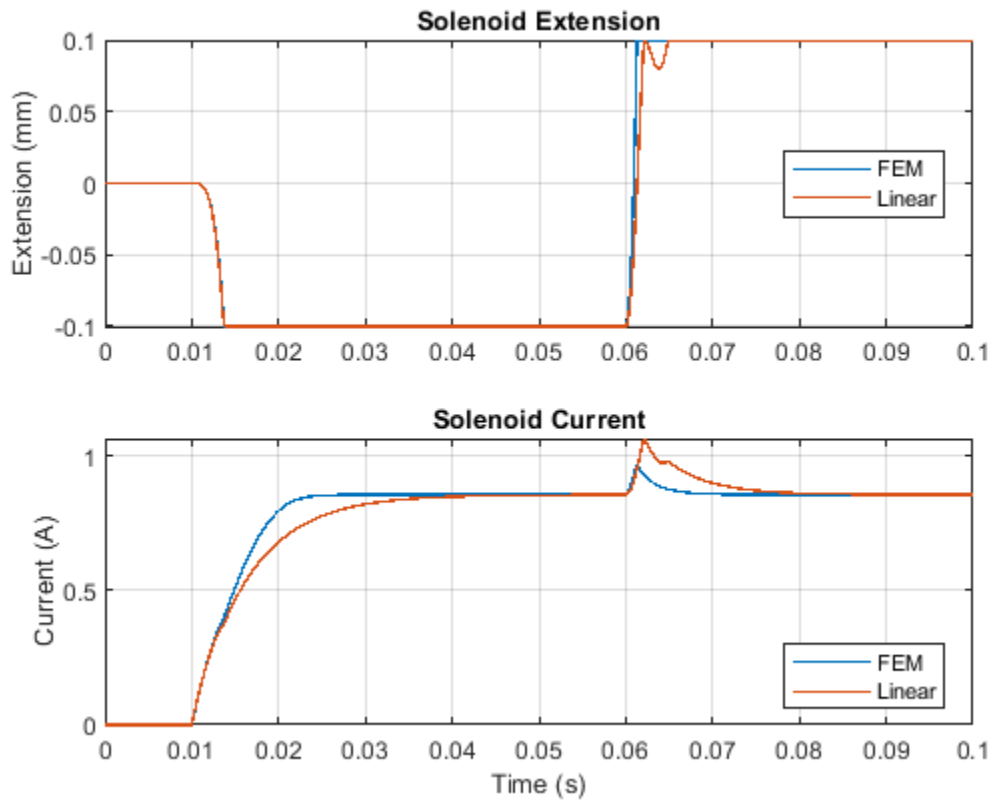
Simulation Results from Simscape Logging

The plots below show the flux data, including comparisons between methods of calculating the necessary parameters for the FEM-Parameterized Solenoid block.

Flux Linkage vs. Current and Displacement

Partial Derivative of Flux with Respect to Current

Partial Derivative of Flux with Respect to Distance

Force Magnitude vs. Current and Displacement
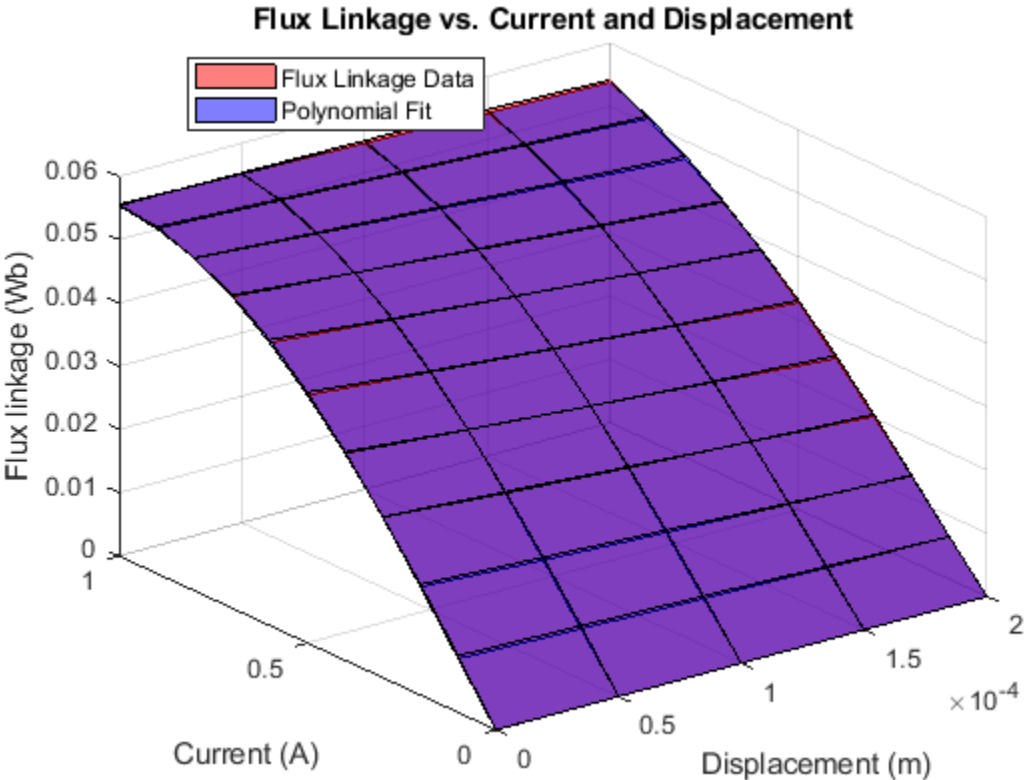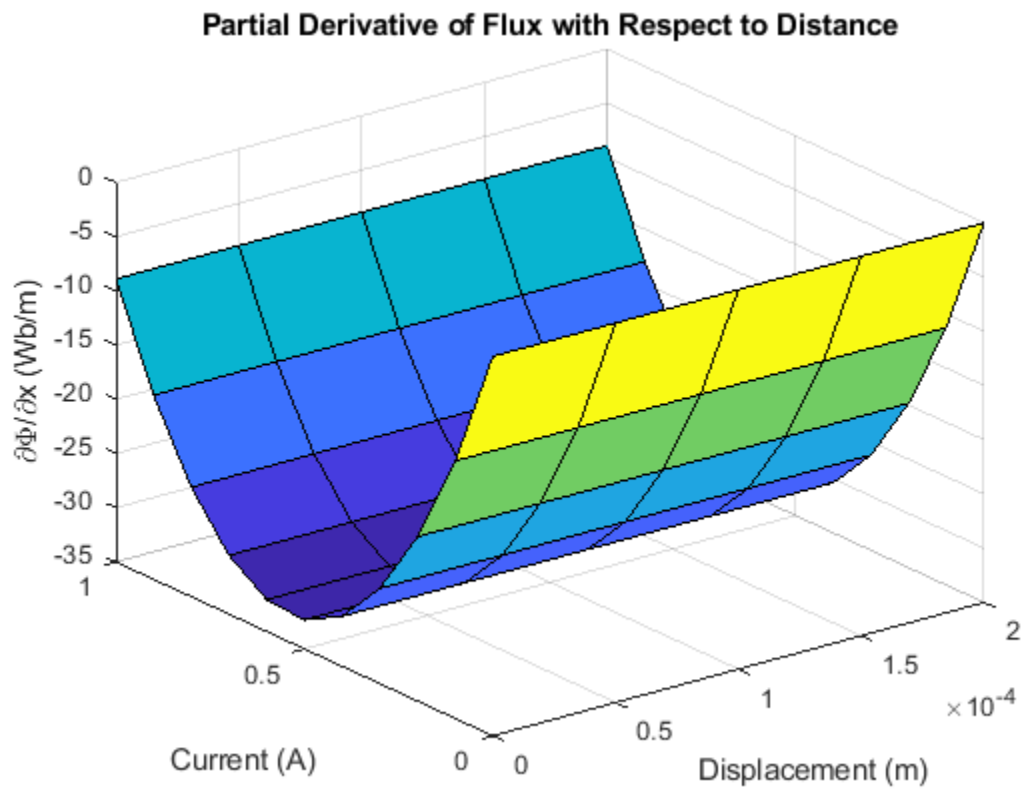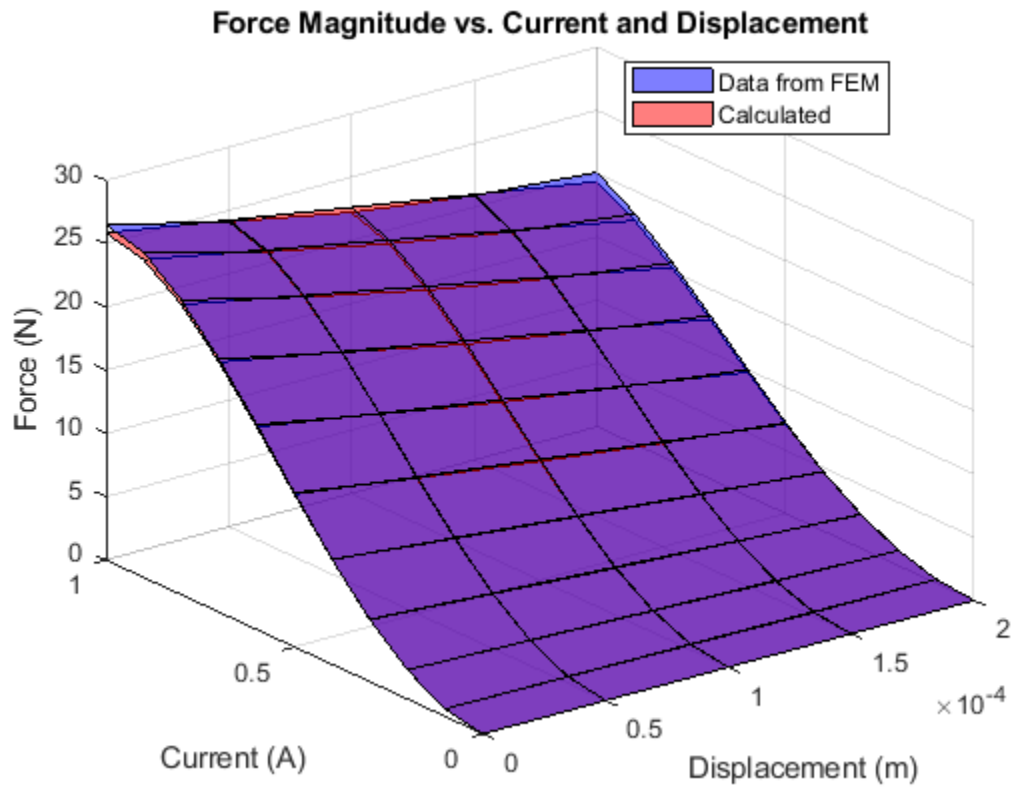
Custom Solenoid with Magnetic Hysteresis

This example shows a limited travel solenoid with return spring. Magnetic hysteresis is modeled using the Reluctance with Hysteresis library block.

Model
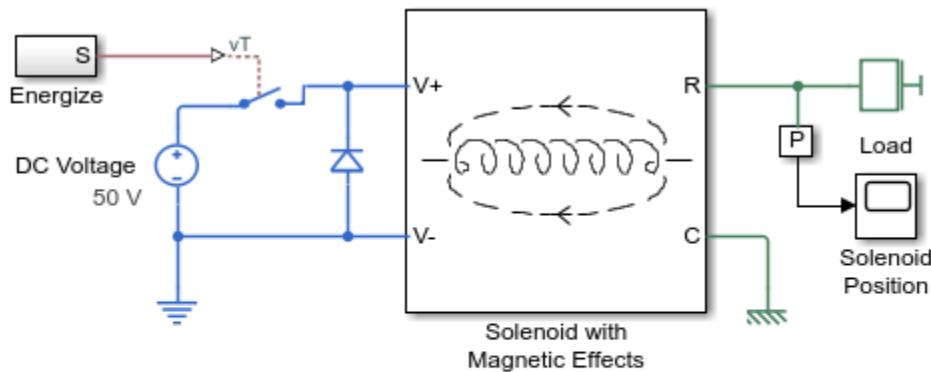
Custom Solenoid with Magnetic Hysteresis

1. Plot position and B-H curve of solenoid (see code)
2. Explore simulation results using sscexplore
3. Learn more about this example

Solenoid with Magnetic Effects Subsystem
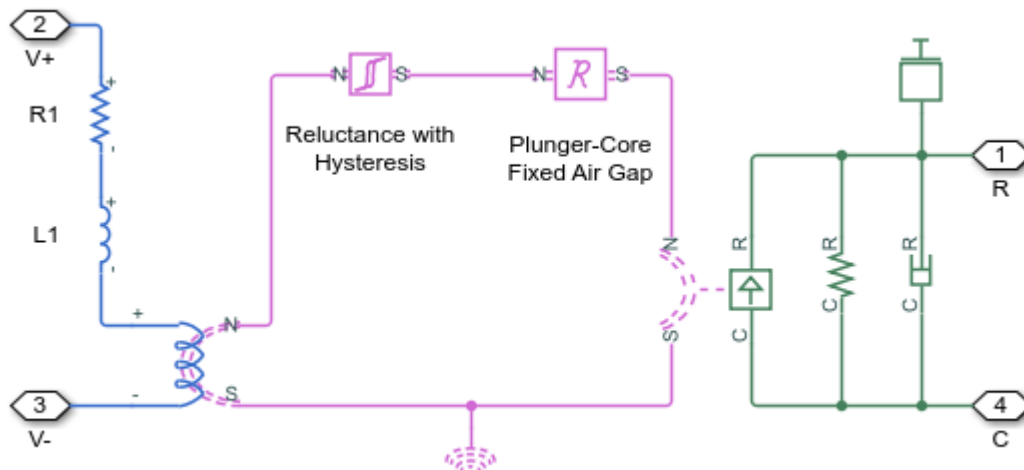
Simulation Results from Simscape Logging

The plot below shows the solenoid position when pulsed on and off, plus the corresponding B-H trajectory.
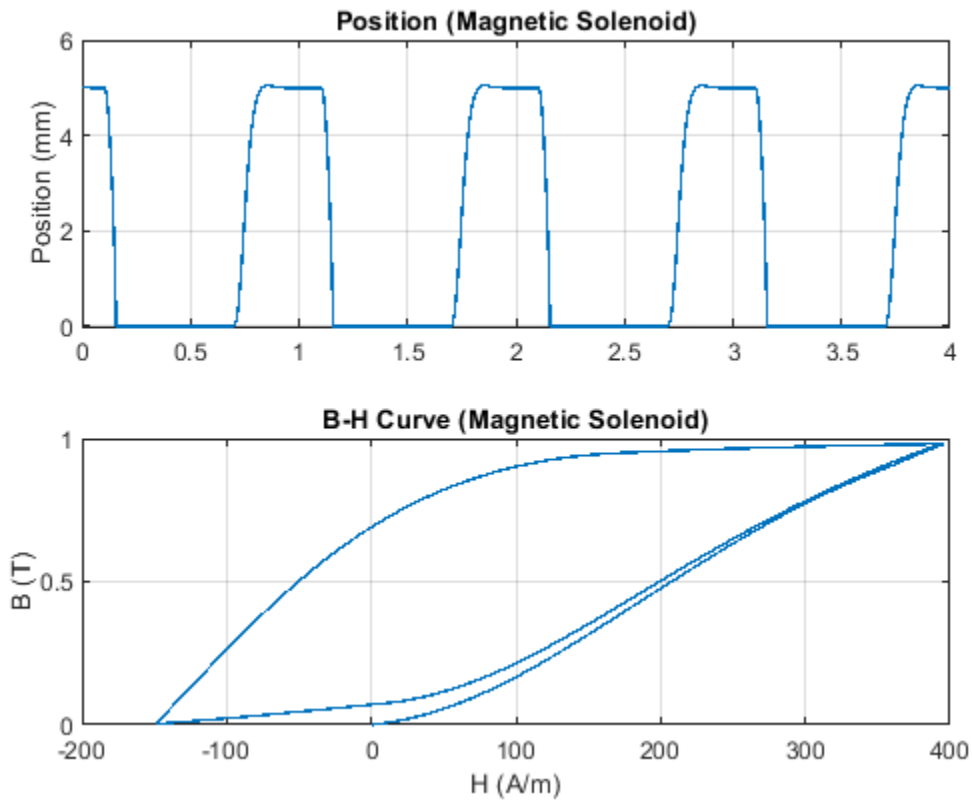
Torque Motor Parameterization

This example shows how manufacturer data for torque as a function of current and angle can be used to model a torque motor. The datasheet shows linear characteristics for rotor angles between 20 and 70 degrees and for currents where saturation does not occur. Data in this range is used to parameterize the simplified model of the torque motor. Using MATLAB® to process the data points extracted from the datasheet, we can convert manufacturer data into motor parameters that are often obtained from finite element software.

The motor models show similar results when tested under conditions where the datasheet shows linear behavior. When tested over the full range, the behaviors deviate as specified in the datasheet.

Model

Torque Motor Data	
Resistance	2.6 Ohms
Inductance	15 mH
Torque Constant	80 mNm/A
Weight	320 g
Rotor Diameter	40 mm
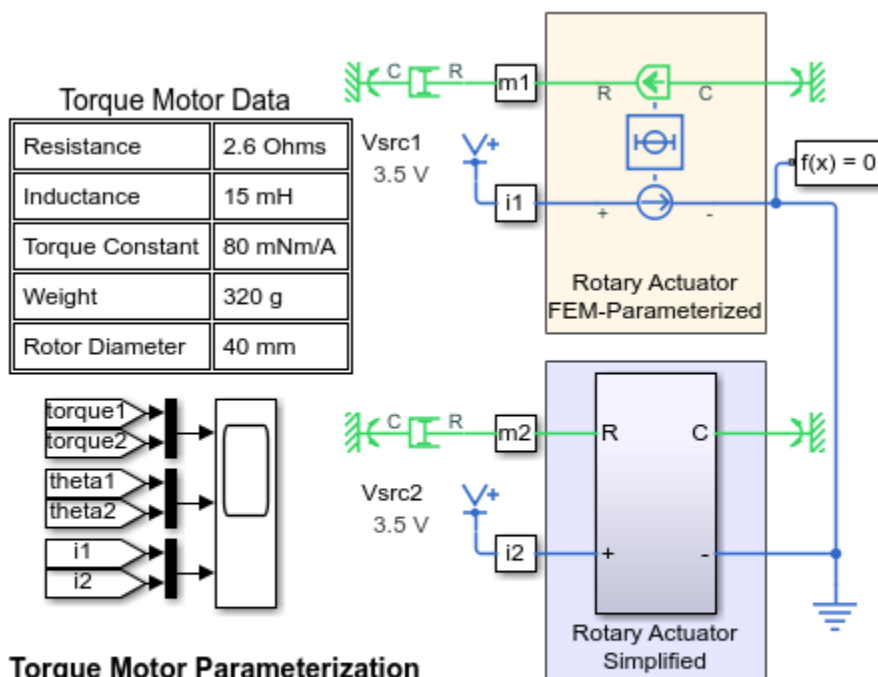
Torque Motor Parameterization
1. Plot shaft angle of motors (see code)
2. Set range for test: Full, Linear region
3. Calculate and plot flux data (see code)
4. Explore simulation results using sscexplore
5. Learn more about this example

Rotary Actuator Simplified Subsystem
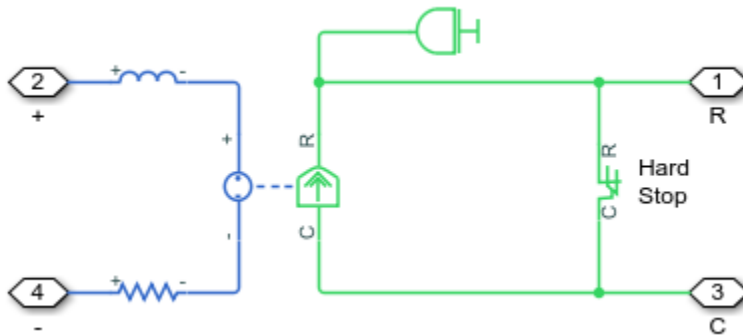
Obtaining Motor Data from Data Sheet

The plot below shows resampled data obtained from a motor data sheet. It shows torque produced at different rotor angles at different current levels. For some conditions (such as 2 amps, 20 deg to 70 deg), the torque is constant, but at other levels it is highly nonlinear.
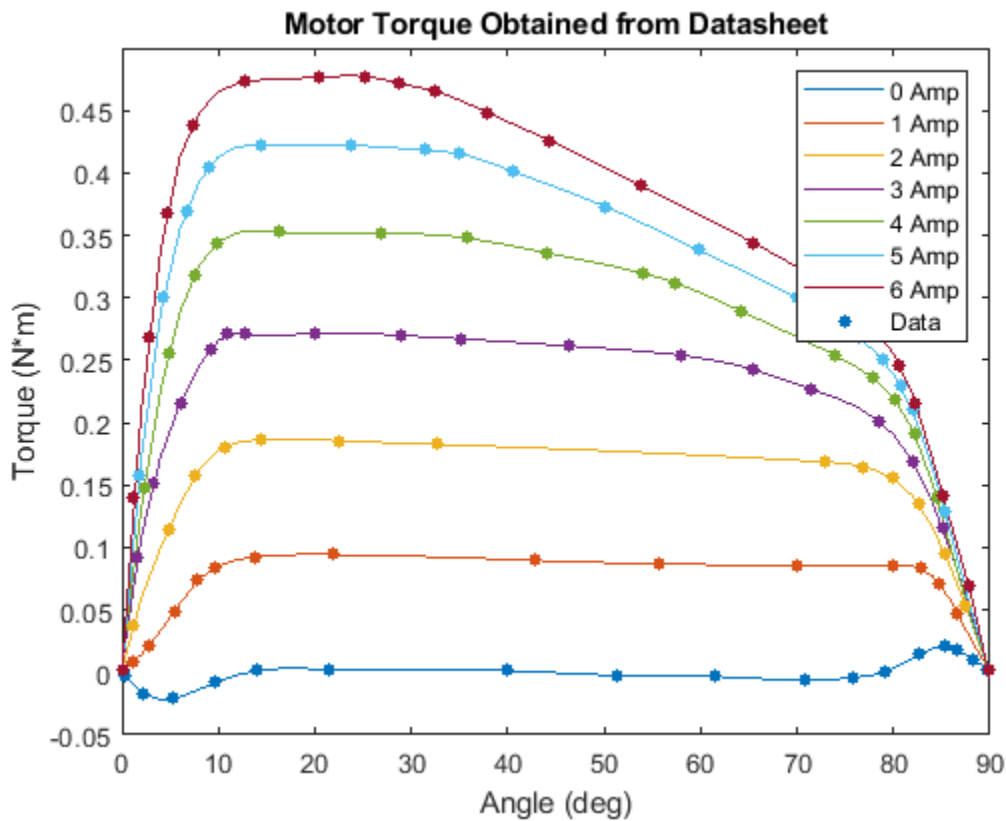
To parameterize our motor model, we need to obtain flux partial derivative with respect to angle. This script estimates dPhi/dx from torque. First, we mirror the datasheet to obtain data for negative currents and plot it as a surface.

Datasheet as Surface (Mirrored for Negative Currents)
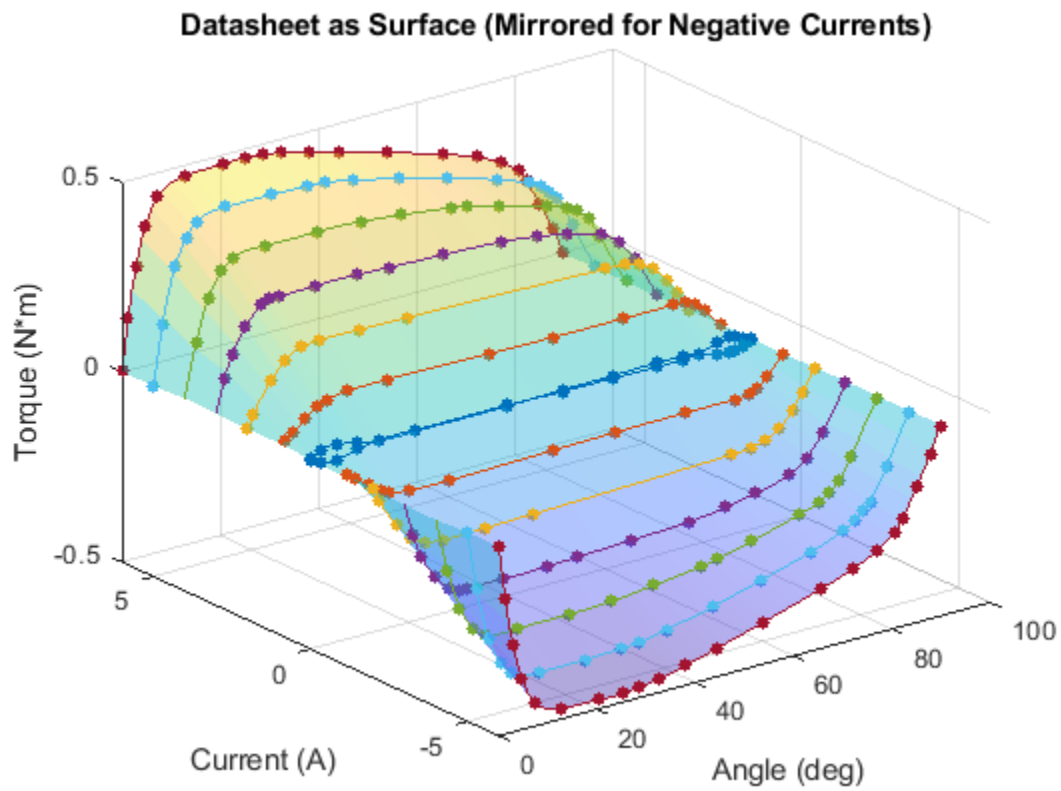
Next, we use MATLAB to fit polynomial curves to the surface along lines of constant angle.

Polynomial Fit to Torque Data by Angle
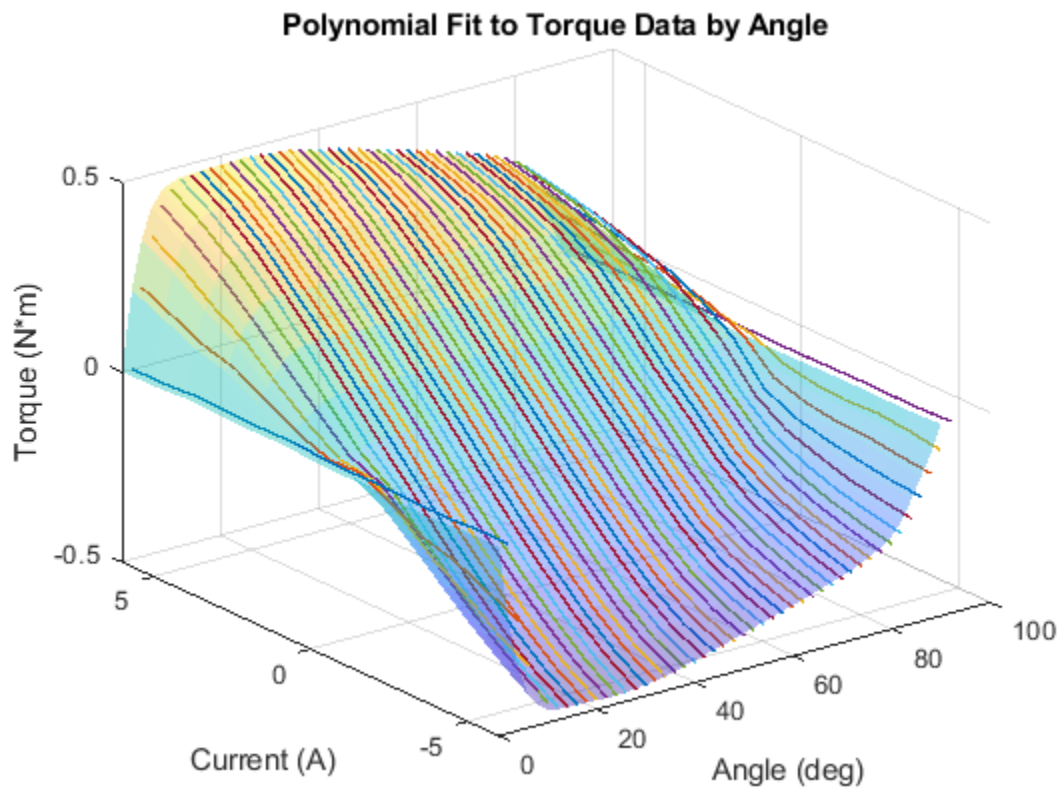
Finally, we use MATLAB to obtain the derivative of the polynomial along those curves. Extracting a lookup table from this surface yields the parameters we need for our motor model.

Partial Derivative of Flux with Respect to Angle
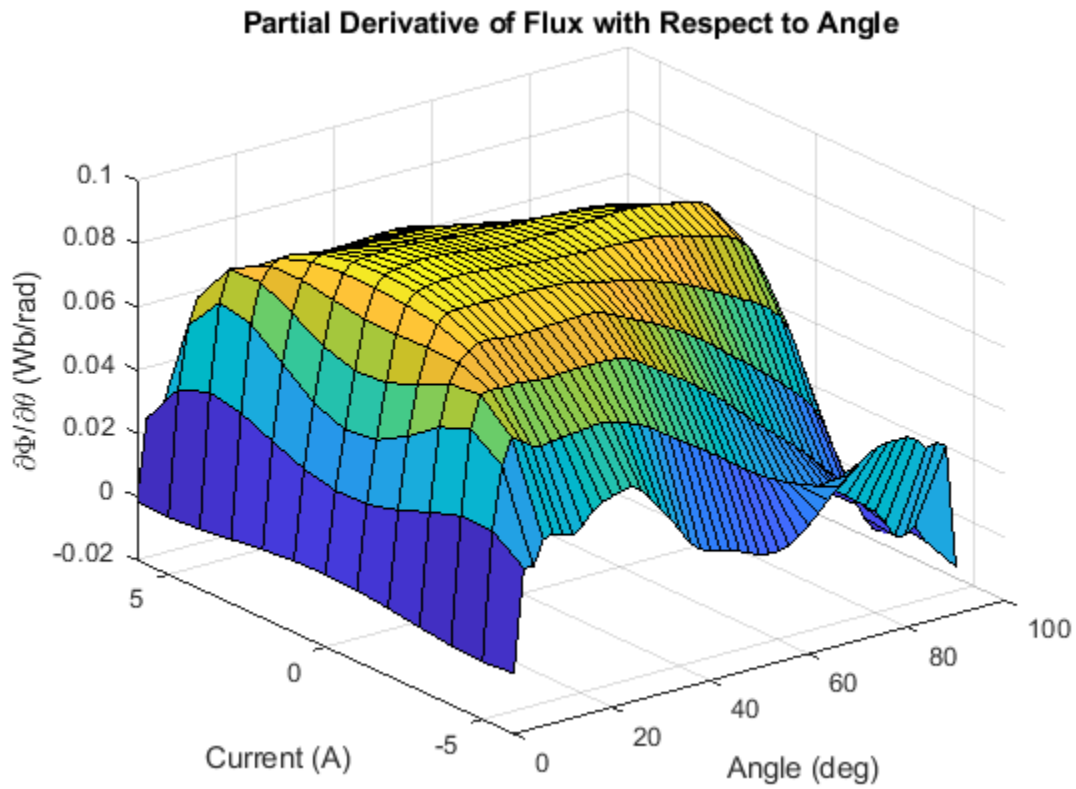
Simulation Results from Simscape Logging

The plot below shows the behavior of the FEM-Parameterized Rotary Actuator and simplified model built from Simscape™ Foundation Library elements. This test was performed over the range of travel where the finite-element data is linear, so the results are similar.
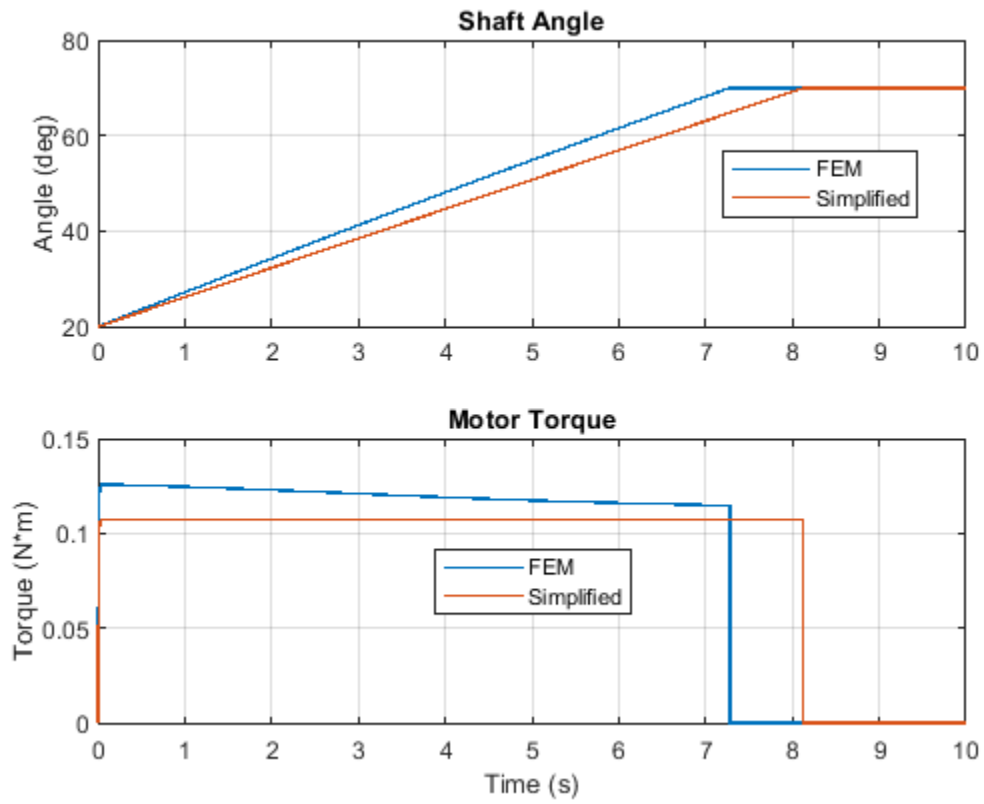
Performing the test over wider range over the range of travel where the finite-element data is nonlinear shows the effect of our parameterization as two motors behave very differently.
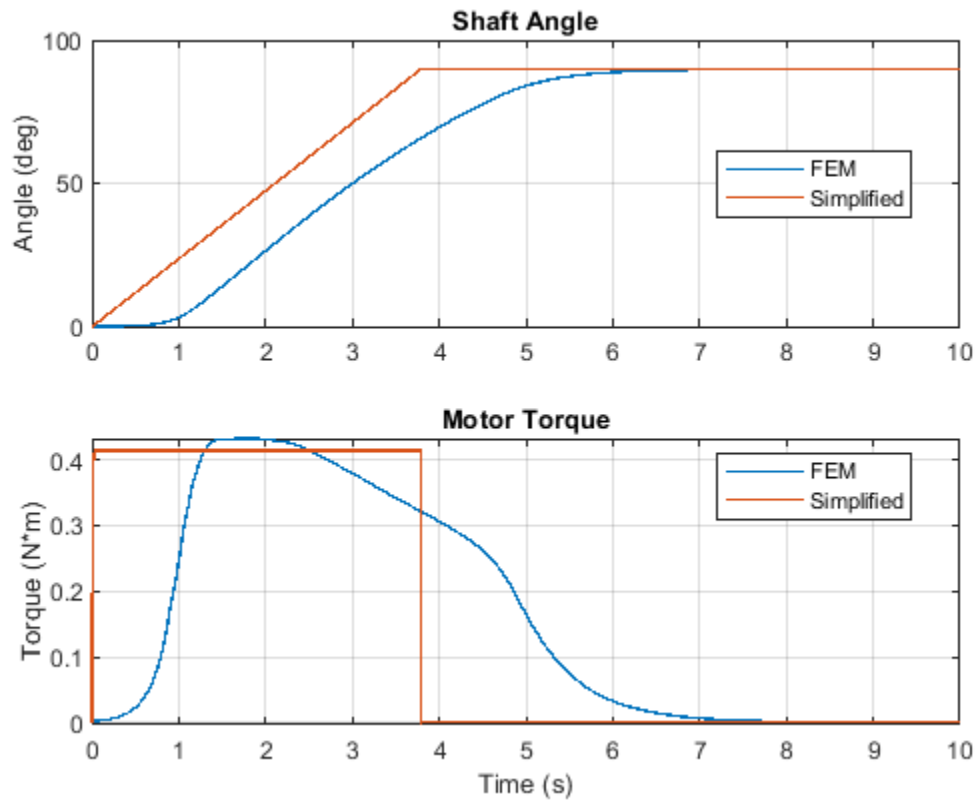
Automotive Alternator Charging a Battery

This example shows how alternator behavior can be abstracted to a DC model that simulates efficiently. This test harness first ramps the alternator speed linearly from zero to a typical idle speed of 900 RPM. When the generated voltage is sufficient to overcome the forward voltage drop associated with the rectifier diodes, the battery charging current starts to ramp up. The test harness then ramps up the speed to 5000 RPM, and the alternator has to back off the field voltage to maintain the regulated voltage. The model captures the increase in stator resistance as the alternator heats up, this reducing device performance

Model
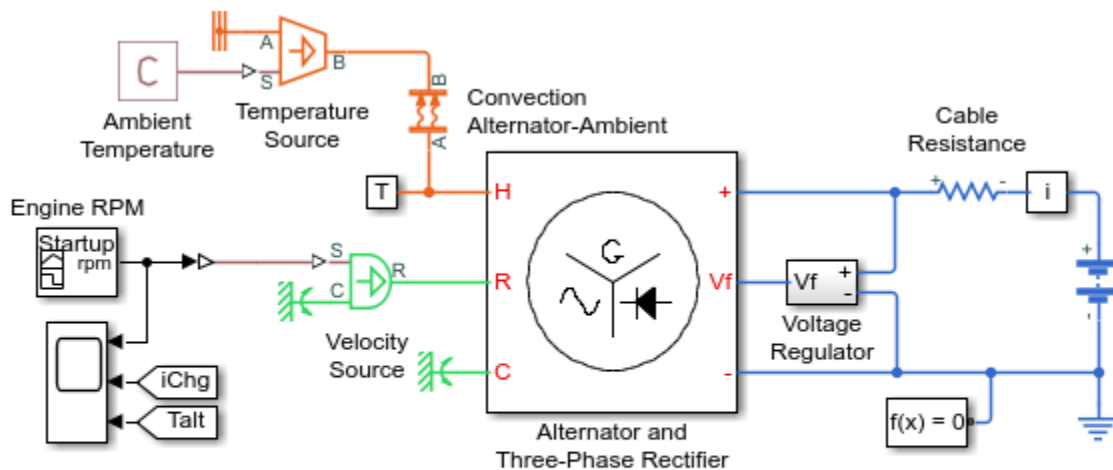
Automotive Alternator Charging a Battery
1. Plot charge current applied to battery (see code)
2. Explore simulation results using sscexplore
3. Learn more about this example

Simulation Results from Simscape Logging

The plot below shows the behavior of the alternator as the engine speed varies.
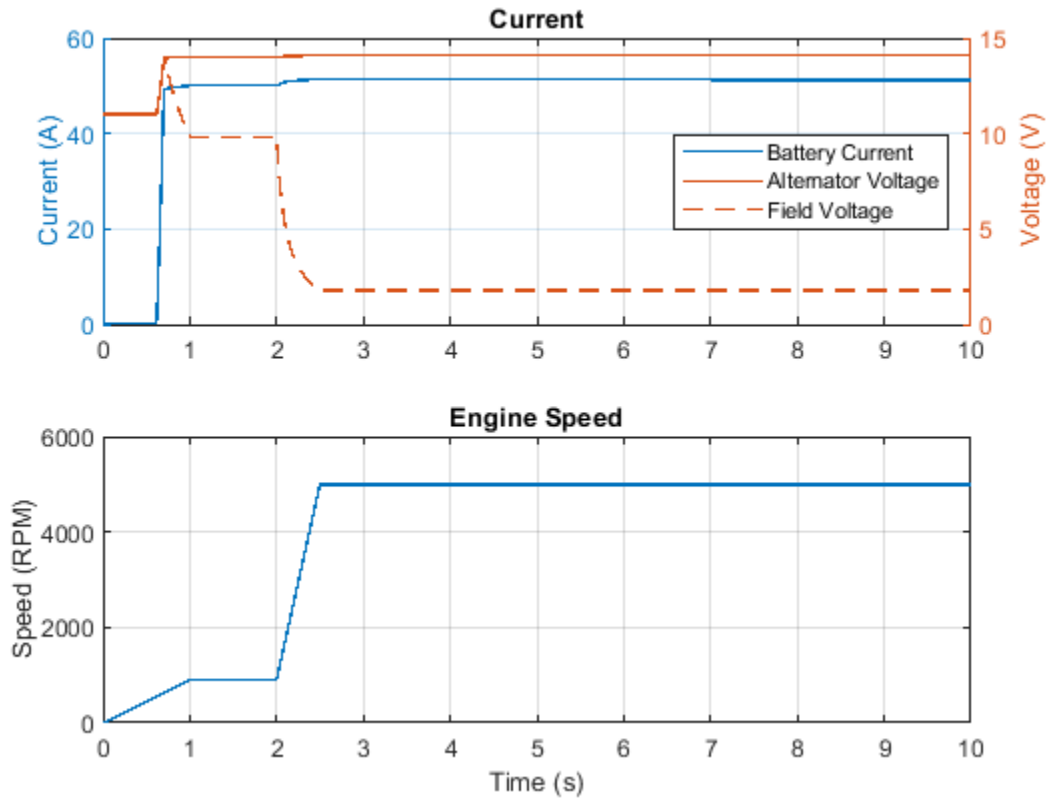
Digital Potentiometer Parameterized from Datasheet

This model shows how to model a digital potentiometer such as is used to control audio amplifiers from a digital circuit or microprocessor-controlled system. The model also shows how you can create your own custom blocks in order to extend the Simscape™ Electrical™ library.

The datasheet specifies an end-to-end resistance R of 10K ohms and a quantization of 16 steps. To look under the Digipot masked subsystem, select the block and type Ctrl-U. The low (L) to wiper (W) resistance is R*N/16, and high (H) to wiper resistance is R*(16-N)/16 where N is the value of an internal counter. The internal counter is enabled by the chip-select (CS) pin, and counts each time the up/down (U/D) pin goes high. The direction of the stepping (up or down) is set by the state of input U/D when digipot is first enabled by the chip select input CS.

In this circuit, the op-amp gain is set by R2/R1 where R2 is the Digipot block resistance. R1 is set to 10K and R2 starts at 10K*8/16 = 5K. Hence the initial gain is 0.5, and the output voltage amplitude is 0.5 volts for a 1 volt peak input sinewave. The Pulse Generator increases the counter N by 1 every second, and the op-amp gain tends to 1 as N approaches 16.

The Digipot block has been implemented as a masked subsystem, and as such is suitable for re-use in other models. Use of triggered and enabled Simulink® subsystems is used to implement the asynchronous counter. To support asynchronous operation, the discrete-time integrator's sample time is set to -1.

Sometimes digipots will only change their resistance when the wiper current is zero. For example, in audio applications this helps prevent audible clicks when the state changes. To implement this, another edge-triggered block can be added after the Pulse Counter block, its trigger signal being driven by a wiper current measurement. Another possible enhancement is to model the wiper resistance by adding a resistor between the wiper port W and the junction between the two variable resistors.

Model
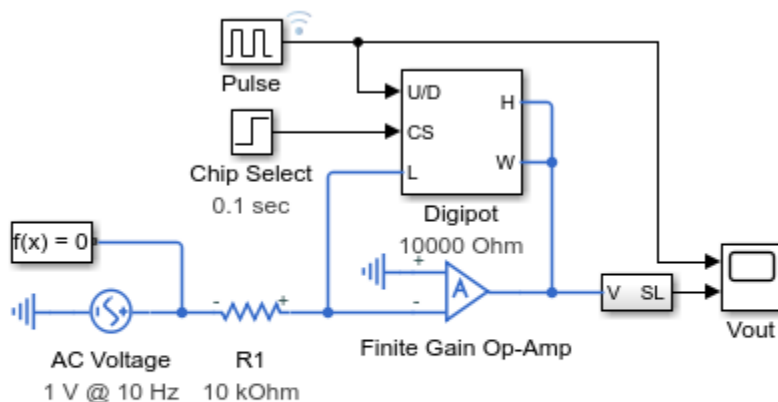
Digital Potentiometer Parameterized from Datasheet
1. Plot voltages in circuit (see code)
2. Explore simulation results using sscexplore
3. Learn more about this example

Digipot Subsystem
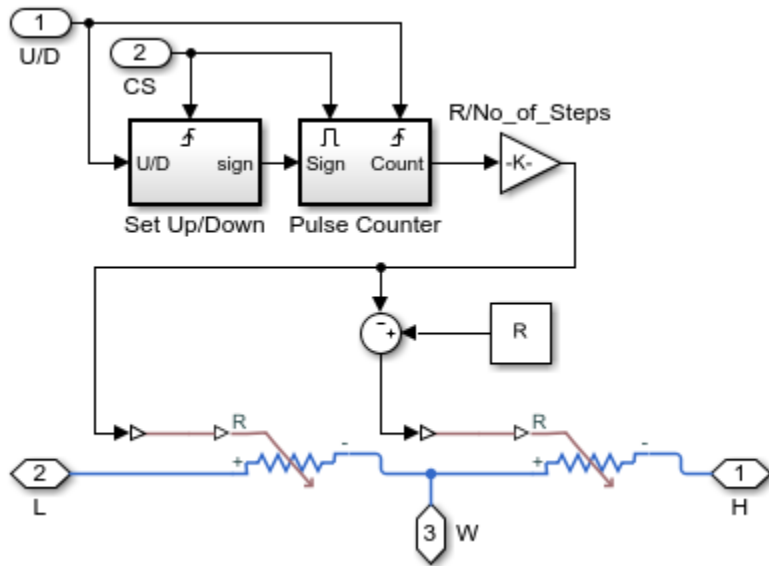
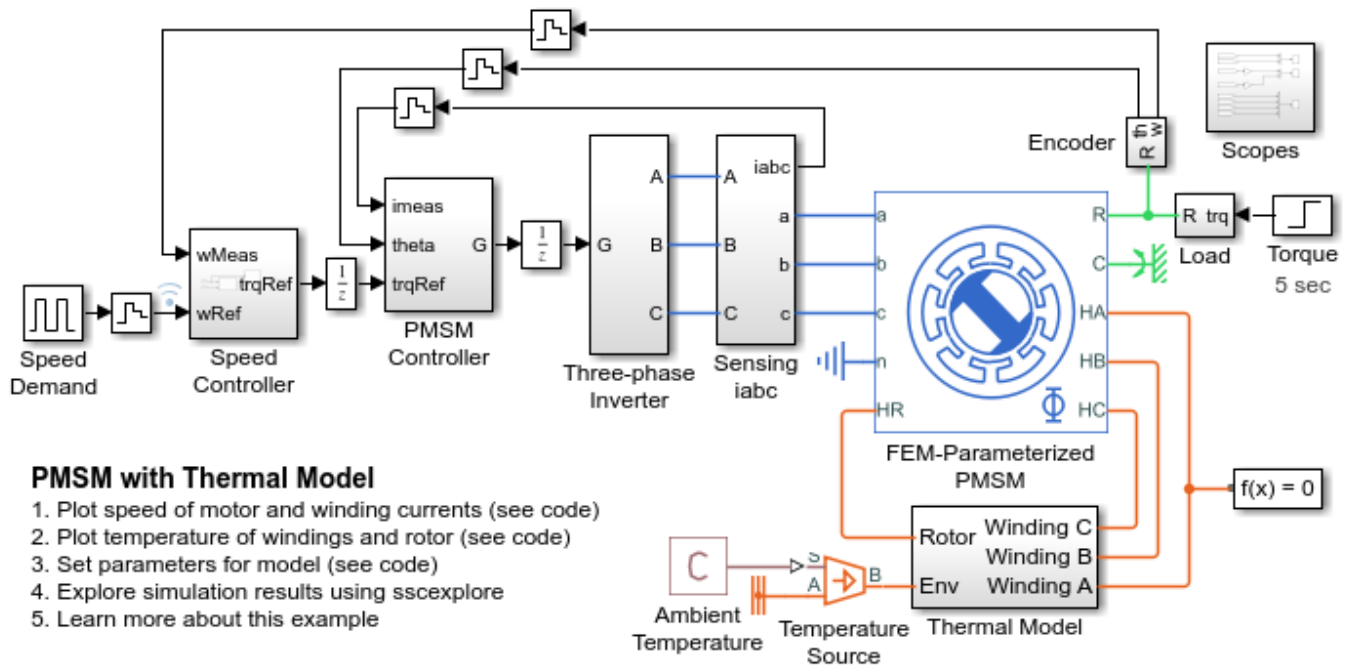**Simulation Results from Simscape Logging**

The plot below shows the input and output voltages of an amplifier whose gain is controlled by a digital potentiometer. The gain is adjusted on the rising edges of the pulses applied to the digital potentiometer.
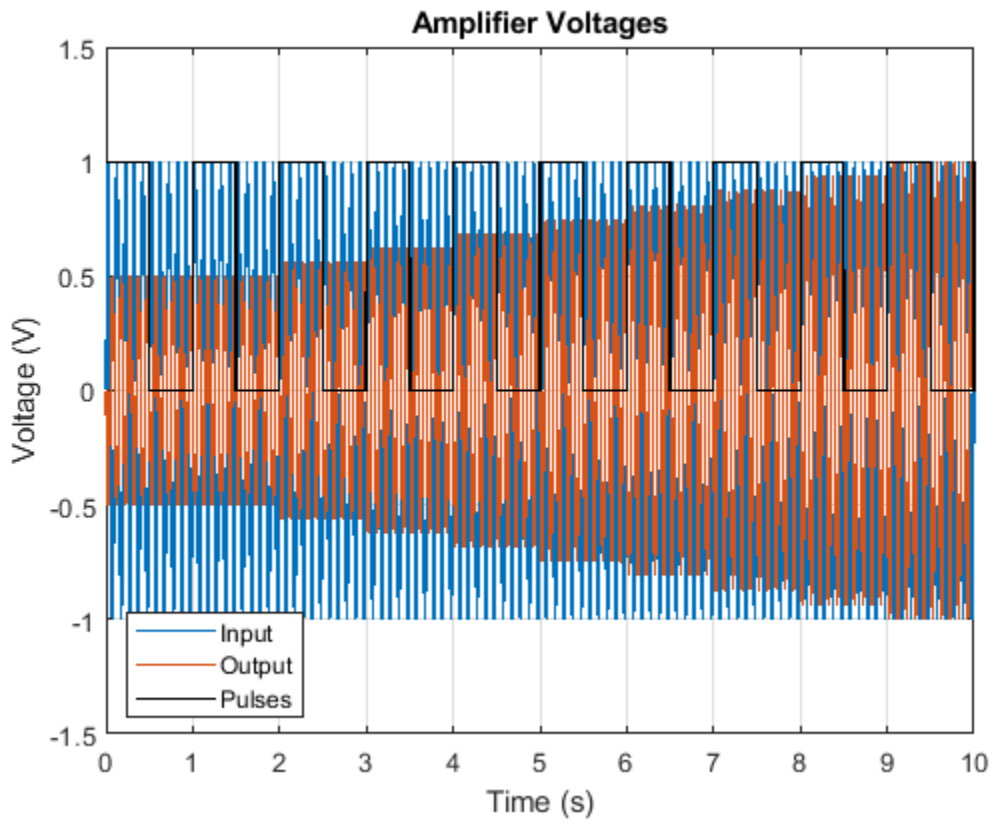
Switched Capacitor Analog to Digital Converter

This example shows how a sigma-delta ADC (analog to digital converter) uses sigma-delta modulation to convert an analog input signal into a digital output signal. The analog input to the sigma-delta ADC controls an oscillator that produces pulses of fixed voltage and duration, but with period between pulses being inversely proportional to the analog input. The oscillator pulses are integrated over a fixed time interval to give a digital representation of the analog input signal.

Model
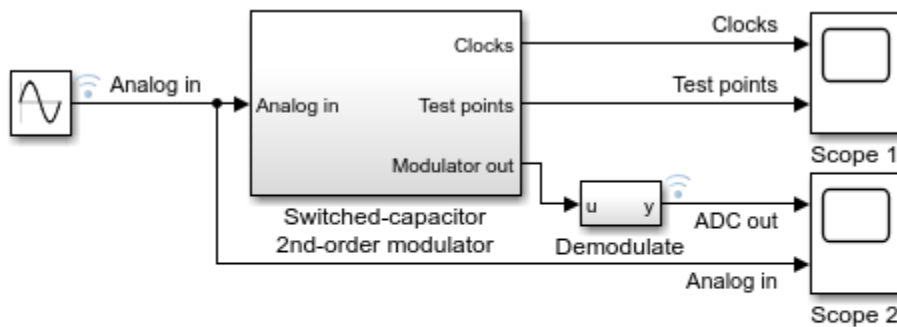
Switched Capacitor Analog to Digital Converter
1. Plot voltages at input and output of converter circuit (see code)
2. Explore simulation results using sscexplore
3. Learn more about this example

Summer and Integrator 1 Subsystem
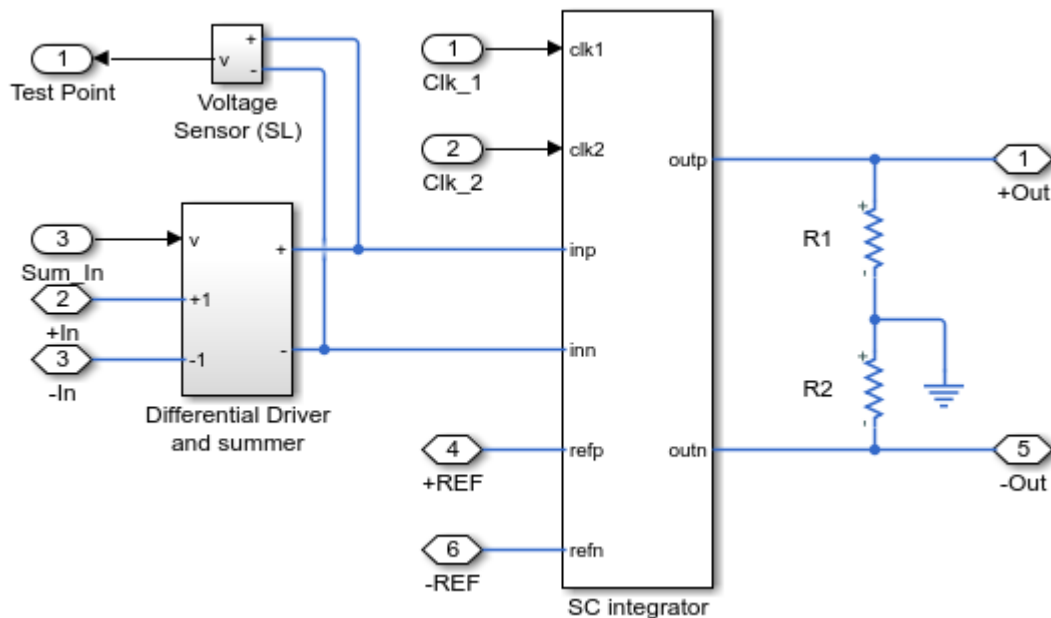
SC Integrator Subsystem
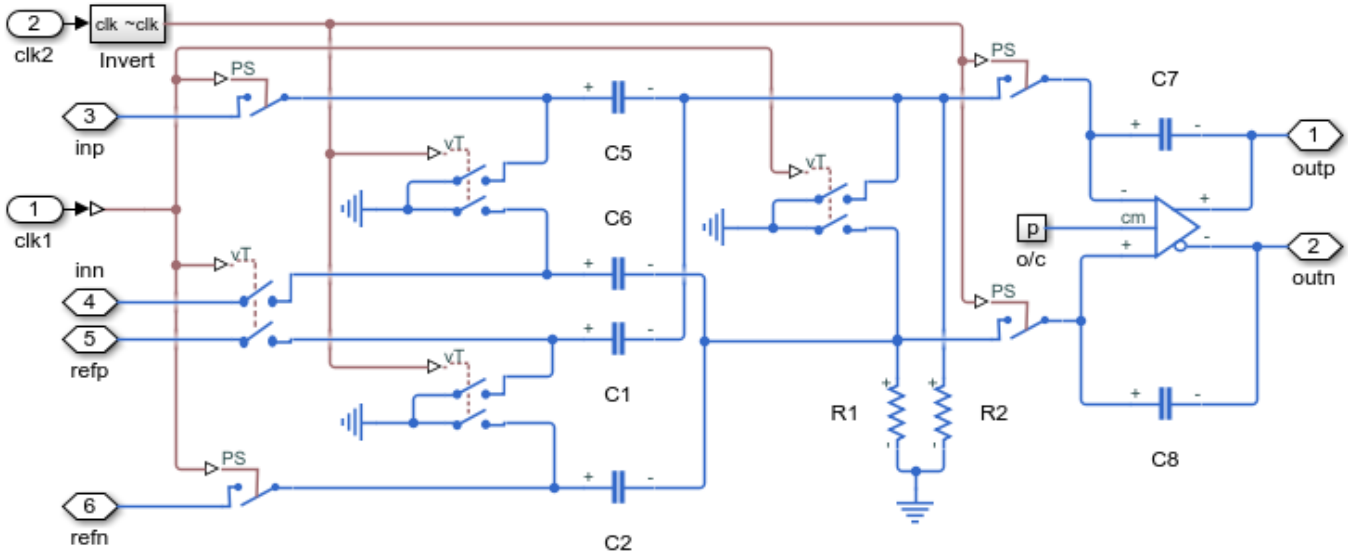
Simulation Results from Simscape Logging

The plot below shows the input and output of a switched capacitor analog-to-digital converter circuit model.
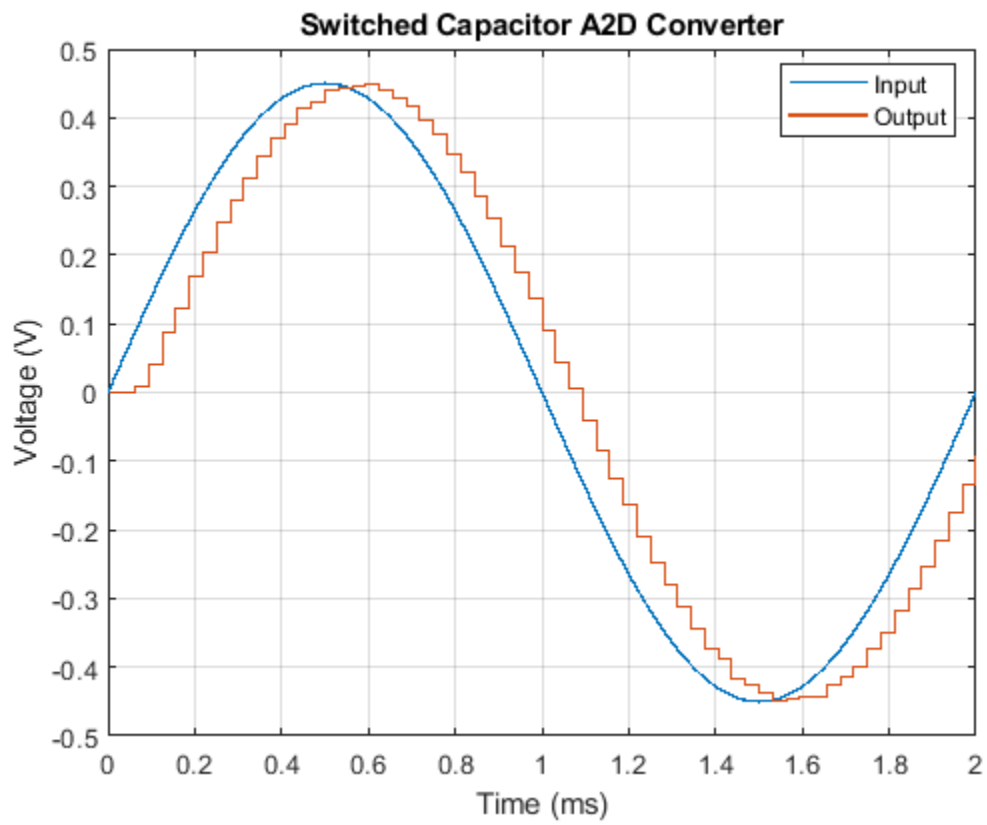
Delta Sigma ADC with Noise

This example shows a simple implementation of a sigma delta analog-to-digital converter. An input in the range 0 to Vref (=1V) is integrated until it causes the integrator to reset. The time to reset is proportional to the input value. Demodulation of the pulses is performed by a low-pass filter. The Asynchronous Sample & Hold block behaves like an edge-triggered D-type flip-flop, passing input U to output Y only on a rising edge of the clock. This model can be used to explore and understand the effect of op-amp impairments such as equivalent input noise on converter accuracy. To turn off the noise, open block Vn and select 'Disabled' for the noise mode.

The Solver Configuration block is set to use the fixed-step trapezoidal Simscape™ local solver. This is selected for simulation speed. The simulation step size is parameterized by workspace parameter Tsim, the value of which should be increased from some small value until the point where results are affected. The parameter Tsim also sets the noise sample time in block Vn.

Model
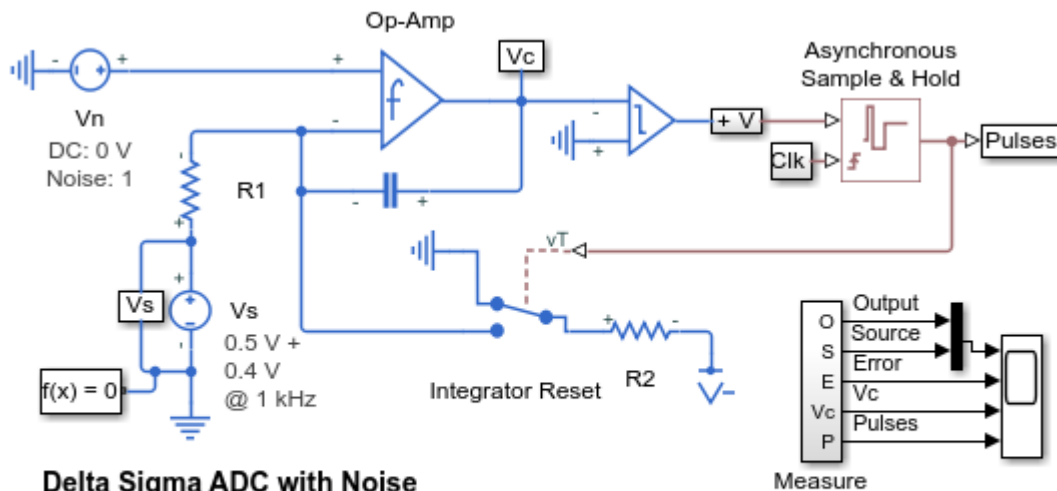
Delta Sigma ADC with Noise
1. Plot voltages from circuit (see code)
2. Set Noise: Enable, Disable
3. Explore simulation results using sscexplore
4. Learn more about this example

Simulation Results from Simscape Logging

The plot below shows the outputs of the Delta Sigma converter circuit.
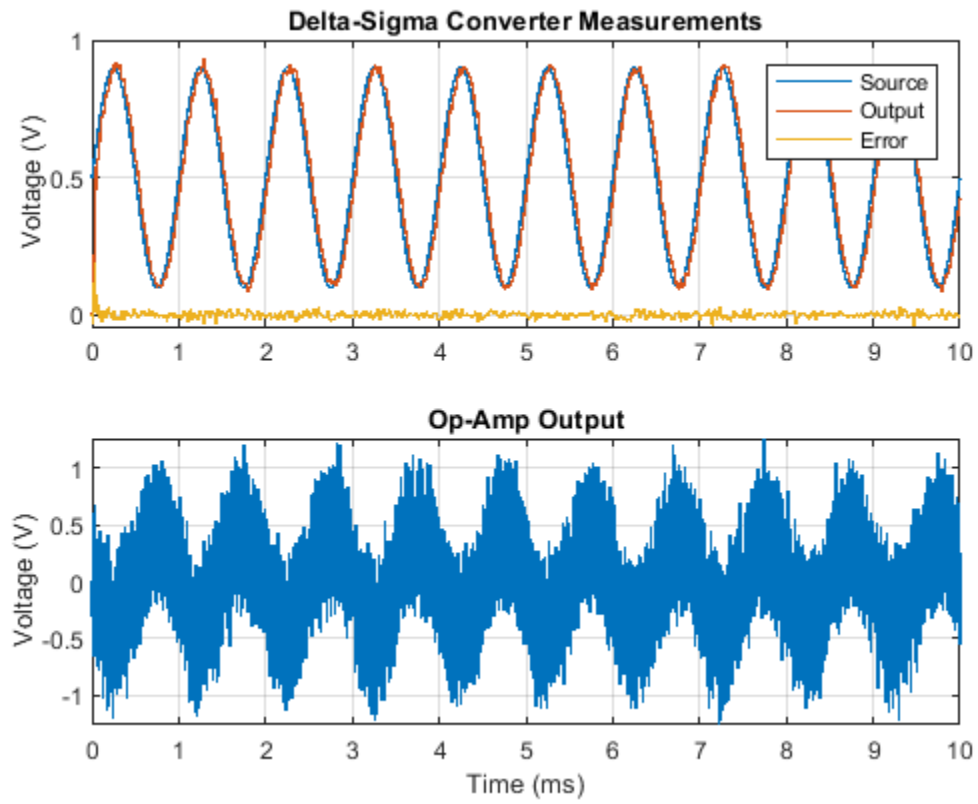
Analog Anti-Aliasing Filter

This example shows an analog implementation of an anti-aliasing filter for use with an A-to-D converter. The filter cut-off frequen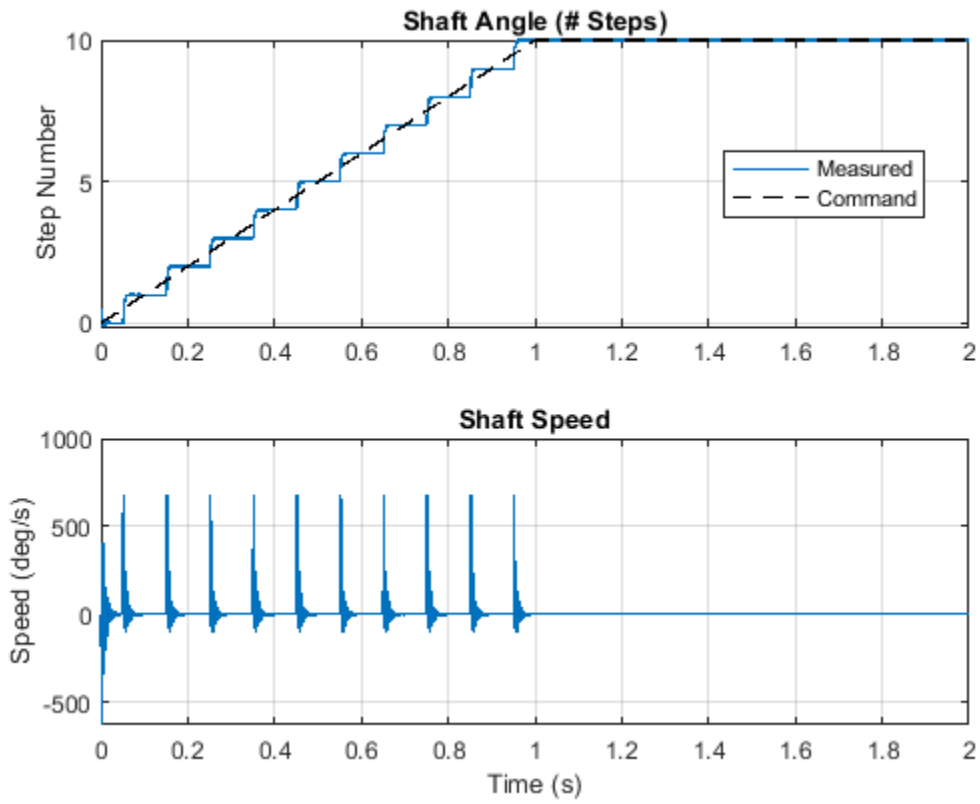cy is set to 500Hz in order to match the A-to-D converter sampling frequency of 1kHz. The test signal incorporates a desired 50Hz sinusoid plus a higher frequency component at 1100Hz that cannot be captured with a 1kHz A-to-D sampling frequency. The scope shows the captured signal without and with anti-aliasing. With the anti-alias filter the 50Hz sine wave amplitude is correctly measured with an amplitude of 1 and corresponding power of 0.5W, i.e., 27dBm for a 1ohm reference load.

Model
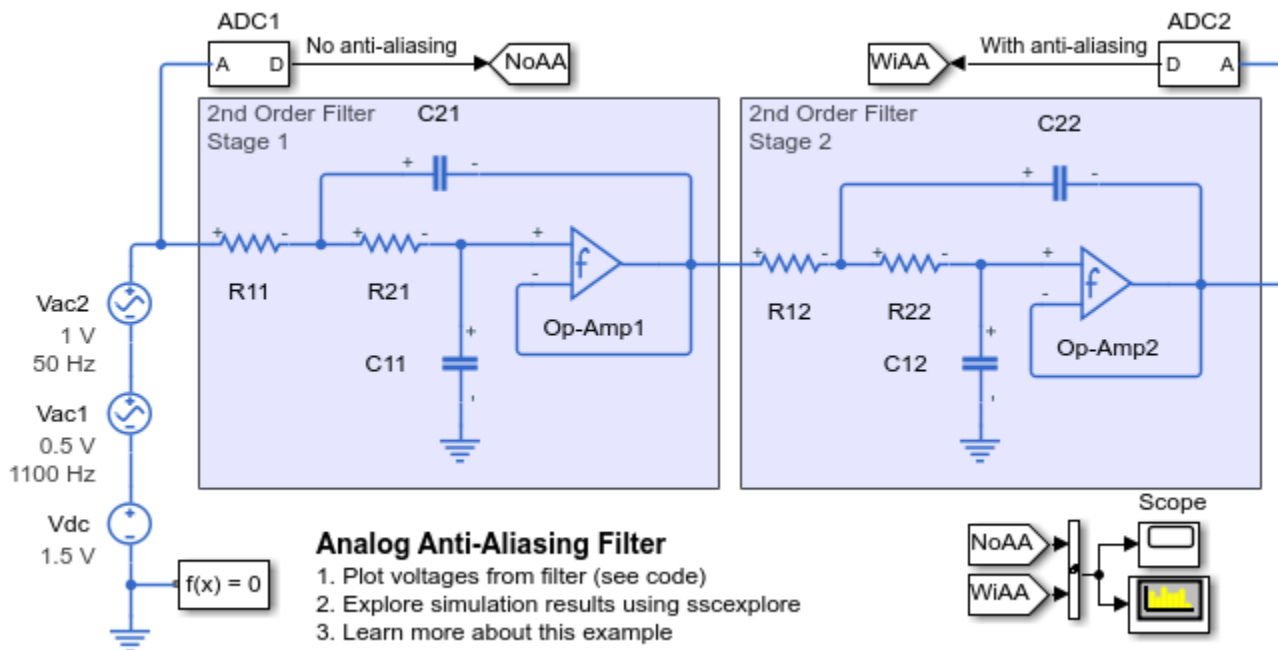
Simulation Results from Simscape Logging

The plot below shows the input and output voltages of the anti-aliasing filter.

Voltages from Anti-aliasing Filter
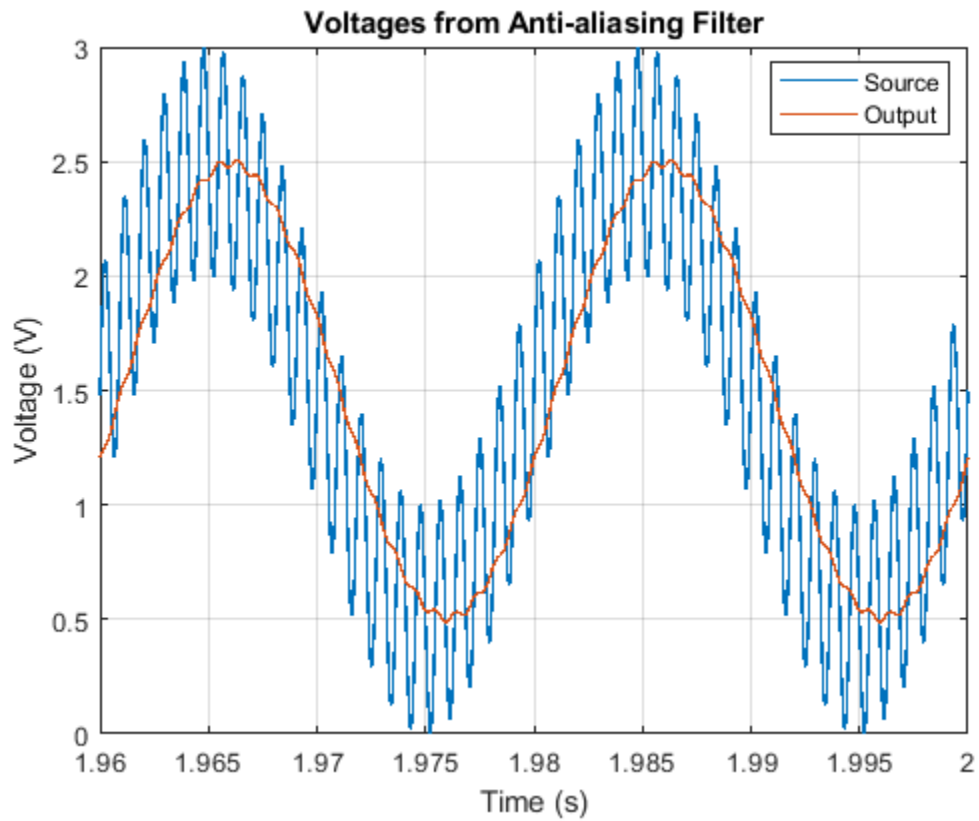
Simulation Results from Spectrum Analyzer

With the anti-alias filter the 50Hz sine wave amplitude is correctly measured with an amplitude of 1 and corresponding power of 0.5W, i.e., 27dBm for a 1ohm reference load.
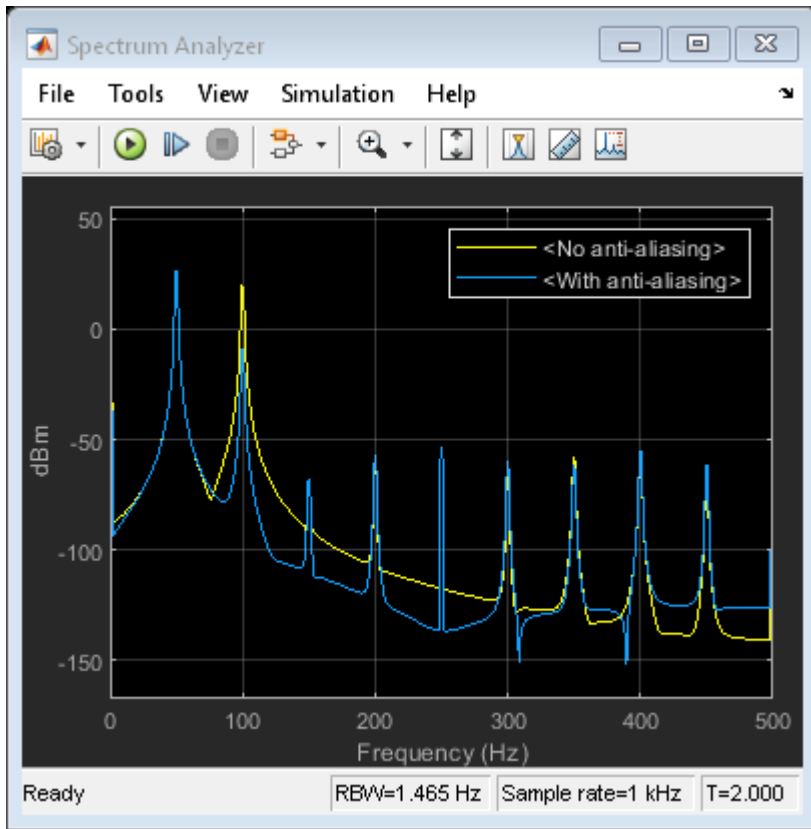
Strain Gauge and Wheatstone Bridge

This model shows how to model a strain gauge and measurement amplifier. The strain gauge forms one leg of a Wheatstone bridge, which is connected to a differential amplifier. A second op-amp is then used to both amplify and apply a low-pass filter to the measurement signal. The op-amps are modeled at a system level, with the user specifying parameters such as open-loop bandwidth, gain and maximum slew rate. In this circuit, the dynamics are primarily set by the low-pass filter. The op-amp bandwidth and maximum slew rate have little impact on the step response.

Model

Strain Gauge and Wheatstone Bridge
1. Check tolerance effects on signal (see code)
2. Explore simulation results using sscexplore
3. Learn more about this example
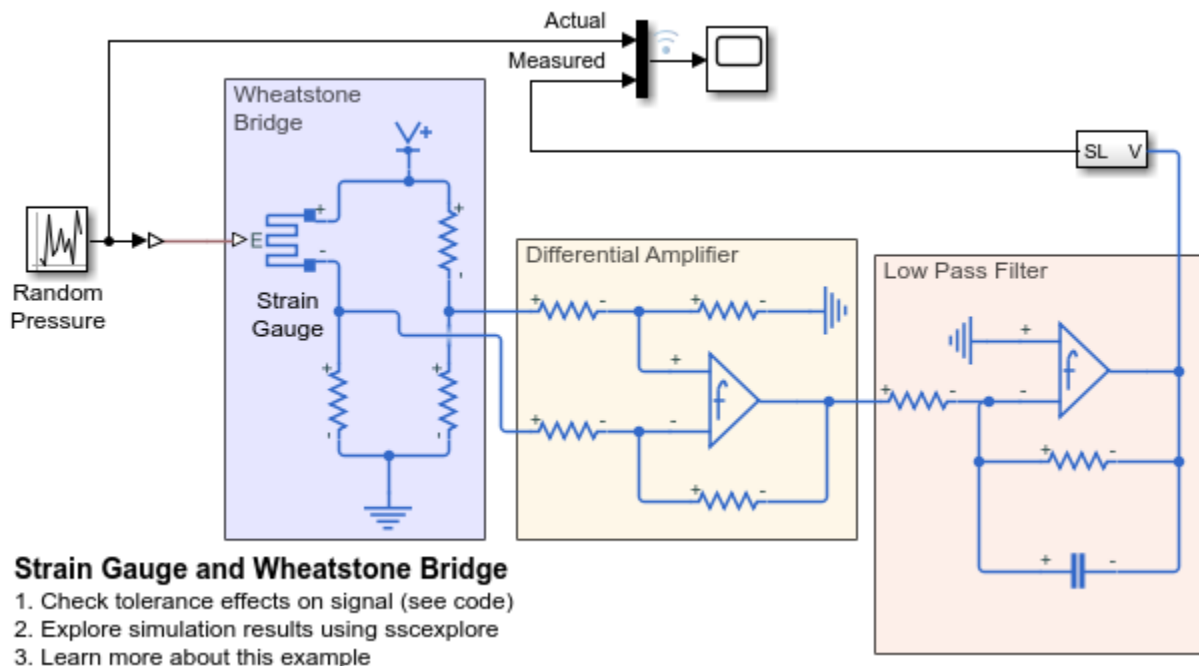
Simulation Results from Simscape Logging

This code plots the actual and measured strain from the model of ee_strain_gauge resulting from two tests. The first test sets the resistor and capacitor in the low pass filter to the maximum value of their tolerance ranges. The second test sets the values for those components to the minimum of their tolerance ranges. The plot shows the effects this has on the strain measurement.
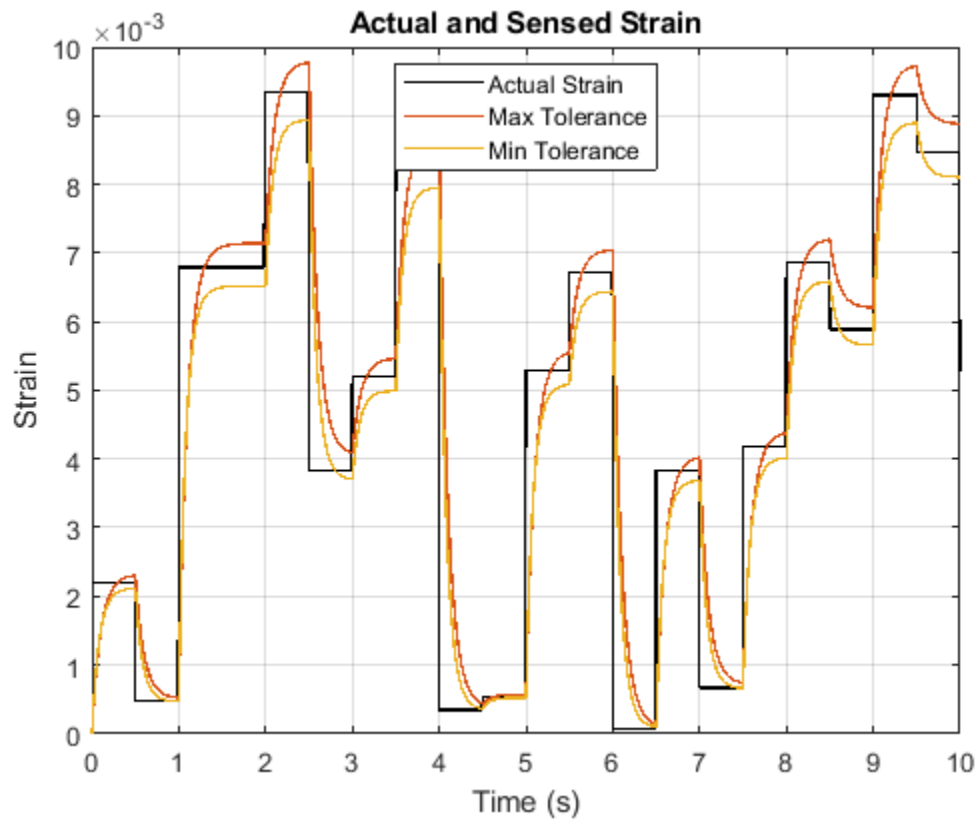
Thermistor-Controlled Fan

This model shows how fundamental thermal, mechanical and electrical components can be used to model a thermistor-controlled fan. The heat-generating device starts producing 2 watts at time zero, and then at 40 seconds this increases to 20 watts. The thermistor therefore heats up, and its resistance decreases thereby increasing the voltage across the PWM reference pins. This increases the PWM frequency which in turn increases average motor current, and the fan speeds up. The additional fan speed increases the convective cooling of the device, moderating the temperature increase of the device.

This is a system-level model such as might be used for selecting an appropriate thermistor characteristic. The convective heat transfer coefficient used to model nominal cooling (i.e. when the fan isn't running) would typically be determined by experiment. Knowing the temperature difference and having an estimate of the device area, the heat transfer coefficient can be calculated. The coefficient for the fan-assisted cooling could then be estimated by running the motor at maximum RPM, and again measuring the temperature difference. The nominal cooling term just also be taken into account when calculating the fan-cooling coefficient.

The Controlled PWM Voltage and H-Bridge blocks have two modes of operation, namely Averaged and PWM. As this is a system-level model, and the thermal time constants are measured in seconds, the Averaged mode of operation is used. The PWM mode replicates the PWM control signal which would typically operate at a few kilohertz.

Model

Thermistor-Controlled Fan
1. Plot temperature and other conditions in system (see code)
2. Explore simulation results using sscexplore
3. Learn more about this example
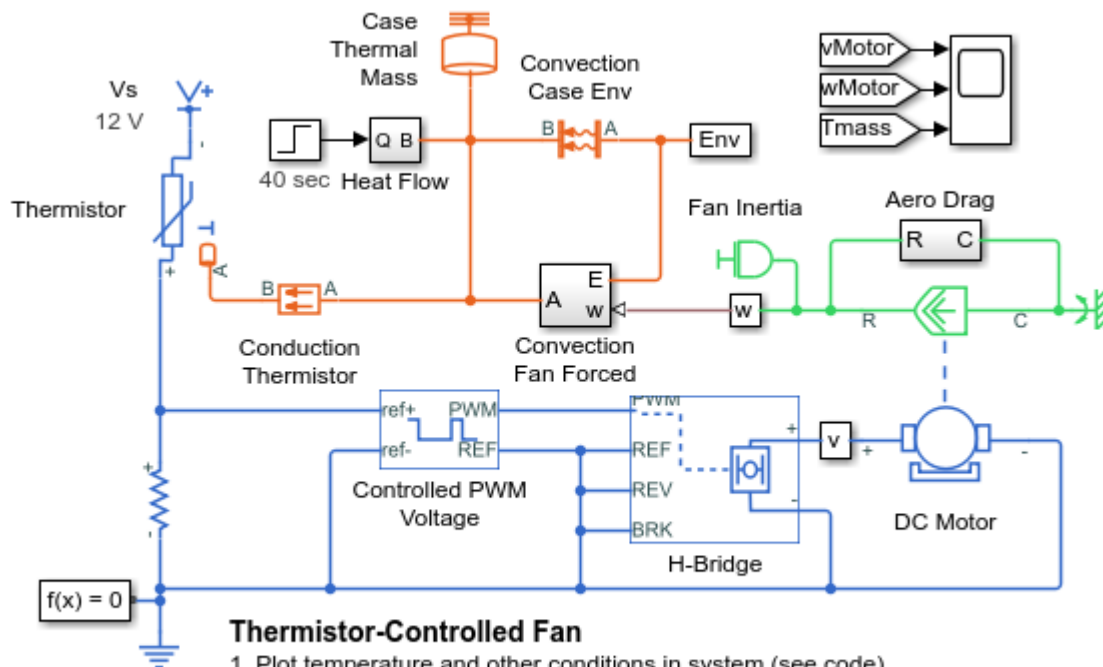
Simulation Results from Simscape Logging

The plot below shows the electrical, mechanical, and thermal behavior of the thermistor controlled motor. As the temperature of the thermistor changes, the voltage applied to the motor changes, which alters the speed of the motor and the convective heat transfer from the case. The system reaches steady state after a short period of time.
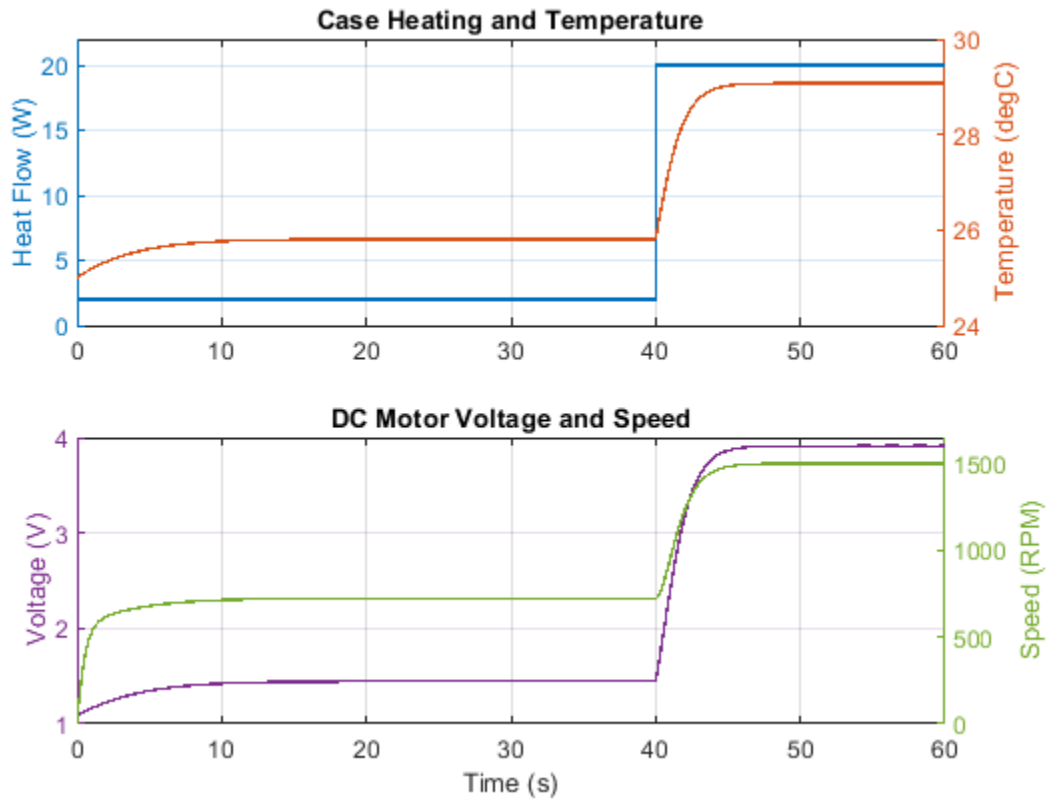
JFET Amplifier and Frequency Response Analysis

This example shows an audio amplifier circuit based on an N-channel JFET. The desired operating point is taken to be Vds=5V, Id=2mA and Vgs=-2V. The manufacturer datasheet gives the JFET forward transfer conductance and output conductance values as 3mS and 50uS. These values are used to populate the mask of the N-Channel JFET block.

The bias resistor values are calculated as follows:

Resistor R1 effectively ties the gate to ground. Hence the voltage across resistor R3 is -Vgs, and must be 2V. Hence R3=-Vgs/Id=2/2e-3 = 1K ohm.

The total voltage across R3, the JFET drain-source connections and R2 must be 15V. Hence the voltage across R2 is 8V if Vds is 5V, and R2=8/2e-3 = 4K ohm.

C3 has to be large enough such that at the lowest frequency of interest (20Hz), it is effectively a short circuit. C4 is chosen so that the loss in gain compared to the mid-band gain is about 6dB.

If you have Simulink® Control Design™, then to plot the frequency response, open the model ee_amp_jfet. On the Apps tab, under Control Systems, click Model Linearizer. In the Model Linearizer, on the Linear Analysis tab, in the Linearize section, click Bode. The linearization points were defined in this model by right-clicking on a Simulink line, and selecting Linearization Points.

Model
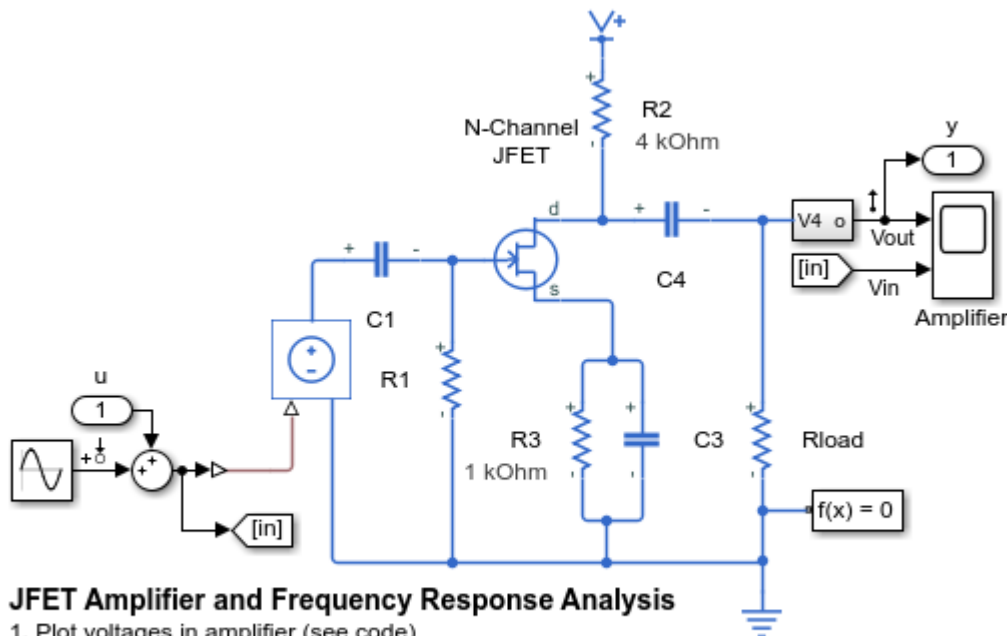
JFET Amplifier and Frequency Response Analysis
1. Plot voltages in amplifier (see code)
2. Linearize circuit to view frequency response (see code)
3. Explore simulation results using sscexplore
4. Learn more about this example
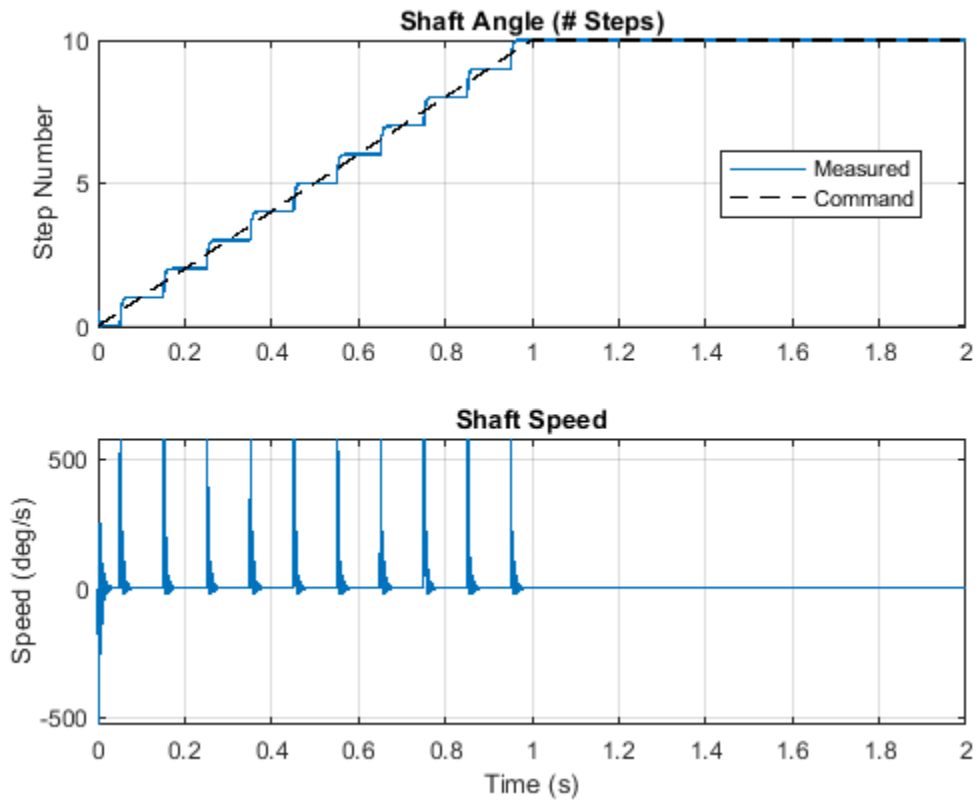
Simulation Results from Simscape Logging

The plots below shows the voltage from the amplifier circuit. The upper plot shows the gain of the amplifier, and the lower plot shows that the JFET is being tested near its desired operating point.

Frequency Response
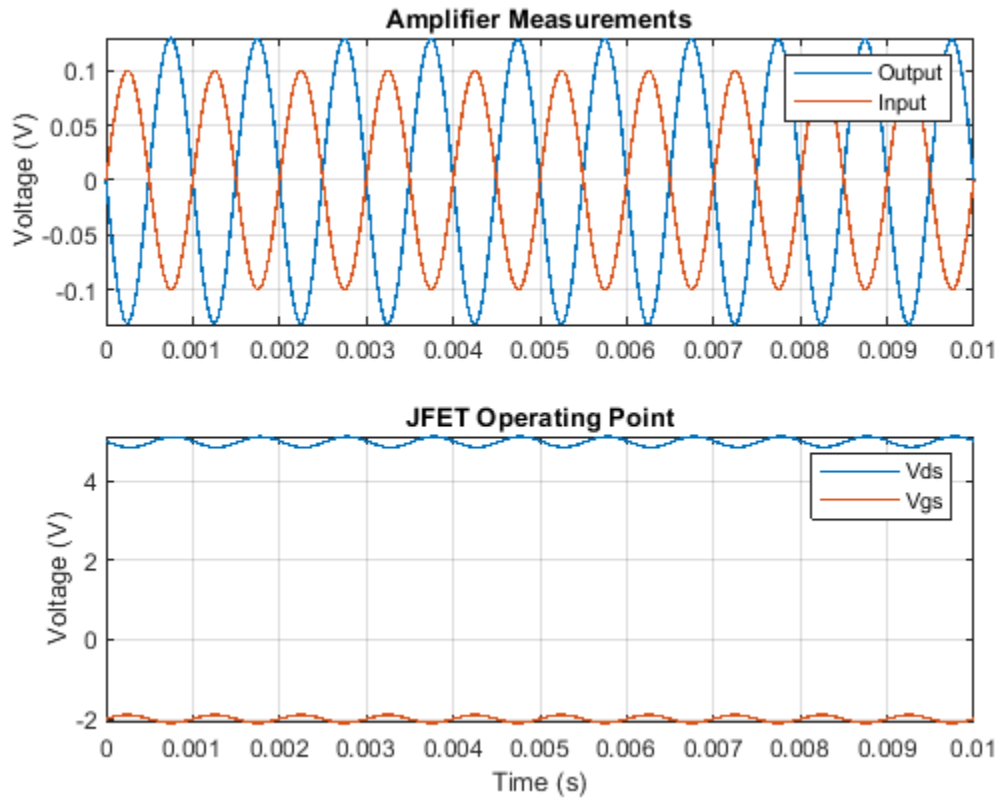
Frequency Response (JFET Amplifier)
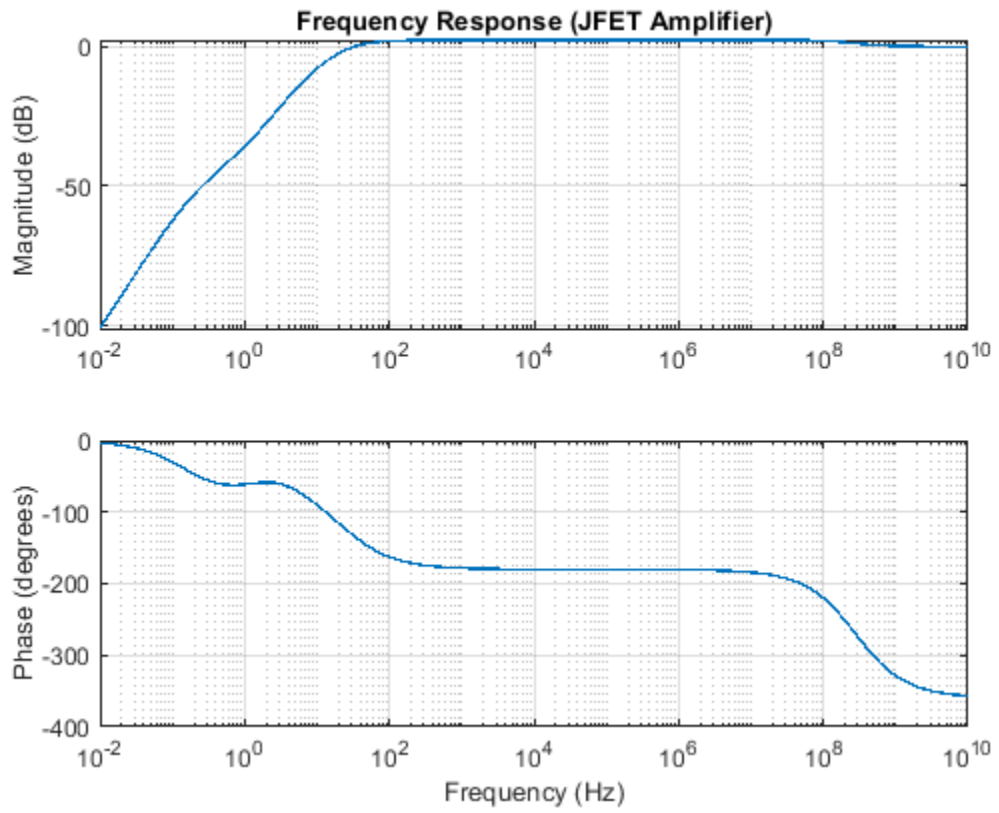
Op-Amp with Noise

This example shows how noise can be incorporated into an electrical simulation. The circuit models an amplifier with gain 100 and a high-frequency roll off frequency of 10MHz. The op-amp adds noise, and it is assumed that the datasheet specifies an equivalent voltage noise density of 20nV/Hz^0.5. This is implemented using the noise voltage source Vn. Optionally, the thermal noise generated by resistors R1 and R2 can also be included by selecting 'Enabled' for the blocks' noise mode. However, running this model with different combinations of noise sources shows that the main source of noise is the equivalent noise voltage.

Model

Op-Amp with Noise
1. Explore simulation results using sscexplore
2. Learn more about this example
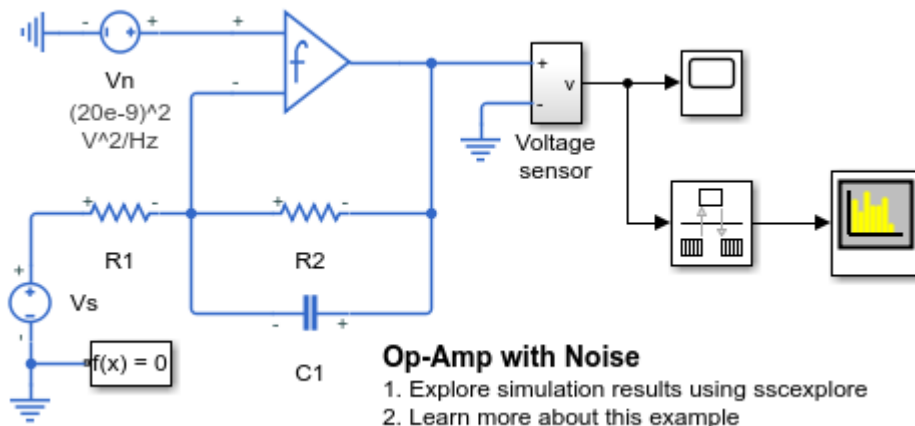
Simulation Results from Spectrum Analyzers
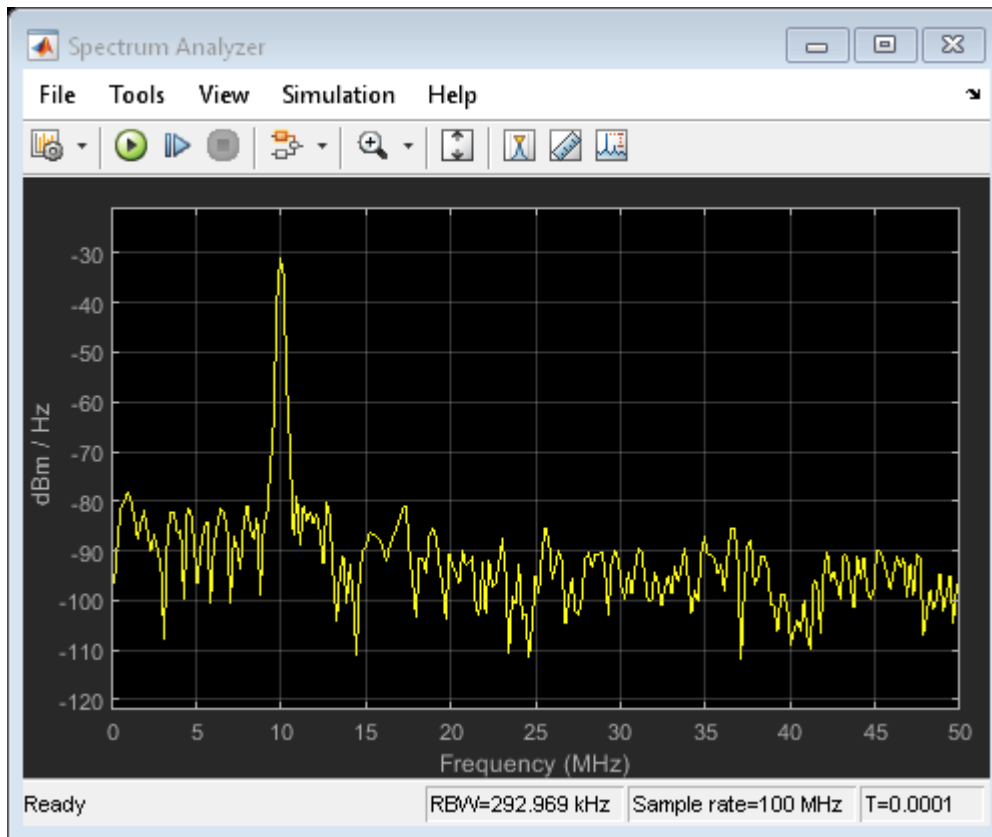
AM Radio Receiver

This example shows a simplified AM radio receiver. A single tone signal at 2kHz is transmitted with a carrier frequency of 600kHz. The variable capacitor, Cres, in the resonant circuit is used in order to sweep through a certain frequency span. When the resonance passes through 600kHz, the signal is picked up and amplified by a two-stage Class A RF power amplifier. The signal is finally extracted by a diode detector, where it would normally be passed on to an audio amplifier (not included here). The Scope displays the final output, the value of the resonant capacitance, and the received and amplified signals.

Model
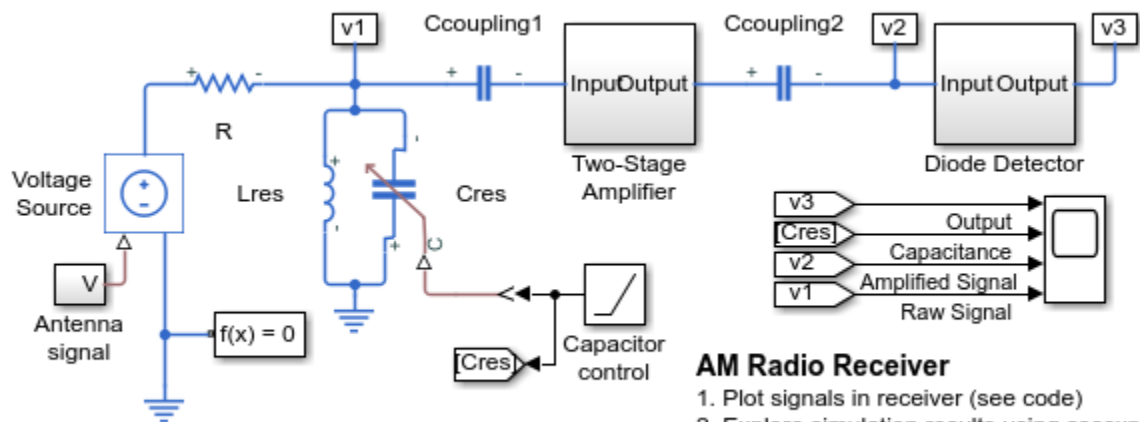
AM Radio Receiver
1. Plot signals in receiver (see code)
2. Explore simulation results using sscexplore
3. Learn more about this example

Diode Detector Subsystem
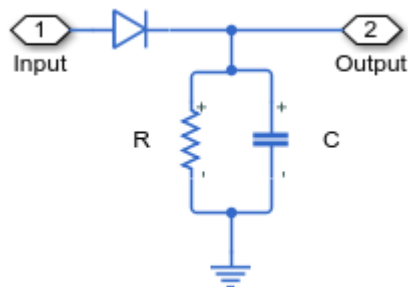
Two-Stage Amplifier Subsystem
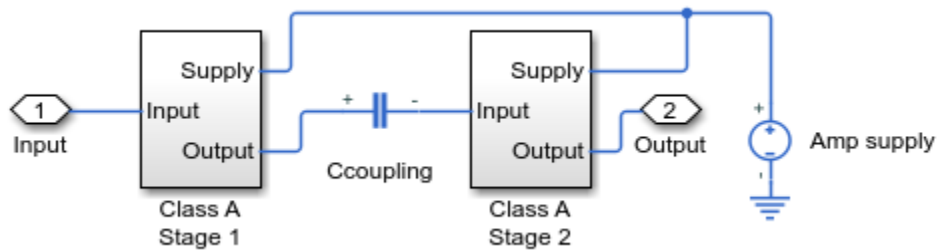
Class A Stage 1 Subsystem
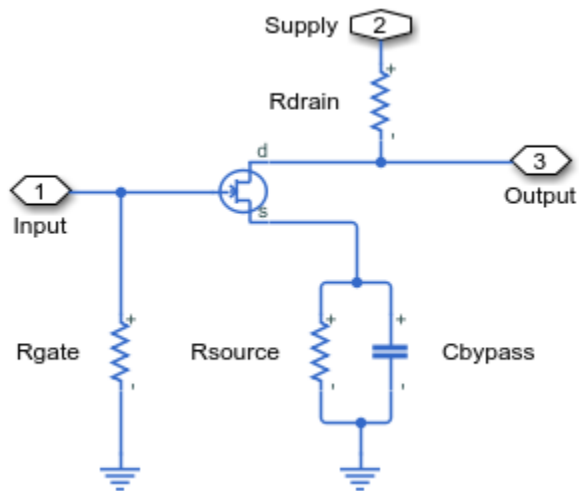
Simulation Results from Simscape Logging

The plots below shows received, amplified, and output signals in the radio receiver. As the resonance in the resonant circuit passes through 600kHz, the signal is picked up and amplified by a two-stage Class A RF power amplifier.
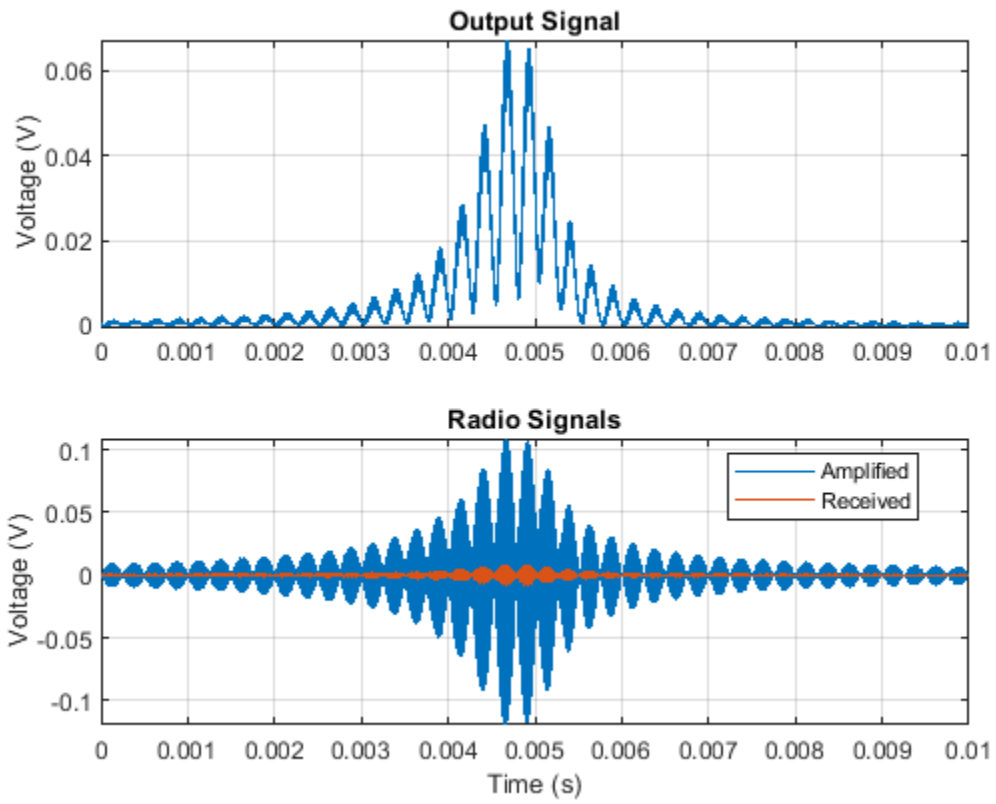
Switching Audio Power Amplifier

This example shows a switching audio power amplifier circuit. Switching audio power amplifiers are more efficient than conventional power amplifiers as switching devices are operating only in the fully-on and fully-off states. This audio power amplifier uses a 1MHz switching frequency and has a PI feedback controller to ensure that output voltage tracks the 2kHz and 2.5kHz sine wave inputs. The power spectrum is plotted in the Spectrum Analyzer, and can be used to inform selection of controller and filter parameters.

Model

Simulation Results from Simscape Logging

The plot below shows the output voltage as compared to the reference voltage. It also shows the dissipated power of the two MOSFETS averaged over 100 PWM cycles.

Simulation Results from Scopes

The power spectrum plotted in the Spectrum Analyzer shows peaks at 2kHz and 2.5kHz, the two components of the reference voltage.

Audio Power Amplifier with H-Bridge

This example shows an H-bridge configuration switching audio power amplifier circuit. Switching audio power amplifiers are more efficient than conventional power amplifiers as switching devices are operating only in the fully-on and fully-off states. This audio power amplifier uses a 1MHz switching frequency and has a PI feedback controller to ensure that output voltage tracks the 2kHz and 2.5kHz sine wave inputs. The power spectrum is plotted in the Spectrum Analyzer, and can be used to inform selection of controller and filter parameters.

Model

Simulation Results from Simscape Logging

The plot below shows the output voltage as compared to the reference voltage. It also shows the dissipated power for the four MOSFETS averaged over 100 PWM cycles.
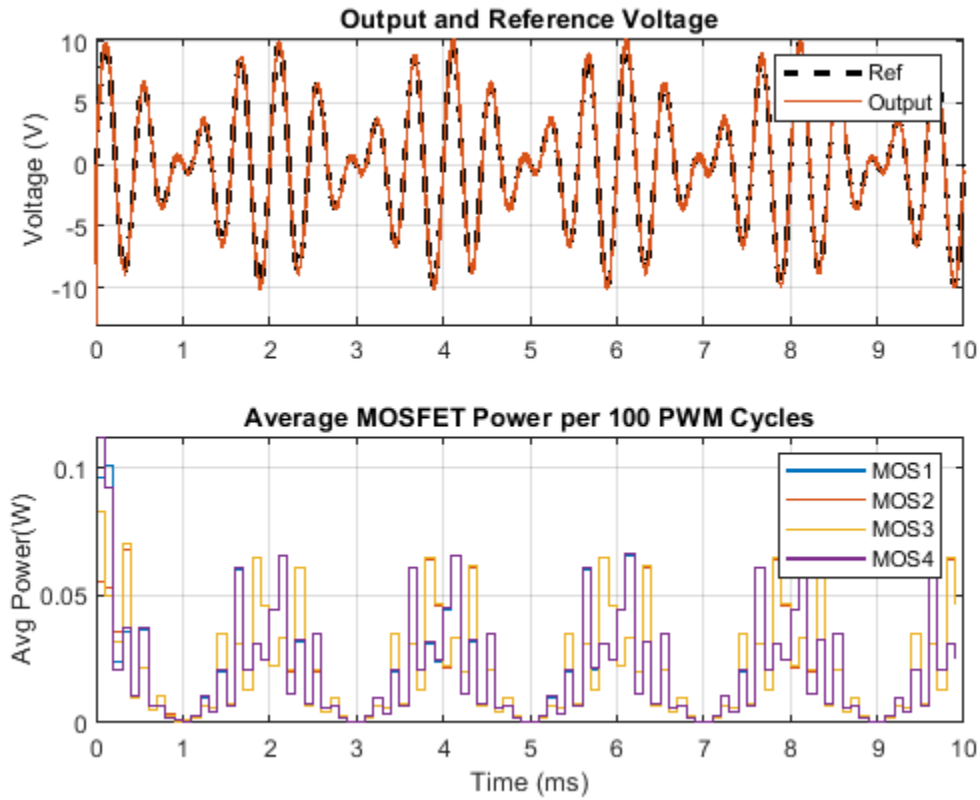
Simulation Results from Scopes

The power spectrum plotted in the Spectrum Analyzer shows peaks at 2kHz and 2.5kHz, the two components of the reference voltage.
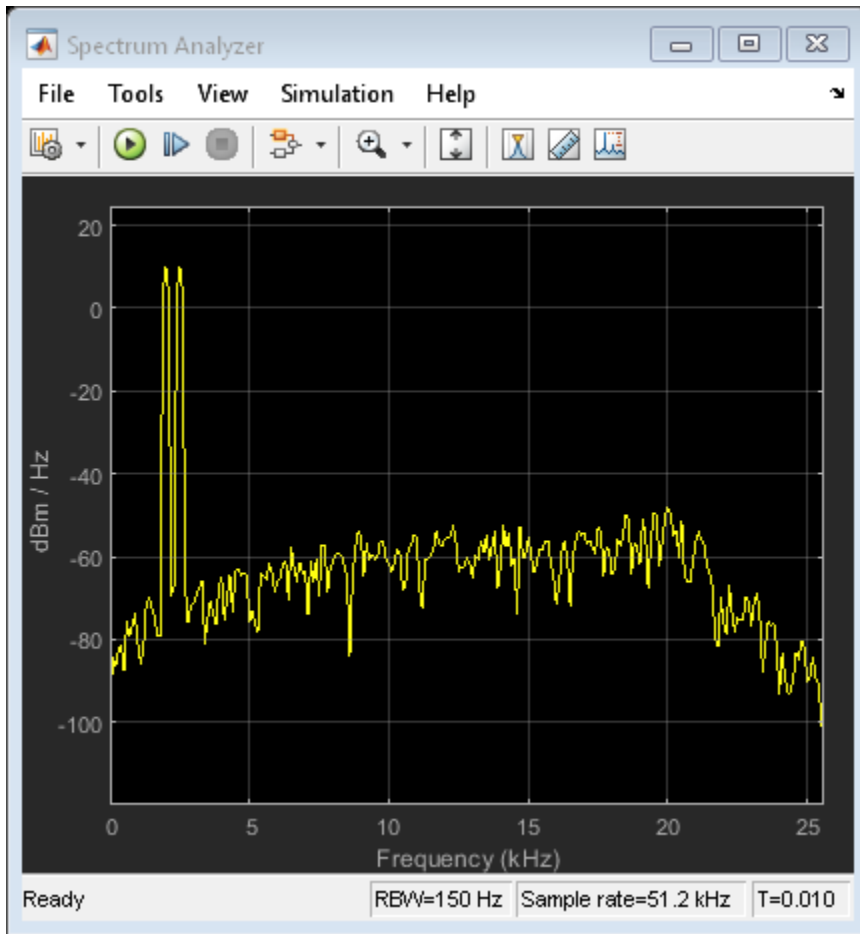
Differential Pair Amplifier

This example shows a differential pair amplifier circuit. The circuit can be used to explore the properties of a differential pair amplifier. The model can be tested using differential and common-mode inputs. The balanced output has zero gain in common-mode provided that the two transistors have identical properties.

Transistor NPN 3 acts as a constant current source that helps stabilize differential-mode gain. The combination of R1, R3 and D1 sets the base voltage to about 1V above the negative power rail. The base-emitter saturation voltage is 0.4V, and hence the voltage across R2 is 0.6V giving an emitter current of 0.6/220=2.7mA. D1 provides temperature compensation, the forward voltage variation with temperature being similar to that of the base-emitter junction. Transistor component values are typical for a BC107, and the diode component values are typical for an IN4148.

This model can be used to obtain the frequency response of the system. The Solver Configuration block option "Start simulation from steady state" is enabled to ensure that the model is linearized about its nominal operating point. MATLAB® command linmod can be used to linearize the model. If you have Simulink® Control Design™, open the model ee_amp_diff_pair. On the Apps tab, under Control Systems, click Model Linearizer. In the Model Linearizer, on the Linear Analysis tab, in the Linearize section, click Bode. The linearization points are defined by right-clicking on a Simulink line, and selecting Linearization Points. If you look at the Sensor block, you will see the linearization output symbol at the output of the PS-Simulink block. Similarly under the Source block you will see a linearization input point symbol on the output of the Sine Wave block.

Model

Differential Pair Amplifier
1. Plot voltages in amplifier (see code)
2. Linearize circuit to view frequency response (see code)
3. Explore simulation results using sscexplore
4. Learn more about this example

Simulation Results from Simscape Logging

The plot below shows the output and input of the differential pair amplifier model. The difference in the base voltages (vb2-vb1) is amplified by more than a factor of 40. The output is measured as the difference between the two collector outputs (vc2-vc1).

Frequency Response

Low-Noise Amplifier

This example shows a typical low-noise audio amplifier circuit. Resistor R2 provides negative feedback to stabilize the overall amplifier gain, making it independent of transistor open-loop forward transfer gain. Circuit gain is approximately defined by (R1+R2)/R1 = 101. The simulation shows that it takes the circuit a few seconds to settle to its steady operating point, and that the output is initially clipped. Input u and output y are included to support linearization.

This model can be used to obtain the frequency response of the system. The Solver Configuration block option "Start simulation from steady state" should be set to ensure that the model is linearized about its nominal operating point. MATLAB® command linmod can be used to linearize the model.

If you have Simulink Control Design, open the model ee_amp_low_noise. On the Apps tab, under Control Systems, click Model Linearizer. In the Model Linearizer, on the Linear Analysis tab, in the Linearize section, click Bode. The linearization points are defined by right-clicking on a Simulink line, and selecting Linearization Points.

Model

Low-Noise Amplifier
1. Plot voltages in amplifier (see code)
2. Linearize circuit to view frequency response (see code)
3. Explore simulation results using sscexplore
4. Learn more about this example
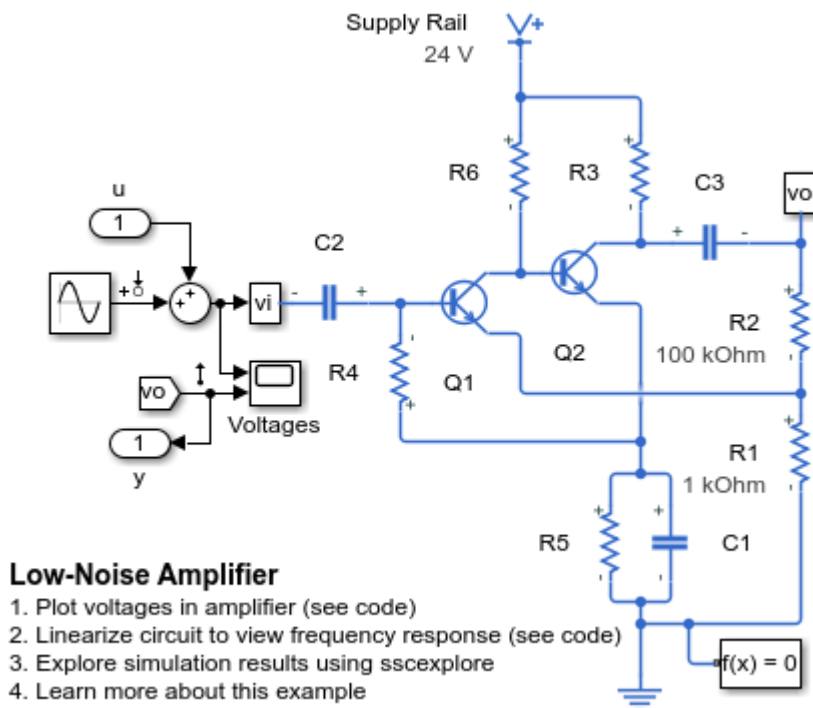
Simulation Results from Simscape Logging

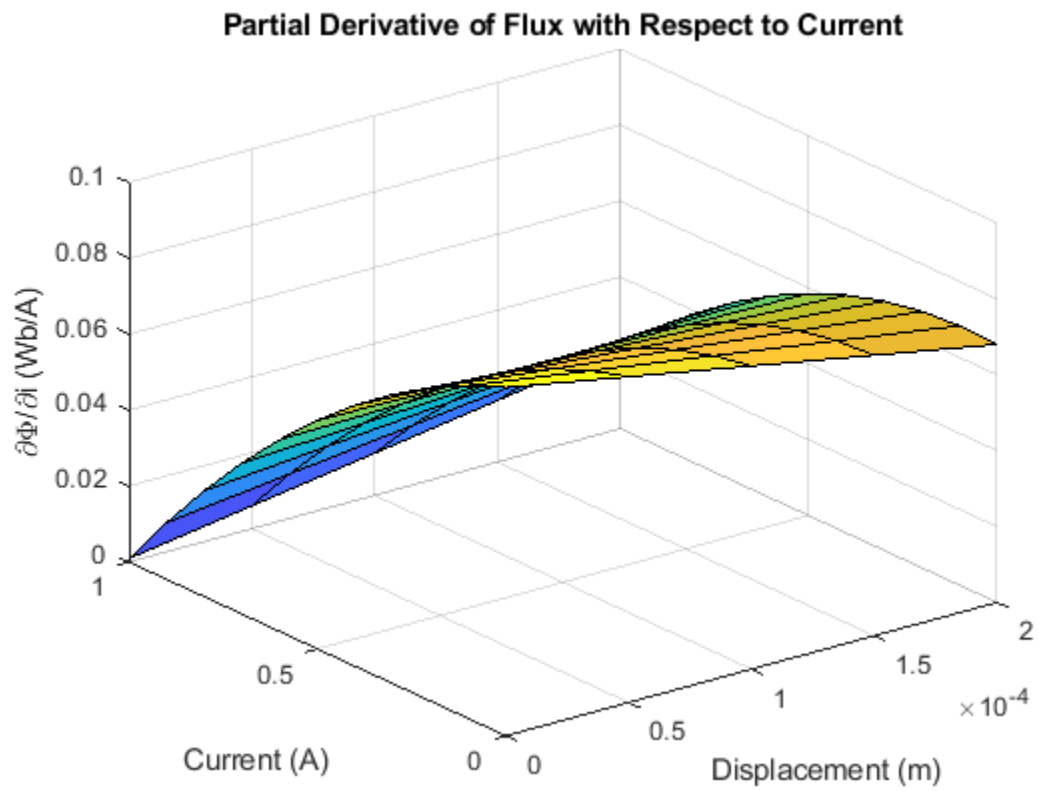This plot shows the amplifier and capacitor voltages. The gain is about 100. For visual comparison, the amplifier subplot shows only the last 10 periods of the simulation. It takes a few seconds for the circuit to settle to its steady operating point due to the charging of the capacitors, shown in the capacitor subplot.

Frequency Response

Frequency Response (Low Noise Amplifier)
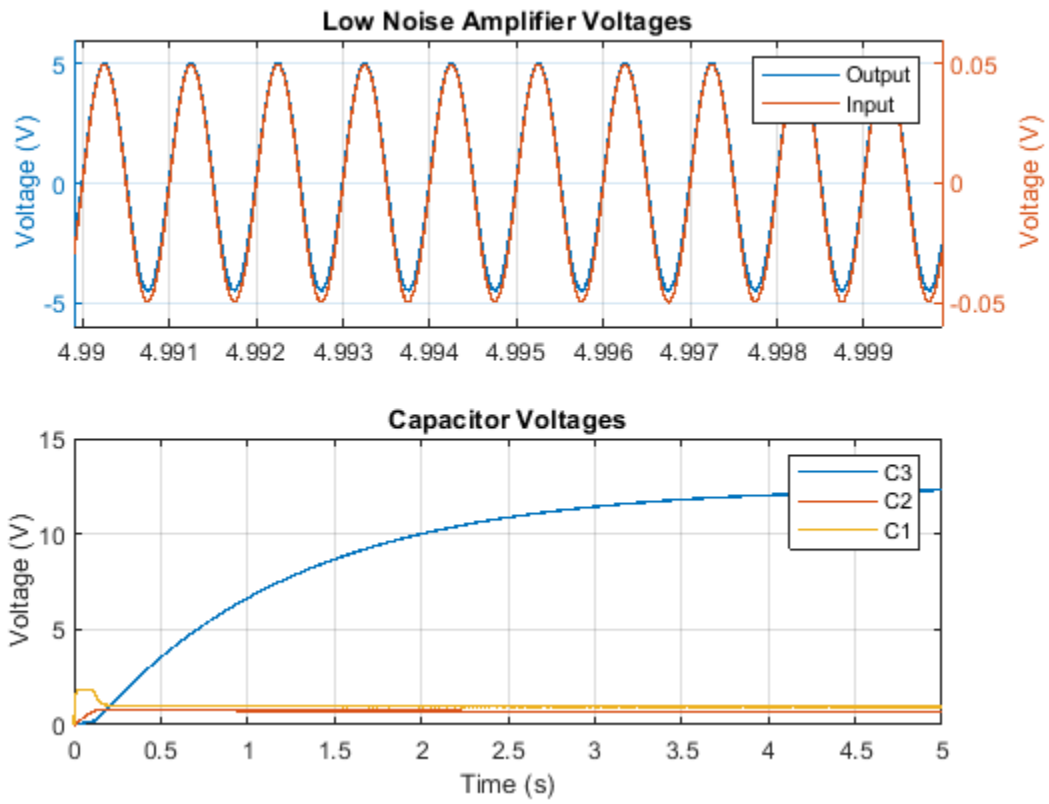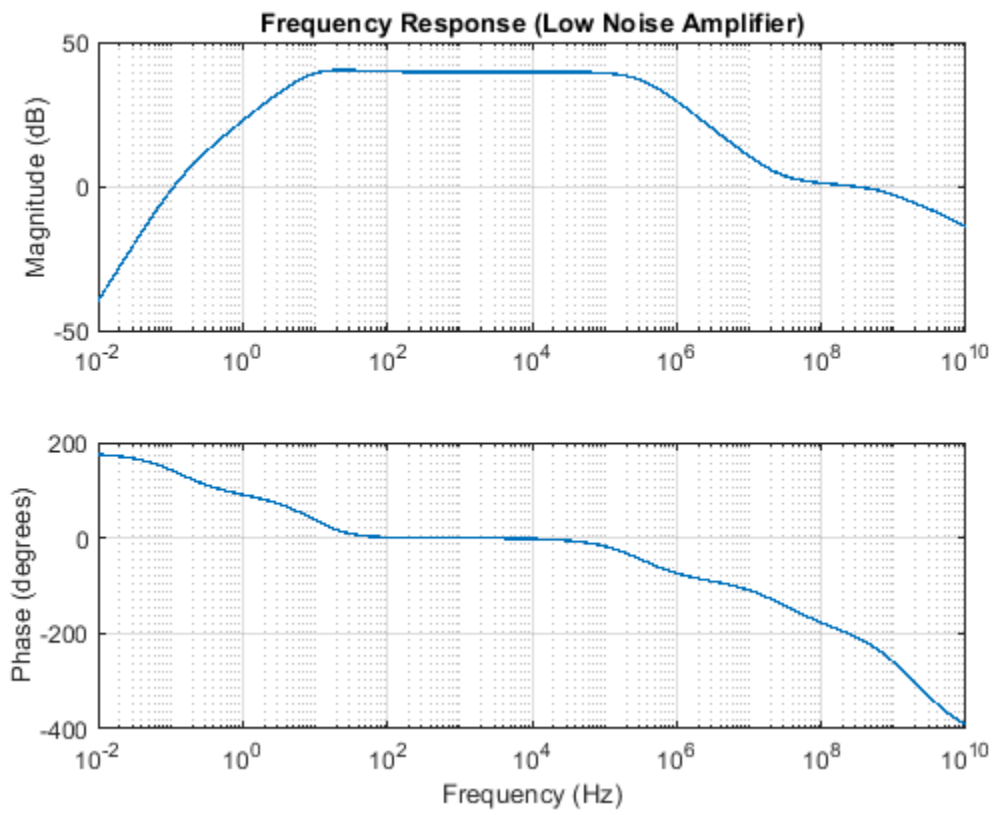
Low-Pass Filter Using Operational Transconductance Amplifiers

This example shows how to model a second-order active low-pass filter. The filter is characterized by the transfer function H(s) = 1 / ((s/w1)^2 + (1/Q)*(s/w1) + 1) where w1 = 2*pi*f1, f1 is the cut-off frequency and Q is the quality factor. Double-click on the Set Design Parameters block to set parameters f1 and Q. The block mask calls a function which sets the parameter values in the model workspace.

This model can be used to generate the filter frequency response. In the ideal case, the gain is zero dB at DC, -20*log10(1/Q) dB at frequency f1, and should attenuate at -12dB/octave at high frequency. The model can be used to determine the impact of impairments, such as finite transconductance gain, on the filter frequency response. Using an operational transconductance amplifier permits digital control of the filter by varying the values of the two current sources.

Model
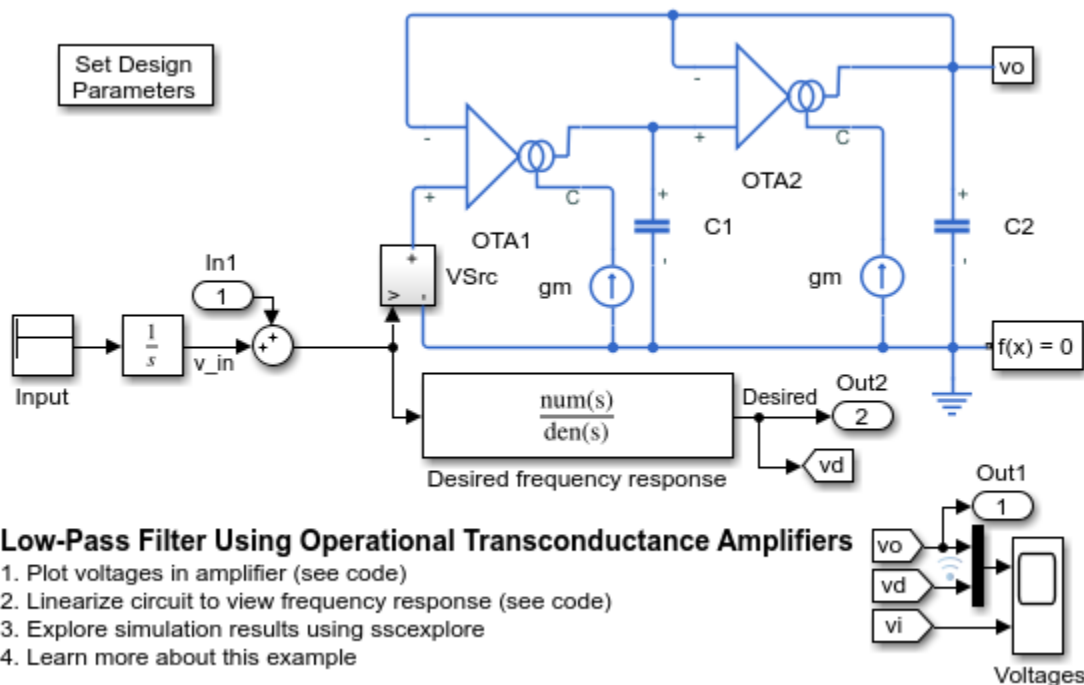
Simulation Results from Simscape Logging

The plot below shows the response of the filter to a brief voltage pulse. This response can be analyzed numerically to determine the frequency response of the filter. The result from the circuit is compared with the result from a transfer function which was specified using the desired frequency response behavior, and we see that the results match nearly perfectly.
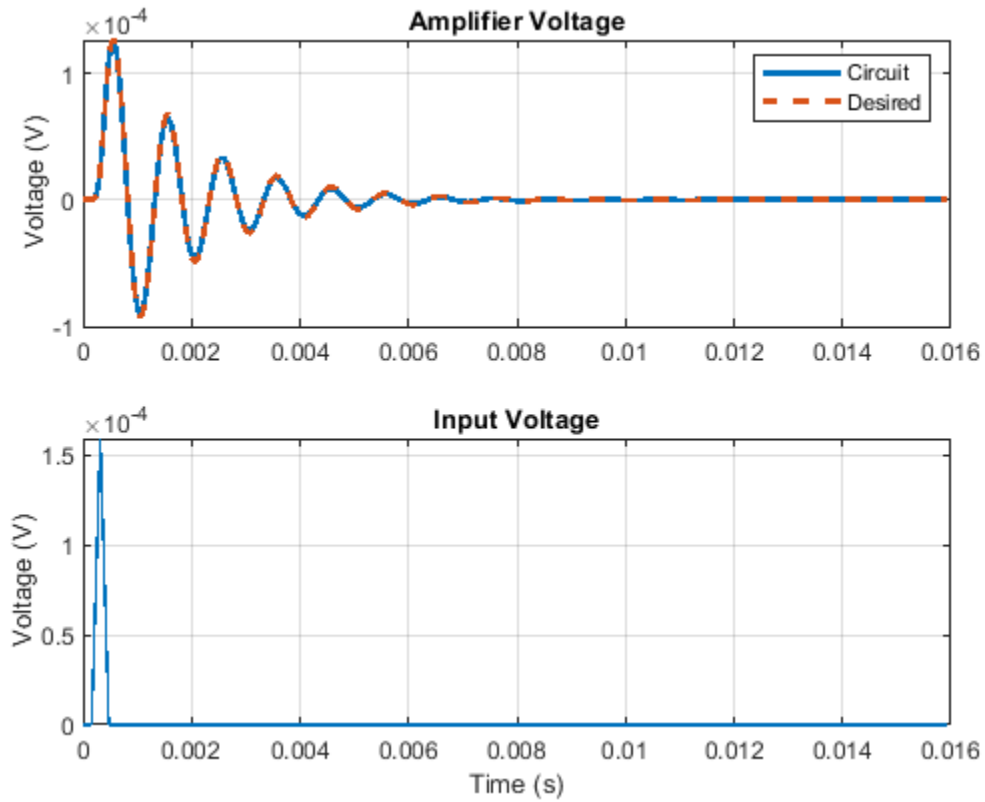
Frequency Response

The plot below shows the frequency response of the filter. The gain is zero dB at DC, -20*log10(1/Q) dB at the cutoff frequency, and attenuates at -12dB/octave at high frequency. The frequency response obtained from the circuit nearly perfectly matches the results from a transfer function specified using the desired frequency response behavior.
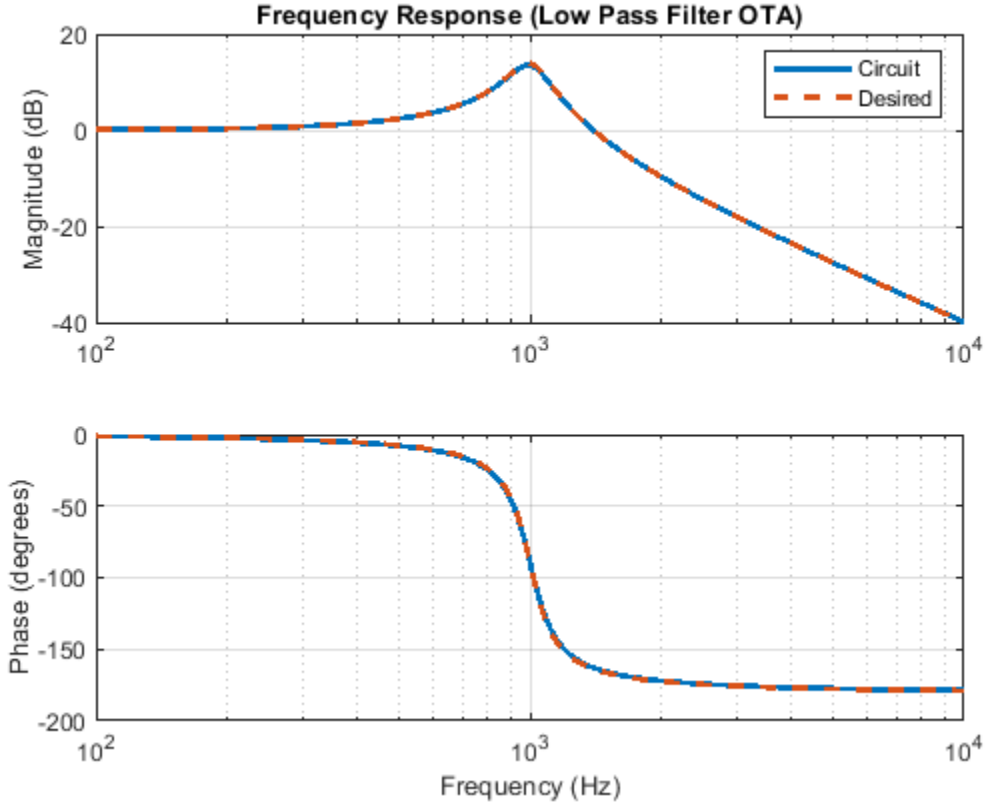
Controllable Phase Shifter

This example shows an implementation of a first order phase shifting filter. The filter is characterized by the transfer function H(s) = (sC - gm1)/(sC + gm1). Double-click on the Set Design Parameters block to set the desired phase shift, amplitude of the input signal, and the frequency of the input signal. The block mask calls a function which sets the parameter values in the model workspace.

This model can be used to generate the filter frequency response. Using operational transconductance amplifiers permits digital control of the phase delay by varying the values of the current source gm1.

Model
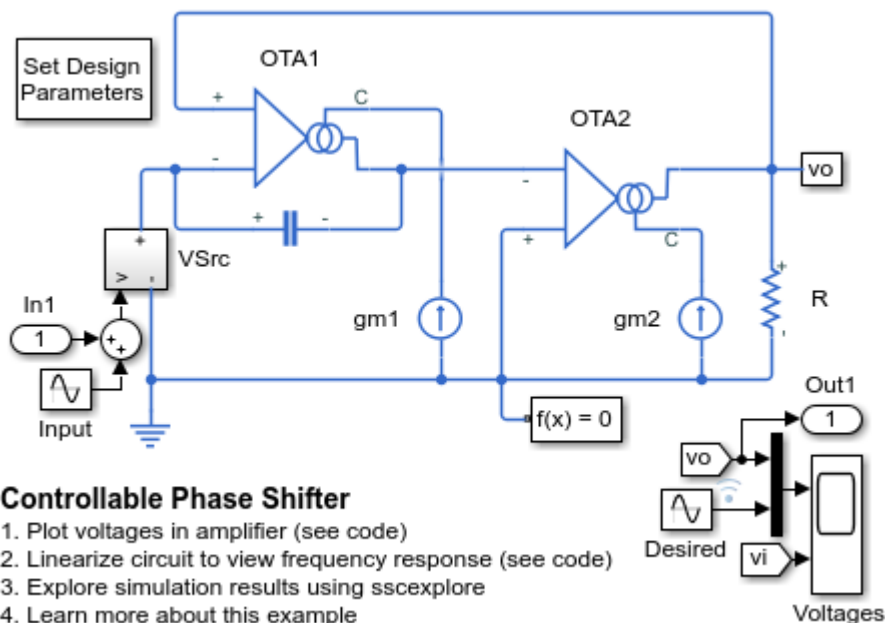
Controllable Phase Shifter
1. Plot voltages in amplifier (see code)
2. Linearize circuit to view frequency response (see code)
3. Explore simulation results using sscexplore
4. Learn more about this example

Simulation Results from Simscape Logging

The plot below shows the input and output voltages of the phase shifting filter. The gain is 1, and the output signal is offset by the specified phase.
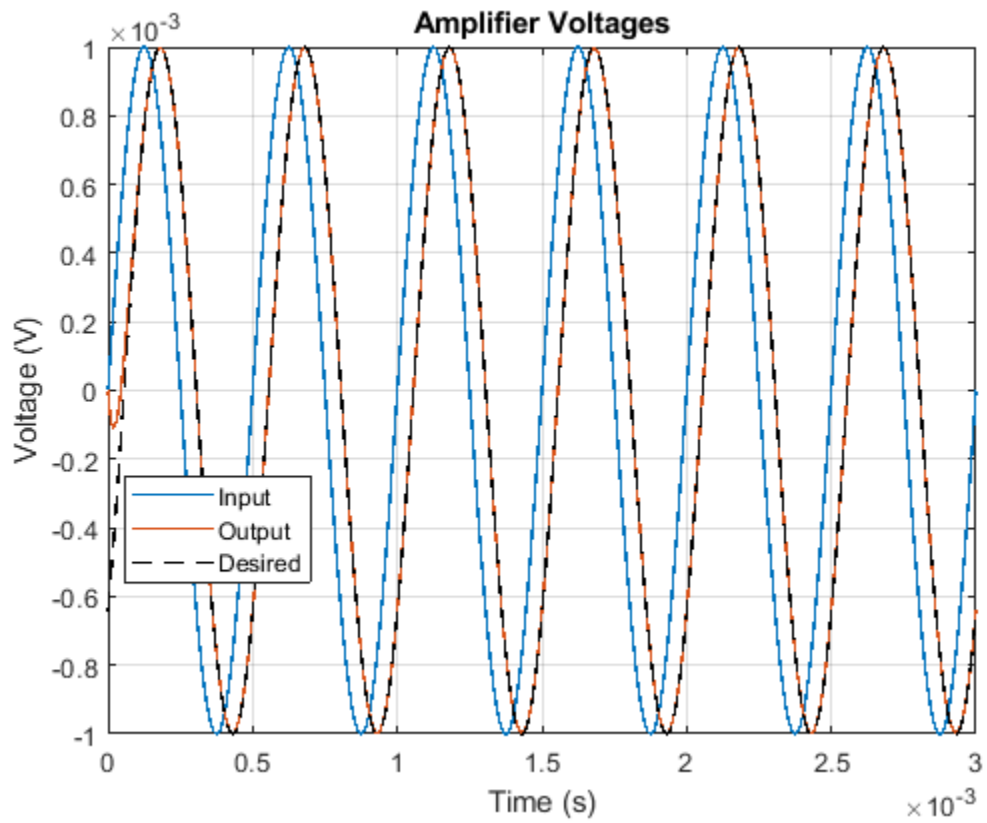
Frequency Response

The plot below shows frequency response of the phase shifting filter. The specified operating condition is shown on the frequency response plot.
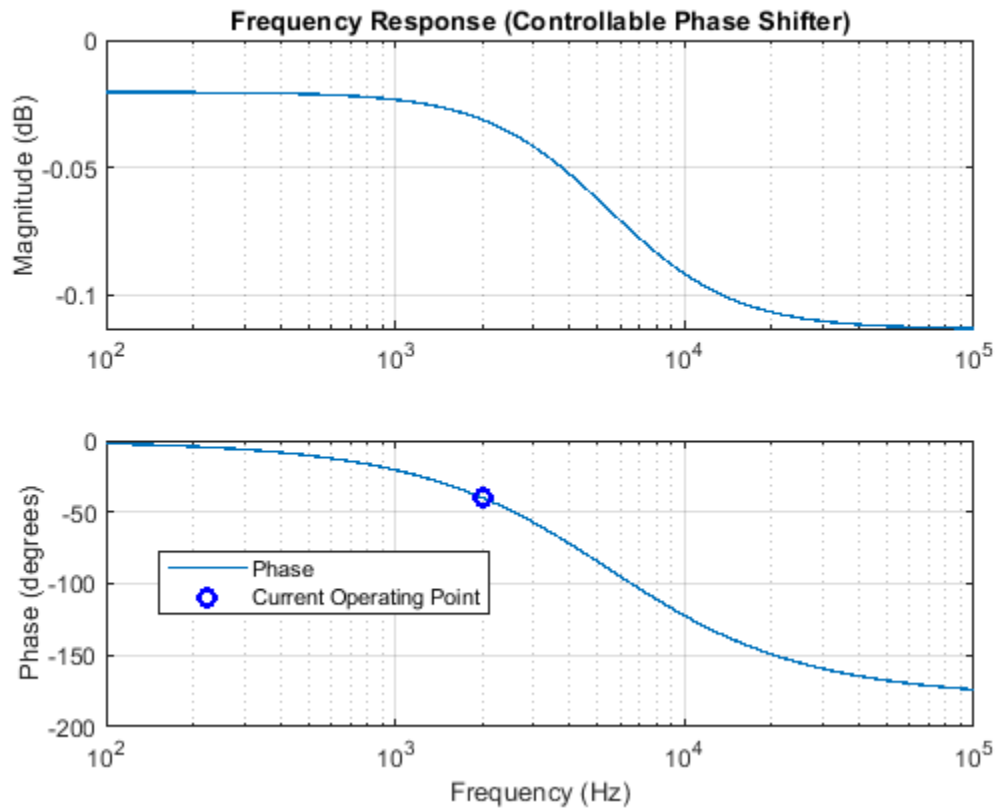
Fourth-Order Sallen-Key Lowpass Filter

This example shows an implementation of a fourth-order Sallen-Key low-pass filter using Operational Amplifiers (OPAs). The filter design parameters, cut-off frequency (f1) and DC gain (K), are specified by double-clicking on the Set Design Parameters block. Pass-band ripple is predefined to be 1dB using a Chebyshev response. The block mask calls a function which sets the parameter values in the model workspace.

This model can be used to generate the filter frequency response. In the ideal case, the gain is equal to 20*log10(K) dB at DC, 20*log(K) at the cut-off frequency, maximum value 20*log10(K) + 1, and -24dB/octave attenuation at high frequency.

Model
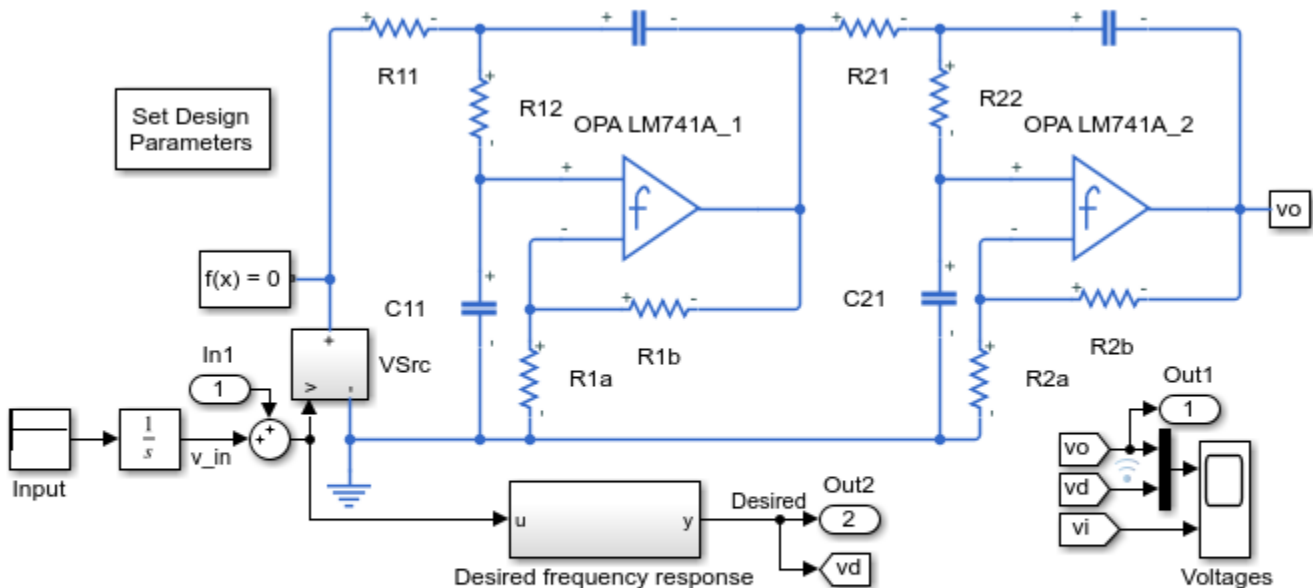
Fourth-Order Sallen-Key Lowpass Filter
1. Plot voltages in circuit (see code)
2. Linearize circuit to view frequency response (see code)
3. Explore simulation results using sscexplore
4. Learn more about this example

Simulation Results from Simscape Logging

The plot below shows the response of the filter to a brief voltage pulse. This response can be analyzed numerically to determine the frequency response of the filter. The result from the circuit is compared with the result from a transfer function which was specified using the desired frequency response behavior, and we see that the results match nearly perfectly.
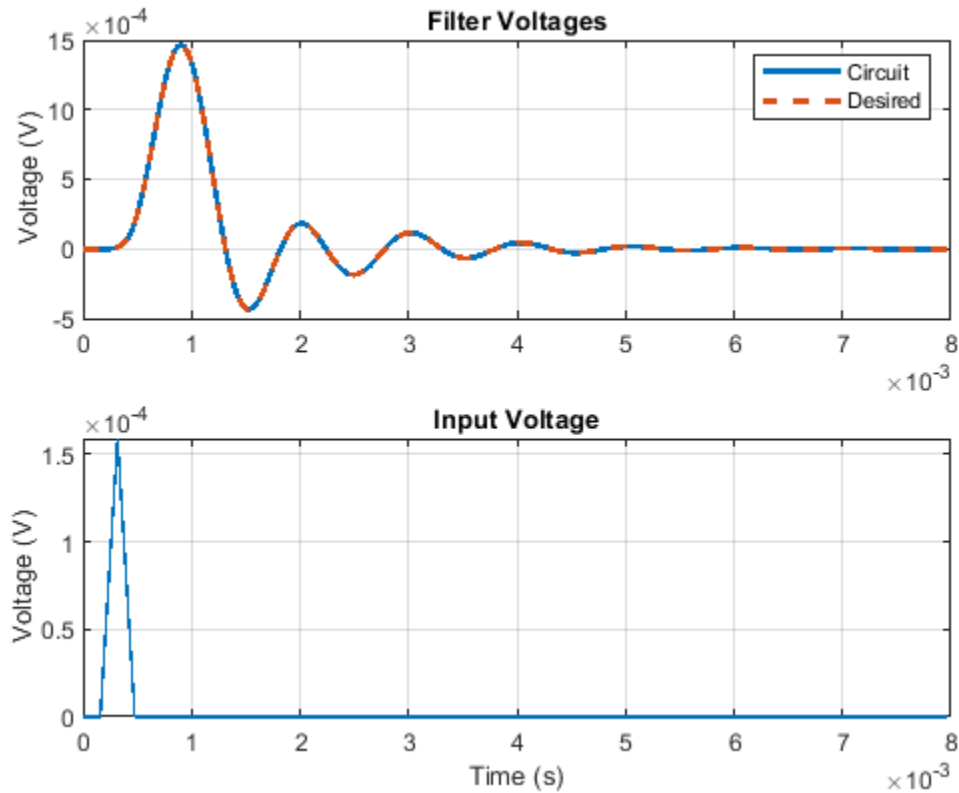
Frequency Response

The plot below shows the frequency response of the filter. The gain is equal to 20*log10(K) dB at DC, 20*log(K) at the cut-off frequency, maximum value 20*log10(K) + 1, and -24dB/octave attenuation at high frequency. The frequency response obtained from the circuit nearly perfectly matches the results from a transfer function specified using the desired frequency response behavior.
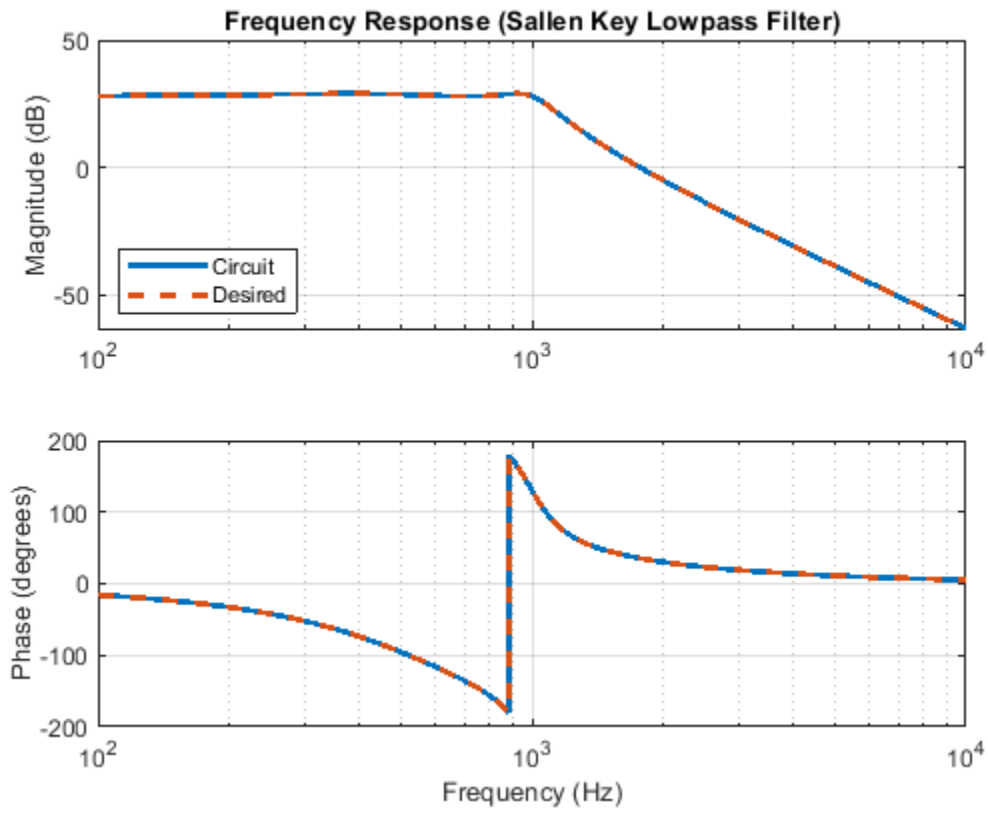
Interactive Generation of MOSFET Characteristics

This example allows the user to explore the impact of parameter choices on the I-V and C-V characteristics for a surface-potential-based MOSFET model. To open this example, in the MATLAB® Command Window, type: ee_mosfet_characteristics.

| Main | Ohmic Resistance | Channel Capacitances | Body Diode | Temperature Dependence |

Primary Effects

Gain: 5 A/V^2   Body factor: 0.5 V^0.5

Flatband voltage: -1.05 V   Surface potential at strong inversion: 0.95 V

Second-Order Effects

Velocity saturation factor: 0 V^-1

Channel-length modulation factor: 0

Channel-length modulation voltage: 0.05 V

Linear-to-saturation transition coefficient: 8

Open Model
Save Parameters
Reset Parameters

Resimulate

Updates trigger resimulation

Vds range: 0 20   Vgs range: -5 5

Vgs steps: 1.5 3   Vds steps: 1 3

Interactive Generation of LDMOS Characteristics

This example allows the user to explore the impact of parameter choices on the I-V and C-V characteristics for a surface-potential-based p-channel LDMOS field-effect transistor model. To open this example, in the MATLAB® Command Window, type: ee_p_ldmos_characteristics.
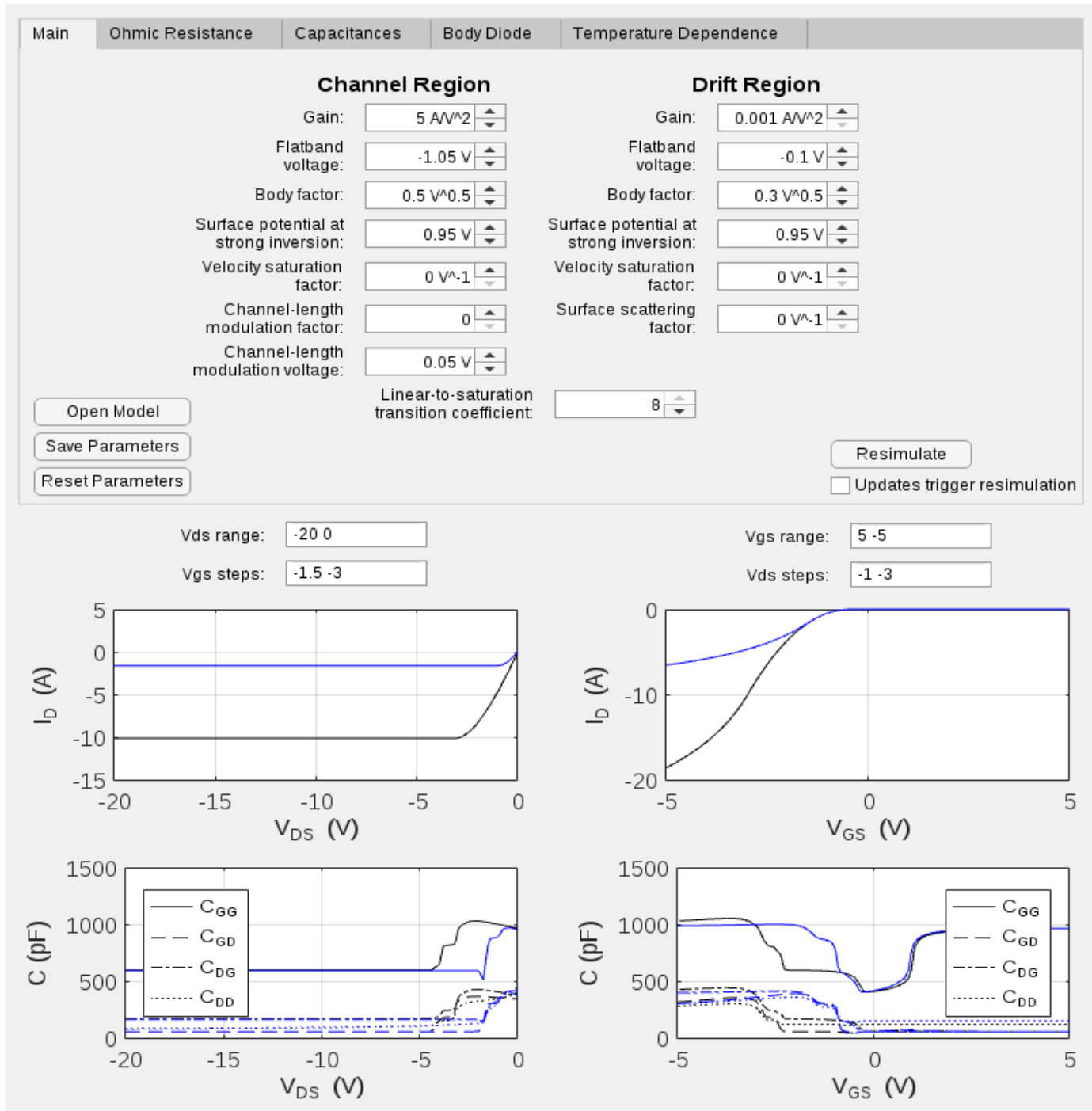
Interactive Generation of Thermal MOSFET Characteristics

This example allows the user to explore the impact of parameter choices on the I-V and C-V characteristics for a surface-potential-based thermal MOSFET model. To open this example, in the MATLAB® Command Window, type: ee_thermal_mosfet_characteristics.
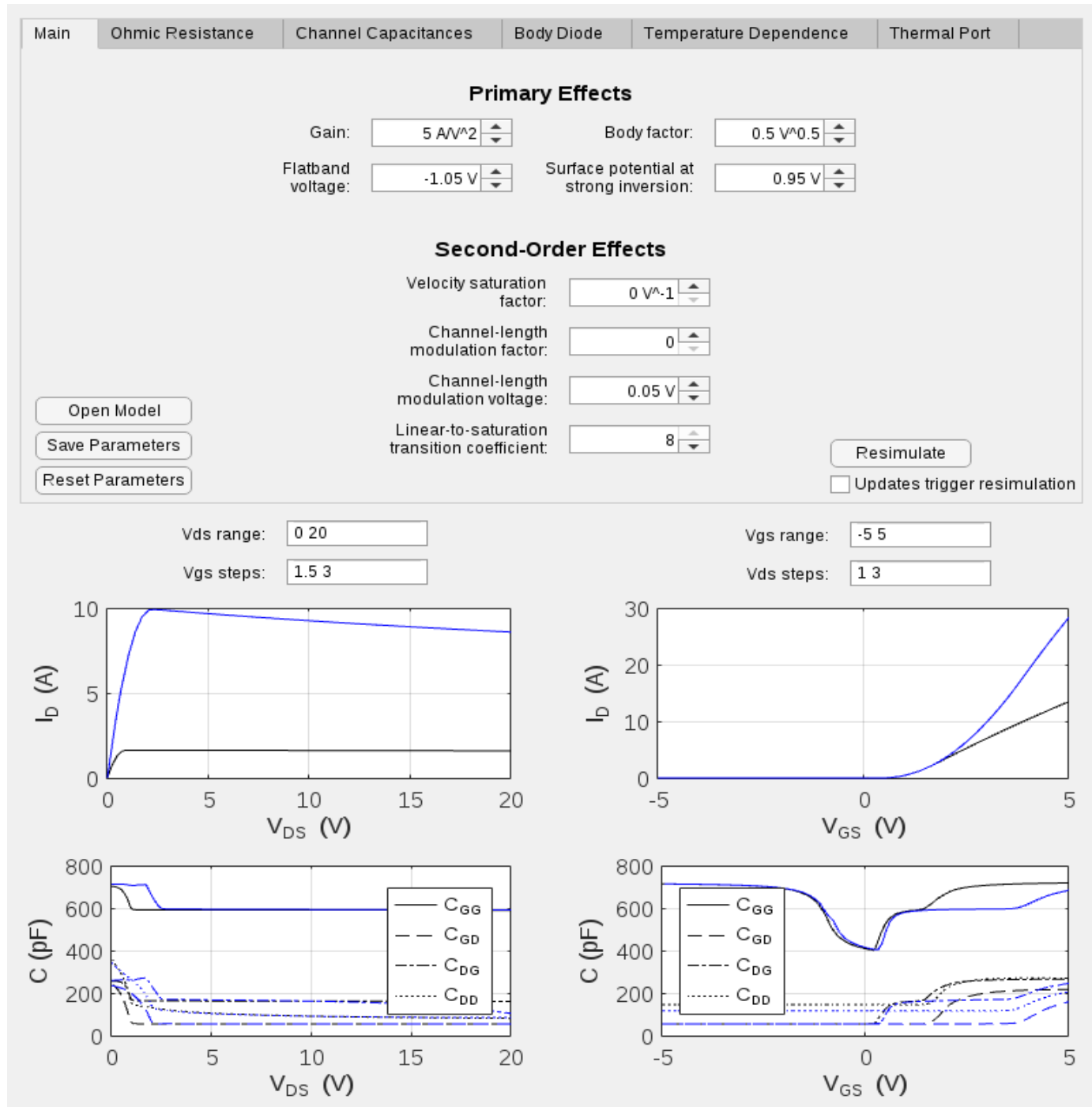
MOSFET Characterization Using Y Parameters

This example shows the generation of I-V and C-V characteristics for an NMOS transistor. Define the bias conditions for the gate-source and drain- source voltage sweeps and the types of plots to be generated by double- clicking the Define Sweep Parameters block. Then click on the "Generate plots" hyperlink in the model . The output capacitance, C_oss, is only shown for sweeps of the drain-source voltage. Note that the C-V characteristics can take several minutes each to generate.

This type of analysis can be used in order to compare against a manufacturer datasheet to confirm a correct implementation of the transistor parameters. This applies to both the DC and AC transistor characteristics. You can also use this model to examine the nonlinear capacitance characteristics as functions of the bias conditions.

Double-click the Analyzer block to see the underlying circuit. It superimposes an AC small-signal voltage on top of a DC bias voltage. The DC characteristics are obtained by filtering out the AC part of the response. The small-signal Y-parameters are obtained by subtracting the DC current from the total (AC+DC) current, and then dividing by the small-signal excitation voltage.

Model
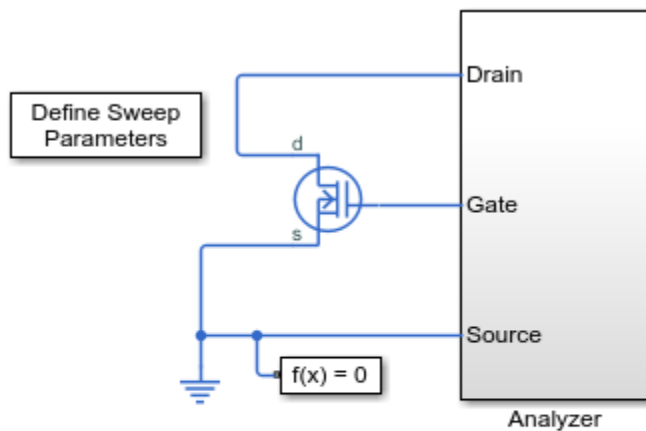
MOSFET Characterization Using Y Parameters
1. Characteristics: Define Sweep, Generate plots (see code)
2. Explore simulation results using sscexplore
3. Learn more about this example

Simulation Results from Simscape Logging

The plots below show Id, Ciss, Crss and Coss as functions of Vgs and/or Vds as appropriate for the parameters chosen. Successive simulations are run for the range of bias conditions defined. In order to handle small-signal measurements, every bias point is left to settle for a certain number of AC cycles before the measurement is recorded for plotting.
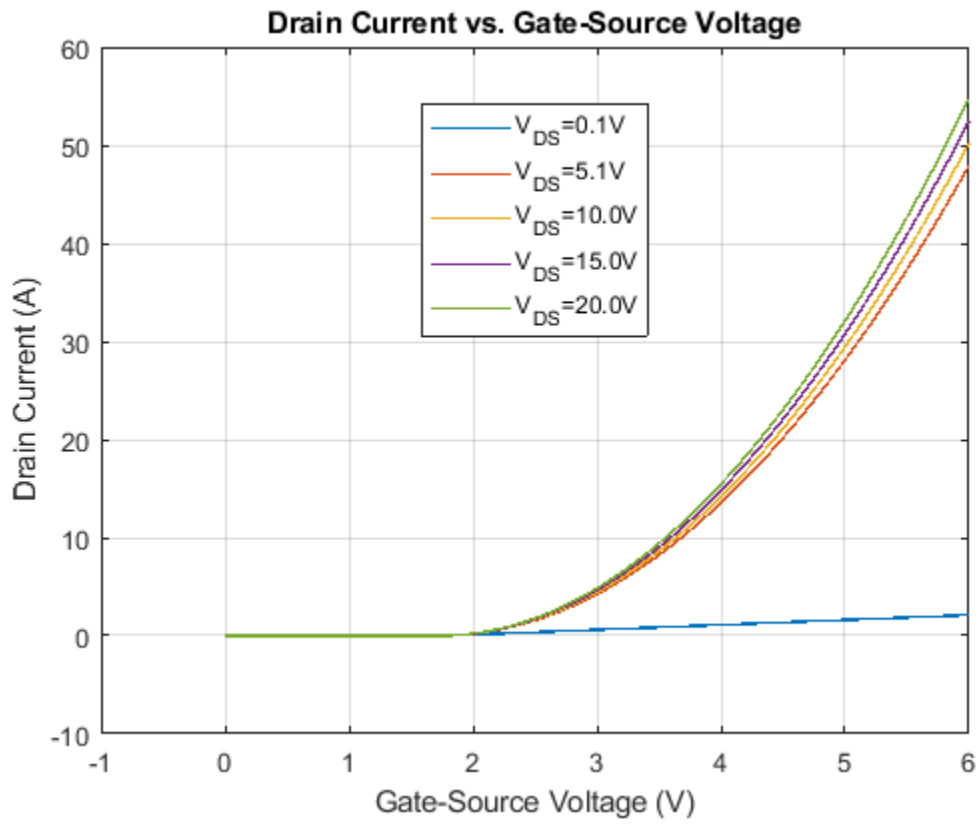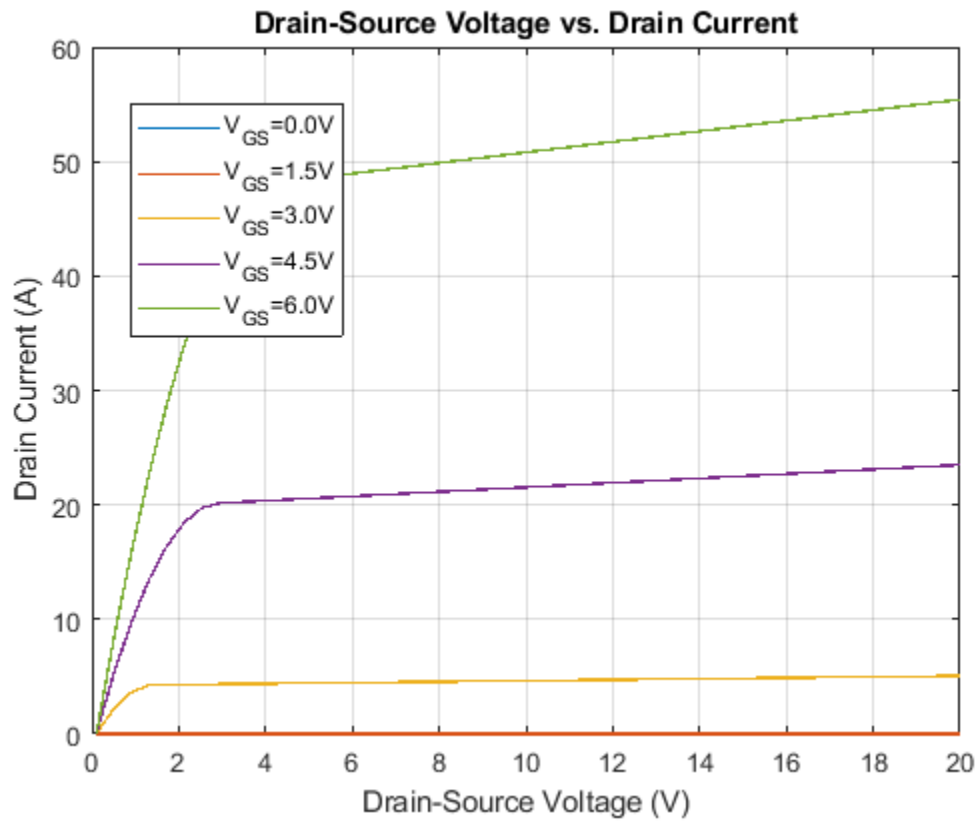
Solar Cell Power Curve

This example shows how to generate the power-voltage curve for a solar array. Understanding the power-voltage curve is important for inverter design. Ideally the solar array would always be operating at peak power given the irradiance level and panel temperature.

Model
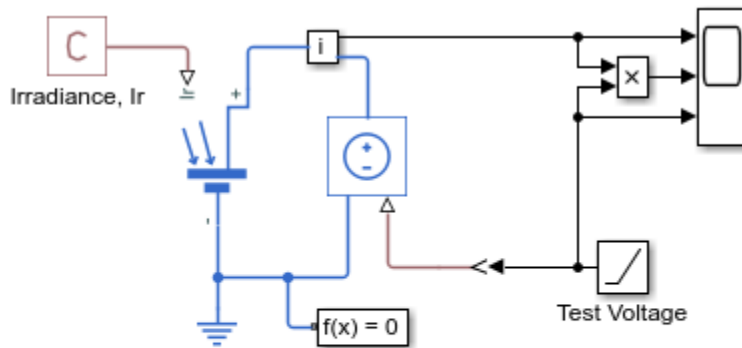
Solar Cell Power Curve
1. Plot power curves at varying conditions (see code)
2. Explore simulation results using sscexplore
3. Learn more about this example

Simulation Results from Simscape Logging

This example shows how to generate the power-voltage curve for a solar array. Understanding the power-voltage curve is important for inverter design. Ideally the solar array would always be operating at peak power given the irradiance level and panel temperature.
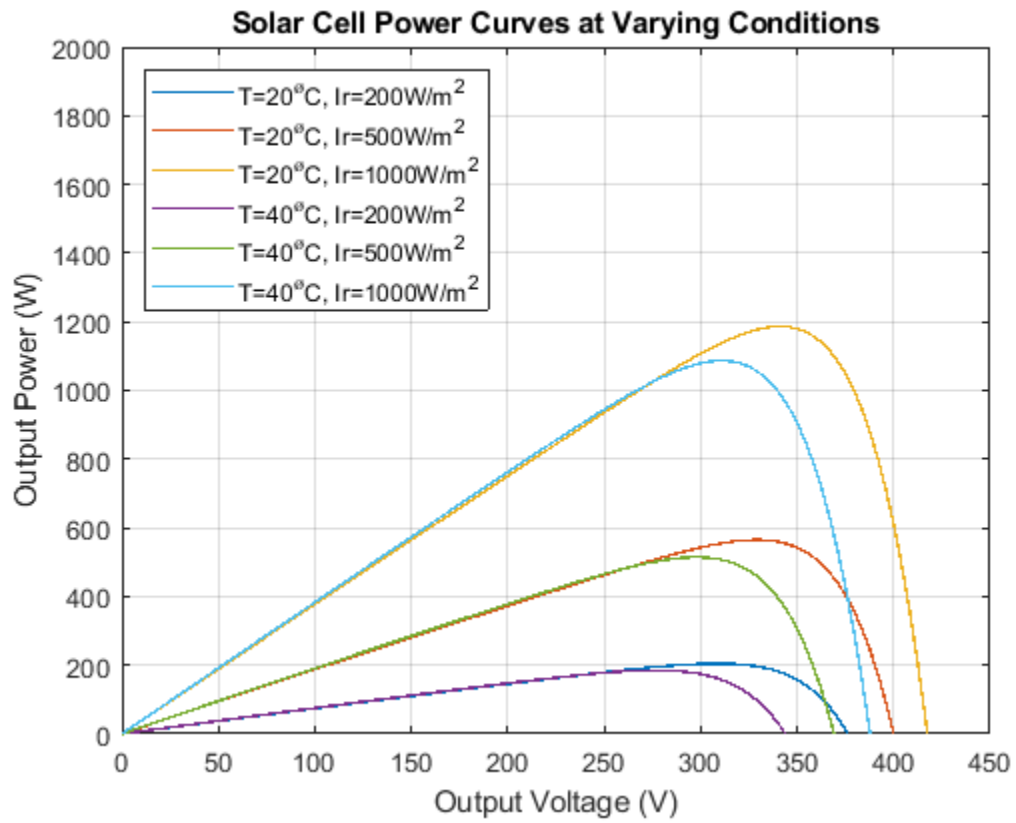
Use of Peltier Device as Thermoelectric Cooler

This example shows a Peltier device working in cooling mode with a hot side temperature of 50 degC. In cooling mode, the Coefficient of Performance (COP) of the Peltier cell is equal to the total heat transferred through the Thermoelectric cooler (TEC) divided by the electric input power, COP = Qc/Pin.

The first subplot shows the COP as a function of current for several temperature differences. It can be observed that, for the same current value, the COP value is lower for larger temperature differences. This happens because at higher temperature differences, the natural heat conduction from the hot side to the cold side tends to be greater.

The temperature source connected to the hot side of the Peltier module allows to represent an ideal heat sink. That is, a sink capable of dissipating an infinite amount of heat without increases in temperature at the hot side. This representation gives us a good approximation for the COP and cooling flow curves.

The second graph shows how the heat flow evolves with the input current. As the current increases, the heat extracted from the cold side increases until it reaches a maximum value. After this peak, due to joule heating, increases in current lead to a decrease in the cooling flow.

The last graph shows the operating current that maximizes the COP value for each temperature difference. This optimum operating point provides useful information when choosing the TEC controller output current.

The Peltier block used in this model is defined by:

- Seebeck coefficient = 220e-6 V/K;
- Internal resistance = 0.02 Ohm;
- Thermal conductance = 1.5e-3 W/K.

Model
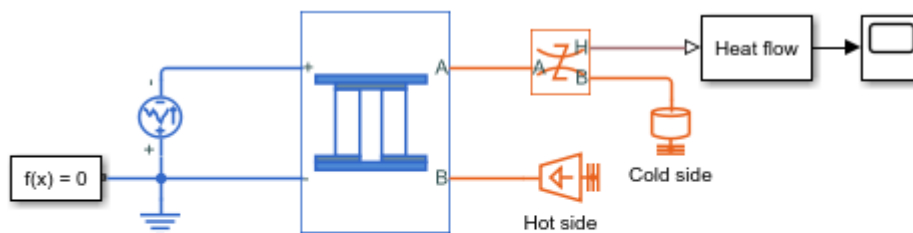
Use of Peltier Device as Thermoelectric Cooler

1. Plot characteristic curves for the Peltier cell (see code)
2. Explore simulation results using sscexplore
3. Learn more about this example

Simulation Results from Simscape Logging

The plot below shows the results for the Peltier Device's coefficient of performance and heat pumping capacity against input current at various temperature differences.
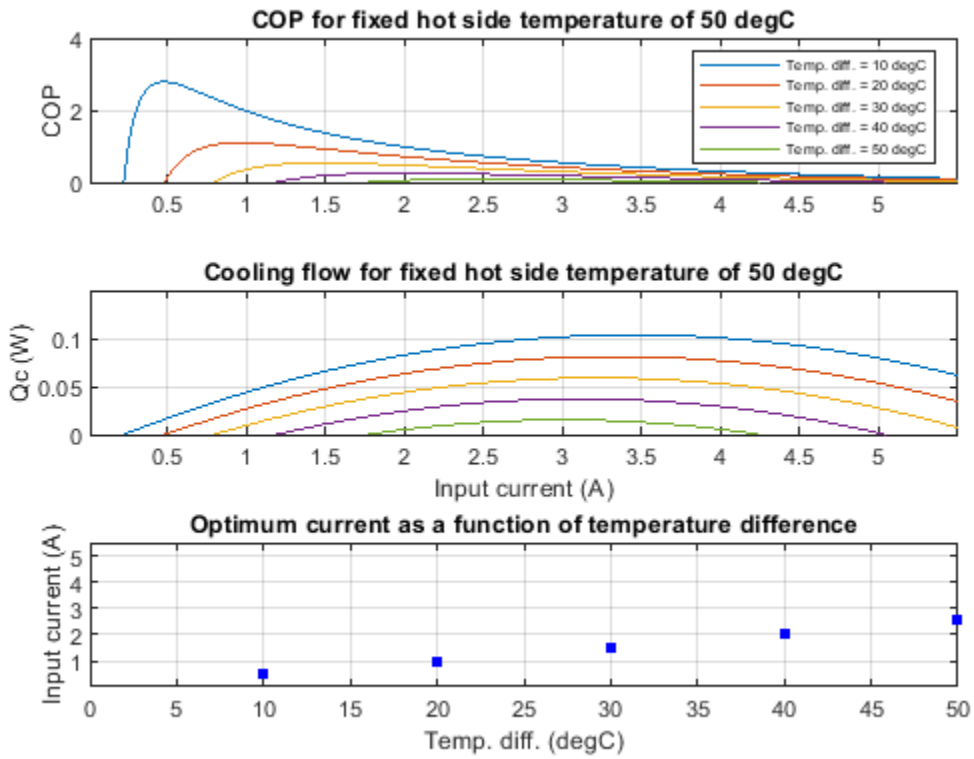
IGBT Characteristics

This example shows generation of the Ic versus Vce curve for an insulated gate bipolar transistor. Define the vector of gate-emitter voltages and minimum and maximum collector-emitter voltages by double-clicking the block labeled 'Define Conditions (Vge and Vce)'. Click on the hyperlink 'plot curves' in the model to run the simulations plot the simulation results.

This type of plot can be compared against a manufacturer datasheet to confirm a correct implementation of the transistor parameters.

Model
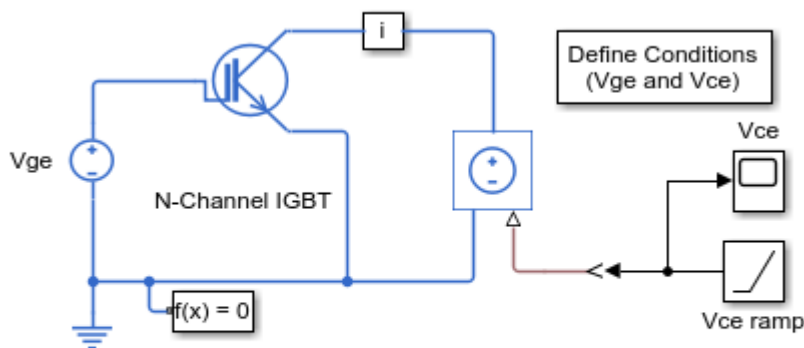
IGBT Characteristics
1. IGBT curves: Define Vge and Vce, plot curves (see code)
2. Explore simulation results using sscexplore
3. Learn more about this example

Simulation Results from Simscape Logging

The plot below shows the collector current vs collector-emitter voltage characteristics for a range of gate-emitter voltages.

IGBT Curves: Ic vs Vce for Varying Gate-Emitter Voltage
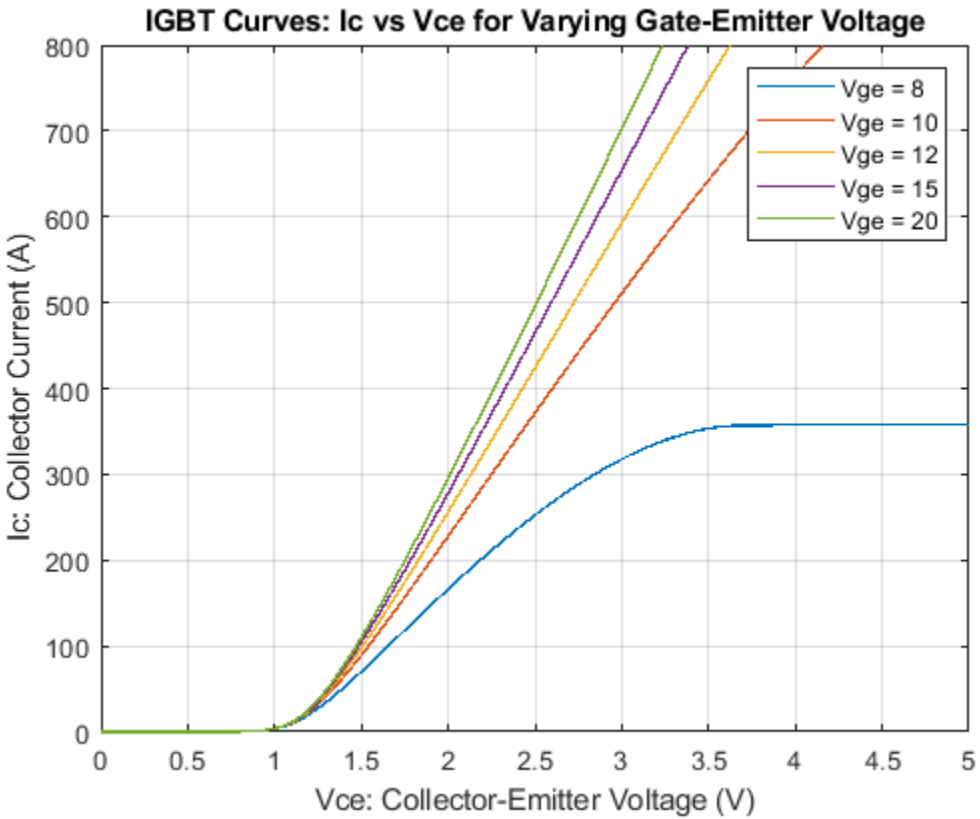
IGBT Thermal Characteristics

This example shows generation of the Ic versus Vce curve for an IGBT at two different temperatures. To generate the plot, click on the hyperlink in the model labeled 'Plot IGBT curves'.

On the plot, the solid lines are from running the model, and the dotted lines are digitized datasheet data. The N-Channel IGBT component has been parameterized using Vce values for when Ic is 400A and Vge is 10V. These two points are intercepted by both model and datasheet plots for Vge=10V. The remaining block parameters must be tuned by simulation so that the datasheet and model plots match as best as is possible. See the N-Channel IGBT block reference page for further information.

Model
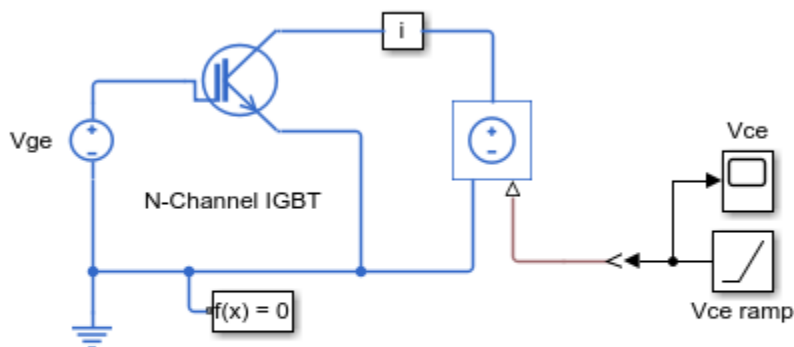
IGBT Thermal Characteristics
1. Plot IGBT curves at two different temperatures (see code)
2. Explore simulation results using sscexplore
3. Learn more about this example

Simulation Results from Simscape Logging

The plot below shows the collector current vs collector-emitter voltage characteristics for a range of gate-emitter voltages. The tests are run at two different temperatures. The results are compared to data points obtained from a manufacturer's data sheet.

You can digitize datasheet information using tools available on MATLAB® Central File Exchange: https://www.mathworks.com/matlabcentral/, such as GRABIT [1].

IGBT Curves: Ic vs Vce for Varying Vge (T = 125 deg C)
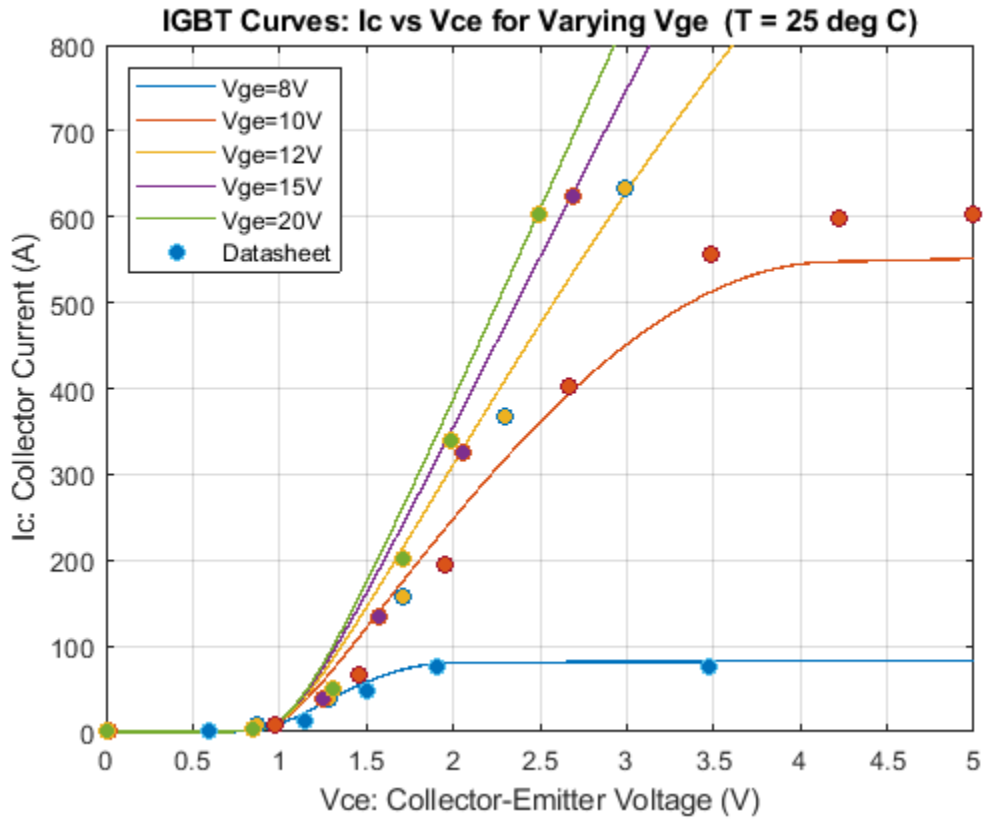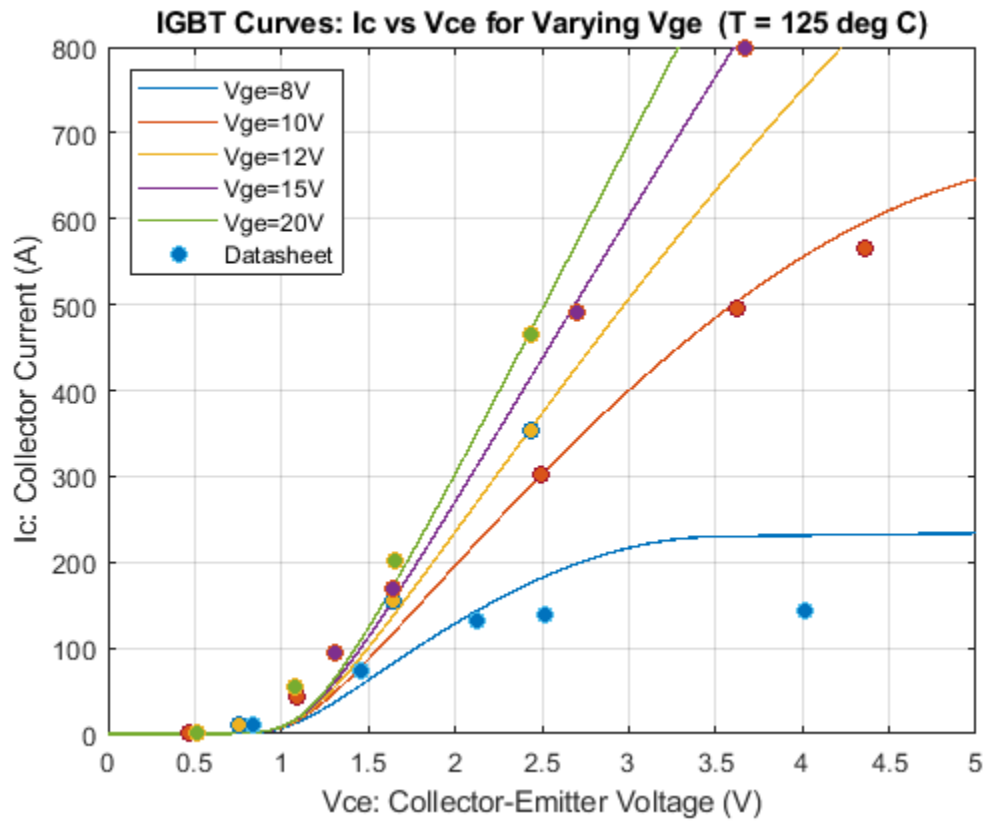
References

1   jiro (2019). GRABIT (https://www.mathworks.com/matlabcentral/fileexchange/7173-grabit), MATLAB Central File Exchange. Retrieved December 16, 2019.

IGBT Dynamic Characteristics

This example shows how the dynamic characteristics of an IGBT depend on its parameters. A prerequisite to matching dynamic characteristics to datasheet values or measured data is to set the parameters defining the static I-V curve. For this, see the 'IGBT Characteristics' example, ee_igbt. With static parameters correctly set, the dynamic parameters can then be set as follows:

1. Set input capacitance, Cies, to datasheet value, or iterate until Vge has the measured rise time up to the Miller length (flat region of Vge at Vge(th)).

2. Set reverse transfer capacitance, Cres, to datasheet value, or iterate until the Miller length matches the measured value.

3. Iterate on the gate-collector oxide capacitance to achieve the measured Vge time constant following the Miller length.

4. Iterate on the total forward transit time, TF, until the off-going collector current, Ic, shows the measured time constant.

Click on the hyperlink 'Plot time responses' in the model to view time responses as a function of parameter values.
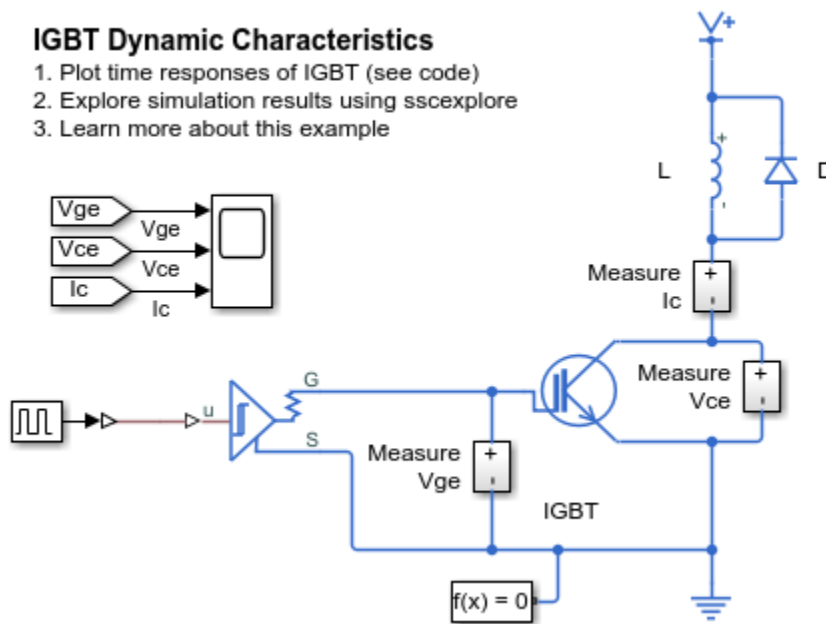
Model

Simulation Results from Simscape Logging

The plots below show the effect of changing IGBT mask parameters Cres, Cox and forward transit time.
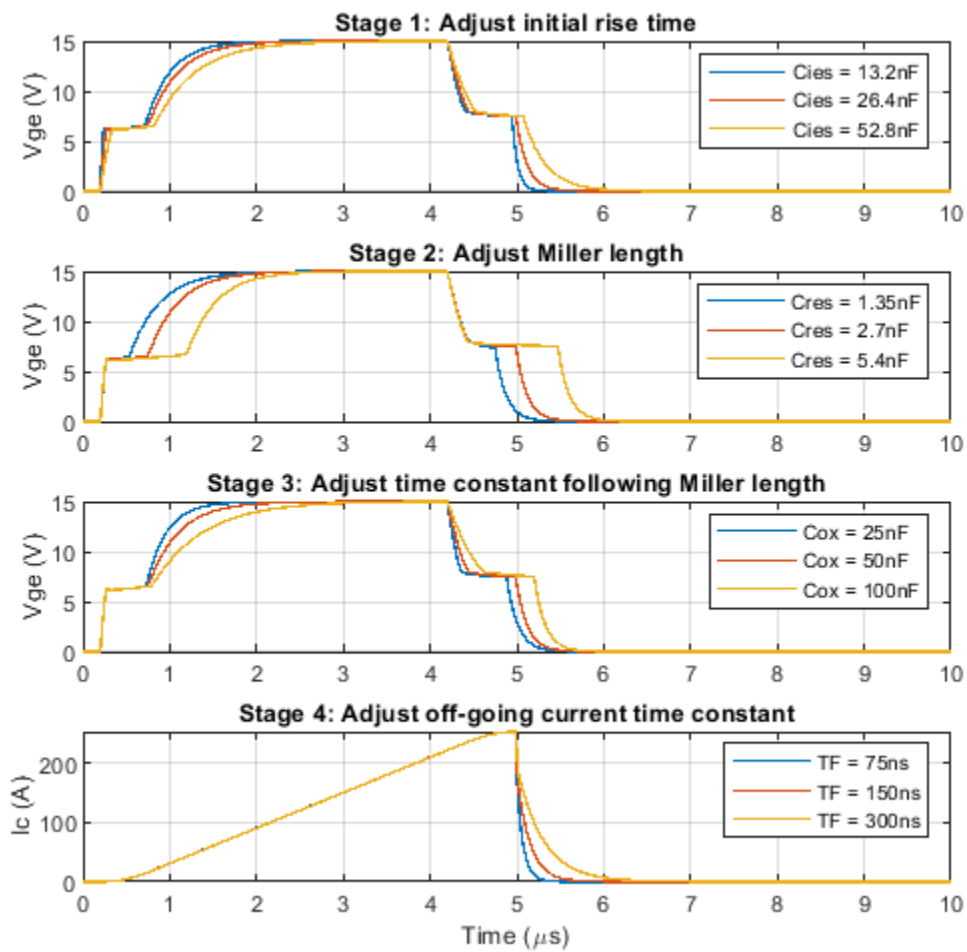
IGBT Behavioral Model

This example test harness can be used to validate the 'Simplified I-V characteristics and event-based timing' variant of the Simscape™ Electrical™ N-Channel IGBT. This variant only requires I-V data corresponding to the on-state gate voltage, and models turn-on rise time and turn-off fall time by making collector-emitter voltage a linear function of simulation time. Advantages of this approach are faster simulation and easier parameterization.

The test harness can be used to demonstrate that timing characteristics for this variant are very similar to those for the 'Full I-V and capacitance characteristics' variant. To select between variants, right-click on the N-Channel IGBT block, and select 'Simscape block choices'.

Both variants have been parameterized from the same datasheet. The only event-based variant parameter that requires tuning is the Miller resistance. This is adjusted so that the current spike matches when turning on a load with pre-existing freewheeling current.

Model
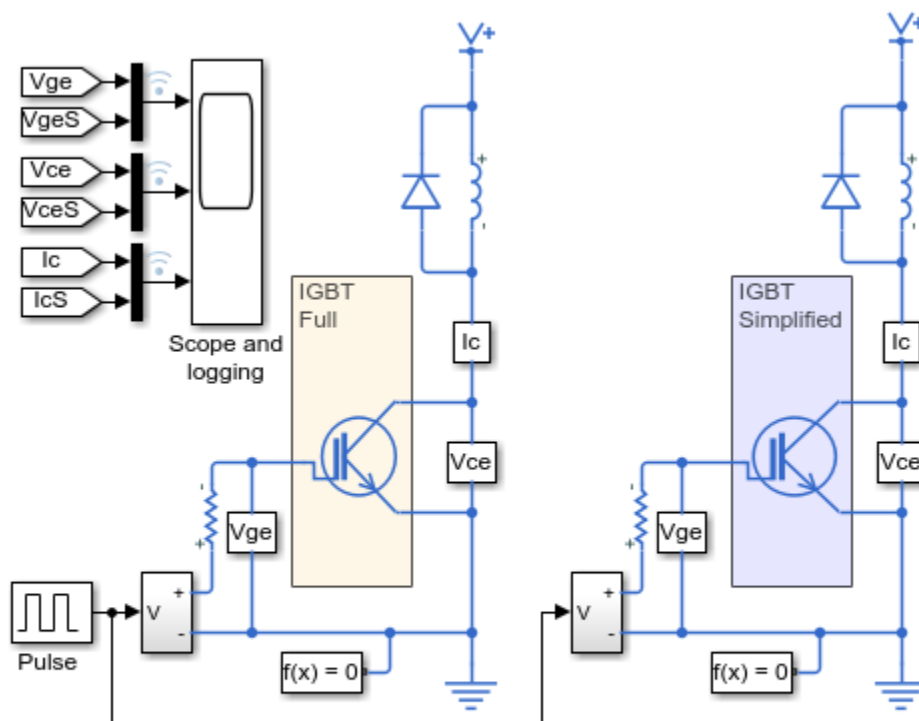
IGBT Behavioral Model
1. Plot voltages to compare the two IGBT models (see code)
2. Explore simulation results using sscexplore
3. Learn more about this example

Simulation Results from Simscape Logging

The plots below compare the behavior of two of the IGBT models available in Simscape Electrical. The simplified variant provides similar behavior as the full variant, and has been tuned so that the current spike matches when turning on a load with pre-existing freewheeling current.
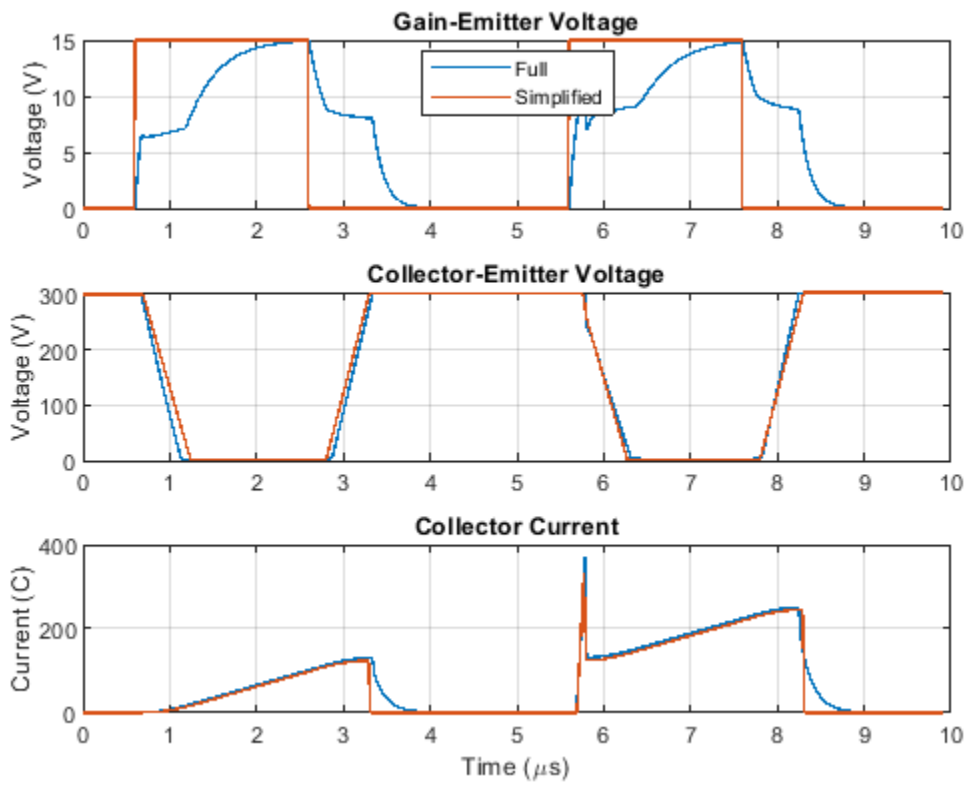
IGBT Loss Characteristics

This example shows how to use Simscape™ Electrical™ detailed switching device models to create tabulated switching loss data. This tabulated data can then be used with the piecewise linear switching device component models to predict total losses in system models configured for fast and/or fixed-step simulation.

Model

Simulation Results from Simscape Logging

The plots below show the turn-on and turn-off switching losses as a function of load current for a range of device temperatures.

Switching Losses vs. Load Current per Device Temperature
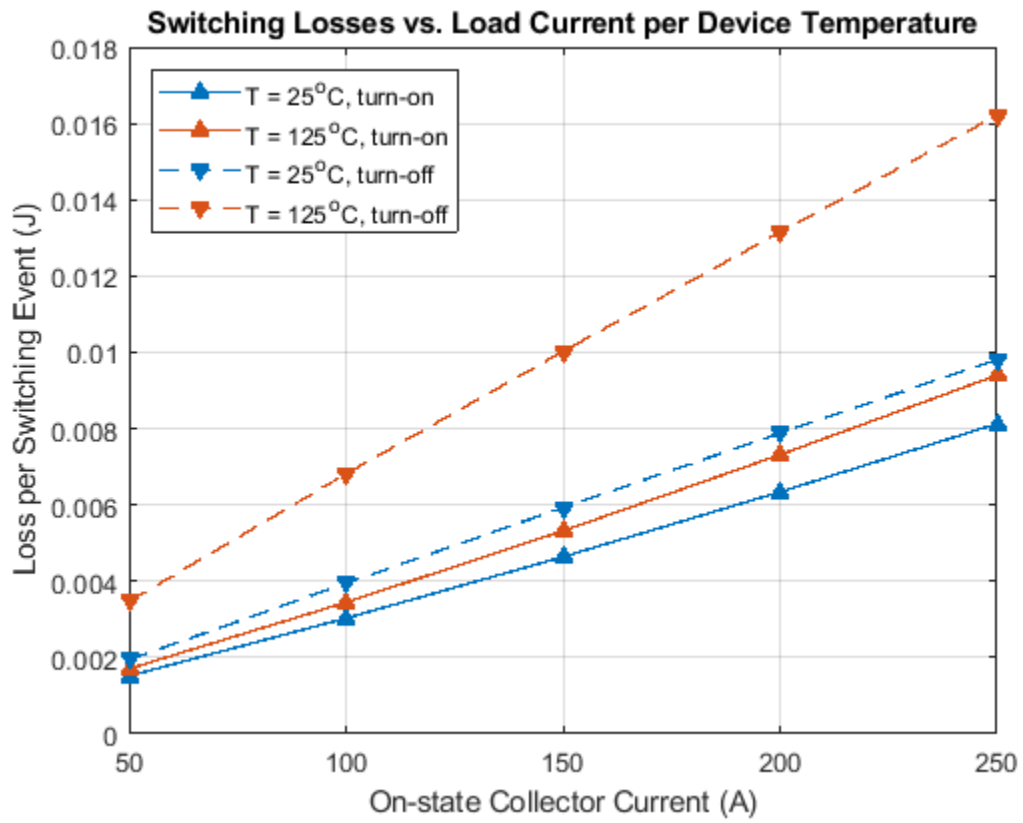
MOSFET Characteristics

This example shows generation of the characteristic curves for an N-channel MOSFET. Define the vector of gate voltages and minimum and maximum drain-source voltages by double clicking on the block labeled 'Define Conditions (Vg and Vds)'. Then click on hyperlink 'plot results' in the model.

This type of plot can be compared against a manufacturer datasheet to confirm a correct implementation of the MOSFET parameters. You can also use this model to examine the MOSFET characteristics in the reverse region by specifying a range of negative Vds values.

To explore the properties of a P-channel MOSFET, copy a P-Channel MOSFET from the library to replace the N-channel device, taking care to swap over the two output connections so that the source is still connected to ground. To specify normal operation, the vector of gate voltages should be negative, and the Vds range of values should be negative.

Model
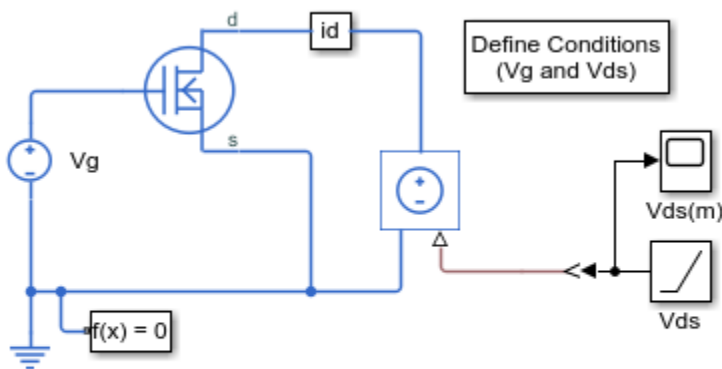
MOSFET Characteristics
1. MOSFET curves: Define test, plot results (see code)
2. Explore simulation results using sscexplore
3. Learn more about this example

Simulation Results from Simscape Logging

The plot below shows drain current vs. drain-source voltage for a range of gate voltages.
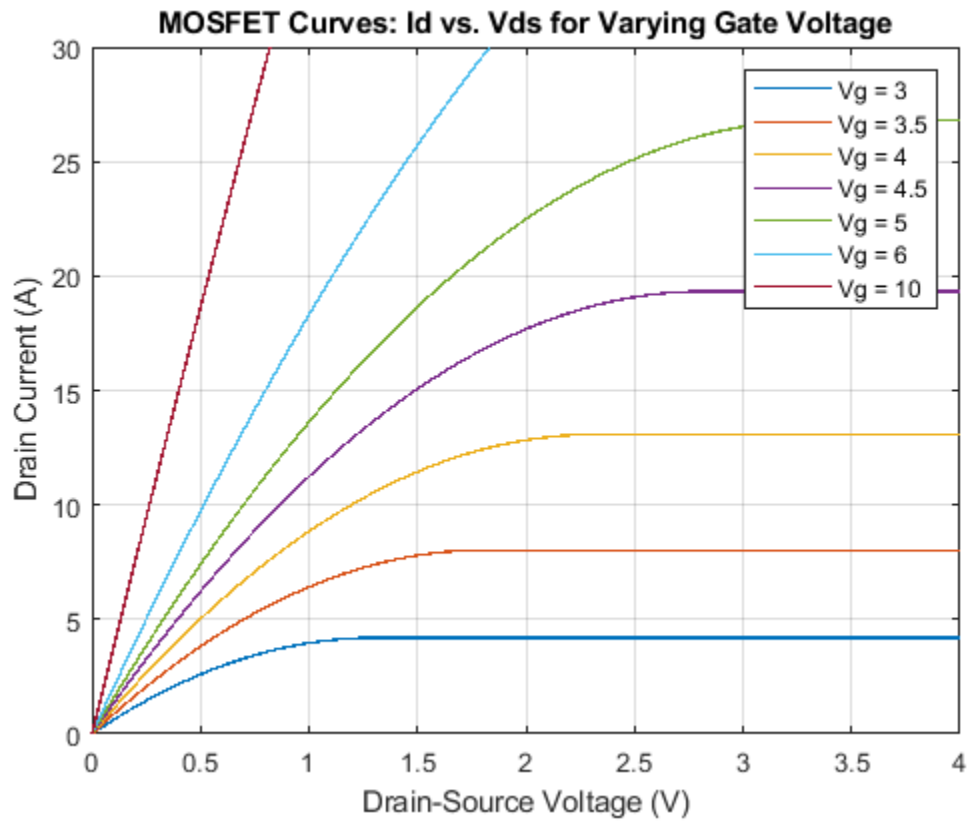
MOSFET Curves: Id vs. Vds for Varying Gate Voltage

Nonlinear Inductor Characteristics

This example shows a comparison of nonlinear inductor behavior for different parameterizations. Starting with fundamental parameter values, the parameters for linear and nonlinear representations are derived. These parameters are then used in a Simscape™ model and the simulation outputs compared.

Specification of Parameters

Fundamental parameter values used as the basis for subsequent calculations:

- Permeability of free space, $\mu_0, \mathrm{H/m}$
- Relative permeability of core, μ_r
- Number of winding turns, N_w
- Effective magnetic core length, l_e, m
- Effective magnetic core cross-sectional area, $A_e, \mathrm{m^2}$
- Core saturation begins, $B_{sat_{begin}}, \mathrm{T}$
- Core fully saturated, B_{sat}, T

```
mu_0 = pi*4e-7;
mu_r = 3000;
Nw = 10;
le = 0.032;
Ae = 1.6e-5;
B_sat_begin = 0.75;
B_sat = 1.5;
```

Calculate Magnetic Flux Density and Magnetic Field Strength Data

Where:

- Magnetic flux density, B, T
- Magnetic field strength, $H, \mathrm{A/m}$

Linear representation:

- $B = \mu_0 \mu_r H$

Nonlinear representation (including coefficient, a):

- $B = B_{sat} \tanh(a.H)$

```
% Use linear representation to find value of H corresponding to B_sat_begin
H_sat_begin = B_sat_begin/(mu_0*mu_r);
% Rearrange nonlinear representation to calculate coefficient, a
a = atanh( B_sat_begin/B_sat )/H_sat_begin;

% Linear representation
H_linear = [-500 500];
B_linear = mu_0*mu_r*H_linear;
```

```
% Nonlinear representation
H_nonlinear = -5*H_sat_begin:H_sat_begin:5*H_sat_begin;
B_nonlinear = B_sat*tanh(a*H_nonlinear);
```

Display Magnetic Flux Density Versus Magnetic Field Strength

The linear and nonlinear representations can be overlaid.

```
figure,plot( H_linear, B_linear, H_nonlinear, B_nonlinear );
grid( 'on' );
title( 'Magnetic flux density, B, versus Magnetic field strength, H' );
xlabel( 'Magnetic field strength, H (A/m)' );
ylabel( 'Magnetic flux density, B (T)' );
legend( 'B=\mu_0 \mu_r H', 'B-H curve', 'Location', 'NorthWest' )
```


Calculate Magnetic Flux and Current Data

Where:

* Magnetic flux, ϕ, Wb

* Current, I, A

Linear representation:

* $L = \mu_0 \mu_r A_e N_w^2 / l_e$

10-273

- $\phi = IL/N_w$

Nonlinear representation:

- $I = Hl_e/N_w$

- $\phi = BA_e$

```
% Linear inductance
L_linear = mu_0*mu_r*Ae*Nw^2/le;

% Linear representation
I_linear = [-1.5 1.5];
phi_linear = I_linear.*L_linear/Nw;

% Nonlinear representation
I_nonlinear = le.*H_nonlinear./Nw;
phi_nonlinear = B_nonlinear.*Ae;
```

Display Magnetic Flux Versus Current

The linear and nonlinear representations can be overlaid.

```
figure, plot( I_linear, phi_linear, I_nonlinear, phi_nonlinear );
grid( 'on' );
title( 'Magnetic flux, \phi, versus current, I' );
xlabel( 'Current, I (A)' );
ylabel( 'Magnetic flux, \phi (Wb)' );
legend( '\phi=I L/N_w', '\phi-I curve', 'Location', 'NorthWest' );
```
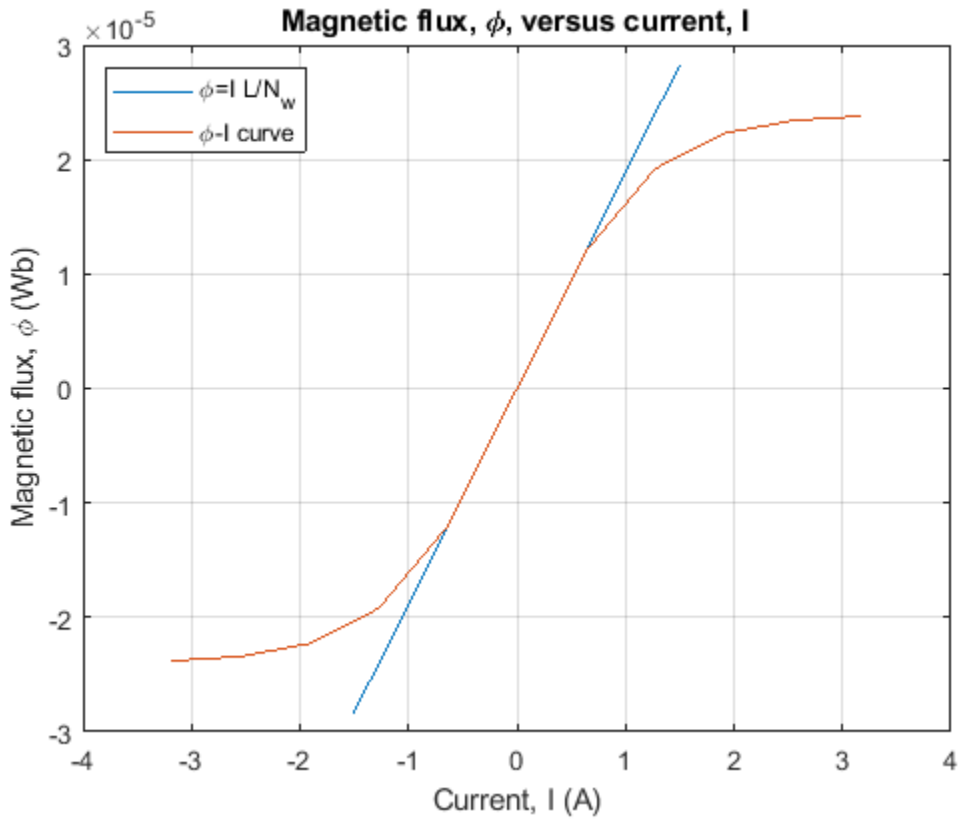
Magnetic flux, ϕ, versus current, I

Use Parameters in Simscape Model

The parameters calculated can now be used in a Simscape model. Once simulated, the model is set to create a Simscape logging variable, simlog.

```
modelName = 'ee_nonlinear_inductor';
open_system( modelName );
sim( modelName );
```
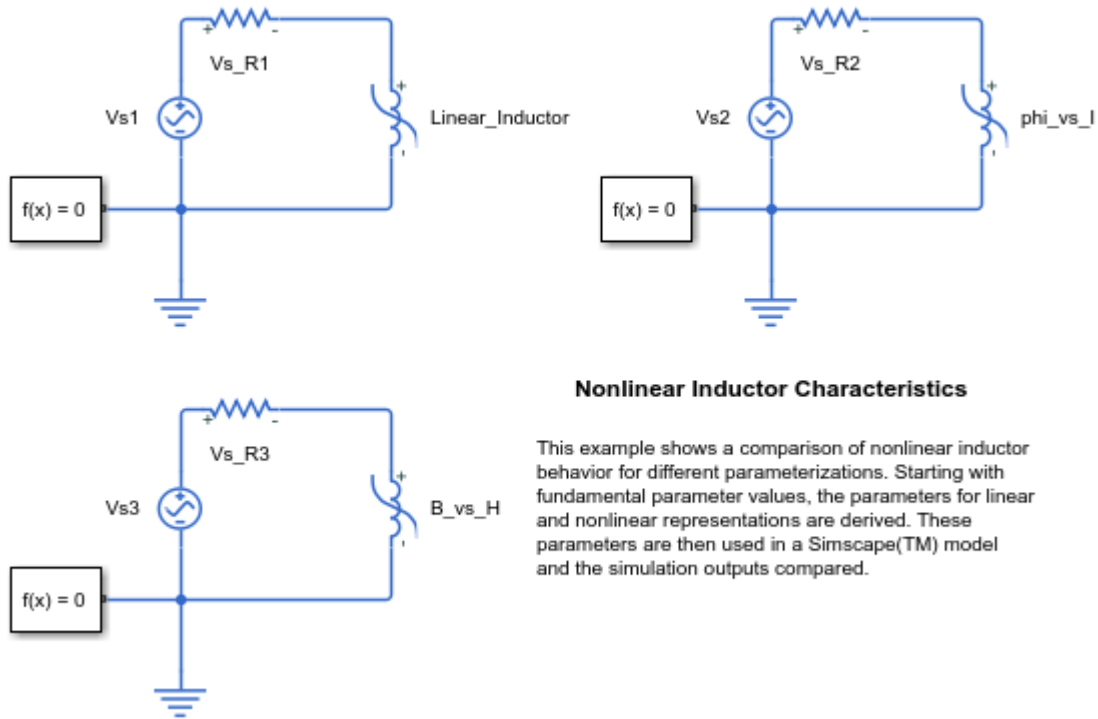
Nonlinear Inductor Characteristics

This example shows a comparison of nonlinear inductor behavior for different parameterizations. Starting with fundamental parameter values, the parameters for linear and nonlinear representations are derived. These parameters are then used in a Simscape(TM) model and the simulation outputs compared.

Conclusion

The state variable for all representations is magnetic flux, ϕ. Current, I, and magnetic flux, ϕ, can be obtained from the Simscape logging variable, simlog, for each representation. Overlaying the simulation results from the representations permits direct comparison.

```
figure, plot( ...
    simlog.Linear_Inductor.inductor.i.series.values,...
    simlog.Linear_Inductor.inductor.phi.series.values,...
    simlog.B_vs_H.inductor.i.series.values,...
    simlog.B_vs_H.inductor.phi.series.values,...
    simlog.phi_vs_I.inductor.i.series.values,...
    simlog.phi_vs_I.inductor.phi.series.values,...
    'o'...
    );
grid( 'on' );
title( 'Magnetic flux, \phi, versus current, I' );
xlabel( 'Current, I (A)' );
ylabel( 'Magnetic flux, \phi (Wb)' );
legend( 'Linear (single inductance)', 'B-H characteristic', '\phi-I characteristic', 'Location',
```

Magnetic flux, ϕ, versus current, I
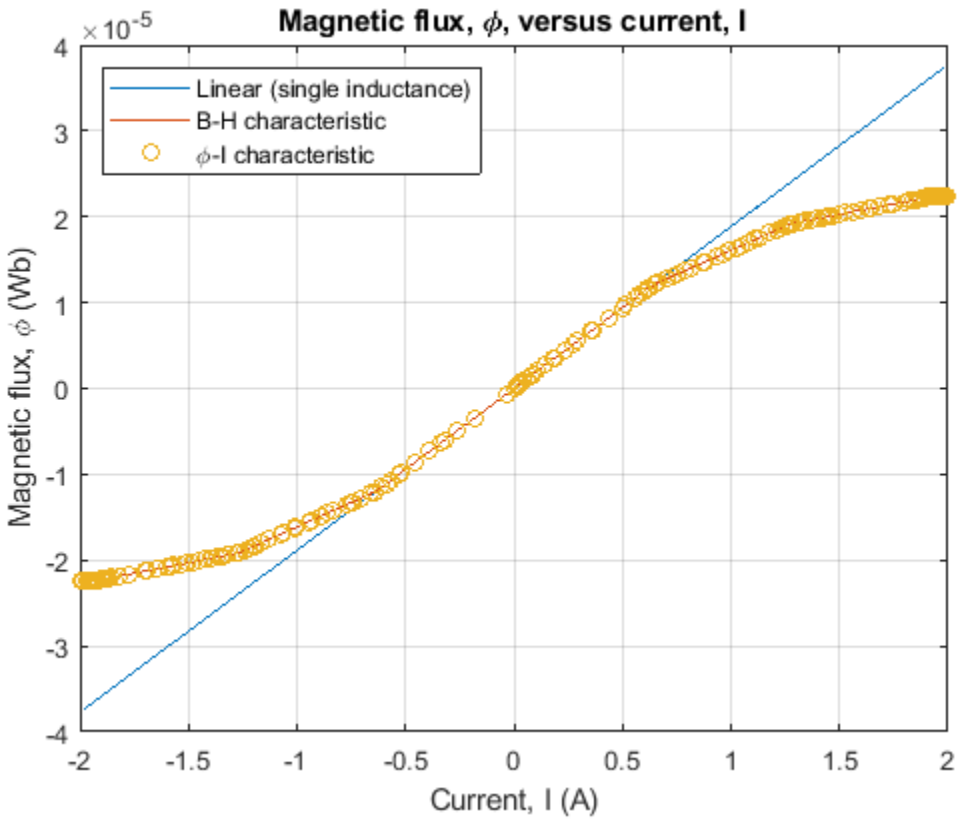
Inductor With Hysteresis

This example shows how modifying the equation coefficients of the Jiles-Atherton magnetic hysteresis equations affects the resulting B-H curve. The simulation parameters are configured to run four complete AC cycles with initial field strength (H) and magnetic flux density (B) both set to zero.

Parameters that set the shape of the anhysteretic curve are not perturbed as selecting values for these parameters is relatively easy. The remaining three parameters affect the B-H curve in multiple ways, and some iteration is typically needed to match a nominal B-H curve. The following steps are a good starting point:

1. Tune c to match the initial gradient when starting at B=H=0. As c approaches 1 the gradient will match that of the anhysteretic curve. Making it smaller reduces the initial gradient.

2. Tune K to get the desired H-axis intercepts. A good initial guess for K is the actual value of the desired intercept.

3. Gradually increase alpha (starting from a value like 1e-6) to fine tune the B-axis intercepts. Making alpha bigger increases the intercept values.

Model
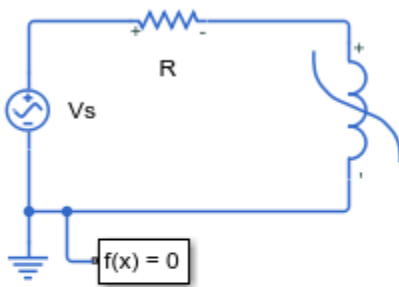
Inductor With Hysteresis
1. Test coefficients that affect B-H curve
   and plot results (see code)
2. Explore simulation results using sscexplore
3. Learn more about this example

Simulation Results from Simscape Logging

The plots below show how the individual Jiles-Atherton hysteresis coefficients affect the hysteresis curve for a nonlinear inductor. The model is simulated with a nominal set of Jiles-Atherton hysteresis equation coefficients, and then re-runs the model with perturbations applied to each coefficient individually.
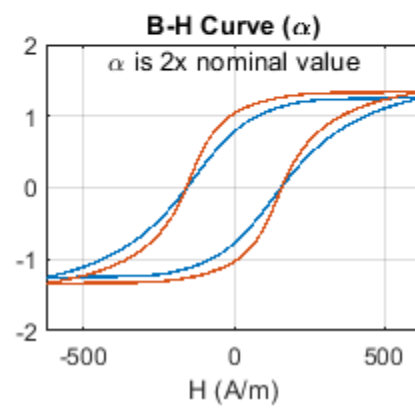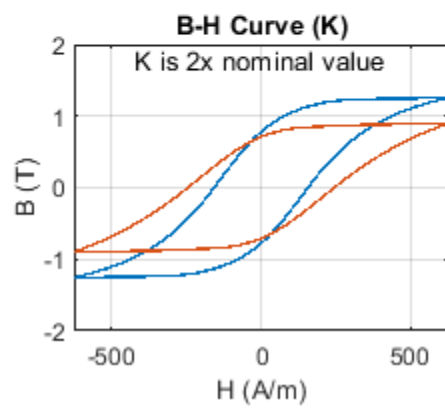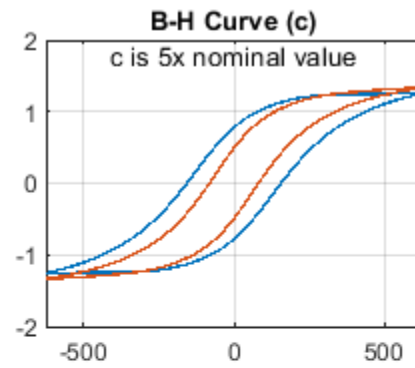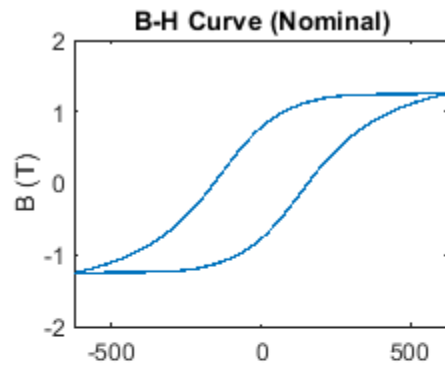
Nonlinear Transformer Characteristics

This example shows calculation and confirmation of a nonlinear transformer core magnetization characteristic. Starting with fundamental parameter values, the core characteristic is derived. This is then used in a Simscape™ model of an example test circuit which can be used to plot the core magnetization characteristic on an oscilloscope. Model outputs are then compared to the known values.

Specification of Parameters

Fundamental parameter values used as the basis for subsequent calculations:

- Permeability of free space, $\mu_0, \mathrm{H/m}$
- Relative permeability of core, μ_r
- Number of primary turns, N_1
- Number of secondary turns, N_2
- Effective magnetic core length, l_e, m
- Effective magnetic core cross-sectional area, $A_e, \mathrm{m^2}$
- Core saturation begins, $B_{sat_{begin}}, \mathrm{T}$
- Core fully saturated, B_{sat}, T

```
mu_0 = pi*4e-7;
mu_r = 3000;
N1 = 23;
N2 = 29;
le = 0.032;
Ae = 1.6e-5;
B_sat_begin = 0.6;
B_sat = 1.2;
```

Calculate Magnetic Flux Density Versus Magnetic Field Strength Characteristic

Where:

- Magnetic flux density, B, T
- Magnetic field strength, $H, \mathrm{A/m}$

Linear representation:

- $B = \mu_0 \mu_r H$

Nonlinear representation (including coefficient, a):

- $B = B_{sat} \tanh(a.H)$

```
% Use linear representation to find value of H corresponding to B_sat_begin
H_sat_begin = B_sat_begin/(mu_0*mu_r);
% Rearrange nonlinear representation to calculate coefficient, a
a = atanh( B_sat_begin/B_sat )/H_sat_begin;
```

```
% Nonlinear representation
H_nonlinear = 0:25:750;
B_nonlinear = B_sat*tanh(a*H_nonlinear);
```

Use Parameters in Simscape Model

The parameters calculated can now be used in a Simscape model. Once simulated, the model is set to output a Simscape logging variable, simlog, and some signals using output ports, yout. Circuit parameters are:

- Voltage source magnitude, $V_s = 10\text{V}$

- Voltage source frequency, $Freq_{Hz} = 60\text{Hz}$

- Voltage source resistance, $R_{Vs} = 10\Omega$

- Operational amplifier input resistance, $R_1 = 1\text{k}\Omega$

- Operational amplifier feedback resistance, $R_2 = 1\text{M}\Omega$

- Operational amplifier feedback capacitance, $C_2 = 1\mu\text{F}$

```
% Circuit parameters
Vs = 10;
Freq_Hz = 60;
R_Vs = 10;
R_1 = 1e3;
R_2 = 1e6;
C_2 = 1e-6;

% Open model and simulate
modelName = 'ee_nonlinear_transformer';
open_system( modelName );
simOut = sim( modelName );
yout = get(simOut, 'yout');
simlog = get(simOut, 'simlog');

% Collect internal Simscape logging data for comparison
I_simscape = simlog.Nonlinear_Transformer.Lm.i.series.values;
phi_simscape = simlog.Nonlinear_Transformer.Lm.phi.series.values;

% Collect model output data for comparison (as used for oscilloscope)
I_primary = yout(:,1);
int_V_secondary = yout(:,2);
```
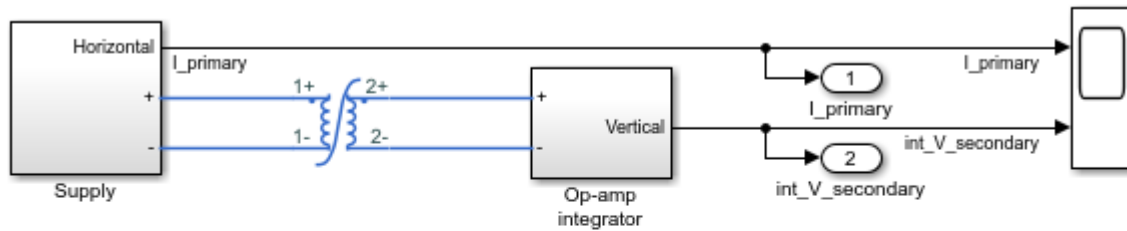
Nonlinear Transformer Characteristics

This example shows calculation and confirmation of a nonlinear transformer core magnetization characteristic. Starting with fundamental parameter values, the core characteristic is derived. This is then used in a Simscape(TM) model of an example test circuit which can be used to plot the core magnetization characteristic on an oscilloscope. Model outputs are then compared to the known values.

Calculations on Logging and Output Data

The data needs to be processed to provide the Magnetic field strength and Magnetic flux density data for comparison. Where:

- Magnetomotive force, F_m, A
- Magnetic flux, ϕ, Wb
- Operational amplifier input voltage, V_{in}, V
- Operational amplifier output voltage, V_{out}, V

Equations to be used are as follows:

- $F_m = I.N_1$
- $H = F_m/l_e$
- $B = \phi/A_e$
- $V_{out} = \frac{1}{R_1 C_2} \int V_{in}.dt + c$
- $\phi = \frac{1}{N_2} \int v.dt$

```
% Internal logging data
H_simscape = I_simscape.*N1./le;
B_simscape = phi_simscape./Ae;

% Oscilloscope scaling and model output data
H_measured = I_primary.*N1./le;
phi_measured = (int_V_secondary.*R_1.*C_2)./N2;
B_measured = phi_measured./Ae;
```

Conclusion

The three characteristics can now be overlaid:

- Defined characteristic: calculated from fundamental parameters

- Characteristic from logging: calculated from internal Simscape logging data
- Characteristic from measurement: obtained by measurement and calculation using electronic test circuit

Due to leakage and parasitic parameters, the characteristic obtained from the electronic test circuit differs to the defined characteristic. However, the test circuit and its parameterization is shown to find the characteristic for the given transformer within suitable tolerances.

```
figure, plot( ...
    H_nonlinear,...
    B_nonlinear,...
    'o',...
    H_simscape,...
    B_simscape,...
    H_measured,...
    B_measured,...
    '--'...
    );
grid( 'on' );
title( 'Magnetic Flux Density, B, Versus Magnetic Field Strength, H' );
xlabel( 'Magnetic field strength, H (A/m)' );
ylabel( 'Magnetic flux density, B (T)' );
legend( 'Defined characteristic', 'Characteristic from logging',...
    'Characteristic from measurements', 'Location', 'NorthWest' );
```
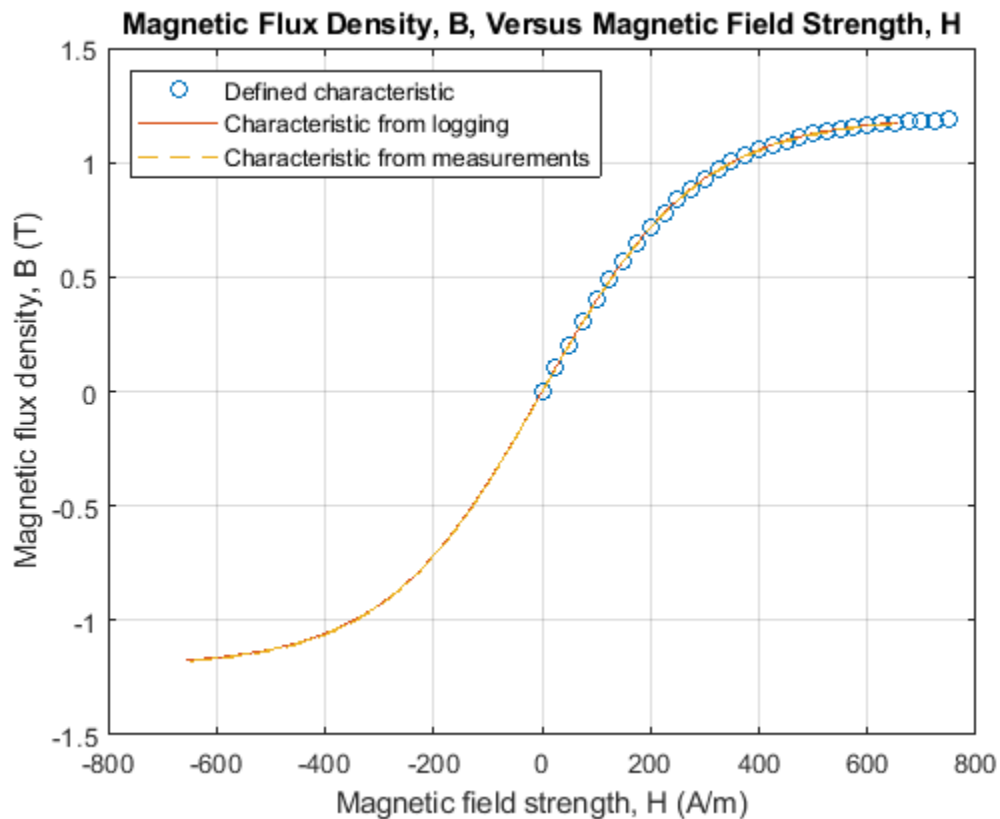
NPN Bipolar Transistor Characteristics

This example shows generation of the Ic versus Vce curve for an NPN bipolar transistor. Define the vector of base currents and minimum and maximum collector-emitter voltages by double clicking on the block labeled 'Define Conditions (Ib and Vce)'. Run the tests and generate plots of the curves by clicking in the model on hyperlink 'plot curves'.

This type of plot can be compared against a manufacturer datasheet to confirm a correct implementation of the transistor parameters. You can also use this model to examine the transistor characteristics in the reverse region by specifying a range of negative Vce values. In this region, the gain is defined by the Reverse current transfer ratio BR parameter. Increase this parameter above one to produce a reverse current gain.

To explore the properties of a PNP bipolar transistor, open model ee_pnp.

Model
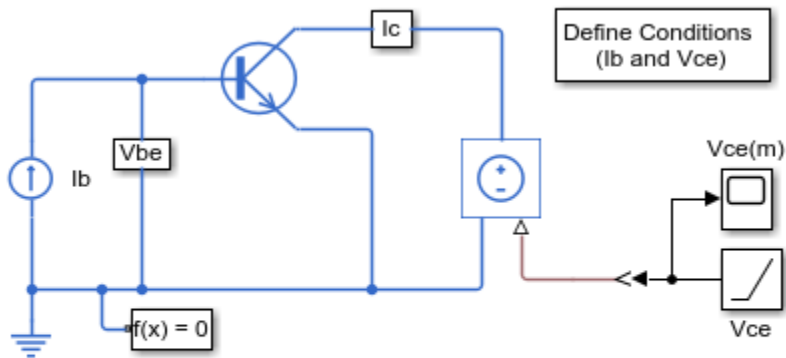
NPN Bipolar Transistor Characteristics
1. Transistor curves: Define Ib and Vce, plot curves (see code)
2. Explore simulation results using sscexplore
3. Learn more about this example

Simulation Results from Simscape Logging

The plot below shows collector current (Ic) versus collector-emitter voltage (Vce) characteristics for different levels of base current (Ib).

NPN Transistor: Ic vs. Vce at Various Base Currents (Ib)
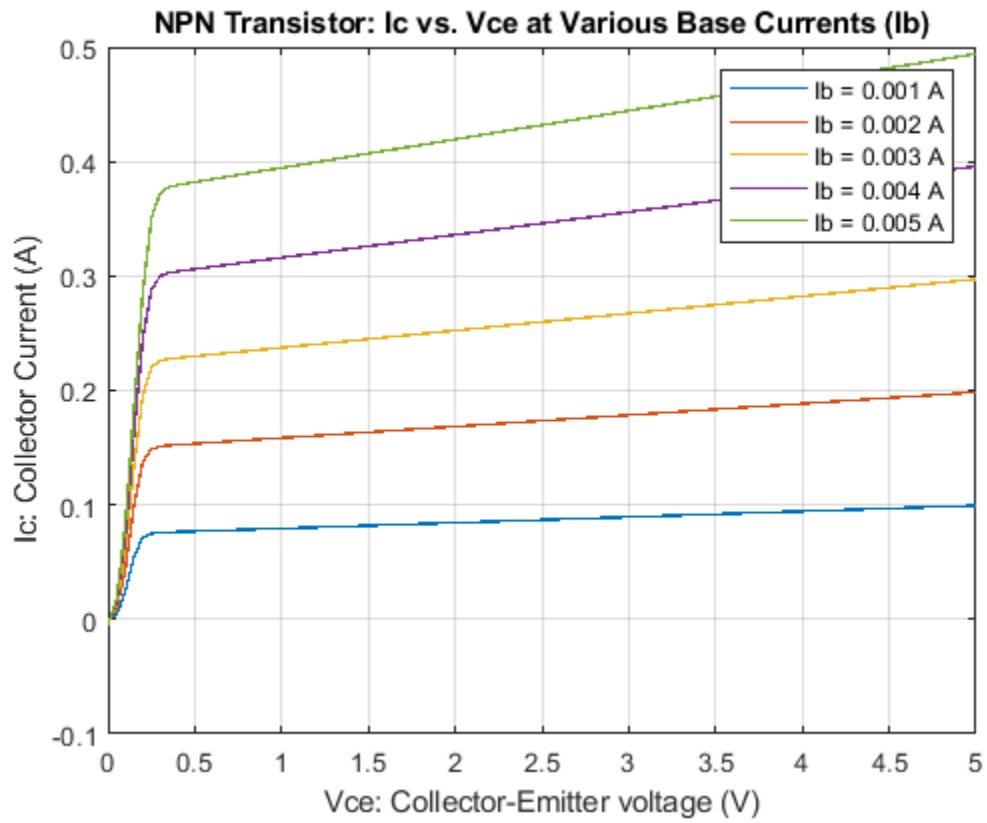
PNP Bipolar Transistor Characteristics

This example shows generation of the Ic versus Vce curve for a PNP bipolar transistor. Define the vector of base currents and minimum and maximum collector-emitter voltages by double clicking on the block labeled 'Define Conditions (Ib and Vce)'. Run the tests and generate plots of the curves by clicking in the model on hyperlink 'plot curves'.

This type of plot can be compared against a manufacturer datasheet to confirm a correct implementation of the transistor parameters. You can also use this model to examine the transistor characteristics in the reverse region by specifying a range of positive Vce values. In this region, the gain is defined by the Reverse current transfer ratio BR parameter. Increase this parameter above one to produce a reverse current gain.

To explore the properties of an NPN bipolar transistor, open model ee_npn.

Model
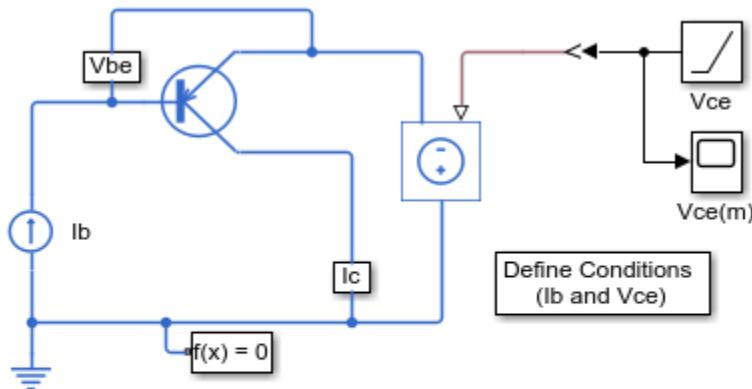
PNP Bipolar Transistor Characteristics
1. Transistor curves: Define Ib and Vce, plot curves (see code)
2. Explore simulation results using sscexplore
3. Learn more about this example

Simulation Results from Simscape Logging

The plot below shows collector current (Ic) versus collector-emitter voltage (Vce) characteristics for different levels of base current (Ib).

PNP Transistor: Ic vs. Vce at Various Base Currents (Ib)
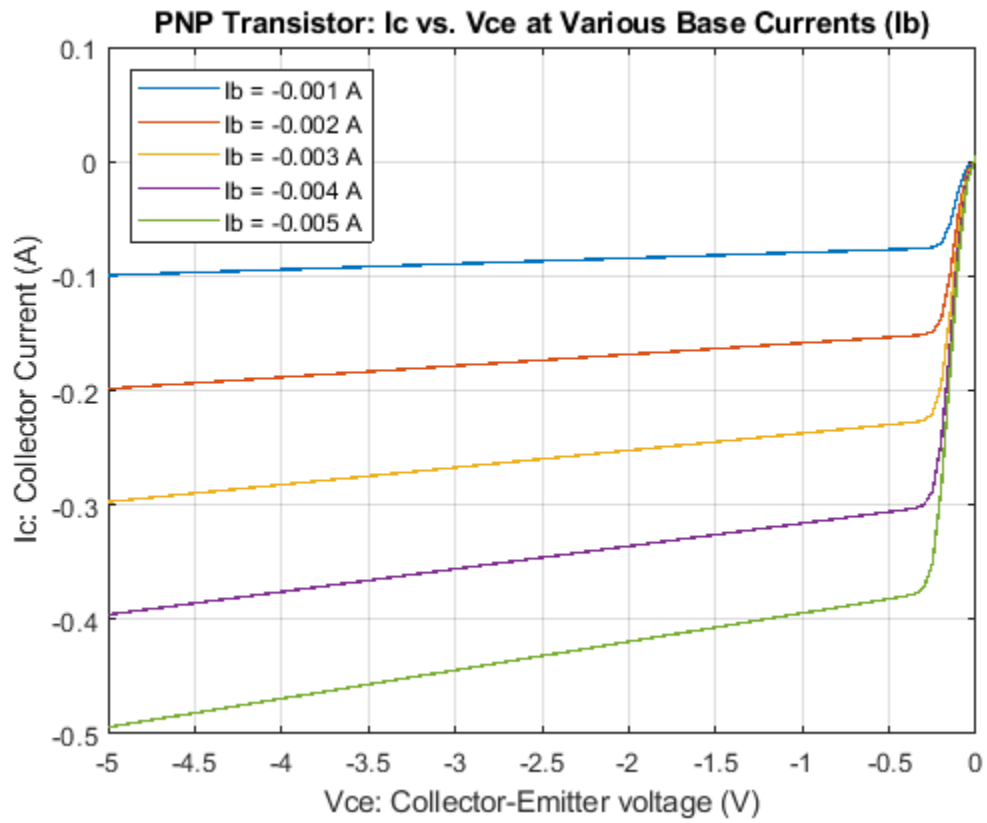
Schottky Barrier Diode Characteristics

This example shows generation of the current versus voltage curve for a Schottky barrier diode. Define the vector of temperatures for which to plot the characteristics by double clicking on the block labeled 'Define Temperatures for Tests'. Run the tests and plot the I-V curves by clicking in the model on the hyperlink 'plot curves'.

The datasheet for this device gives Vf = 0.4V when If = 10mA, and Vf = 0.65V when If = 100mA. The ohmic resistance is set to one over the gradient of the datasheet I-V curve at higher voltages. The temperature dependence is then modeled by selecting the default energy gap and saturation current temperature exponent values for a Schottky barrier diode.

The plot produced by this test model can be used to validate the implementation against the datasheet I-V plots.

Model
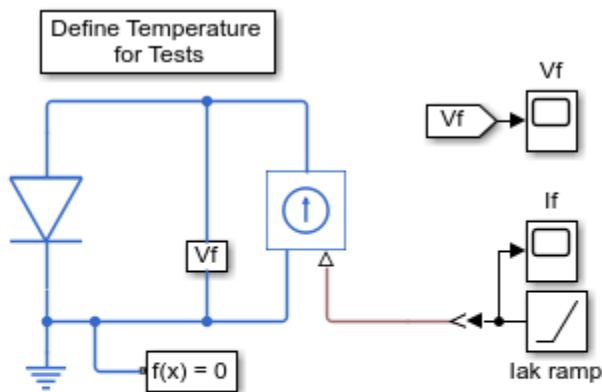
Schottky Barrier Diode Characteristics
1. I-V curves: Select temperatures, plot curves (see code)
2. Explore simulation results using sscexplore
3. Learn more about this example

Simulation Results from Simscape Logging

The plot below shows the I-V characteristic for a Schottkey barrier diode extracted from simulation results. The model is simulated once for each specified temperature.

Schottky Barrier Diode I-V Characteristic
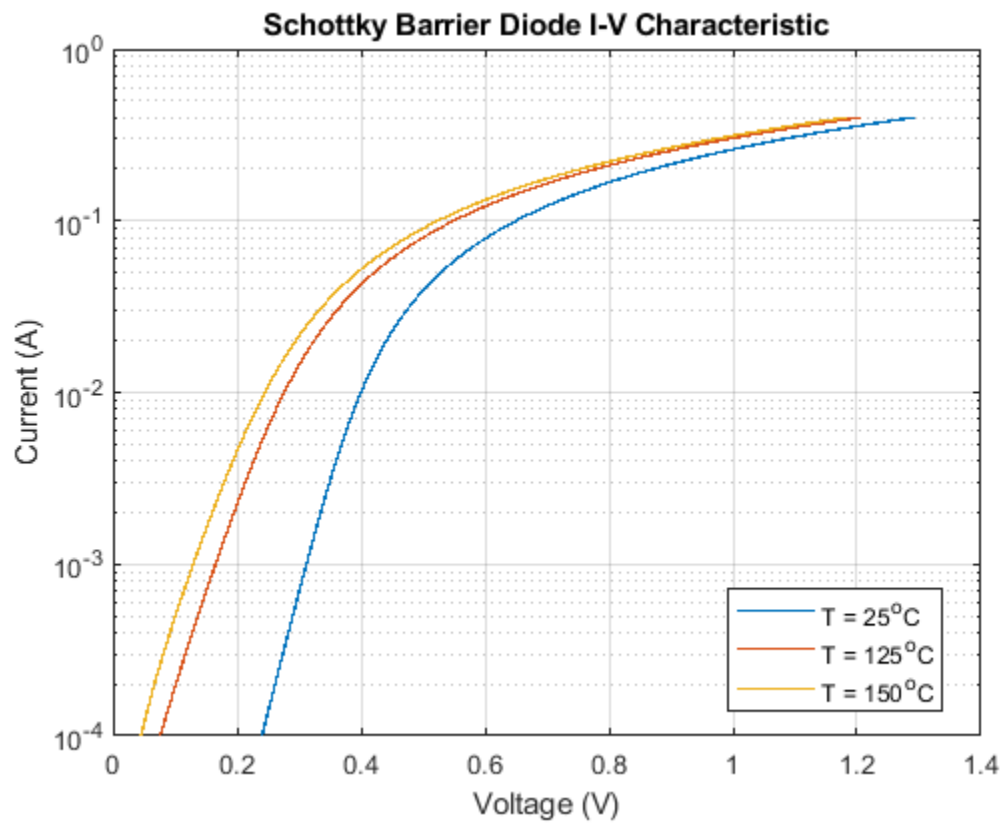
Thyristor Static Behavior Validation

This example shows validation of the static behavior of the Thyristor block against its mask values. Mask values are closely related to datasheet values, and the thyristor block uses these values to calculate the coefficients of the equations used to model it.

The model can be configured to run four separate tests: gate trigger, latch current, on state, and off state. Use the hyperlinks in the model to select the desired test. Each subsystem variant contains more information on the test that is run.

Model
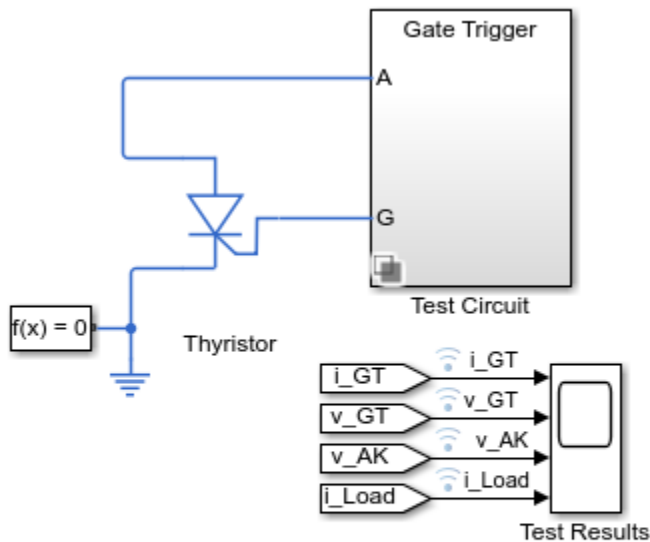
Thyristor Static Behavior Validation
1. Run test and plot results (see code)
2. Select Test: Gate Trigger, Latch Current, On State, Off State
3. Explore simulation results using sscexplore
4. Learn more about this example

Simulation Results from Simscape Logging

Gate Trigger Test

This test ensures that the thyristor turns on when a pulse of 3uA or more is applied to the gate. Specified conditions are 12V supply and 120 ohm load. The value of the internal shunt resistor Rs is critical to ensuring that the device only turns on when the pulse exceeds 3uA.

This circuit also verifies that when the gate current is 3uA, the gate-cathode voltage v_GK is 0.6V as specified in the Thyristor mask.

The supply voltage must be ramped up to 12V to prevent dV/dt gate triggering at turn-on.

Latch Current Test

This test circuit determines the load current below which the thyristor will turn off. The Variable resistor increases between 1K and 12K during the simulation. The scope plot i_Load shows that the holding current is approximately 1mA.

The Thyristor block does not let you define a value for holding current. It is an emergent property of the other mask values. The Product of NPN and PNP forward current gains parameter on the Advanced tab has some influence over the holding current. Reducing if from 10 to 2 doubles the holding current to 2mA.
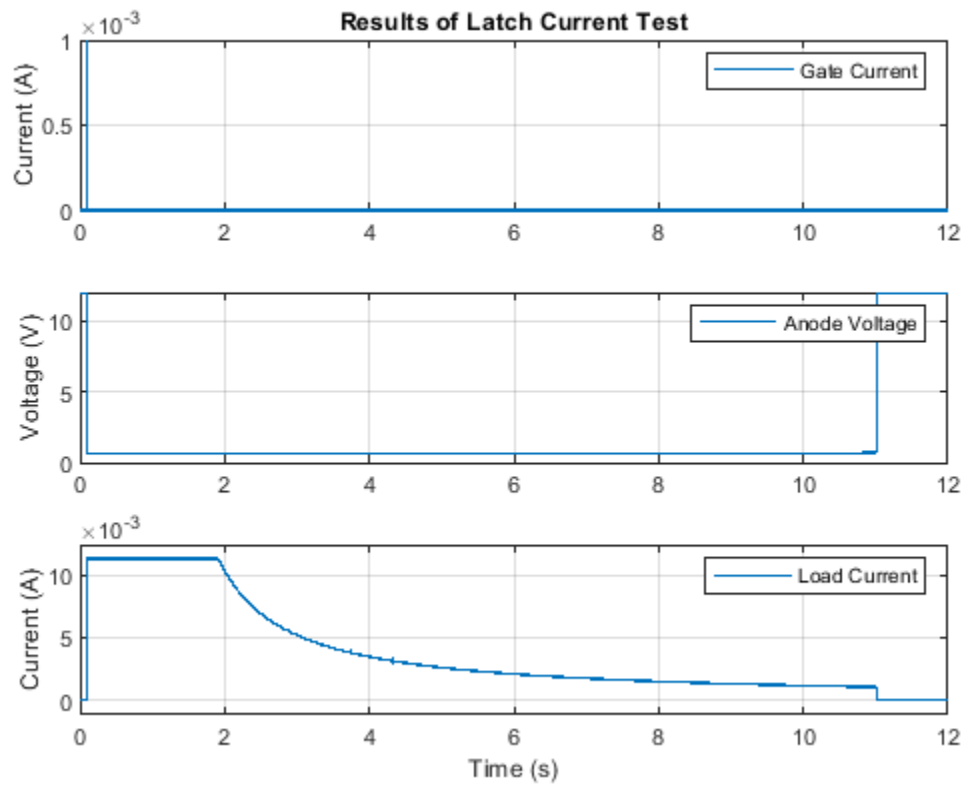
On-State Test
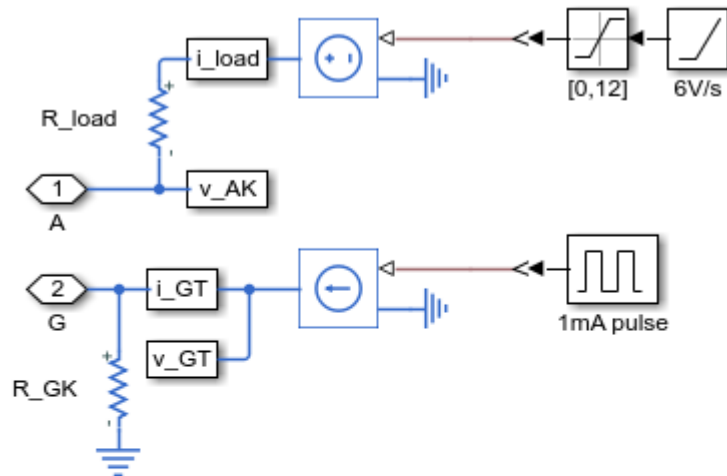
This test ensures that the thyristor on-state voltage drop is correct. The Thyristor mask parameters define the voltage drop V_T to be 1.2V when the current I_T is 1A. To test this, the load resistor must be set to (12-1.2)/1 = 10.8ohms.

Note the use of the external R_GK resistor. This reduces the triggering sensitivity, the trigger current being increased to approximately 0.6V/1000ohms = 0.6mA. The critical current will be slightly less than 0.6mA due to increased anode-gate current which contributes to the gate current.
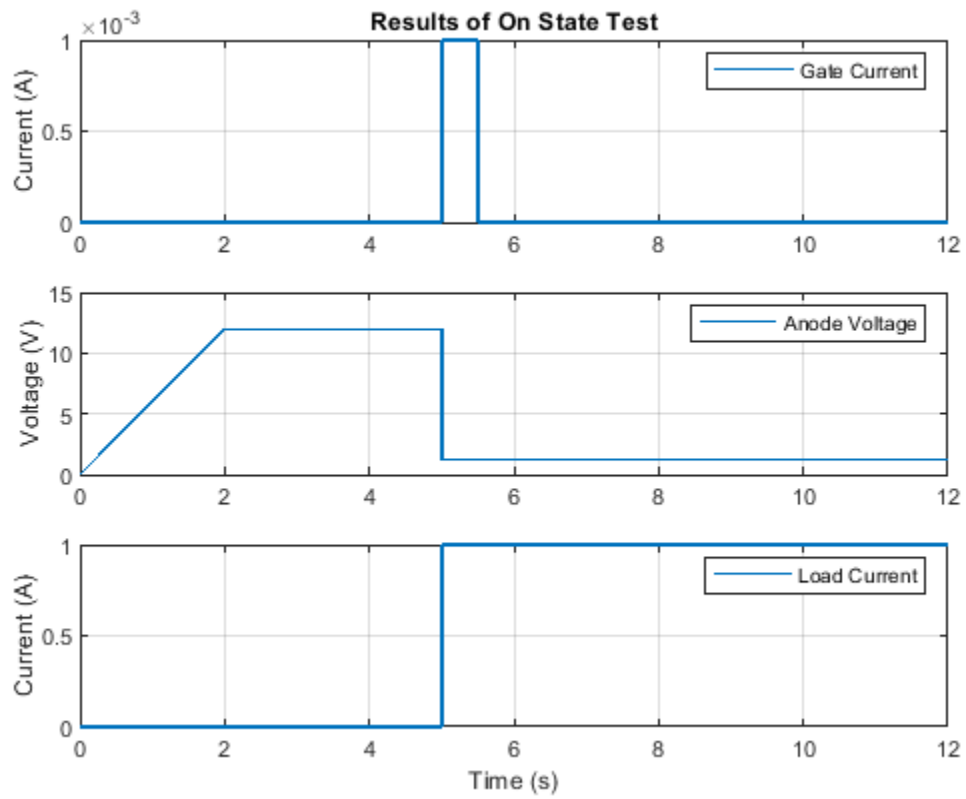
Off-State Test
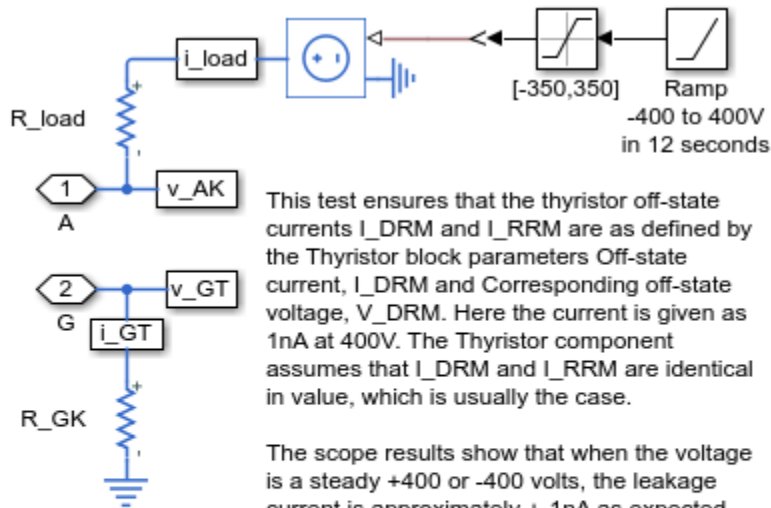
This test ensures that the thyristor off-state currents I_DRM and I_RRM are as defined by the Thyristor block parameters Off-state current, I_DRM and Corresponding off-state voltage, V_DRM. Here the current is given as 1nA at 400V. The Thyristor component assumes that I_DRM and I_RRM are identical in value, which is usually the case.

The scope results show that when the voltage is a steady +400 or -400 volts, the leakage current is approximately +-1nA as expected. The values are not exact due to approximations made when solving for the thyristor component internal parameter values.

The abrupt change in current when the voltage is ramped is due to the current required to charge the thyristor junction capacitances.

Thyristor Dynamic Behavior Validation

This example shows validation of the dynamic behavior of the Thyristor block against its mask values. Mask values are closely related to datasheet values, and the Thyristor block uses these values to calculate the coefficients of the equations used to model it. Double-click on each of the test subsystems for further information on the tests.

dV/dt triggering
test circuit

v_s1 — Supply, Vs

v_GK1 — v_GT

i_Load1 — i_Load

dV/dt triggering
test results

Turn-on delay
test circuit

i_GT2 — i_GT

v_GT2 — v_GK

i_Load2 — i_Load

Turn-on delay
test results

Turn-off delay
test circuit

v_s3 — Supply, Vs

i_GT3 — v_GT

i_Load3 — i_Load

Turn_off delay
test results

Thyristor Dynamic Behavior Validation

This example shows validation of the dynamic behavior of the
Thyristor block against its mask values. Mask values are closely
related to datasheet values, and the Thyristor block uses
these values to calculate the coefficients of the equations
used to model it. Double-click on each of the test
subsystems for further information on the tests.

PWM Circuit Using 555 Timer

This example shows a pulse-width-modulated (PWM) output implemented using a 555 Timer in astable mode. The duty cycle is set by a potentiometer, P1. The potentiometer is controlled during run-time via Duty Cycle Control Knob. The scope shows the resultant output from the 555 Timer. To end the simulation, click on the Stop button.

Model
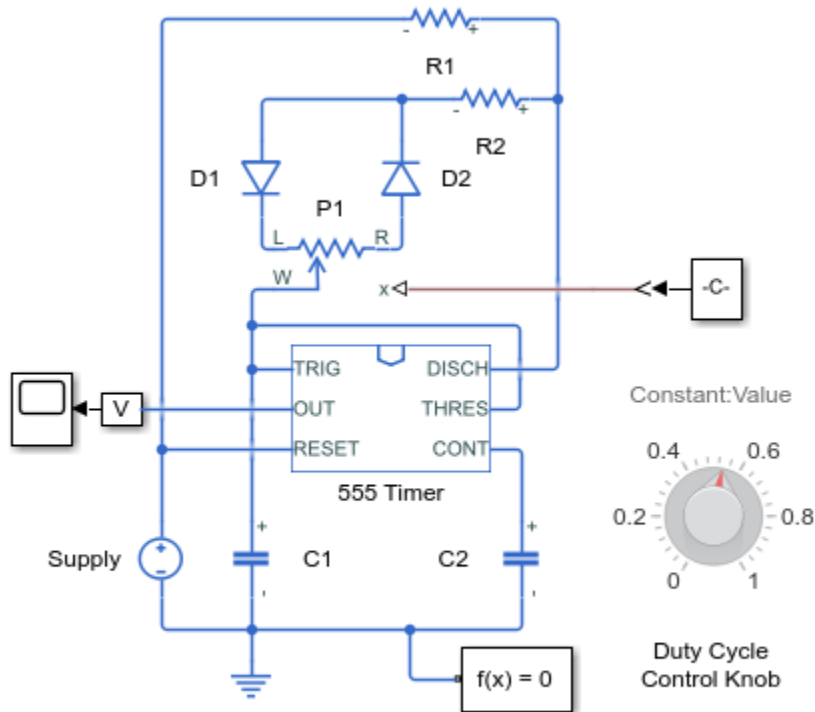
PWM Circuit Using 555 Timer
1. Explore simulation results using sscexplore
2. Learn more about this example

Simulation Results from Scopes
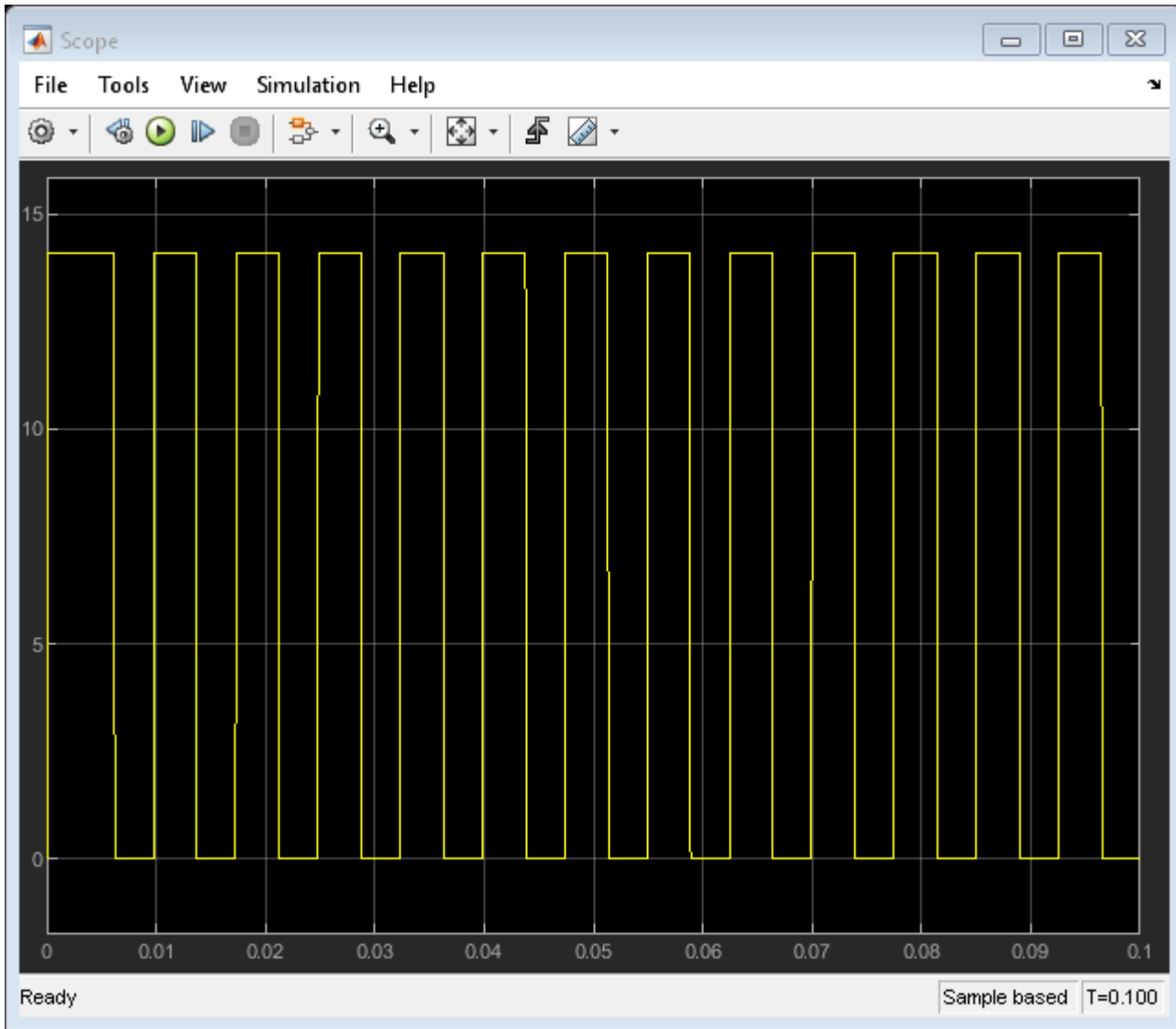
Triangle Wave Generator

This example shows an implementation of a triangle wave generator circuit using two op-amps. The first stage of the circuit is a comparator constructed from an op-amp. The output of the comparator is limited to about plus or minus 5 volts by the two zener diodes. The limits imposed by the zener diodes result in a square wave.

The second stage of the circuit is an integrator. Integrating the square wave creates a triangle wave. The Sine Wave block modulates the waveform amplitude via the Variable Resistor block, and the DC Voltage Source can be used to add a DC offset. See the Example - Modeling a Triangle Wave Generator section of the Simscape™ Electrical™ User Guide for more information on how to construct this model using the Simscape and Simscape Electrical block libraries.

The two Band-Limited Op-Amp blocks are parameterized based on an LM7301 device. The datasheet gives the gain as 97dB which is equivalent to a gain of 10^(97/20)=7.1e4. Input resistance is given as 39e6 ohms, and slew rate as 1.25V per microsecond. Bandwidth is given as 4MHz. No value is given for the output resistance, so this is set to zero. In practice the output resistance will be small compared to output series 1000 ohm resistor in this circuit.

Model
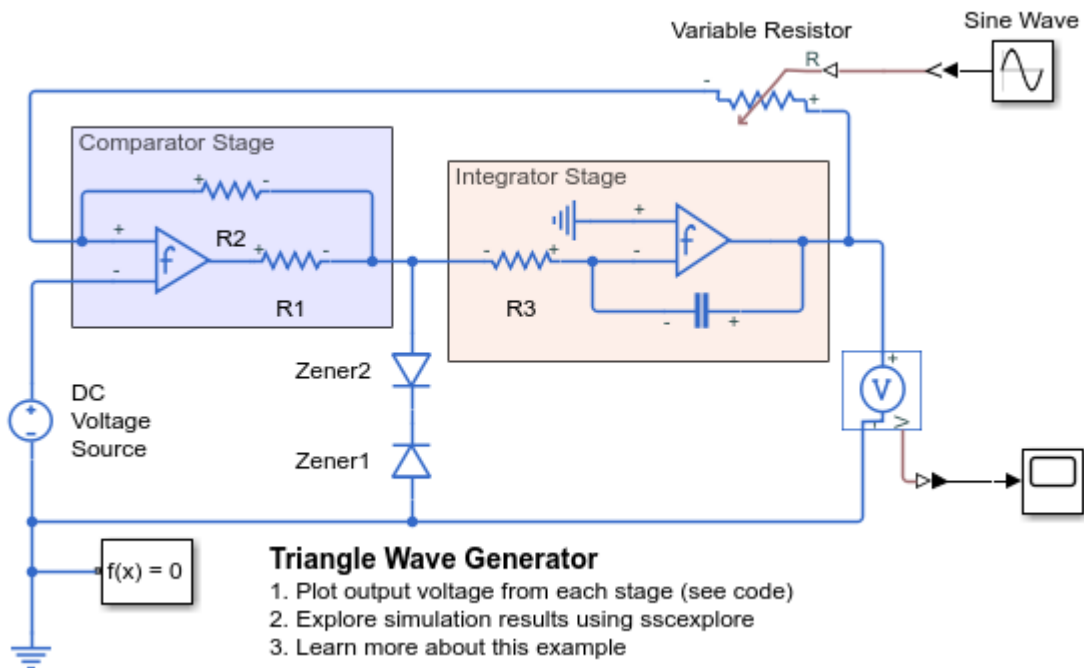
Simulation Results from Simscape Logging

The plot below shows the output voltage of each stage for the triangle wave circuit. The comparator with limits imposed by the zener diodes creates a square wave. Integrating the square wave produces a triangle wave.
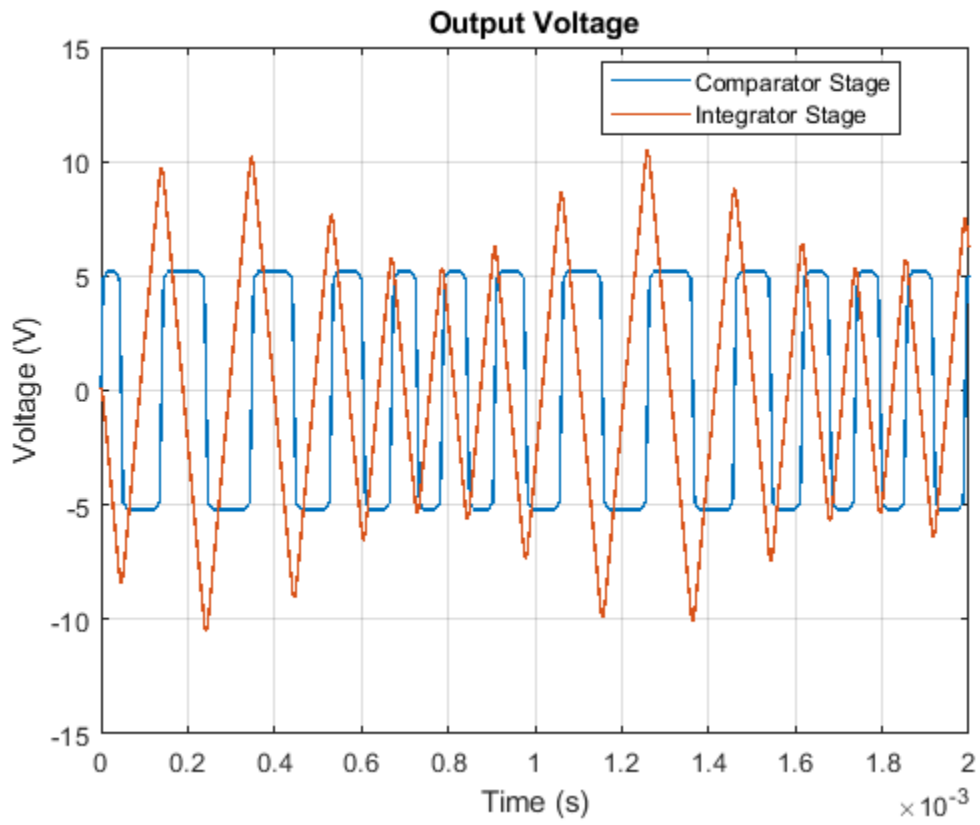
Astable Oscillator

This example shows an implementation of an astable oscillator circuit. The circuit output voltage V2 oscillates in an unstable fashion between high and low states.

At time zero both transistors are off and the voltages across C1 and C2 are zero. C1 starts to charge via R4 and Transistor 1 base-emitter junction. Similarly C2 charges via R2 and Transistor 2's base-emitter junction. Hence voltages V1 and V2 both rise, and resistors R1 and R3 start to conduct, the current in R1 being slightly higher as R3 > R1. R1 acts to increase the potential on Transistor 1 base. Transistor 1 is therefore less saturated than Transistor 2, and V1-V2 increases. Eventually C2 becomes fully charged, and Transistor 2 base current stops, turning the transistor off. V2 then goes high, and C1 starts charging again, thereby turning on Transistor 1. When C1 becomes fully charged, Transistor 1 switches off, and the astable circuit continues to cycle between these two states.

This circuit is an example of a numerically challenging circuit for network simulators. The numerical challenge is due to the exponentially increasing gradients occurring during switching. The default transistor ohmic resistance and junction capacitance terms are added to improve the numerical properties of the circuit.

Model
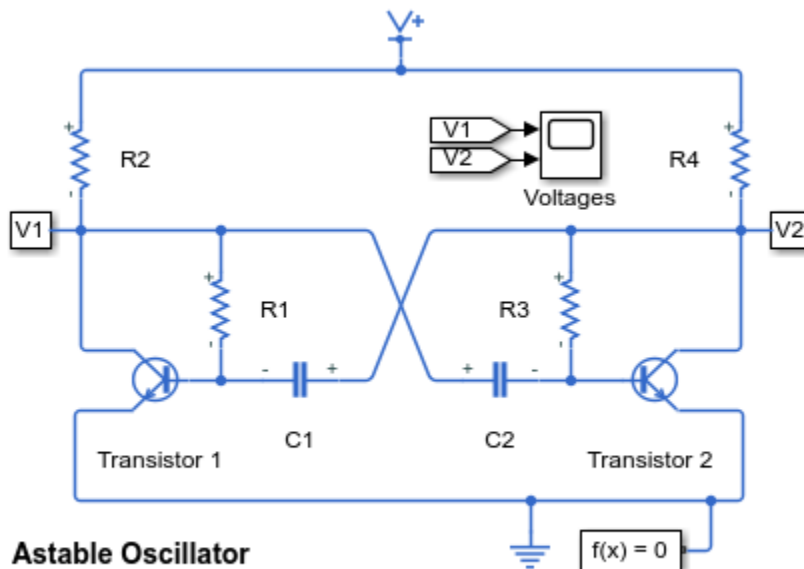
Astable Oscillator

1. Plot voltages (see code)
2. Explore simulation results using sscexplore
3. Learn more about this example

Simulation Results from Simscape Logging

The plots below show voltages in the astable oscillator circuit. The charging and discharging of capacitors C1 and C2 turns the transistors on and off.
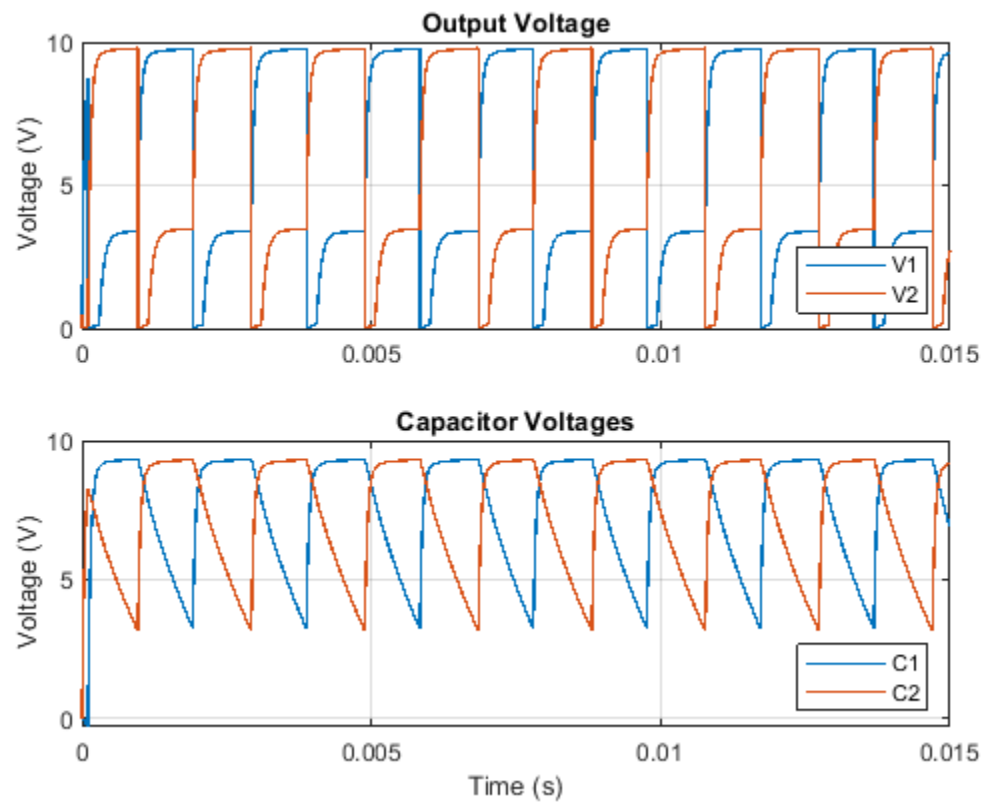
LC Oscillator Based on Colpitts Circuit

This example shows an implementation of a Colpitts oscillator circuit with a nominal frequency of 9MHz. The frequency of oscillation is given by 1/(2*pi*sqrt(L1*C1*C2/(C1+C2))). LC oscillators have good frequency selectivity due to higher Q levels than are achievable with RC oscillators.

Model
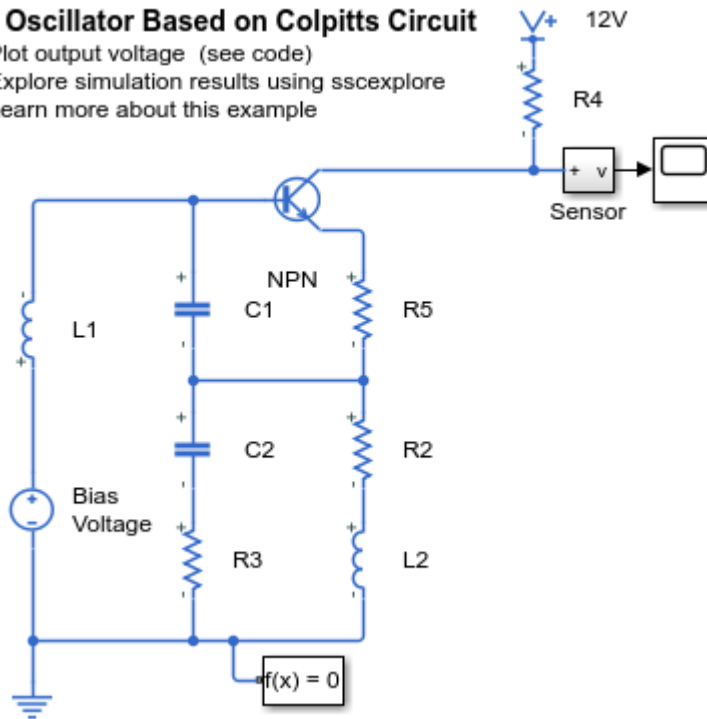
Simulation Results from Simscape Logging

The plots below show the output voltage of the LC oscillator based on Colpitts circuit. The lower plot shows the voltage over 1 microsecond so that we can determine the frequency (around 9 MHz).

Output Voltage

Output Voltage during 1 Microsecond

Voltage-Controlled Oscillator with PI Control

This model shows a voltage-controlled oscillator with feedback control to regulate the output voltage. The oscillation frequency is controlled by the reverse bias voltage applied to the to the varactor diode. The model shows how behavioral and component-level modeling can be mixed when designing a circuit. Provided that the variable trans-conductance device has a fast dynamic response relative to the oscillation frequency, the implementation details are not important. Similarly, the PI controller is in abstracted form, and could either be implemented with op-amps, or in software.

Model

Simulation Results from Simscape Logging

The plots below shows the output voltage of the voltage-controlled oscillator (vco) circuit. Both the true output and the averaged output are shown. The PI controller ensures that oscillations are centered about the reference value of 0.2 V.
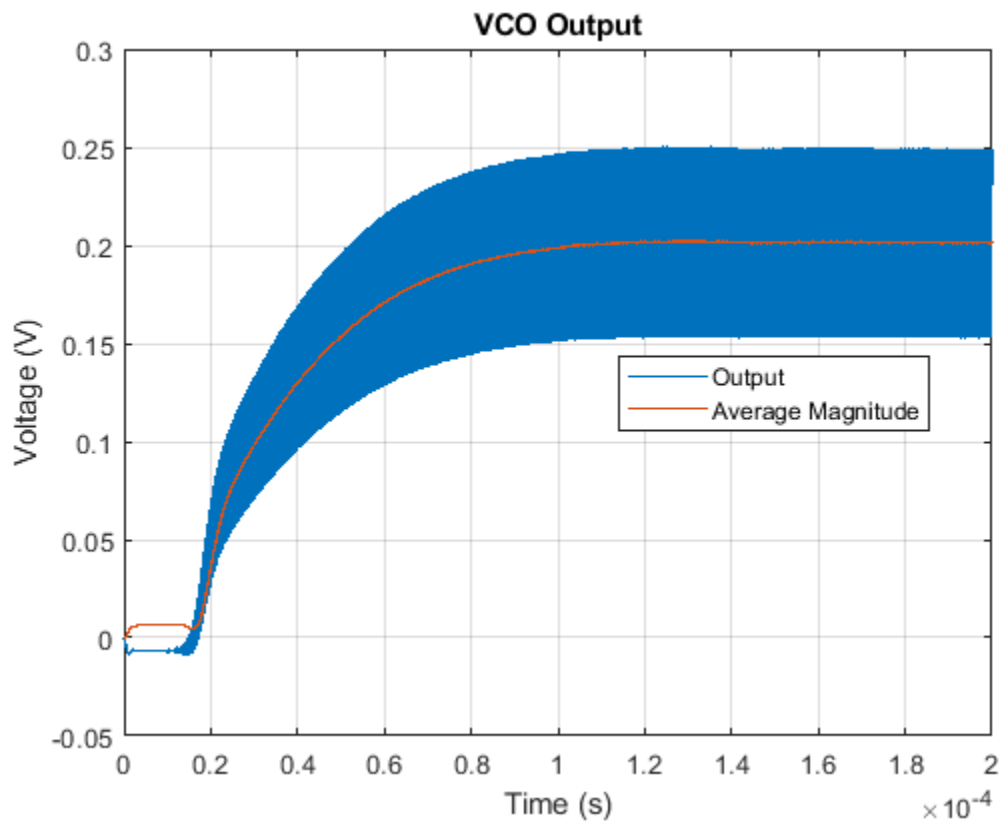
Modeling an Integrated Circuit

This example shows two ways to create a model of an integrated circuit that can be used in conjunction with other Simscape™ Electrical™ blocks. The first approach is to build a behavioral model using Simscape Foundation Library PS blocks and/or Simulink® blocks. The second is to build an implementation model from basic Simscape Electrical blocks. To look under the masks and view the details, select the relevant block and type ctrl-U.

The behavioral model has the advantage of being simpler and potentially running faster. The approach can be extended to complex algorithms as might be implemented on a PIC or ASIC. Note that the model includes a first order lag to model the 1ns propagation delay. In many applications, this time constant will be much faster than the overall circuit dynamics, and may not be of interest. However, removing it potentially introduces an algebraic loop if the gate inputs depend in anyway on the gate output. An alternative is to implement the algorithm (the NOR gate in this case) using only PS blocks rather than Simulink blocks.

The implementation model implements a representation of the actual MOSFET gates used to implement the NOR function. Note that although it is a more representative model of the actual device, it still has limitations. A key challenge is selecting appropriate parameters for the MOSFET blocks based on the datasheet. Here all MOSFETS are assumed to have the same parameters which will not always be the case. The main disadvantage of using an implementation model is that simulation speed is likely to be much slower than the behavioral model.

Model
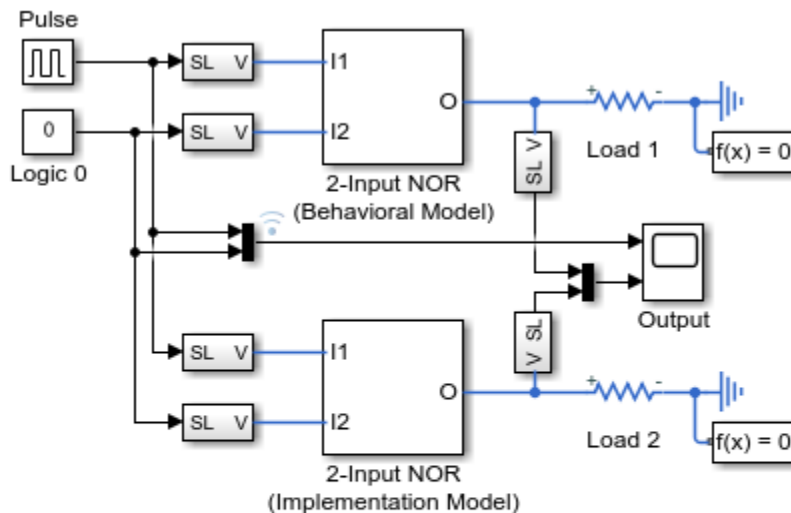
Modeling an Integrated Circuit
1. Plot inputs and outputs for NOR models (see code)
2. Explore simulation results using sscexplore
3. Learn more about this example

2-Input NOR (Behavioral Model) Subsystem
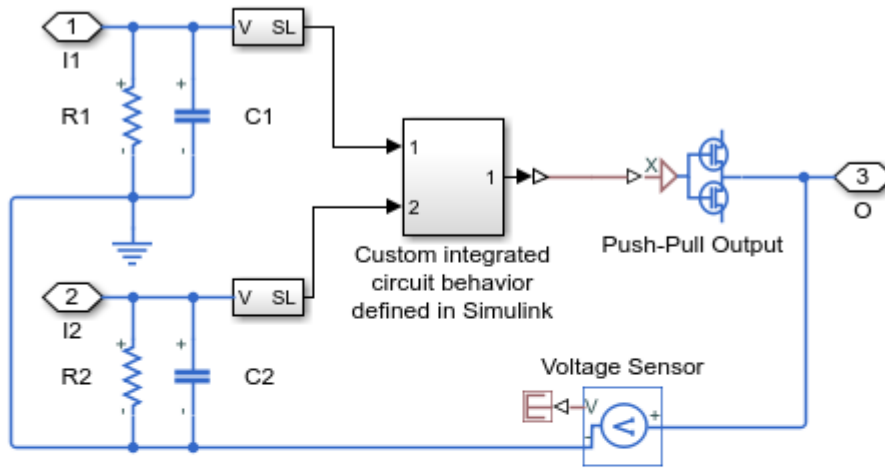
The integrated circuit behavior can be implemented in Simulink(R), or using the Simscape(TM) Foundation Library physical signal blocks. A Simulink implementation should a propagation delay to break any potential algebraic loops created by the external circuit.

The input resistances and capacitances should be set to match the input impedance of the device you are modeling. The Push-Pull Output block should be set to Quadratic. if the output pin must provide significant current and/or short-circuit output conditions are to be simulated.

The Voltage Sensor links the networks attached to the inputs and output so that separate solver blocks are not required for the input and output networks

2-Input NOR (Implementation Model) Subsystem
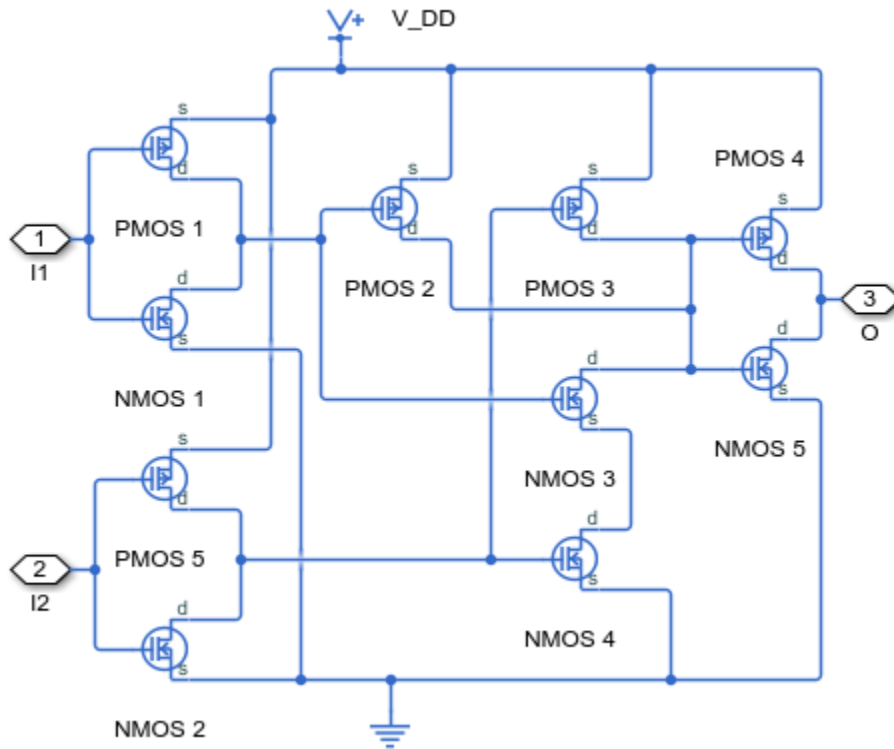
Simulation Results from Simscape Logging

The plots below show the inputs and outputs for the two implementations of the NOR circuit. The behavioral model and implementation model have nearly identical outputs.
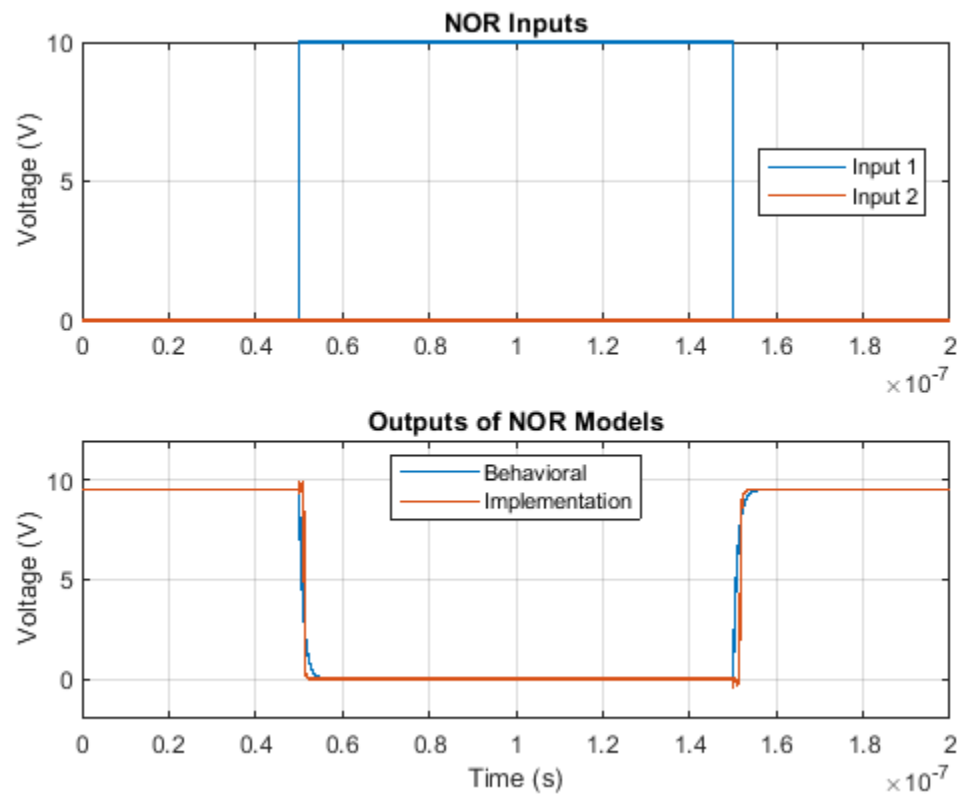
Multiplier Integrated Circuit

This model shows how the Multiplier block can be used to multiply or divide two input voltages. In both cases, slew rate limiting occurs until the final voltage is reached. Note that the input-side circuits must have an Electrical Reference block so that the solver can determine a voltage for every node.

Model
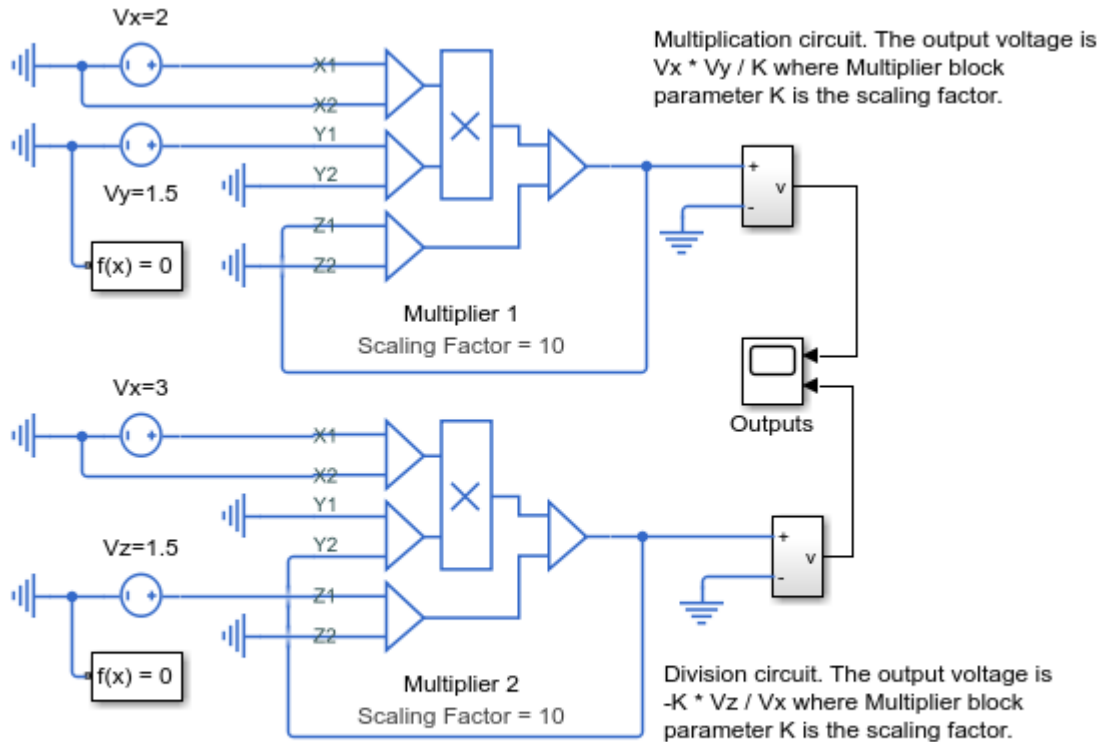
Multiplier Integrated Circuit
1. Explore simulation results using sscexplore
2. Learn more about this example

Simulation Results from Scopes
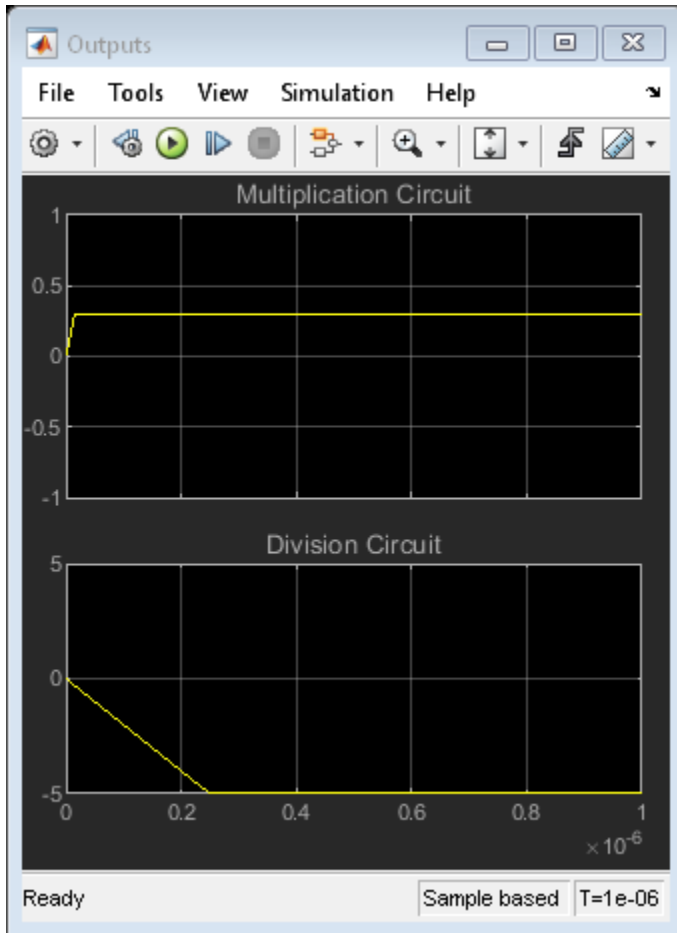
Solar Cell Parameter Extraction From Data

This example shows optimization of the Solar Cell block's parameters to fit data defined over a range of different temperatures. It uses the MATLAB® optimization function `fminsearch`. Other products available for performing this type of parameter fitting with Simscape™ Electrical™ models are the Optimization Toolbox™ and Simulink® Design Optimization™. These products provide predefined functions to manipulate and analyze blocks using GUIs or a command line approach.

Strategy

Fit I-V output curves for an 8 Parameter Solar Cell to data using a 2 step procedure:

1 Optimize parameters in the Solar Cell Main dialog tab to match output curves to data at room temperature.

2 Optimize parameters in the Solar Cell Temperature dialog tab to match output curves to data at non-room temperatures.

Data and Block Setup

The MATLAB data file, *ee_solar_iv_data.mat*, stores Solar Cell data as an array of structures. Each structure contains 3 fields: *temperature* , *i* (current) and *v* (voltage). The Solar Cell block references a data structure to set the device operating temperature, the VPulse block's Pulse value, V2, and to generate simulation output currents at prescribed voltages. The prescribed voltages are set by outputting simulation results at prescribed output times. To set the output times, on the Modeling tab, click Model Settings; in the Configuration Parameters, in the Data Import/Export category, click Additional parameters and specify the Output times. Scopes save the output voltage and current responses as structure data, Io.signal.values and Vo.signal.values.

```
% Load Solar Cell data
load ee_solar_iv_data.mat
% Display the Solar Cell model
Model = 'ee_solar';
open_system(Model)
```
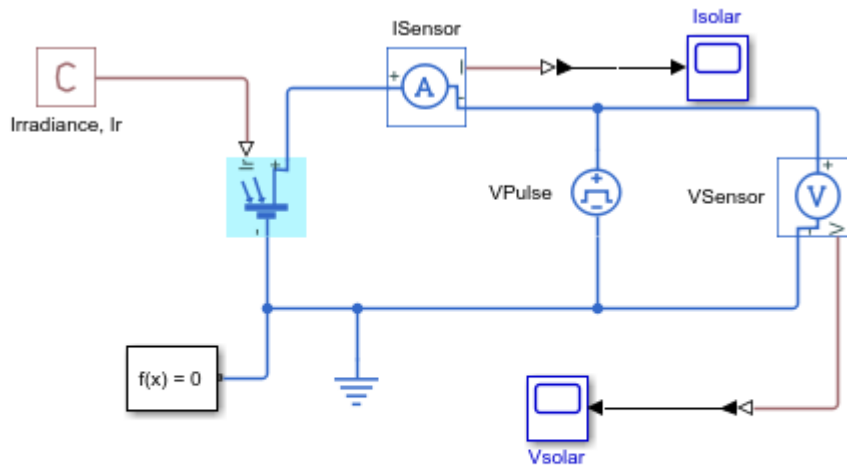
Solar Cell Parameter Tuning

This test model is used by the Solar Cell Parameter Extraction From Data example.
It is invoked by the parameter optimization when tuning parameters to fit the
measured I-V curve data.

```
close_system(Model, 0);
```

Initial Parameter Specification

Starting values for `fminsearch` can be estimated using a combination of Solar Cell block defaults,
data sheet values and the following equations:

$$I_{ph} = I_{sc}$$

$$I_s = \frac{I_{ph}}{(exp(V_{oc}/(.025 * ec)) - 1)}$$

$$R_s = \frac{-dV}{dI}@V_{oc}$$

$$R_p = \frac{-dV}{dI}@I_{sc}$$

List of parameters and initial values prior to optimization

```
ParsListMain = {'Is', 'Iph', 'ec', 'Rs', 'Rp'};
InitGuessMain = [ 3e-7 3.8 1.5 .004 10 ];
ParsListTemp = {'TIPH1', 'EG', 'TXIS1'};
InitGuessTemp = [ .001 1.11 3 ];
```

Since `fminsearch` is an unconstrained nonlinear optimizer that locates a local minimum of a
function, varying the initial estimate will result in a different solution set.

Plot Data Versus Solar Cell Output Using Initial Parameters

Load 8 parameter Solar Cell model and set parameters

```
load_system(Model);
set_param([Model '/Solar Cell'], 'prm', '3')
Pars = reshape([ParsListMain; cellstr(num2str(InitGuessMain'))'],1,[]);
set_param([Model '/Solar Cell'], Pars{:})
Pars = reshape([ParsListTemp; cellstr(num2str(InitGuessTemp'))'],1,[]);
set_param([Model '/Solar Cell'], Pars{:})

% Generate preliminary model curves and plot against data
num_lines = length(iv_data);
v_model = cell(1, num_lines);
i_model = cell(1, num_lines);
legend_info_data  = cell(1, num_lines);
legend_info_model = cell(1, num_lines);
for idx_data = 1:num_lines
    sim(Model);
    v_model{idx_data} = Vo.signals.values;
    i_model{idx_data} = Io.signals.values;
    legend_info_data{idx_data}  = [ 'Temp = '                       ...
        num2str(iv_data(idx_data).temperature) '\circC, Data'];
    legend_info_model{idx_data} = [ 'Temp = '                       ...
        num2str(iv_data(idx_data).temperature) '\circC, Model'];
end
plot([iv_data.v], [iv_data.i], 'd', [v_model{:}], [i_model{:}])
xlabel('Solar cell output voltage (V)');
ylabel('Solar cell output current (A)');
legend([legend_info_data legend_info_model], 'Location', 'Best');
title('Model with Initial Parameter Values');
```
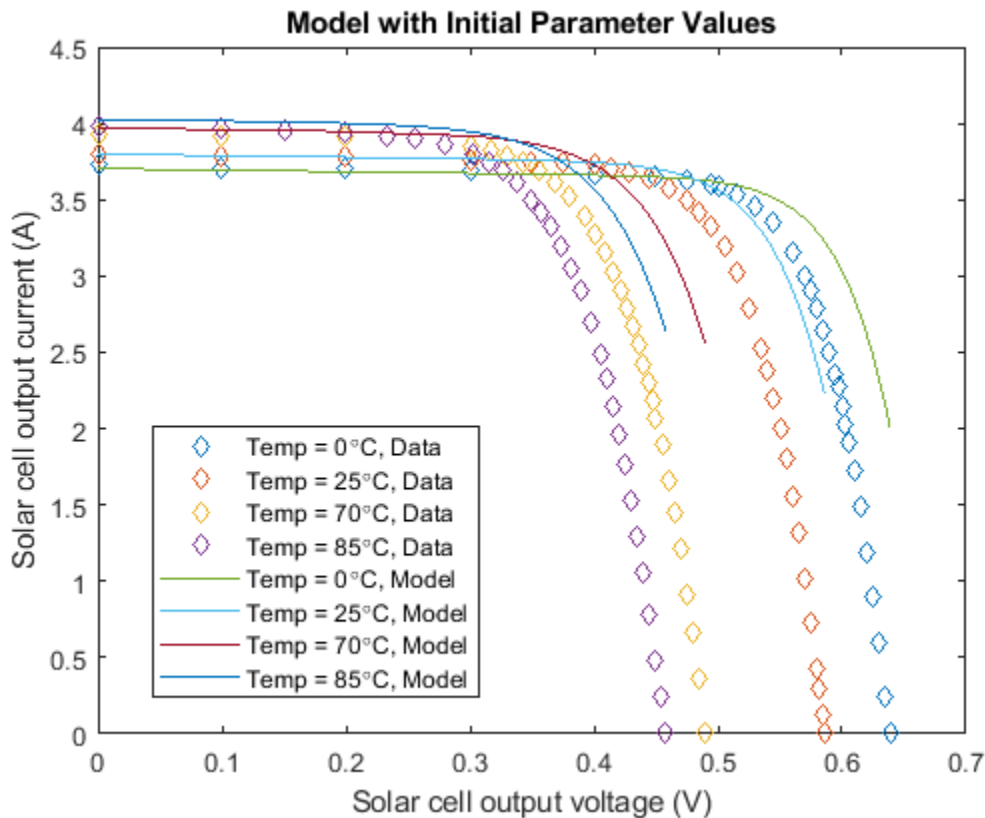
Sum of Squares of Error Calculation

ee_solar_lse is the function to be minimized by fminsearch. This function returns a sum of squares of error for the difference between the solar cell output current and the data. If an invalid parameter value is supplied by fminsearch, the catch statement returns a large value for the error.

Optimize Main Tab Dialog Parameters at Room Temperature (Step 1)

```matlab
% Find room temperature data index
idx_data = find([iv_data.temperature]==25);%#ok

% Optimize parameters in main dialog tab of Solar Cell
ParsList = ParsListMain;
OptParsMain = fminsearch(@ee_solar_lse, InitGuessMain,          ...
    optimset('TolX', 1e-3));

% Update Solar Cell block with optimized parameters
Pars = reshape([ParsList; cellstr(num2str(OptParsMain'))'],1,[]);
set_param([Model '/Solar Cell'], Pars{:});
% Display optimized parameters
display(sprintf(['Optimized parameters for the solar cell main '      ...
    'dialog tab are:\n']));
display(sprintf('\t%5s = %s\n', Pars{:}));
```

```
Optimized parameters for the solar cell main dialog tab are:

      Is = 3.14991e-07
     Iph =     3.80143
      ec =     1.39989
      Rs =  0.00415127
      Rp =     10.1088
```

Optimize Parameters Controlling Temperature Dependence (Step 2)

```matlab
% Find index into data for non-room temperatures
idx_data = find([iv_data.temperature]~=25);

% Optimize parameters in temperature dialog tab of Solar Cell
ParsList = ParsListTemp;
OptParsTemp = fminsearch(@ee_solar_lse, InitGuessTemp,          ...
    optimset('TolX', 1e-3));

% Update Solar Cell block with optimized temperature parameters
Pars = reshape([ParsList; cellstr(num2str(OptParsTemp'))'],1,[]);
set_param([Model '/Solar Cell'], Pars{:});
% Display optimized parameters
display(sprintf(['Optimized parameters for the solar cell '      ...
    'temperature dialog tab are:\n']));
display(sprintf('\t%5s = %s\n', Pars{:}));
```

```
Optimized parameters for the solar cell temperature dialog tab are:

   TIPH1 = 0.00080491
      EG =      1.1385
   TXIS1 =      3.3812
```

Display Optimized Curves

```
for idx_data = 1:num_lines
    sim(Model);
    v_model{idx_data} = Vo.signals.values;
    i_model{idx_data} = Io.signals.values;
end
plot([iv_data.v], [iv_data.i], 'd', [v_model{:}], [i_model{:}])
xlabel('Solar cell output voltage (V)');
ylabel('Solar cell output current (A)');
legend([legend_info_data legend_info_model], 'Location', 'Best');
title('Model with Optimized Parameter Values');
```
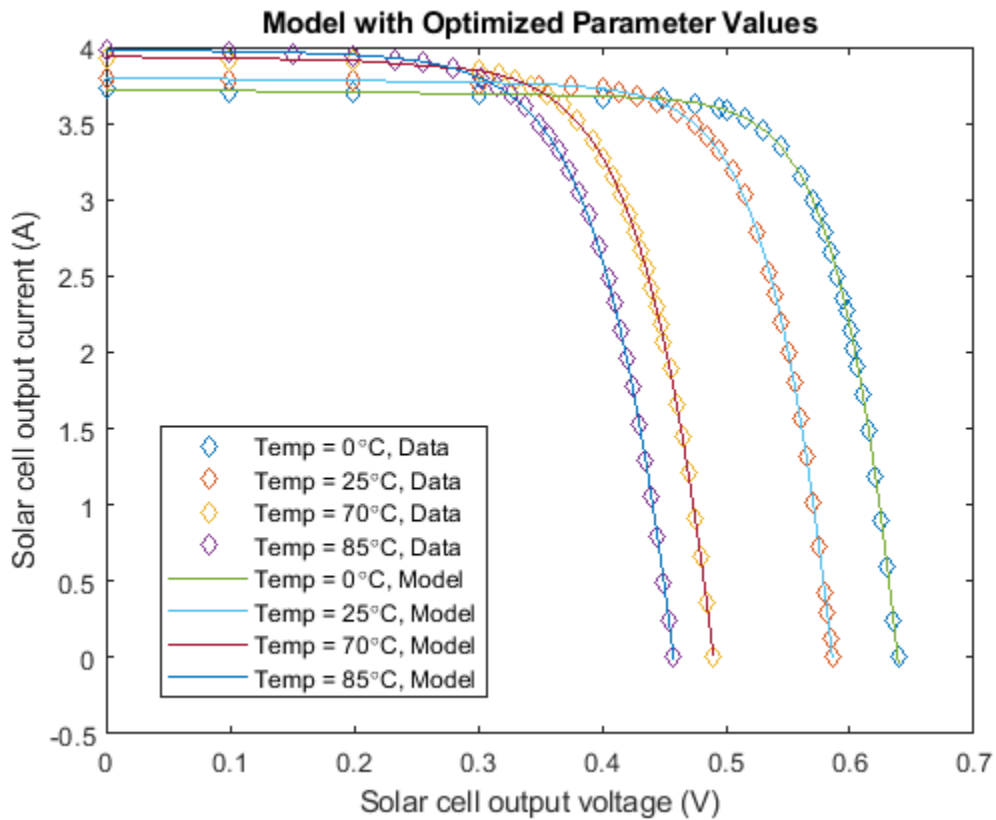


```
bdclose(Model)
```

Master-Slave J-K Flip-Flop

This example shows how to model a J-K flip-flop from Simscape™ Electrical™ logic components. With the two switches in their default positions, both inputs to the flip-flop are set high so its output state toggles each time the clock signal goes low. Initial conditions are passed to the relevant NAND gates via the initialization commands of the block mask.

Model
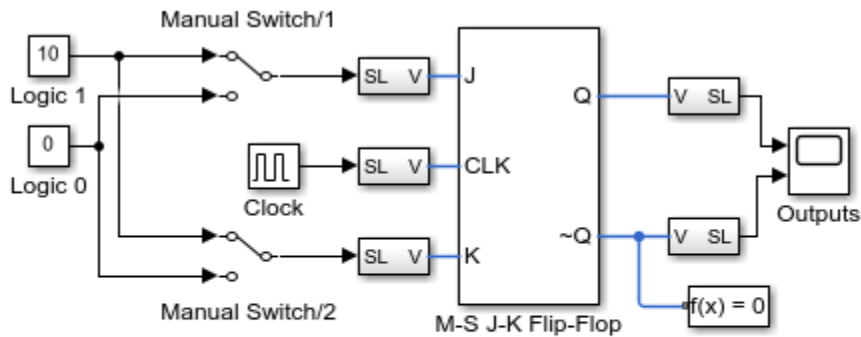
Master-Slave J-K Flip-Flop
1. Plot inputs and outputs for flip-flop (see code)
2. Explore simulation results using sscexplore
3. Learn more about this example

M-S J-K Flip-Flop Subsystem
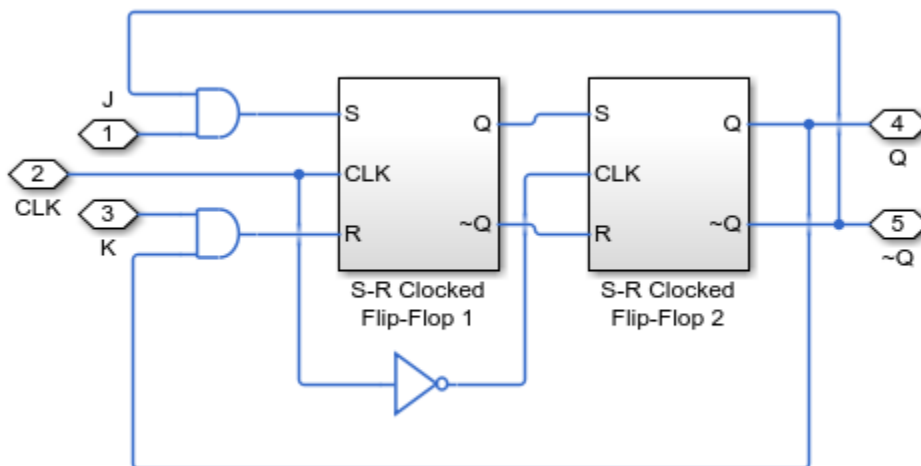
S-R Clocked Flip-Flop 1 Subsystem
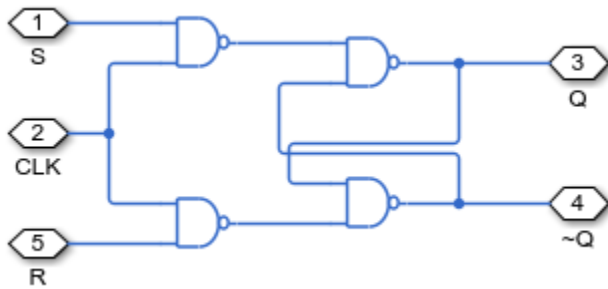
Simulation Results from Simscape Logging

The plots below show the inputs and outputs for the master-slave J-K flip-flop. Both inputs to the flip-flop are set high so its output state toggles each time the clock signal goes low.
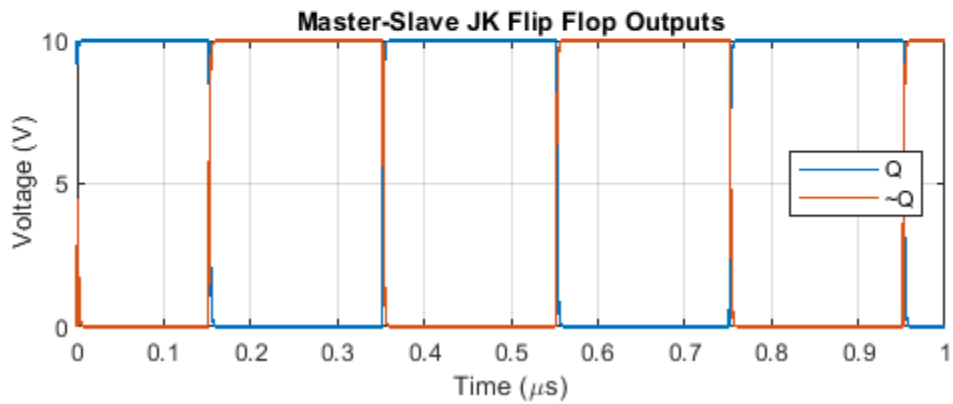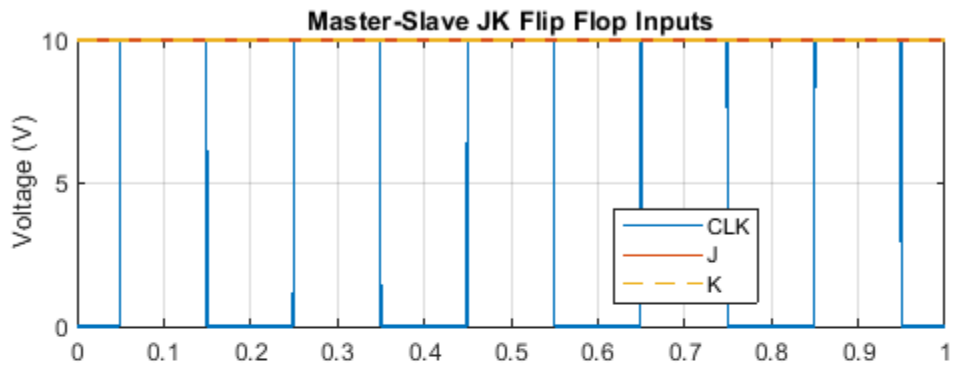
Conducted Emission of a Buck Converter

This example shows a buck converter configured for a measurement of common- and differential-mode noise on the source. In order to simulate the common-mode noise, the capacitive coupling between the circuit and a reference plane must be included in the model. In this circuit, the capacitance between the switching node (between the high- and low-side transistors) and the reference plane is also included.

Ideal line impedance stabilization networks (LISNs) are placed between each terminal of the power supply and the inputs of the buck converter in order to provide a standard impedance and measurement port for the common- and differential-mode noise.

In order to obtain the noise measurement under steady-state conditions, a steady-state operating point has been saved and is used as the starting point for the noise simulation.

Model
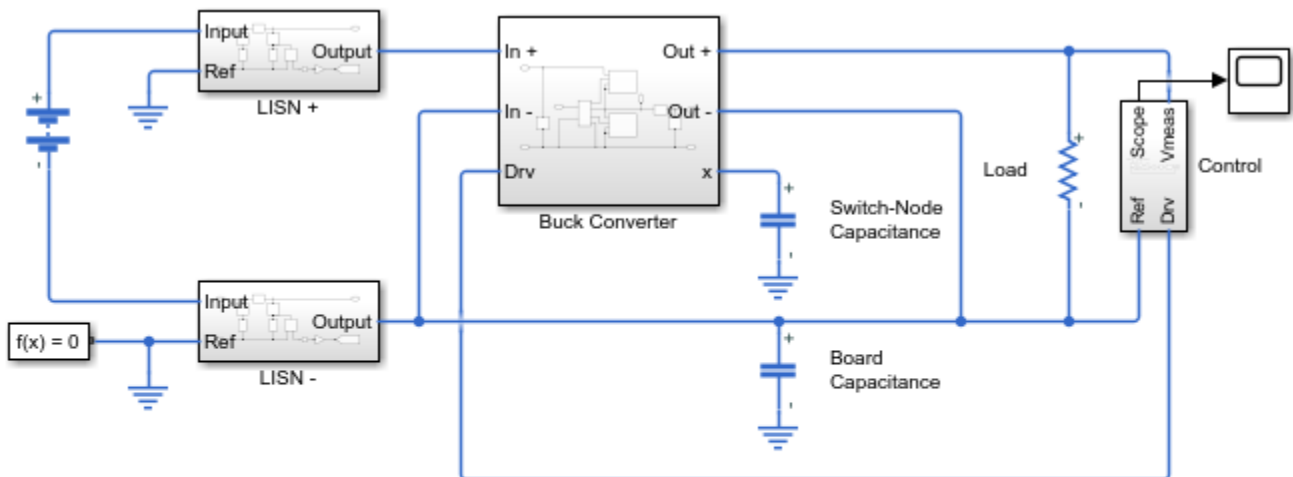
Conducted Emission of a Buck Converter

1. Plot common- and differential-mode noise (see code)
2. See SPICE subcircuit
3. See equivalent Simscape code (doc subcircuit2ssc)
4. Generate steady-state operating point (see code)
5. Explore simulation results using sscexplore
6. Learn more about this example
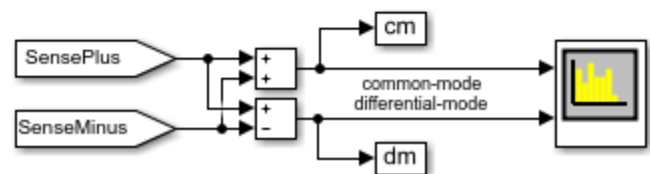
The plot below shows the common- and differential-mode noise for a buck converter operating at a switching frequency of 200kHz.
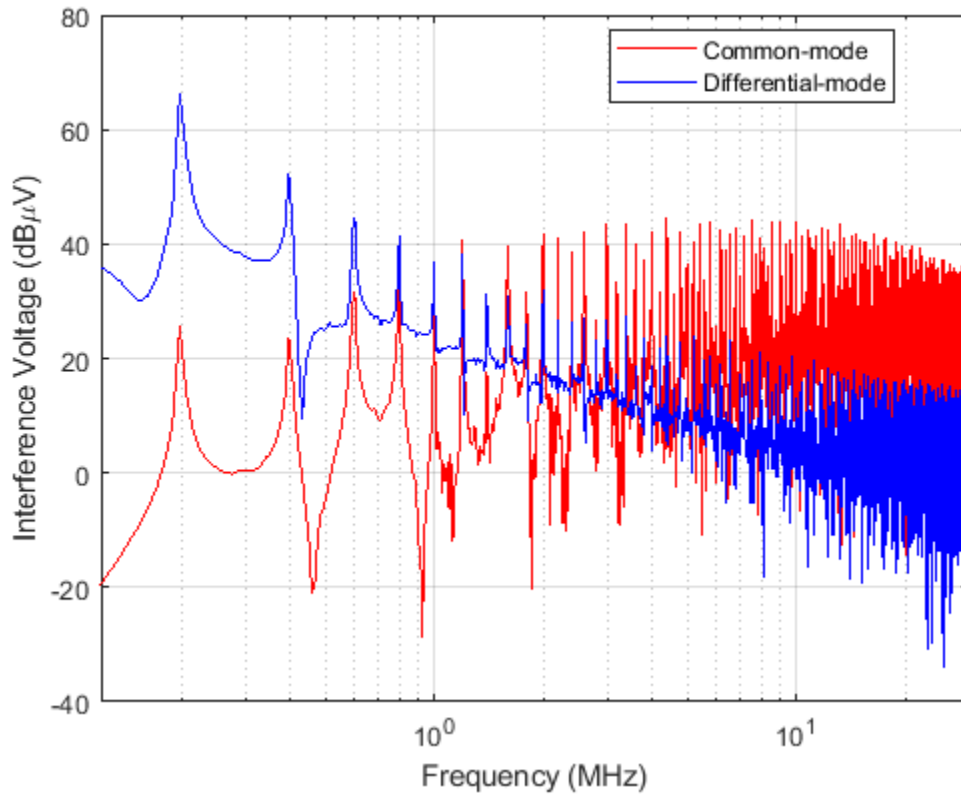
Electrical Transformer with Hysteresis

This example shows how to model a custom transformer that exhibits hysteresis by using the Non Linear Reluctance block in a magnetic circuit. The transformer is rated for a 50W load and steps down from 120V to 12V rms. The magnetizing resistance Rm is modeled in the magnetic domain using an Eddy Loss block.

The transformer initially operates under no-load conditions. At time t=0.05s, the rated load is turned on. For comparison purposes, the transformer is modeled with and without hysteresis by two separate subsystems. The primary and secondary inductance values L1 and L2 plus associated winding numbers N1 and N2 are used to determine the two leakage reluctances.

The magnetic hysteresis losses are determined by calculating the total electrical power flowing into the magnetic circuit from both windings. The difference between power in and power out over a complete AC cycle reflects the hysteresis and eddy losses. In order to obtain just the hysteresis loss, the eddy loss is subtracted from the total. The loss is calculated separately for the no-load and rated load conditions.

Model

Transformer Subsystem

Nonlinear Reluctance
Eddy Loss

Simulation Results from Simscape Logging

```
No-load hysteresis loss = 2.3108 watts
Rated-load hysteresis loss = 2.2044 watts
```
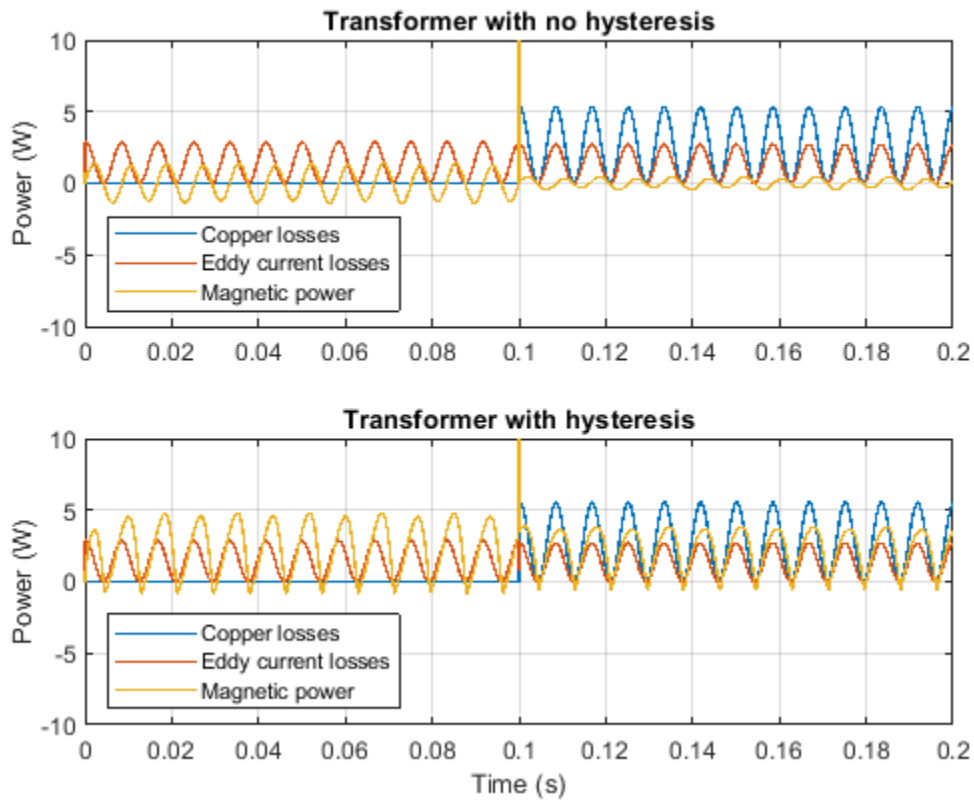
Linear Voltage Regulator with Thermal Effects

This example shows a low-cost voltage regulator circuit whose performance depends on both load current and temperature. Bias resistor R1 ensures that the voltage at the transistor base is close to the rated zener voltage. The regulator output voltage is also approximately at this voltage, the base-emitter voltage being a few tenths of a volt. The precise base-emitter voltage depends on the transistor working point (which in turn depends on the load) and also the temperature. Resistor R2 only serves to provide some protection in the event of a transient output short circuit.

This type of model can be used to validate that selected circuit components result in an adequate level of voltage regulation. It can also be used to size the heatsink required to keep the transistor junction temperature within the permitted operating range. By modeling both electrical and thermal properties, understanding of the trade-offs between electrical and thermal parameter choices is gained.

Better regulation can be achieved by using feedback. See example Linear Voltage Regulator with Feedback (>> ee_voltreg_linear_feedback).

Model
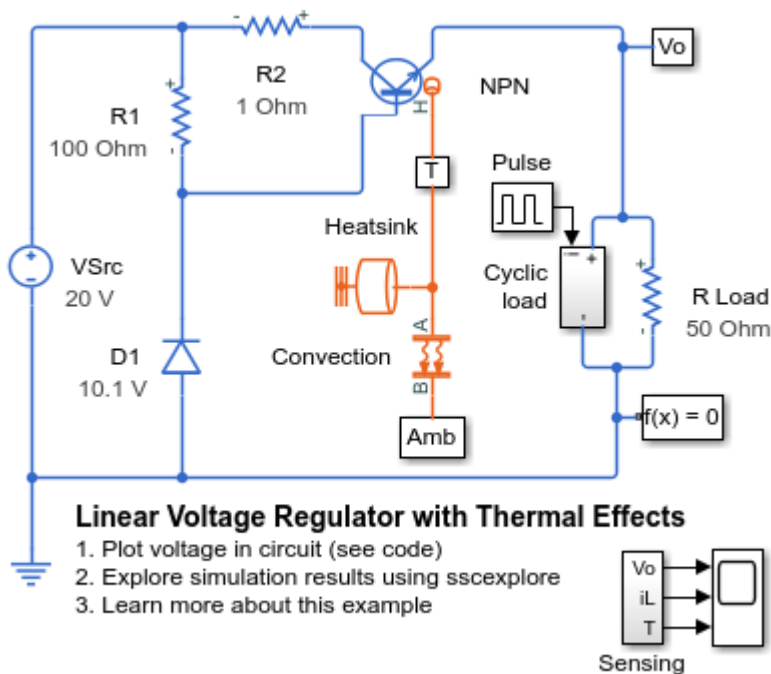
Simulation Results from Simscape Logging

The plot below shows the output of the voltage regulator holding close to the desired output of 10 V. It also shows the temperature of the NPN case and its heatsink as the cyclic load turns on and off.
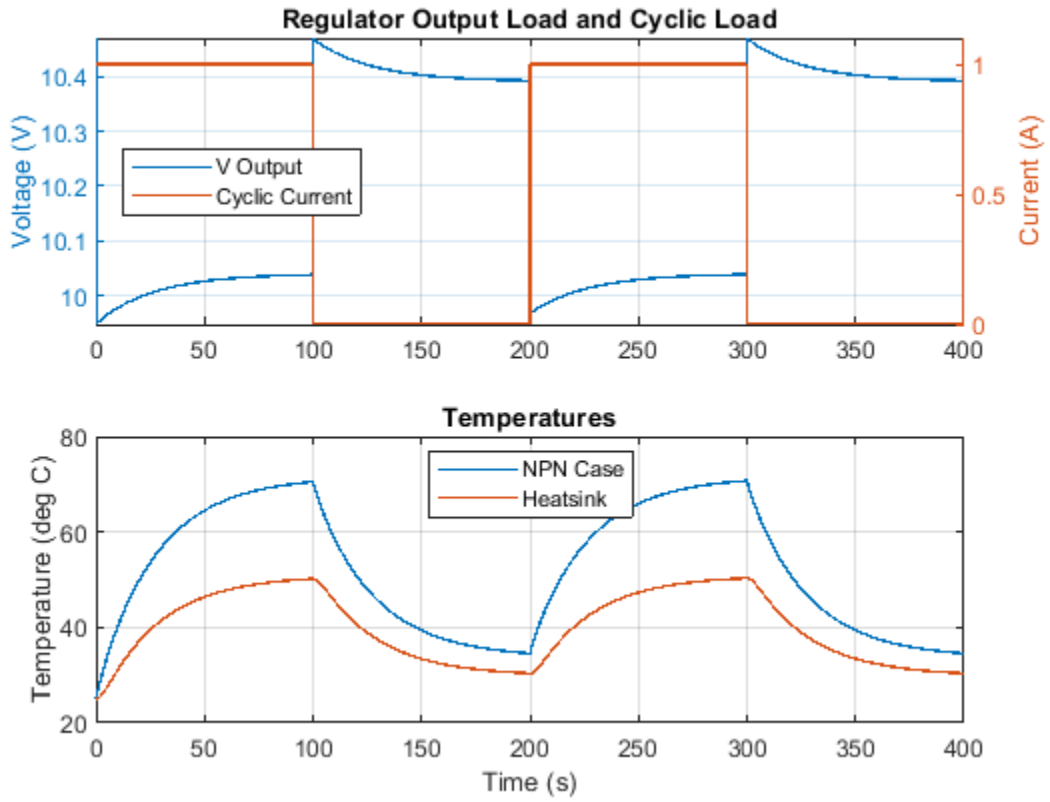
Linear Voltage Regulator with Feedback

This example shows a simple voltage regulator circuit constructed from discrete components. A fluctuating supply is modeled as 20V DC plus a 1V sinusoidal variation. The zener diode D1 sets the non-inverting input of the op-amp to 3.2V, and hence as the op-amp has a large gain, the op-amp inverting input and output are also at 3.2V. Hence the regulator voltage output is regulated to be 3.2*(1000+470)/470=10V. The NPN bipolar transistor is required to provide higher currents than is possible from a typical op-amp. The model can be used to check circuit operation, and to support selection of components to achieve the desired voltage regulation.

Model
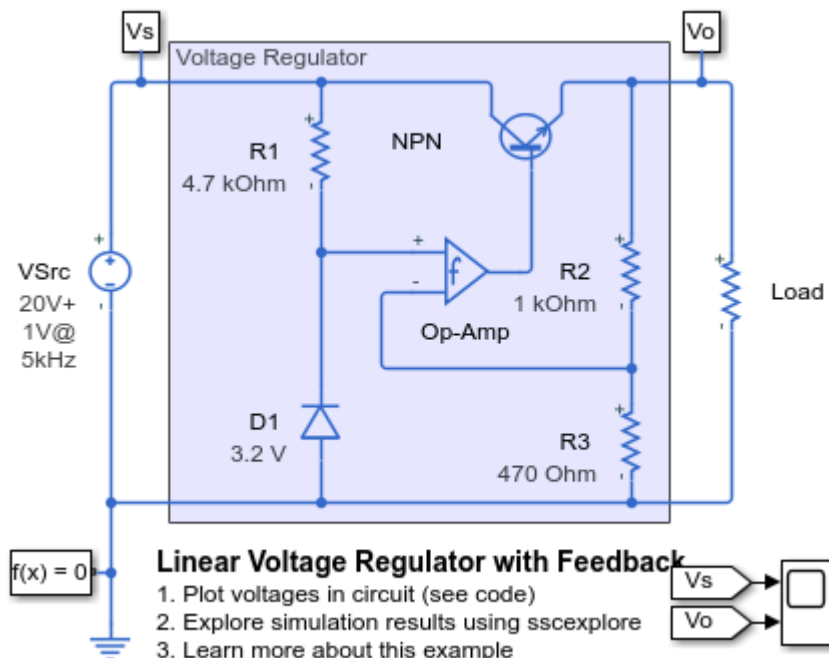
Simulation Results from Simscape Logging

The plot below shows the output of the voltage regulator holding at a constant 10V even as the input voltage fluctuates.
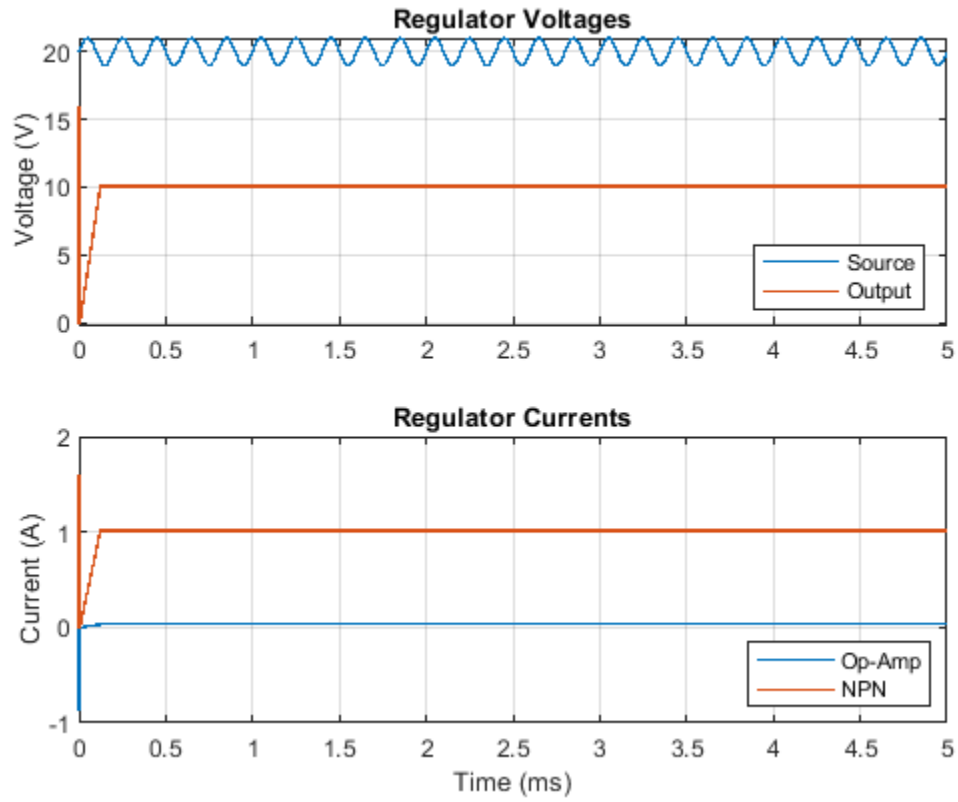
Photovoltaic Generator

This example shows how to create system-level model of a photovoltaic generator that can be used to simulate performance using historical irradiance data. Here the model is tested by varying the irradiance which approximates the effect of varying cloud cover. Power generation steps immediately following the irradiance change. Environmental temperature also varies during the test. The DC-AC converter efficiency is assumed to be a fixed 97 percent, this value having been determined from the ee_solar_inverter example model.

Model
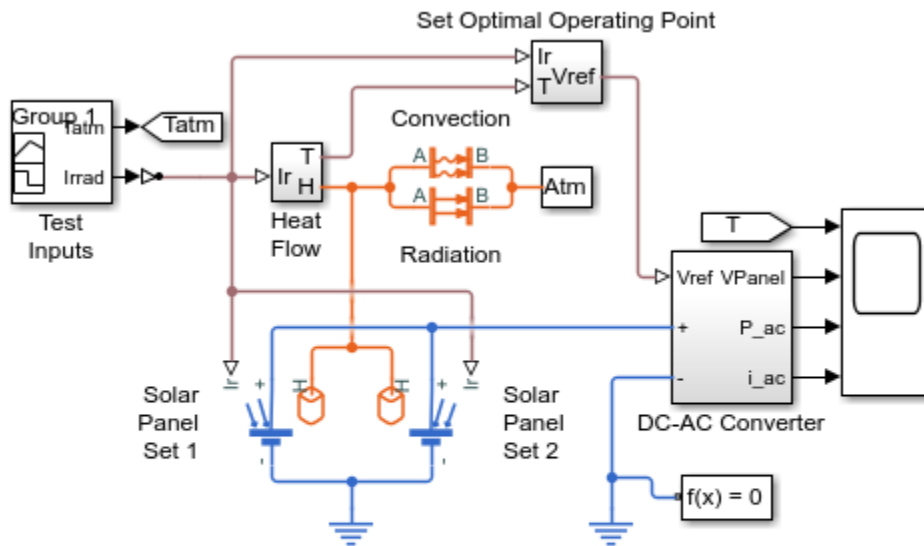
Photovoltaic Generator
1. Plot panel inputs and outputs (see code)
2. Plot voltage with conditions for maximum power (see code)
3. Explore simulation results using sscexplore
4. Learn more about this example

Simulation Results from Simscape Logging

The plot below shows the power output and temperature of the panel over a one hour test. The environmental temperature varies over time, affecting the panel temperature and its conversion efficiency.
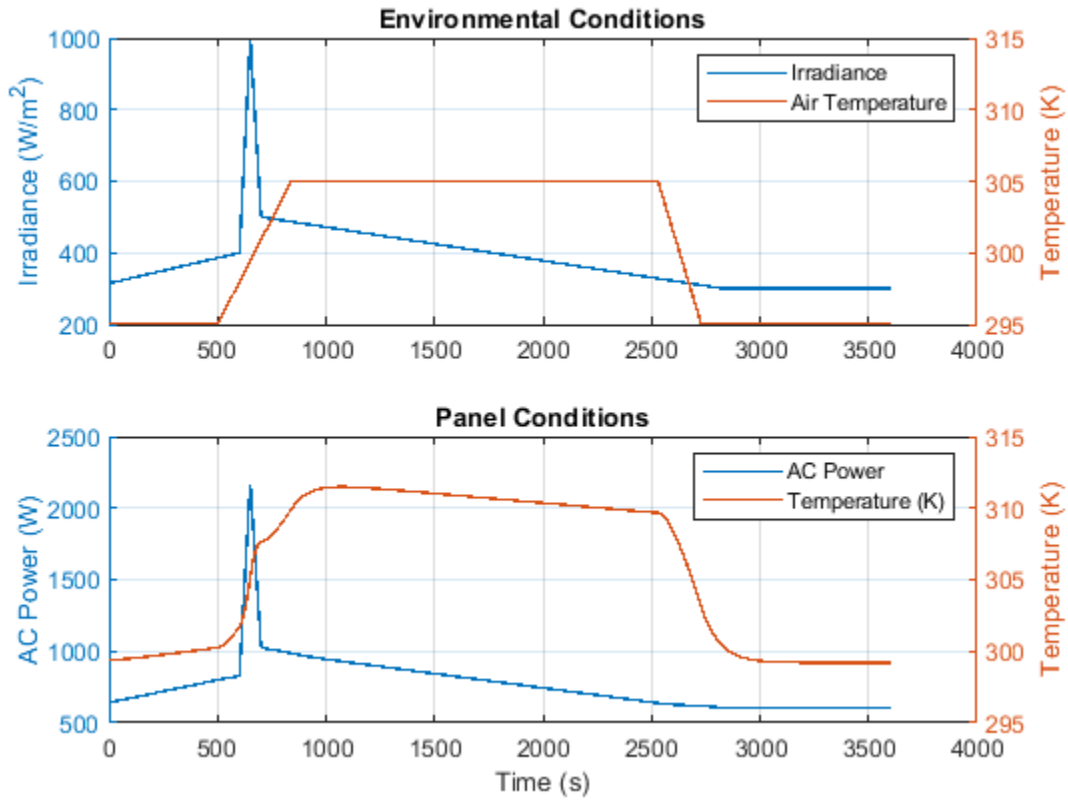
The plot below shows the voltage output of the panel with respect to panel temperature and irradiance. For a given temperature and irradiance, solar panels have a voltage draw that will result in maximum efficiency. The blue dots on the plot are measurements from specific tests, and the surface is a polynomial fit to those points. The control system attempts to keep the voltage drawn from the panel such that it is on this surface and maximizes the efficiency of the panel.

Solar Generator Voltage
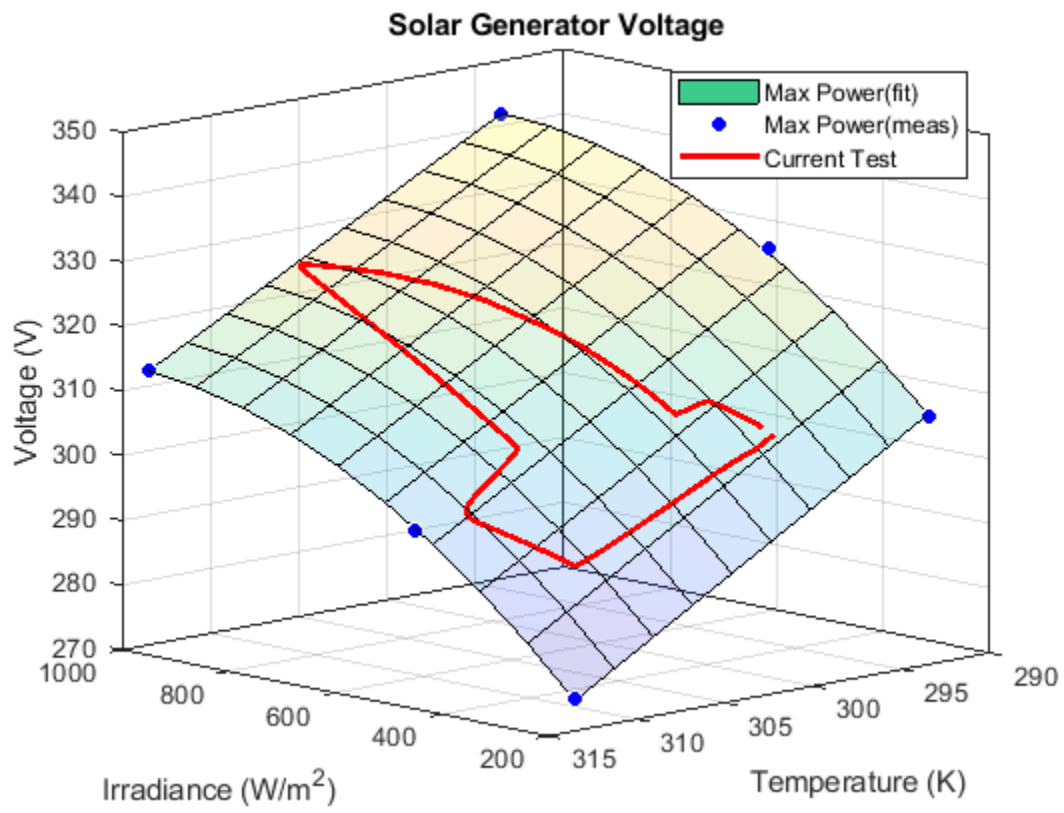
Solar Power Inverter

This example shows how to determine the efficiency of a single-stage solar inverter. The model simulates one complete AC cycle for a specified level of solar irradiance and corresponding optimal DC voltage and AC RMS current. Using the example model ee_solar_characteristics, the optimal values have been determined as 342V DC and 20.05A AC for an irradiance of 1000W/m^2 and panel temperature of 20 degrees Celsius. Inverter efficiency is determined in two independent ways. The first compares the ratio of AC power out to DC power in over one AC cycle. The second calculates losses by component by making use of Simscape™ logging. The small difference in calculated efficiency value is due to differences between trapezoidal integration used by the script and the greater accuracy achieved by the Simulink® variable-step solver.

Model

Solar Power Inverter
1. Plot power dissipated by MOSFETs (see code)
2. Calculate losses by component (see code)
3. Explore simulation results using sscexplore
4. Learn more about this example
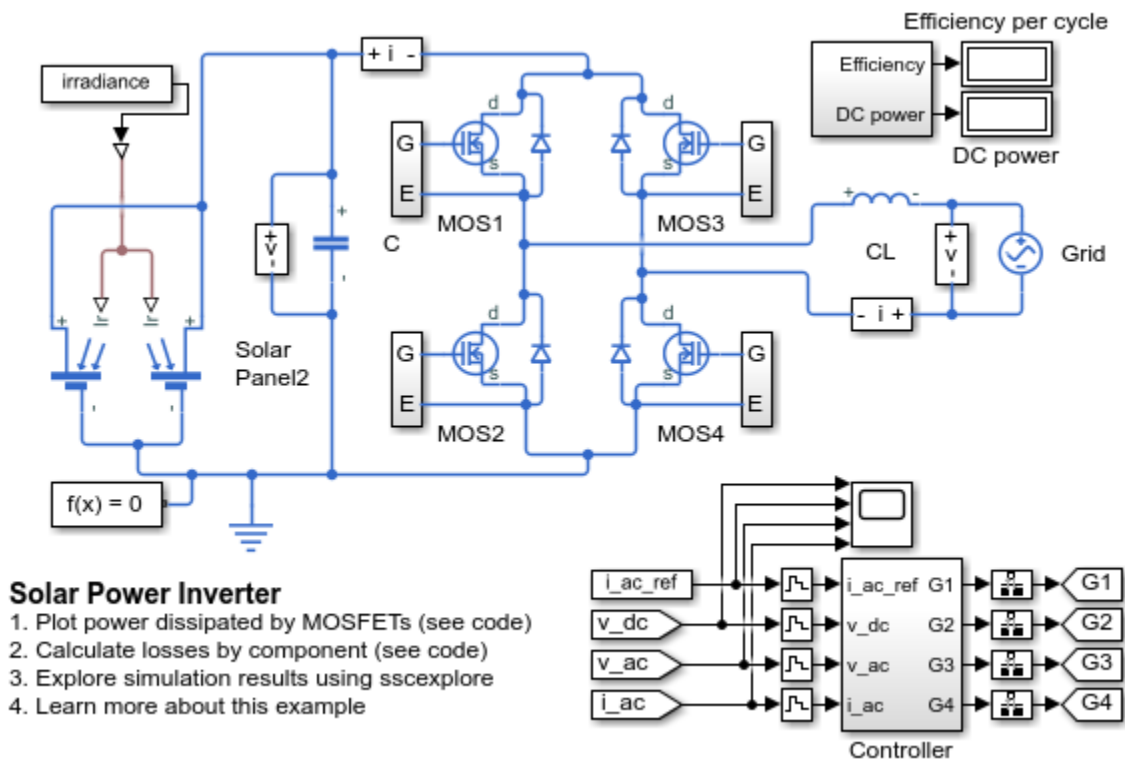
Simulation Results from Simscape Logging

The plots below show the current output from the inverter and the power dissipated by two of the MOSFETs.
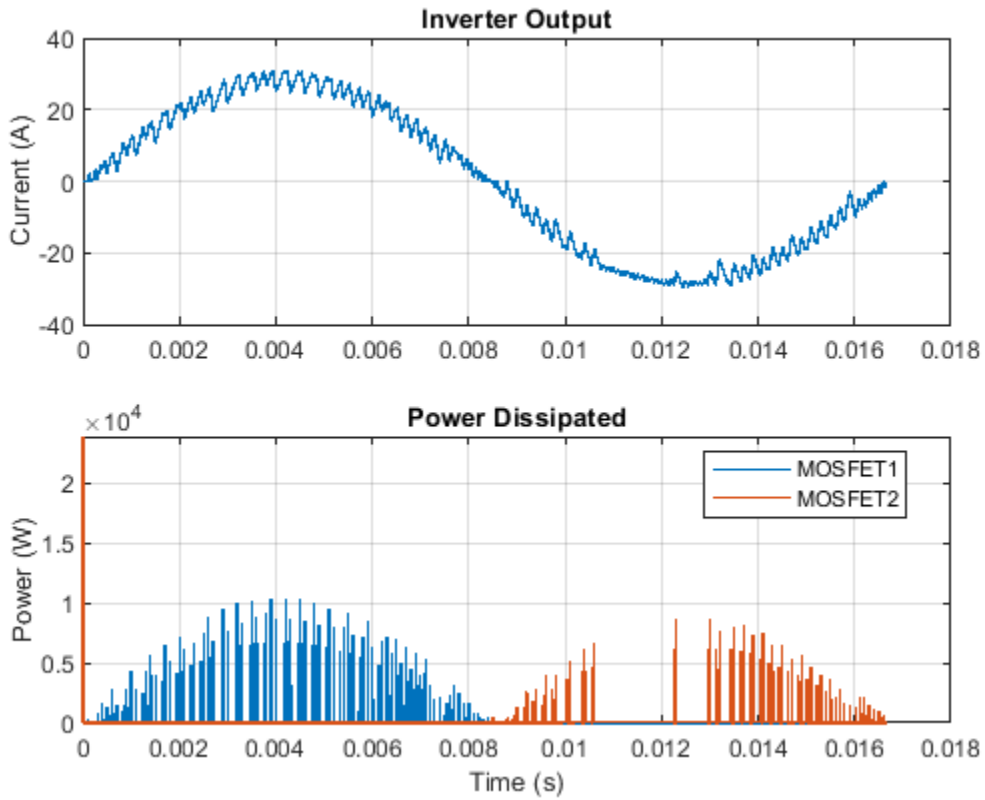
The table below shows the power dissipated by individual components in the ee_solar_inverter model. These totals were calculated from simulation results using logged Simscape variables and the losses calculation utility ee_getPowerLossSummary.

```
Efficiency = 96.7519%
Losses in watts by component are as follows:
          LoggingNode                 Power

    _____      _____

    {'ee_solar_inverter.MOS2' }       18.61
    {'ee_solar_inverter.MOS4' }       17.01
    {'ee_solar_inverter.MOS1' }       15.14
    {'ee_solar_inverter.MOS3' }       13.29
    {'ee_solar_inverter.Diode4'}       3.53
    {'ee_solar_inverter.Diode2'}        2.9
    {'ee_solar_inverter.Diode3'}       2.05
    {'ee_solar_inverter.Diode1'}        1.4
```

Buck Converter

This example shows how to model a switching power supply that converts a 30V DC supply into a regulated 15V DC supply. The model can be used to both size the inductance L and smoothing capacitor C, as well as to design the feedback controller. By selecting between continuous and discrete controllers, the impact of discretization can be explored.

Modeling the switching devices as MOSFETs rather than ideal switches ensures that device on-resistances are correctly represented. The model also captures the switch-on/switch-off timing of the devices, this depending primarily on the gate capacitance values and the PWM driver output resistance.

See example model power_switching_power_supply for an abstracted version of this model that uses ideal switching to give faster simulation times. The model here can be used to determine the on-resistance values required for the ideal switches, plus timing offsets if necessary. Using the ideal switching approach of power_switching_power_supply can be used to simulate more complex power converters.

Workspace variables T_junction1 and T_junction2 are used to define the temperatures at which the two MOSFETs are simulated. Companion models ee_switching_power_supply_thermal and ee_switching_power_supply_thermal_only are used to determine these temperatures.

Model
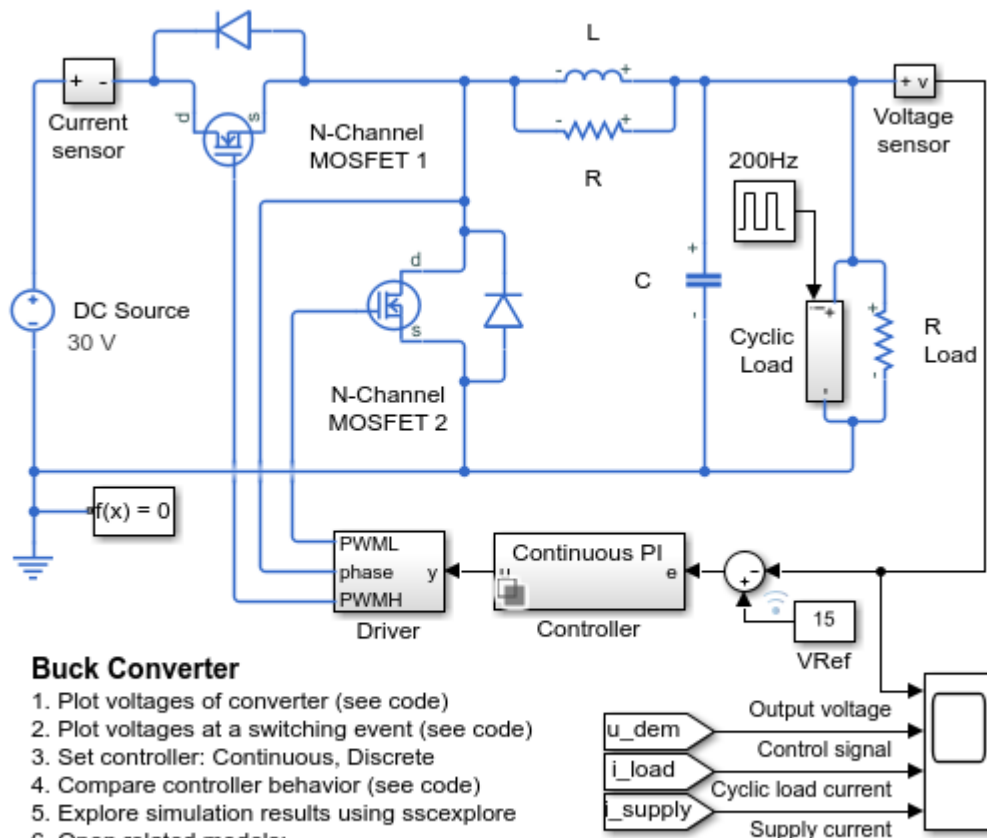
Buck Converter
1. Plot voltages of converter (see code)
2. Plot voltages at a switching event (see code)
3. Set controller: Continuous, Discrete
4. Compare controller behavior (see code)
5. Explore simulation results using sscexplore
6. Open related models:
   switching and heat flow; thermal only; faults
7. Learn more about this example

Simulation Results from Simscape Logging

The plot below shows the output voltage as compared to the reference voltage. It also shows the changing load current and the dissipated power of the two MOSFETS averaged over the PWM cycle.
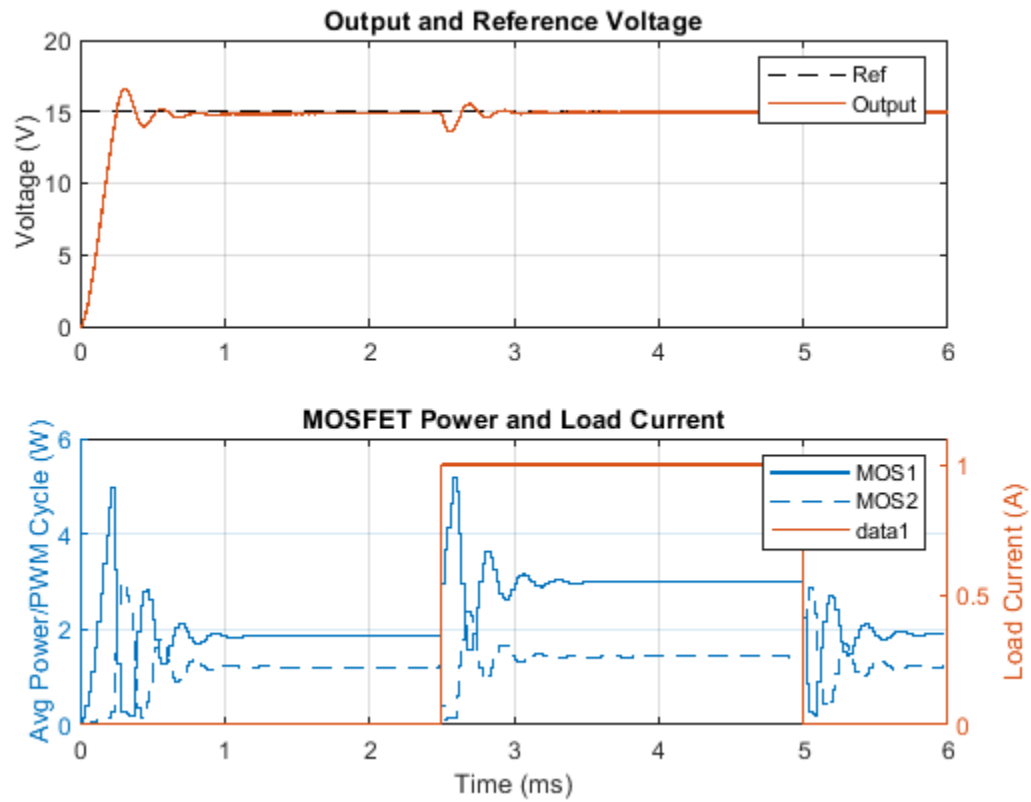
The plot below shows the switch-on/switch off timing of the two MOSFETS and the drain-source current.
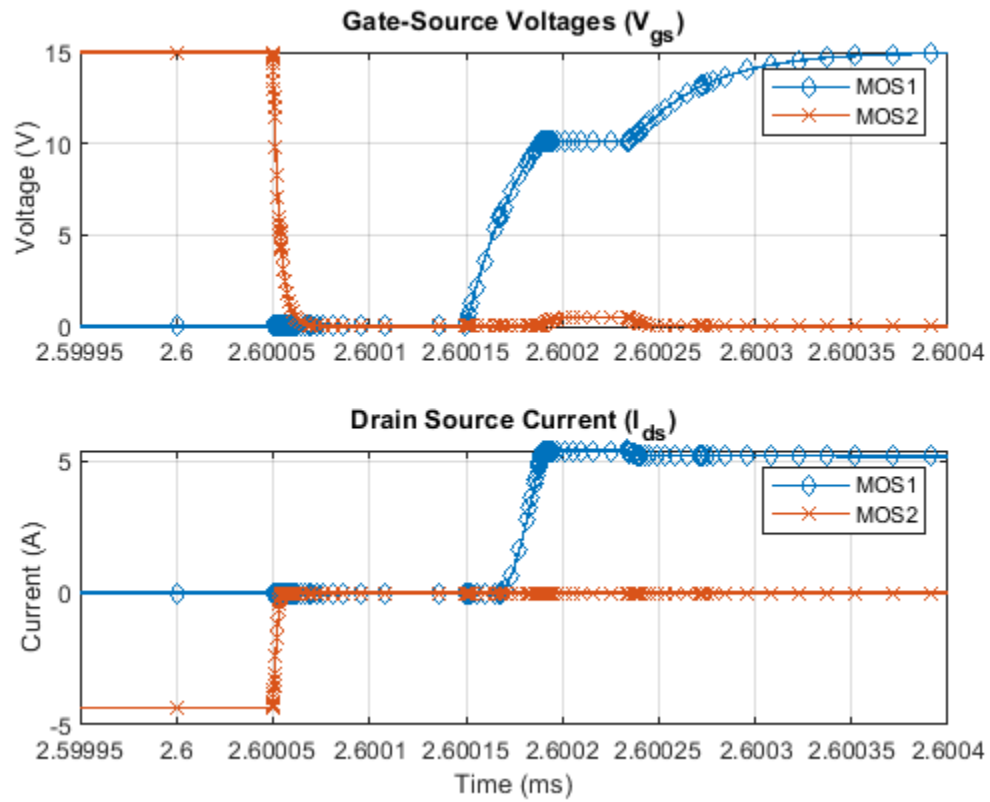
The plots below show the behavior of the different implementations of the PI controller.
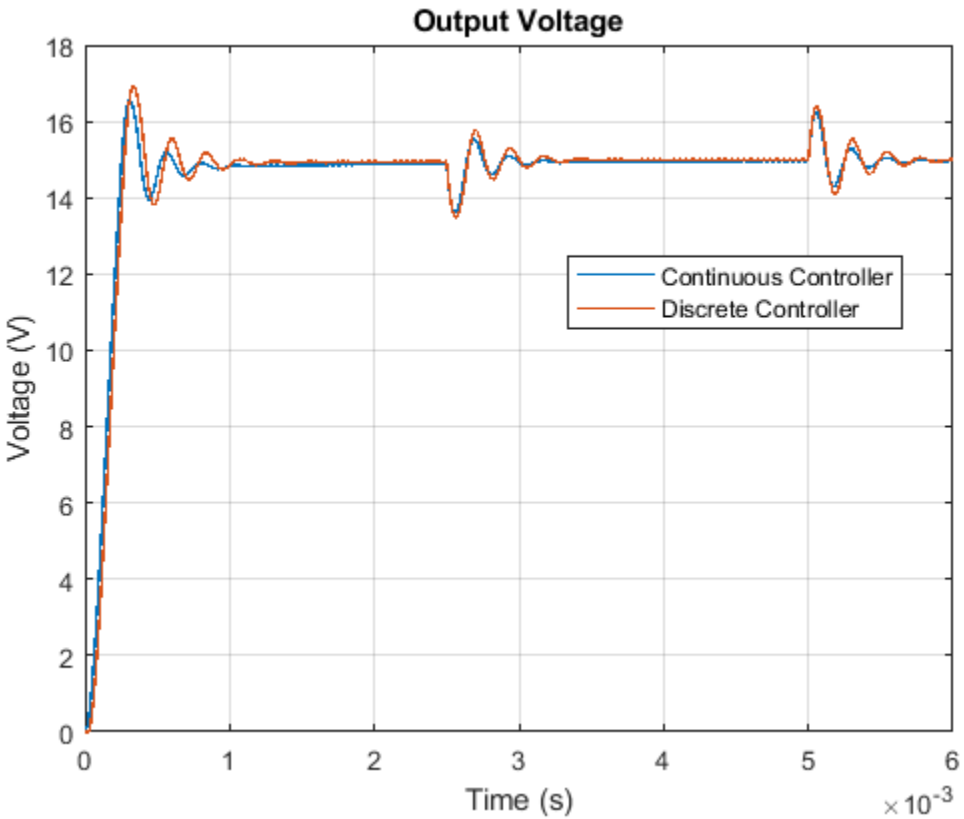
Buck Converter With Thermal Dynamics

This example shows how to model a switching power supply that converts a 30V DC supply into a regulated 15V DC supply. The model can be used to both size the inductance L and smoothing capacitor C, as well as to design the feedback controller. By selecting between continuous and discrete controllers, the impact of discretization can be explored. Modeling the switching devices as MOSFETs rather than ideal switches ensures that device on-resistances are correctly represented. The model also captures the switch-on/switch-off timing of the devices, this depending primarily on the gate capacitance values and the PWM driver output resistance.

See example model power_switching_power_supply for an abstracted version of this model that uses ideal switching to give faster simulation times. The model here can be used to determine the on-resistance values required for the ideal switches, plus timing offsets if necessary. Using the ideal switching approach of power_switching_power_supply can be used to simulate more complex power converters.

The MOSFETs are configured to show thermal ports, these being connected to subsystems modeling the heatsinks and environment. The total heat flow is calculated and shown by Scope 3. For an abstracted model of the thermal dynamics only, see the 'Thermal Characteristics of a Synchronous Buck Converter' example, ee_switching_power_supply_thermal_only. The abstracted model is used to determine initial temperatures for this model. Diode thermal ports are not exposed as their thermal contribution is very small compared to that of the MOSFETs

Model
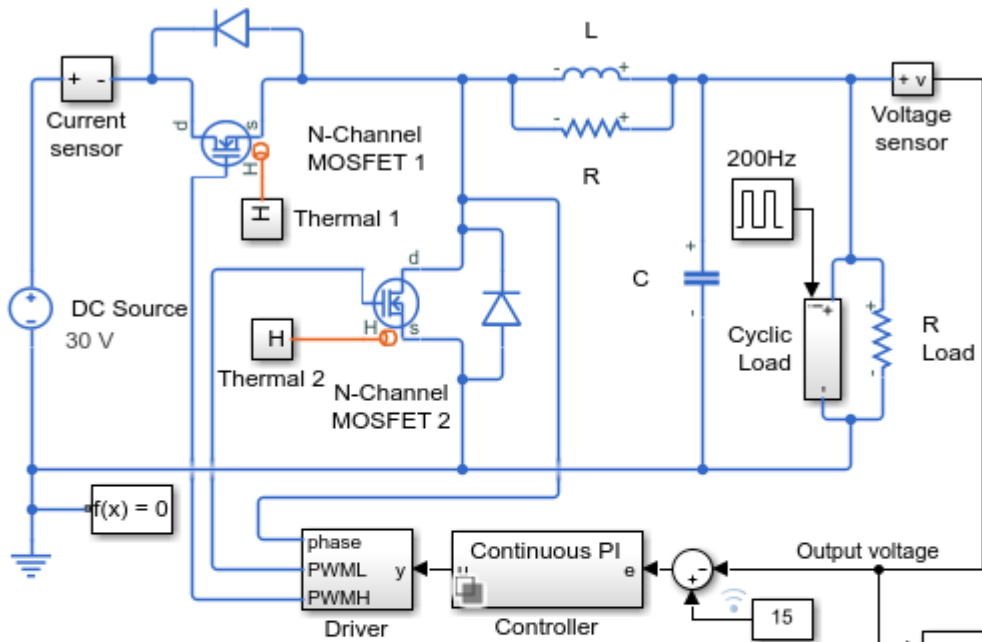
Buck Converter With Thermal Dynamics

1. Plot voltages of converter (see code)
2. Plot voltages at a switching event (see code)
3. Set controller: Continuous, Discrete
4. Compare controller behavior (see code)
5. Explore simulation results using sscexplore
6. Open related models:
   switching and heat flow; thermal only; faults
7. Learn more about this example
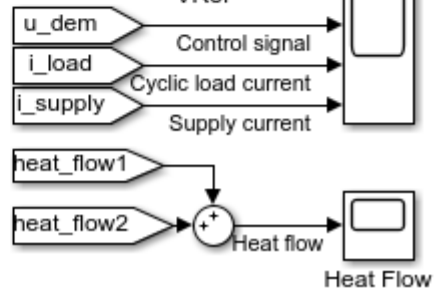
Driver Subsystem
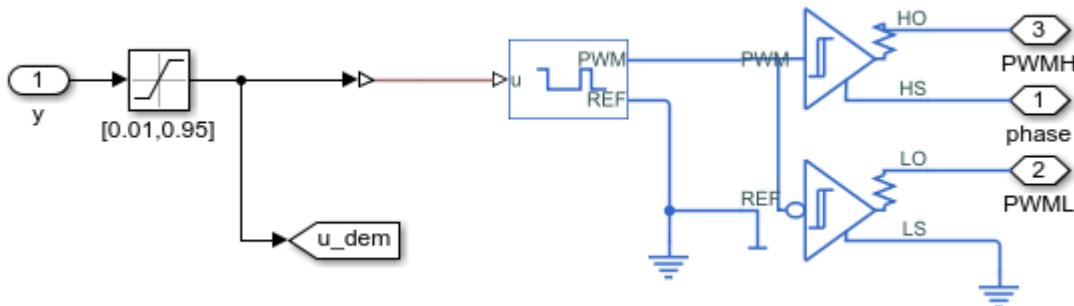
Thermal 1 Subsystem
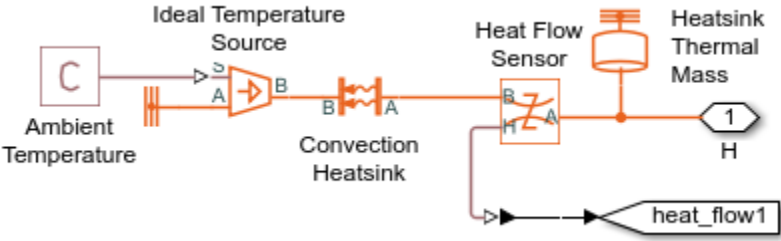
Simulation Results from Simscape Logging

The plot below shows the output voltage as compared to the reference voltage. It also shows the changing load current and the dissipated power of the two MOSFETS averaged over the PWM cycle.
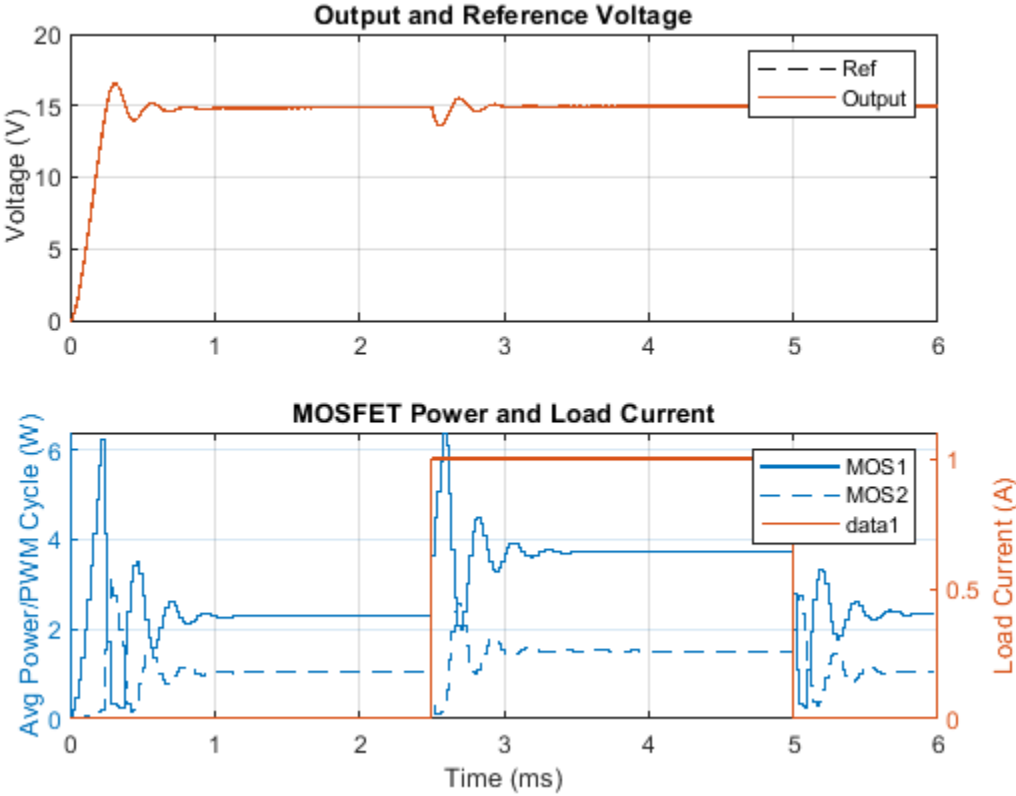
The plot below shows the switch-on/switch off timing of the two MOSFETS and the drain-source current.
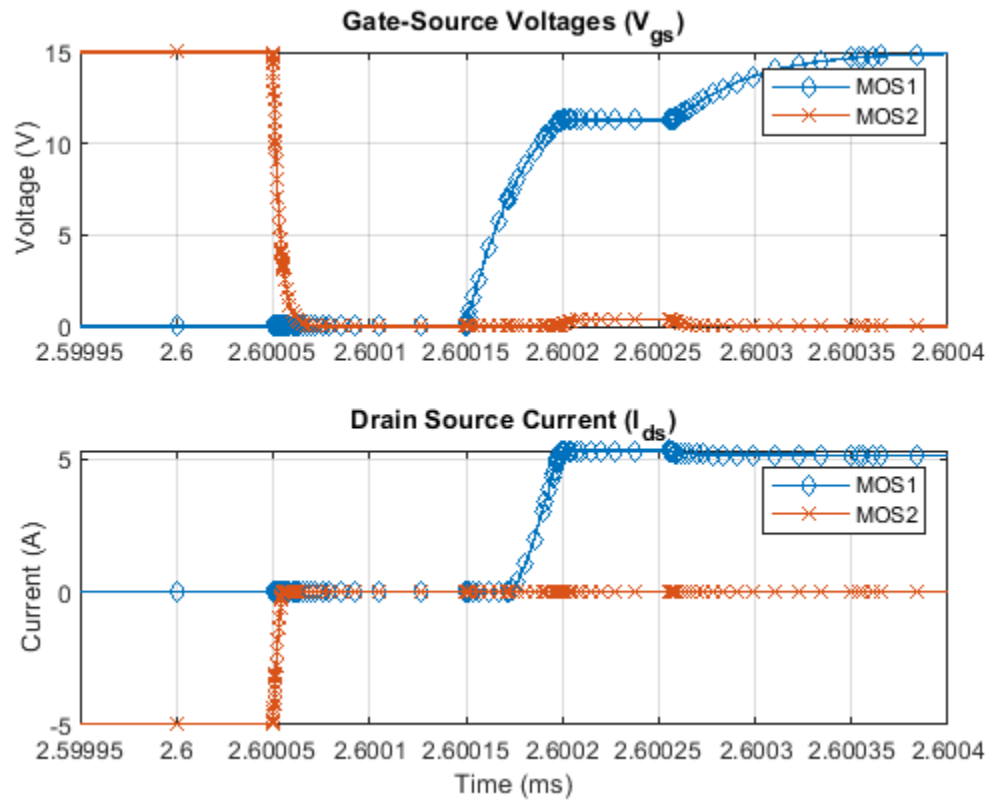
The plots below show the behavior of the different implementations of the PI controller.
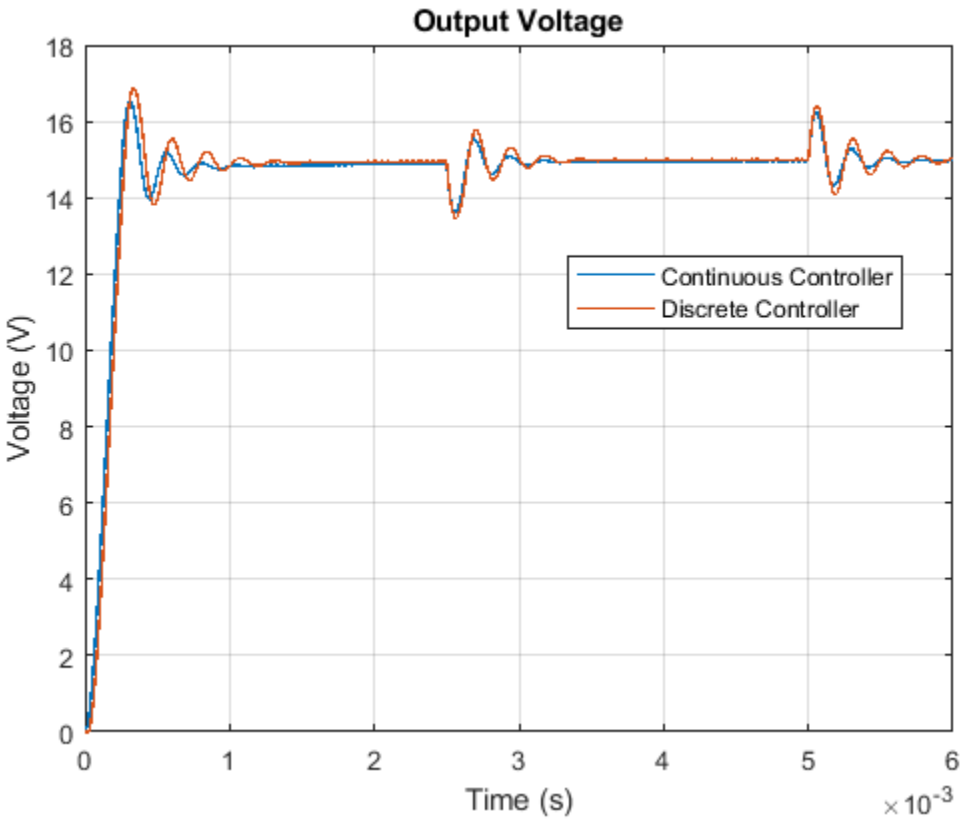
Buck Converter Thermal Model

This example models the thermal dynamics of MOSFETs in a synchronous buck converter. It matches the structure of the Synchronous Buck Converter With Thermal Dynamics model (>> ee_switching_power_supply_thermal). Omitting the electrical switching dynamics allows the simulation to take much larger time steps, dramatically reducing the amount of time it takes for the simulation to calculate steady-state temperatures for the MOSFETS.

The average losses during switching cycles are needed to determine the heat that the MOSFETs produce. This is obtained by running the detailed model (>> ee_switching_power_supply) which postprocesses the logged data and saves the losses to workspace variables P_MOSFET1 and P_MOSFET2. The simplified thermal model can then be run, and the final temperatures of the MOSFETs are saved to workspace variables T_junction1, T_case1, T_heatsink1, T_junction2, T_case2 and T_heatsink2. Re-running the detailed model using these temperatures as the starting conditions yields a more accurate result for the behavior of the system since the device characteristics depend on temperature.

Model
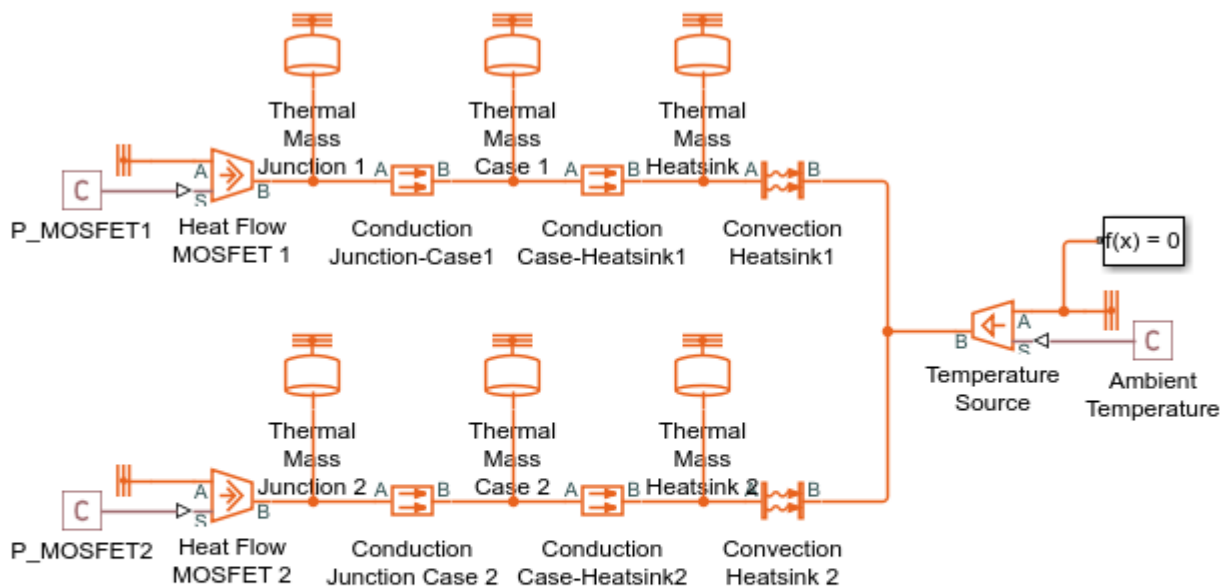
Buck Converter Thermal Model
1. Plot temperatures of MOSFETS (see code)
2. Explore simulation results using sscexplore
3. Open electrical model: switching; switching and heat flow; faults
4. Learn more about this example

Simulation Results from Simscape Logging

The plot below shows the temperature of the MOSFETS over time. Running the simulation for a long period of time enables the steady-state temperatures to be determined. The final temperatures of this simulation can be used as the starting conditions for a detailed electrical simulation where the device characteristics depend on temperature.
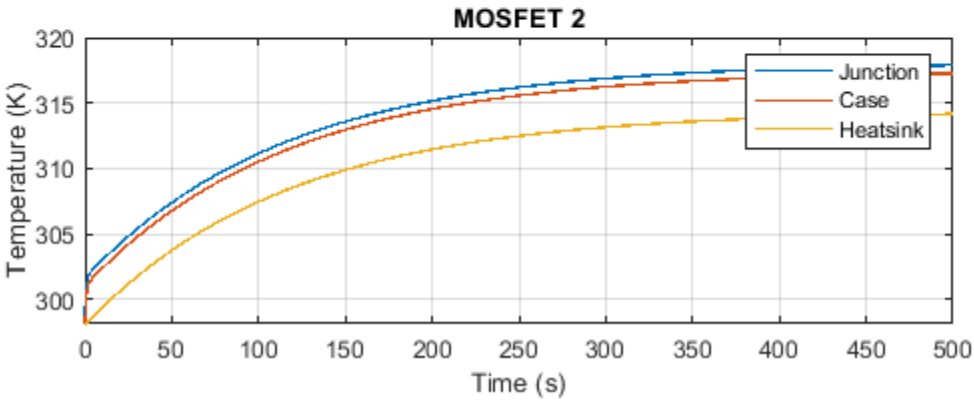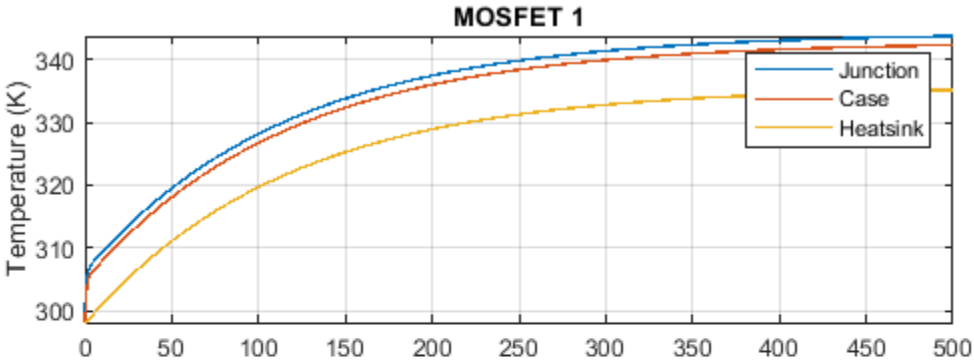
Buck Converter with Faults

This example shows how to model and assess the impact of component tolerances and fault events on the operation of a switching power supply. The R, L, and C components all have tolerances, operational limits, and faults defined. The faults can be enabled within the block dialog or using MATLAB® Commands. The capacitor fault is already enabled to cut in at 1.5e-3 seconds.

Model
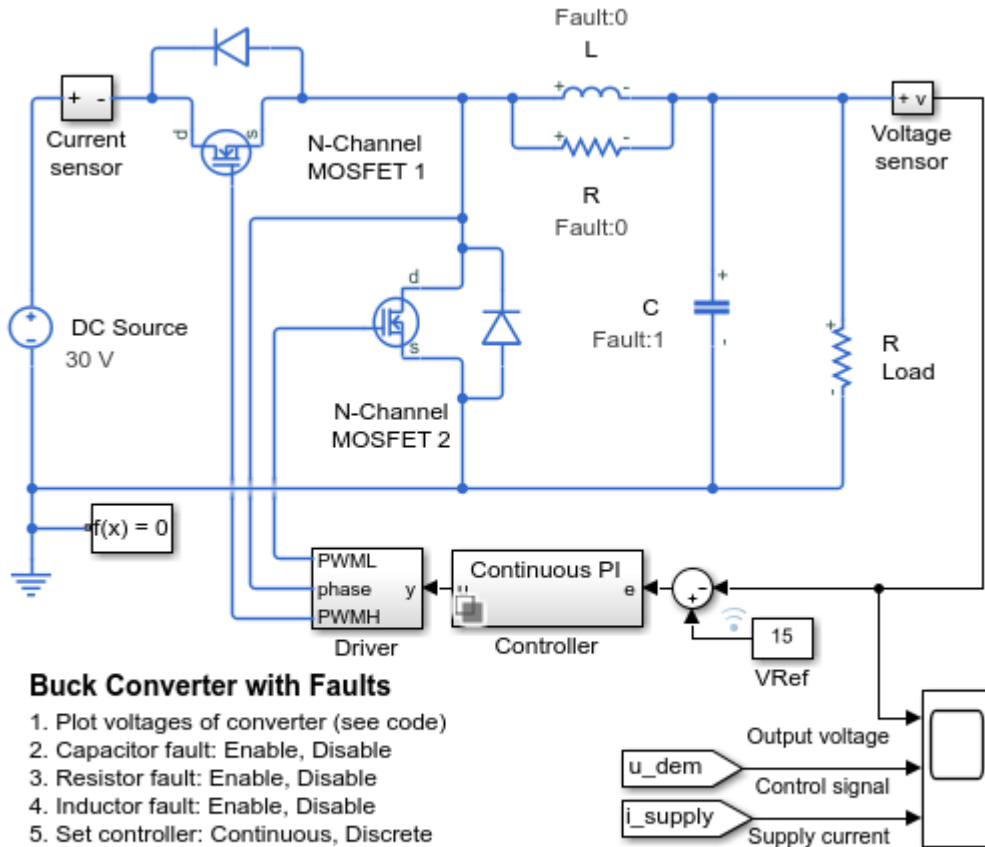
Buck Converter with Faults

1. Plot voltages of converter (see code)
2. Capacitor fault: Enable, Disable
3. Resistor fault: Enable, Disable
4. Inductor fault: Enable, Disable
5. Set controller: Continuous, Discrete
6. Compare controller behavior (see code)
7. Explore simulation results using sscexplore
8. Open related models: switching and heat flow; thermal only
9. Learn more about this example

Simulation Results from Simscape Logging

The plot below shows the output voltage as compared to the reference voltage. It also shows the control signal that adjusts the duty cycle of the PWM signals applied to the MOSFET gates. As the faults occur, the effect can be seen on the control signal and the voltage supplied by the converter.
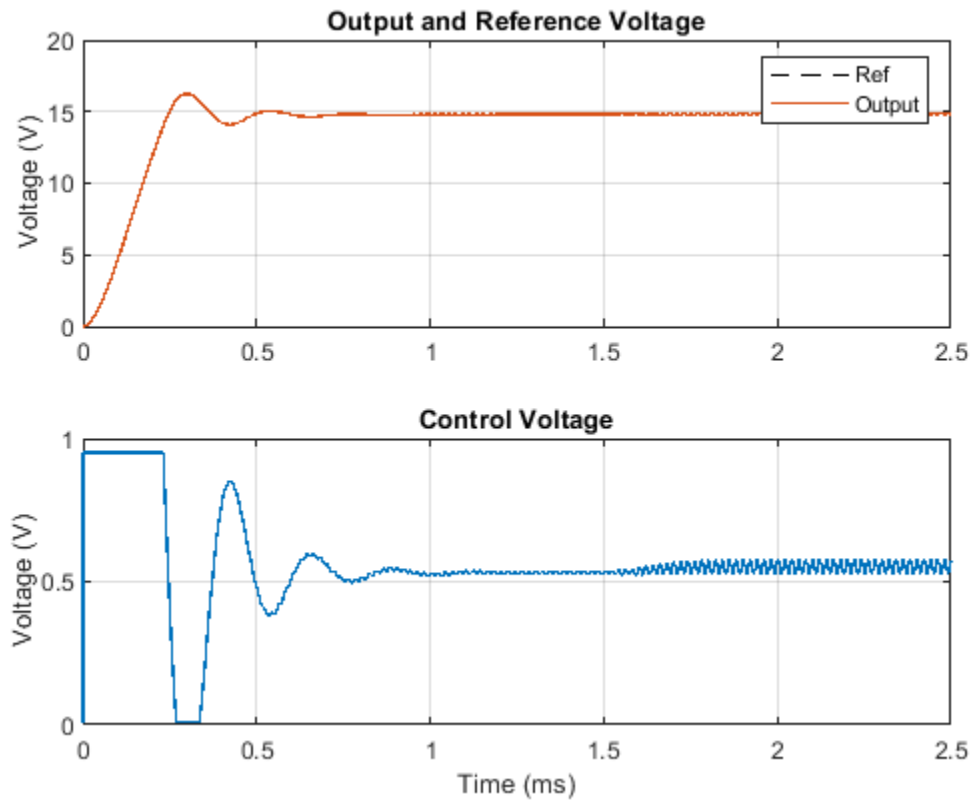
The plots below show the behavior of the different implementations of the PI controller.
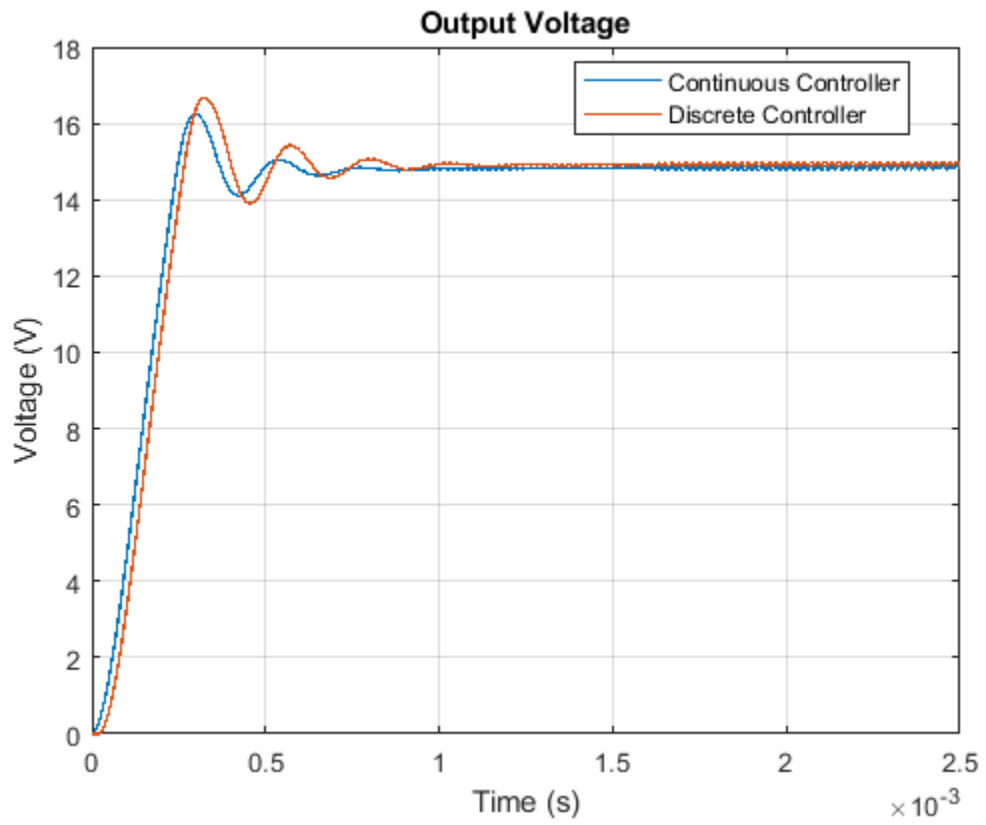
Flyback Converter

This model shows how a flyback converter can step-up a 5V DC source into a 15V DC regulated supply. The voltage is increased by creating a time-varying voltage across a transformer primary. The transformer steps up the voltage which is then rectified back to DC by the diode. Closed-loop control over the output voltage is effected by controlling the switching frequency on the primary side.

The model can be used to size the transformer inductance and smoothing capacitor values, as well as to design the feedback controller gains and switching frequency. The model includes a detailed representation of the switching device, in this case an enhancement-mode MOSFET, which lets you check that the switching device dissipated power is within acceptable limits. The Power Sensor block is used here in averaging mode, thereby calculating the average power over each PWM cycle. Postprocessing data available in Simscape™ logging can also show the power dissipated by the MOSFET.
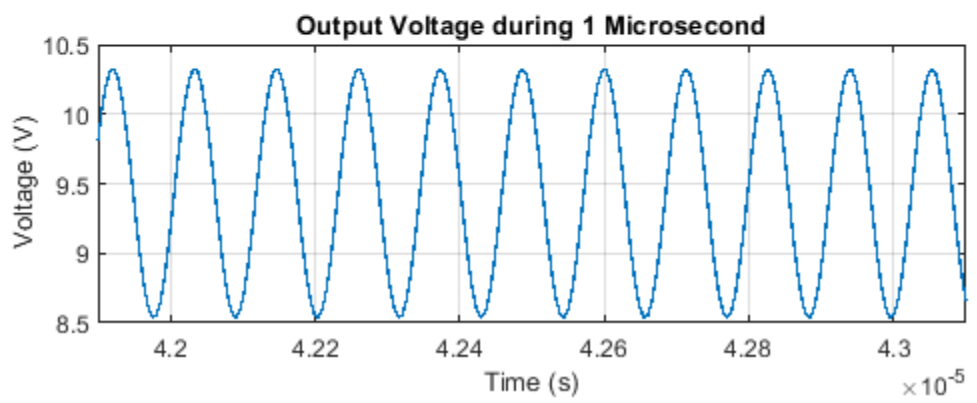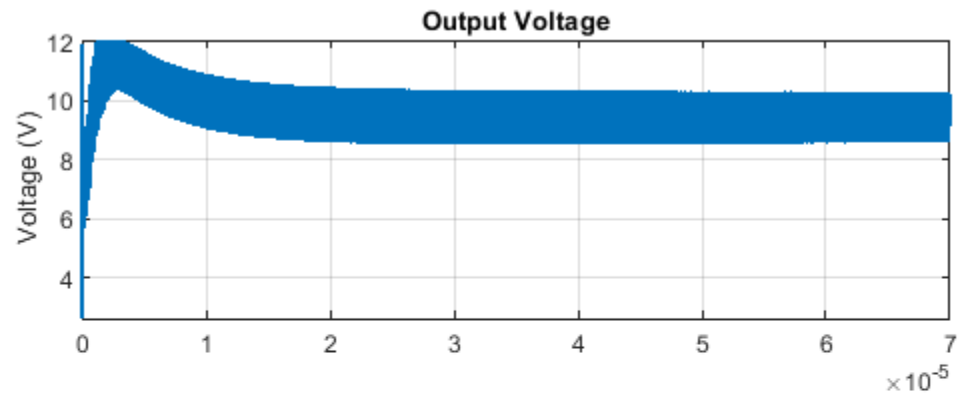
Model
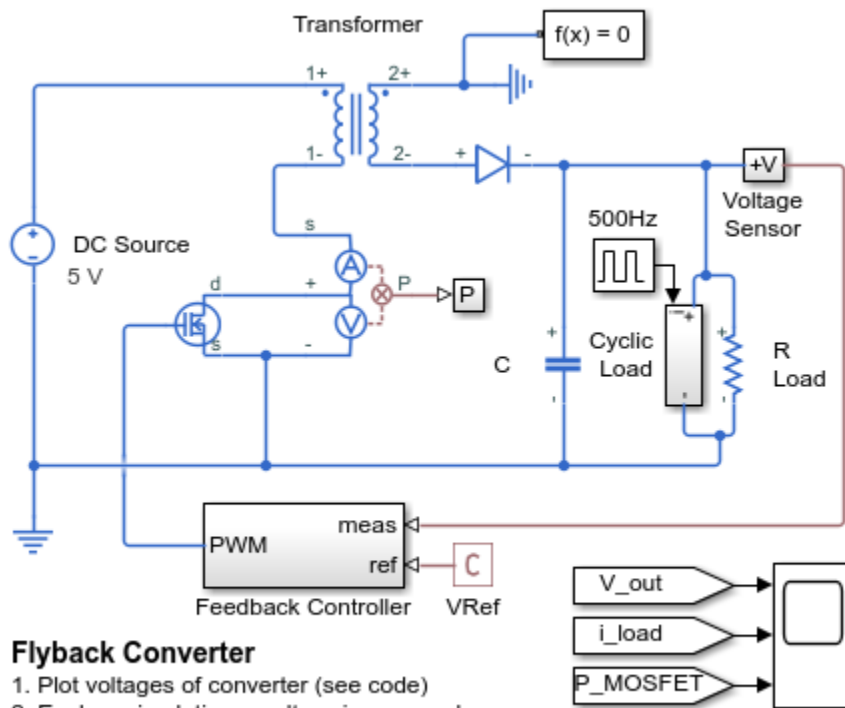
Flyback Converter
1. Plot voltages of converter (see code)
2. Explore simulation results using sscexplore
3. Learn more about this example

Simulation Results from Simscape Logging

The plot below shows the output voltage as compared to the reference voltage. It also shows the changing load current and the dissipated power of MOSFET averaged over the PWM cycle.
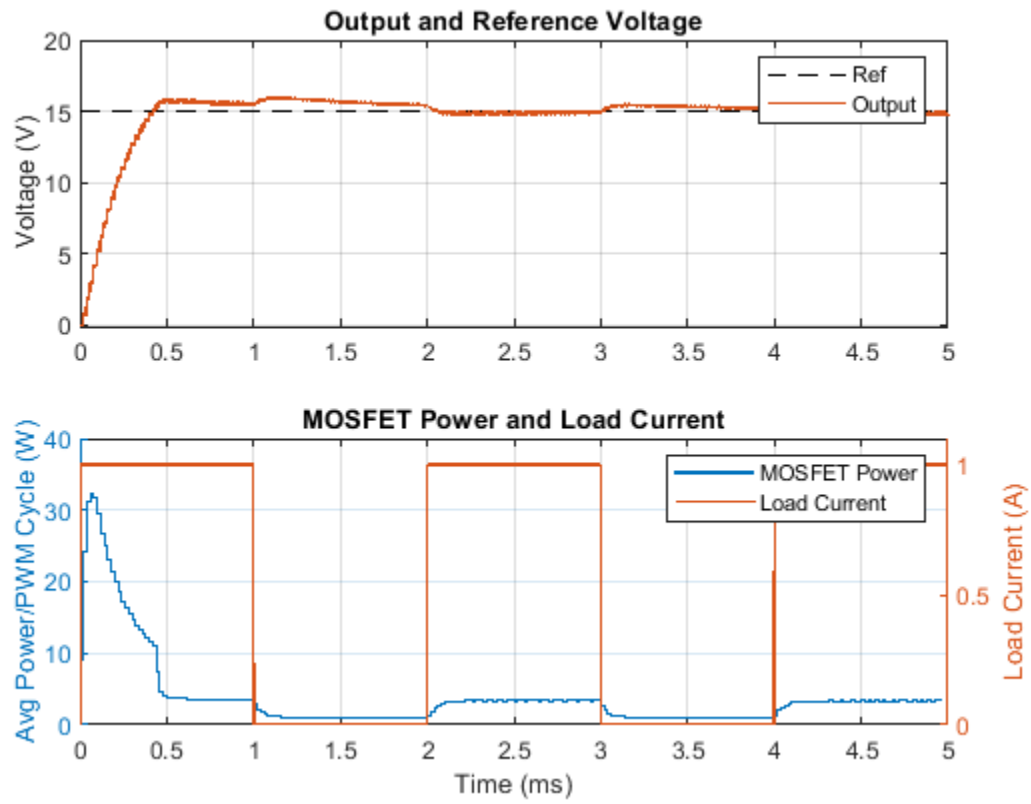
DC-DC LLC Converter

This example shows a DC-DC LLC power converter with frequency control. A simple integral control is implemented in Simulink® in the Controller block, and is designed to achieve a nominal output voltage defined by the variable Vout_nominal. The Output scope shows the frequency control signal, the output voltage, and the reference value for the output voltage. During startup, the reference value is ramped up to its desired setpoint. The design of the LLC powertrain is computed automatically using the first harmonic approximation.

This model contains two variants for the power electronic switches. The detailed version includes a MOSFET with nonlinear characteristics. The abstract version uses a piecewise linear model with an ideal switch, body diode, and output capacitance. The abstract version provides very similar behavior and quicker simulation.

Model
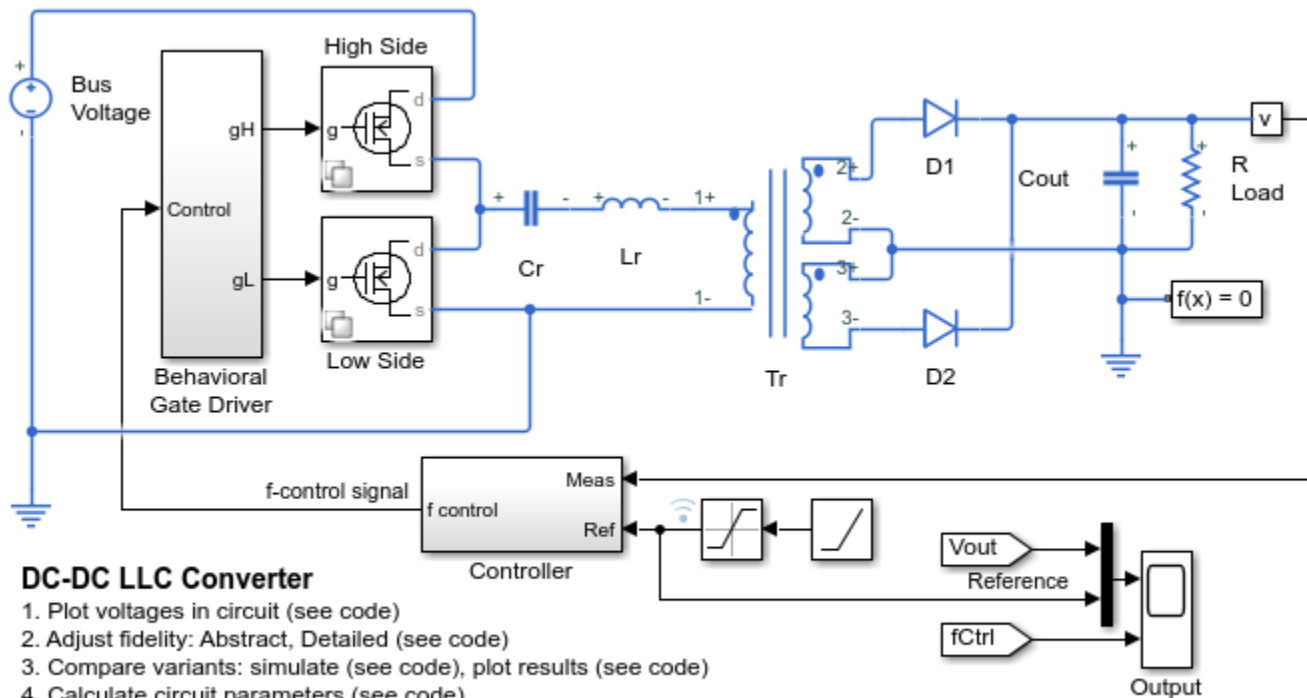
DC-DC LLC Converter

1. Plot voltages in circuit (see code)
2. Adjust fidelity: Abstract, Detailed (see code)
3. Compare variants: simulate (see code), plot results (see code)
4. Calculate circuit parameters (see code)
5. Explore simulation results using sscexplore
6. Learn more about this example

Power Electronic Switch, Detailed Variant
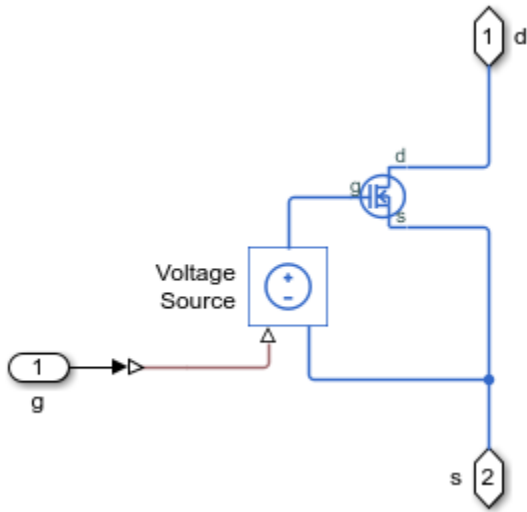
Power Electronic Switch, Abstract Variant
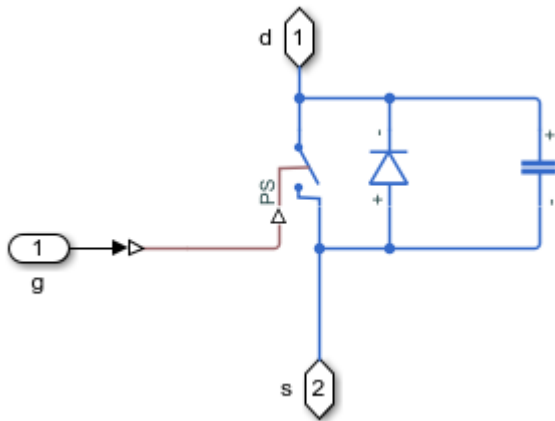
Simulation Results from Simscape Logging

The plot below shows the output voltage as compared to the reference voltage. It also shows the variation in switching frequency as it is adjusted by the control system to track the reference voltage.
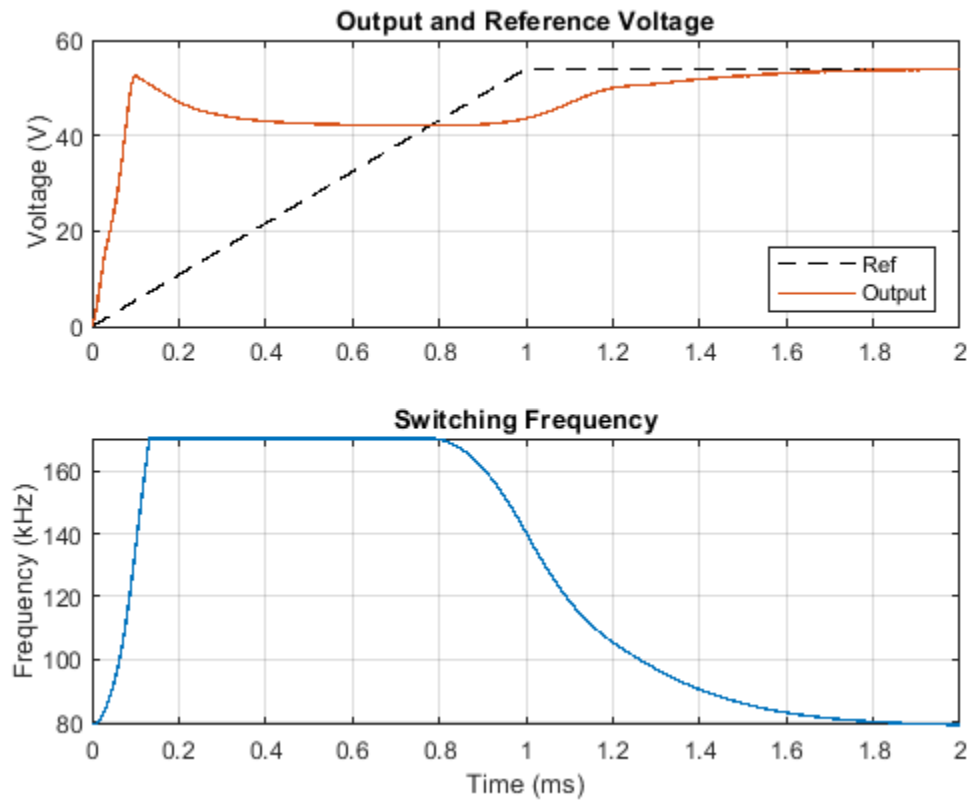
The plots below show the behavior of the abstract models and the detailed model. The results are very similar, indicating that the abstract models can be used without a significant loss of accuracy.

Comparison of Abstract and Detailed Models - Switching Event
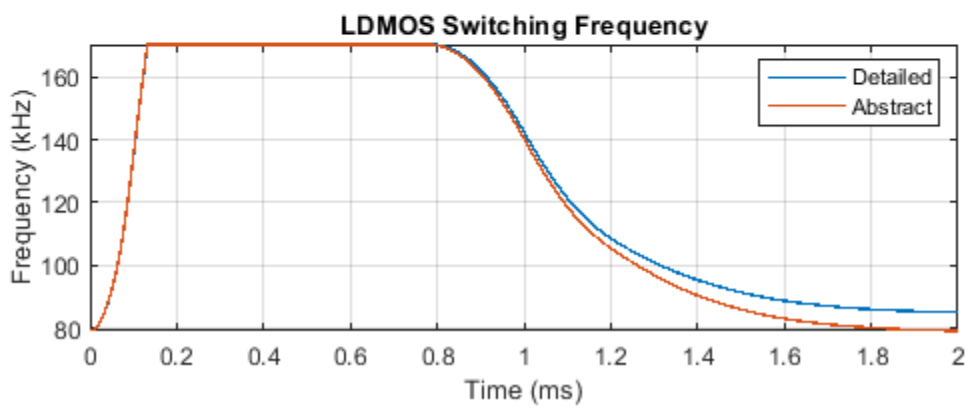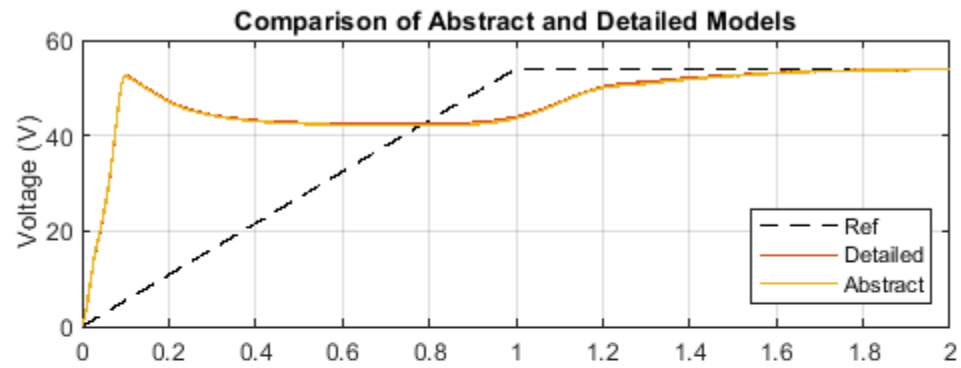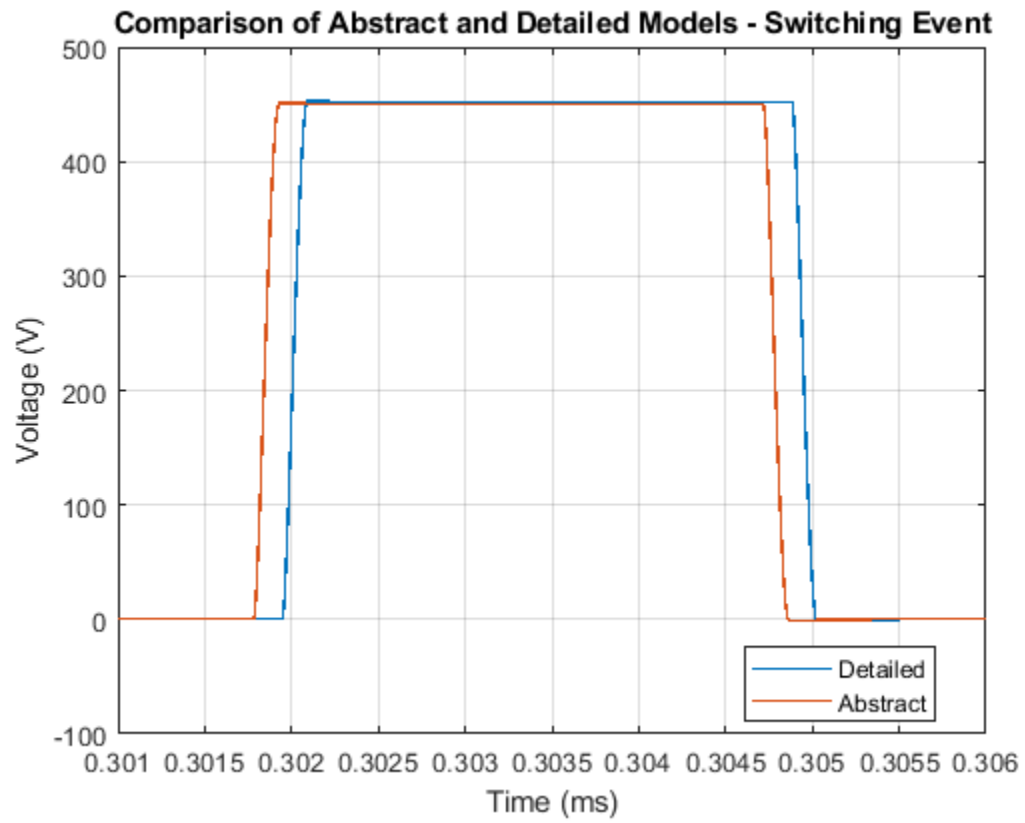
Class E DC-DC Converter

This example shows a Class E power converter with frequency control. A simple integral control is implemented in Simulink® in the Controller block, and is designed to deliver 100W into a 5ohm load. The switch is an LDMOS, high-voltage transistor with a nonlinear capacitance model, and R Trans is the equivalent series resistance of the transformer. The Output scope shows the drain-source voltage for evaluation of the voltage stress on the switch. Note that, due to the nonlinear output capacitance of the transistor, the peak voltage stress is higher than would be expected if the output capacitance were constant. In addition, the scope also shows the frequency control signal, the output voltage, and the reference value for the output voltage. This model can be used to calculate the output power information from components in the circuit.

Model
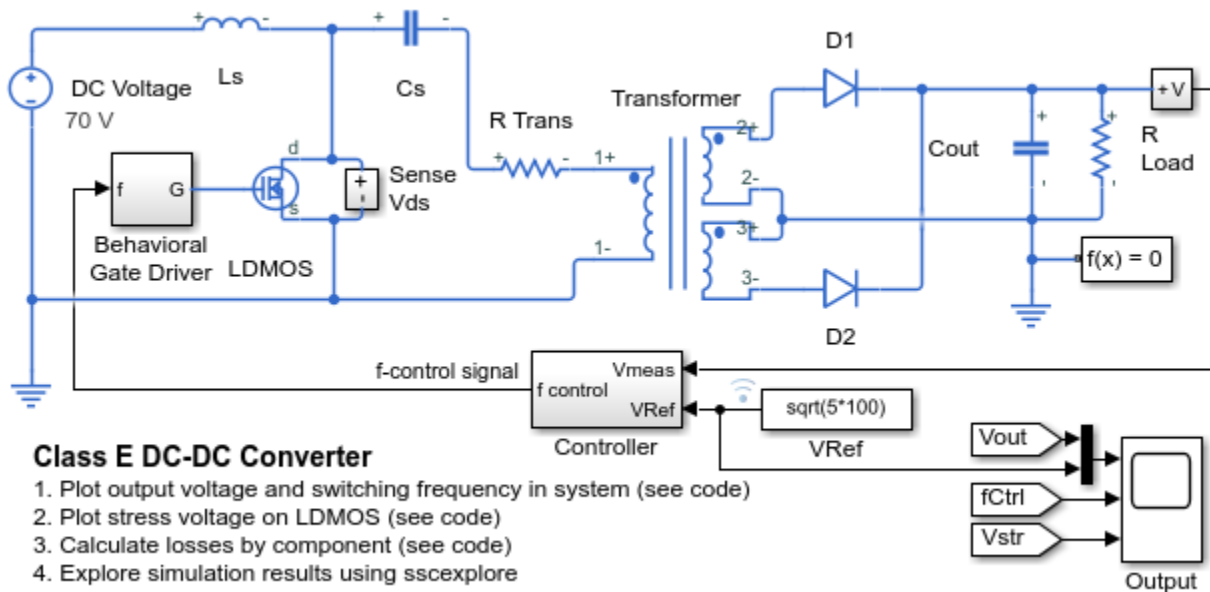
Class E DC-DC Converter
1. Plot output voltage and switching frequency in system (see code)
2. Plot stress voltage on LDMOS (see code)
3. Calculate losses by component (see code)
4. Explore simulation results using sscexplore
5. Learn more about this example

Simulation Results from Simscape Logging

The plot below shows the output voltage as compared to the reference voltage. It also shows the change in switching frequency which is how the control system attempts to make the output track the reference voltage.
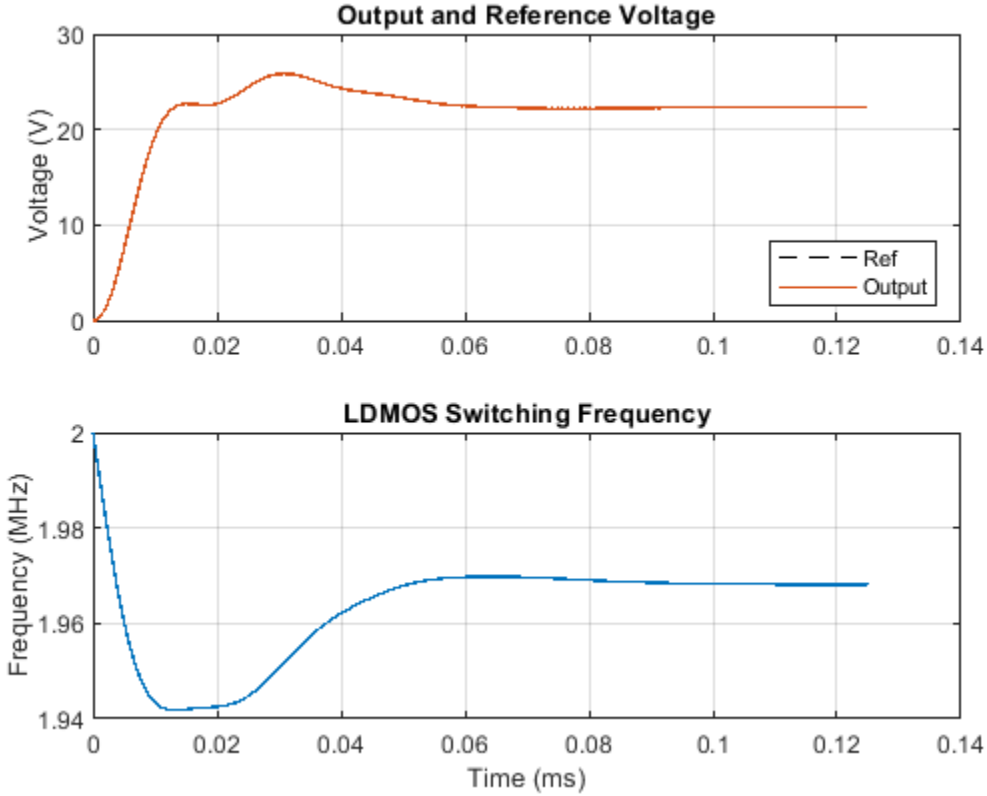
The plot below shows the voltage stress on the LDMOS as compared to the source voltage. This architecture for a power converter requires a significantly higher voltage stress than architectures with two semiconductor devices.

Source and Stress Voltage
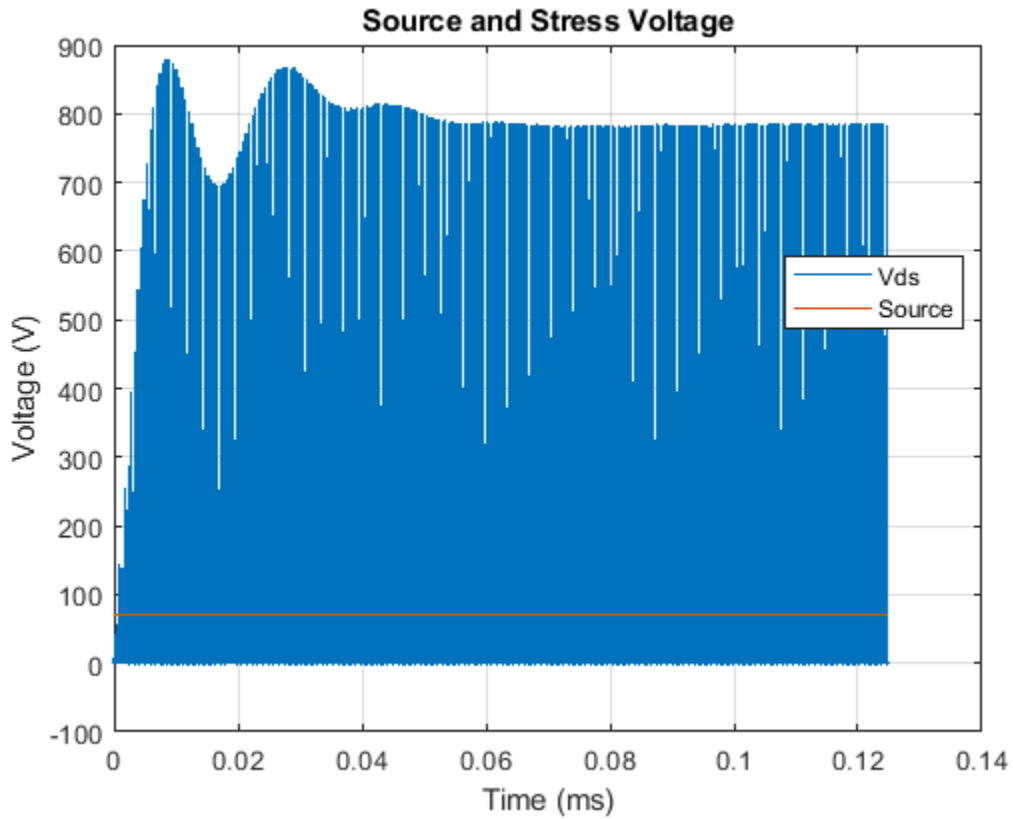
The table below shows the power dissipated by individual components in the ee_converter_dcdc_class_e model. These totals were calculated from simulation results using logged Simscape™ variables and the losses calculation utility ee_getPowerLossSummary. The total output power in the load, as well as the losses are shown.

```
Averaging power data from 0.0001s to 0.000125s.
Total efficiency = 90.4879% for an output power of 99.9026W.
Loss contributions are shown below.

                    LoggingNode                     Power
    _____     _____

    {'ee_converter_dcdc_class_e.LDMOS'        }      3.49
    {'ee_converter_dcdc_class_e.R_Trans.Resistor'}   2.81
    {'ee_converter_dcdc_class_e.D2'           }       1.9
    {'ee_converter_dcdc_class_e.D1'           }      1.79
    {'ee_converter_dcdc_class_e.Cs'           }      0.26
    {'ee_converter_dcdc_class_e.Ls'           }      0.25
    {'ee_converter_dcdc_class_e.Cout'         }         0
```

Linear LED Driver

This example shows a LED driver based on a linear current regulator. The scope shows the light and current output and the supply voltage. The output comes into regulation for a supply voltage greater than about 12V.

Model
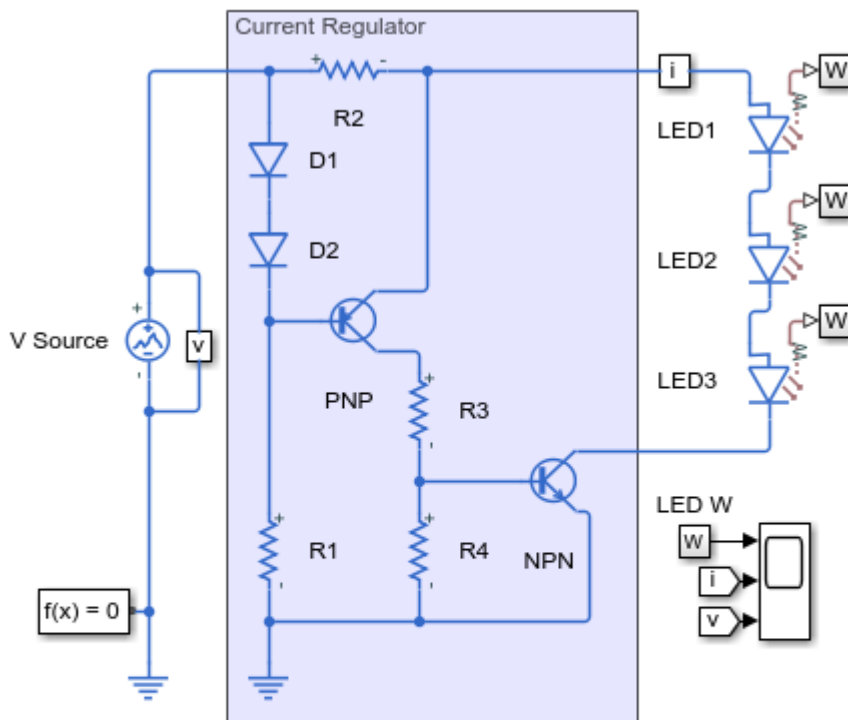
Linear LED Driver
1. Plot voltages in circuit (see code)
2. Plot efficiency of circuit (see code)
3. Explore simulation results using sscexplore
4. Learn more about this example

Simulation Results from Simscape Logging

The plot below shows the output of the current regulator used in an LED driver circuit. Once the supply voltage reaches 12V, the driver circuit regulates its output.
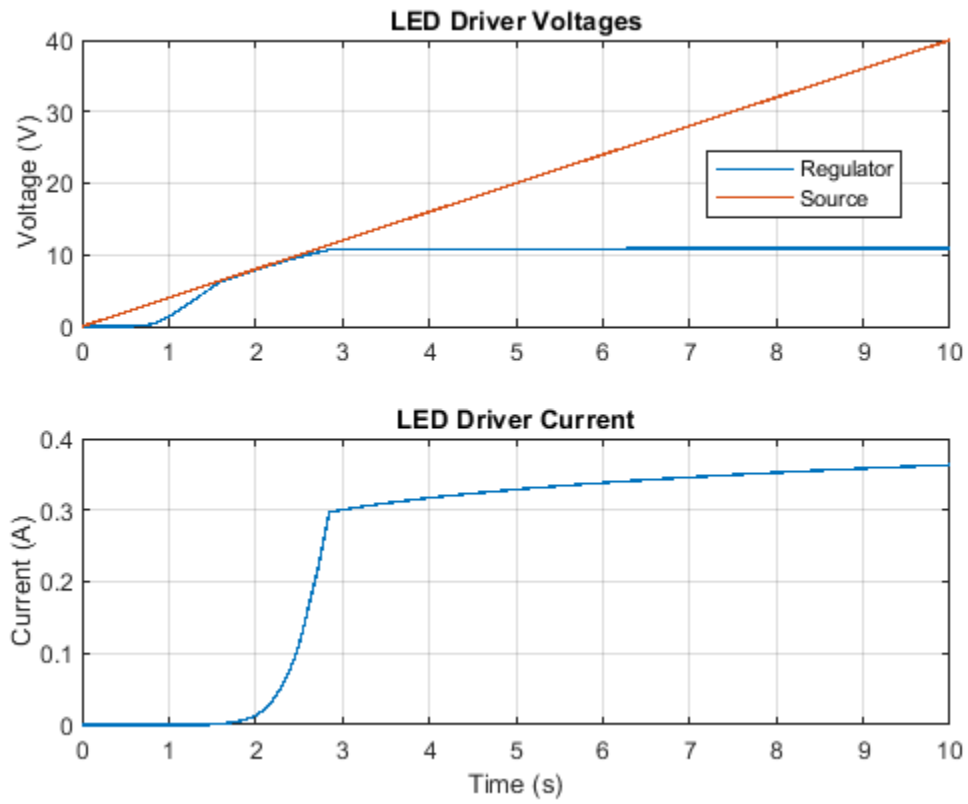
The plot below shows the driver efficiency and LED current as functions of the supply voltage.
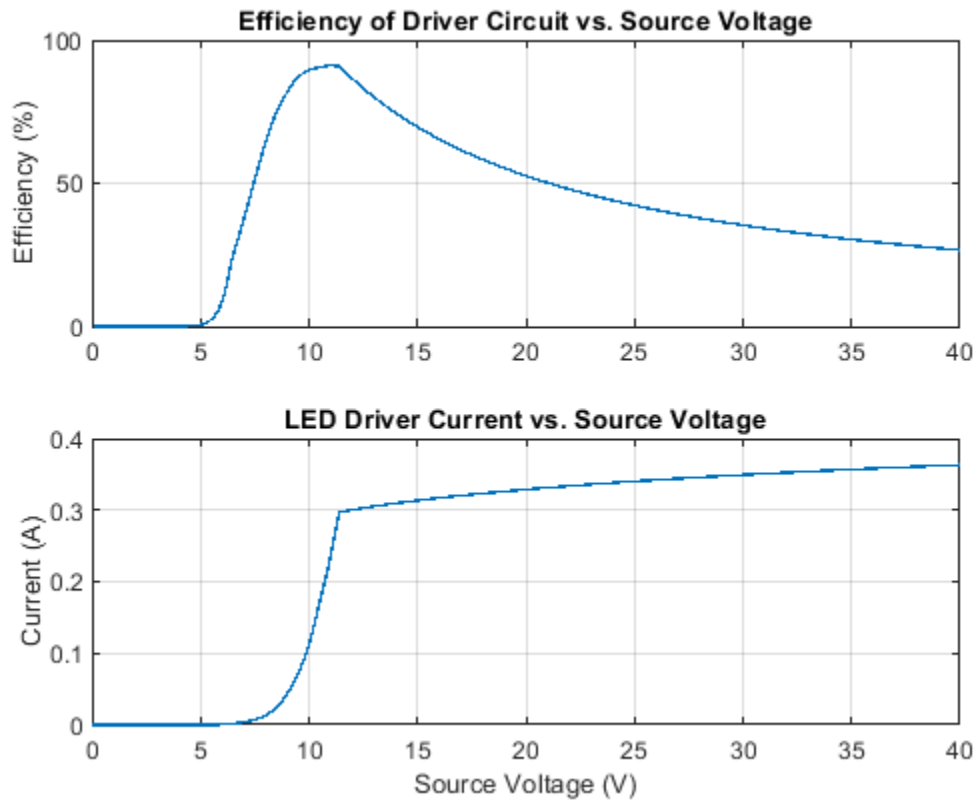
TVS Diode Parameterization

This model shows how to parameterize the Simscape™ Electrical™ diode to represent a Transient Voltage Suppression (TVS) diode. This example is for a TVS diode suited to protecting automotive electronics from voltage transients associated with turning off inductive loads. To view the data extracted from the datasheet, on the Modeling tab, in the Setup section, click Model Settings > Model Properties. On the Callbacks tab, click PreLoadFcn.

Two model options are shown here, the first using the Piecewise Linear Zener diode option, and the second using the Exponential diode option. The two diode blocks are parameterized directly in terms of the workspace datasheet values. Both models are tested here using a 10/1000us test pulse which raises the current to the peak pulse current I_PPM in 10us, and then lets it decay to I_PPM/2 in 1000us. The two scopes show that the max clamping voltage V_C of 18.8V is achieved to an acceptable tolerance.

The piecewise linear diode is the simplest to parameterize. The forward bias characteristics do not need to be accurate as a TVS diode is not operated in this region. A typical forward voltage drop of 0.6V is assumed. Given the [V_F I_F] forward bias data point, the on-resistance is given by (V_F-0.6)/I_F. The reverse bias data for the breakdown voltage V_BR and corresponding current I_T can then be used to determine the off conductance which is equal to I_T/V_BR. The diode reverse breakdown voltage, Vz, is then set equal to V_BR. Finally, zener resistance, Rz, is set so that when the current is I_PPM, the voltage is equal to the specified maximum clamping voltage V_C. A value of (V_C-V_BR)/I_PPM achieves this.

The exponential diode option models the reverse leakage current more accurately as the dependence on voltage is not assumed linear. By choosing to parameterize the exponential diode in terms of IS and N, the value of IS can be set equal to the reverse leakage current just before breakdown. The emission coefficient N is left as the default typical value of 1. Reverse breakdown voltage, BV, is set equal to V_BR. Finally, to ensure that the correct maximum clamping voltage V_C is achieved, the ohmic resistance, RS, must be appropriately set. This can either be by trial and error, or by approximate value of (V_C-V_BR-0.6)/I_PPM.

Model
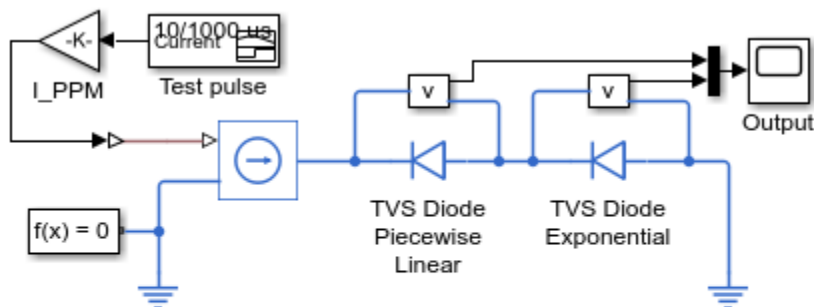
TVS Diode Parameterization
1. Plot v-i curves for diodes (see code)
2. Explore simulation results using sscexplore
3. Learn more about this example

Simulation Results from Simscape Logging

The plot below shows the voltage-current curves for two different diode models. The piecewise linear model and exponential model can provide similar behavior depending upon how they are parameterized.
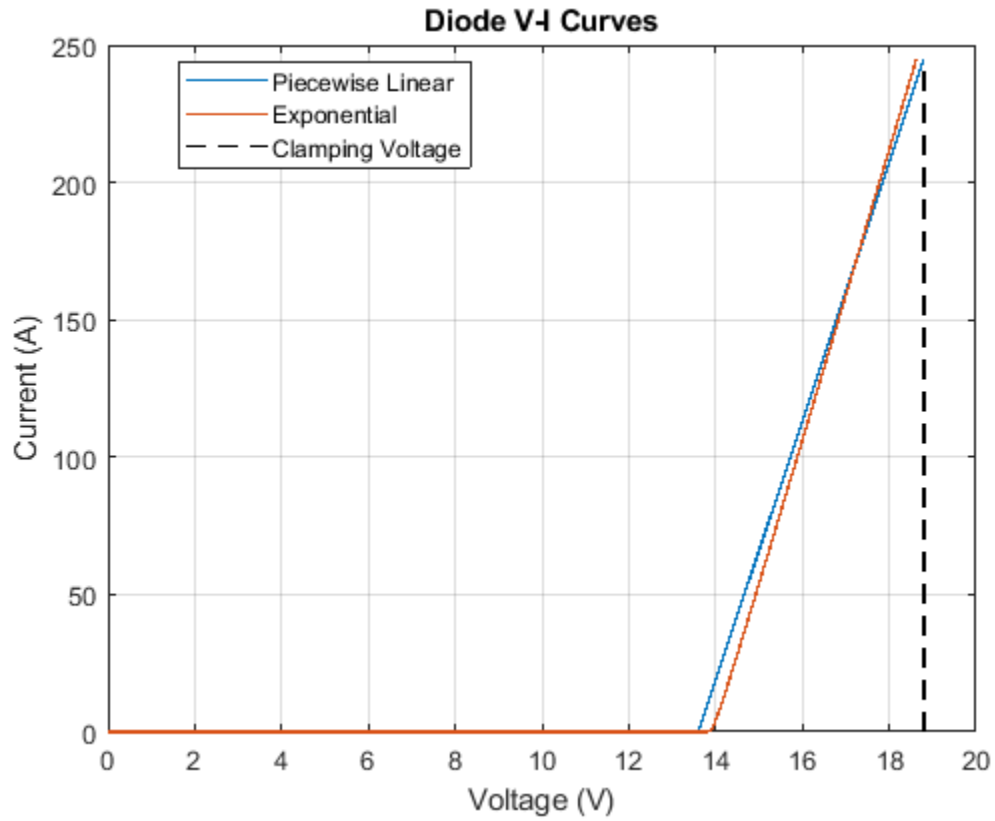
Ultracapacitor With Converter

This example shows how a DC-DC converter can be used to maintain a constant load voltage when drawing power from an ultracapacitor. Initially the converter supplies power to the load, and as it does so, the capacitor voltage drops. The protection circuit disconnects the load when the capacitor voltage drops below a threshold of 4V. At 10 seconds the generator is turned on, and power is supplied to both the load and to the capacitor to recharge it.

Model
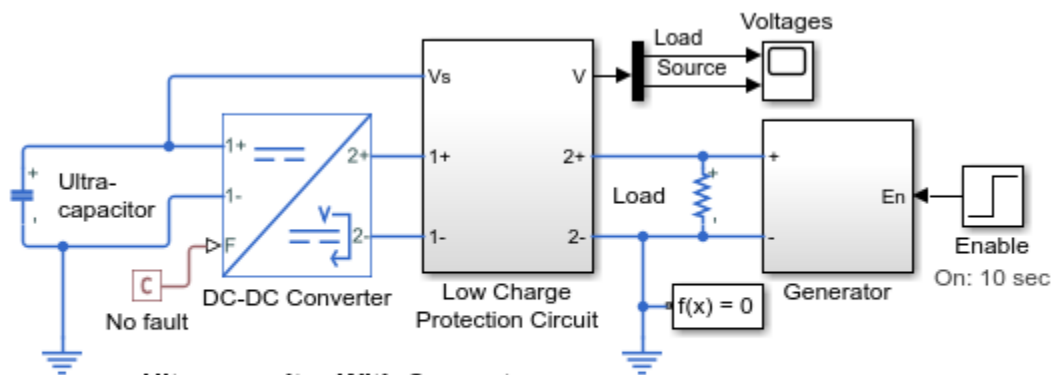
Ultracapacitor With Converter
1. Plot voltages and currents in circuit (see code)
2. Explore simulation results using sscexplore
3. Learn more about this example

Low Charge Protection Circuit Subsystem
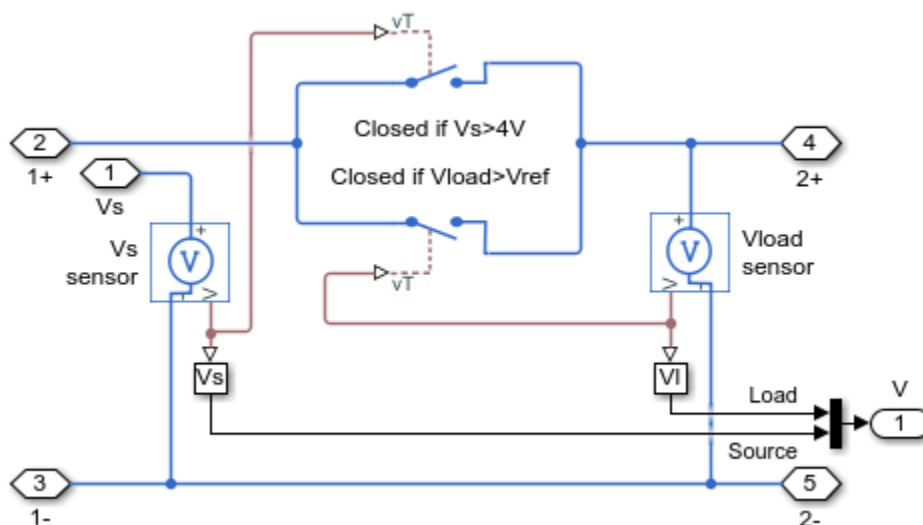
Simulation Results from Simscape Logging

The plot below shows voltage supplied to a resistive load from an DC-DC converter and a generator. The DC-DC converter supplies power to the load. A low charge protection circuit disconnects the load

when the ultracapacitor voltage drops below 4V. At 10 seconds the generator turns on, supplying power to the load and charging the ultracapacitor.
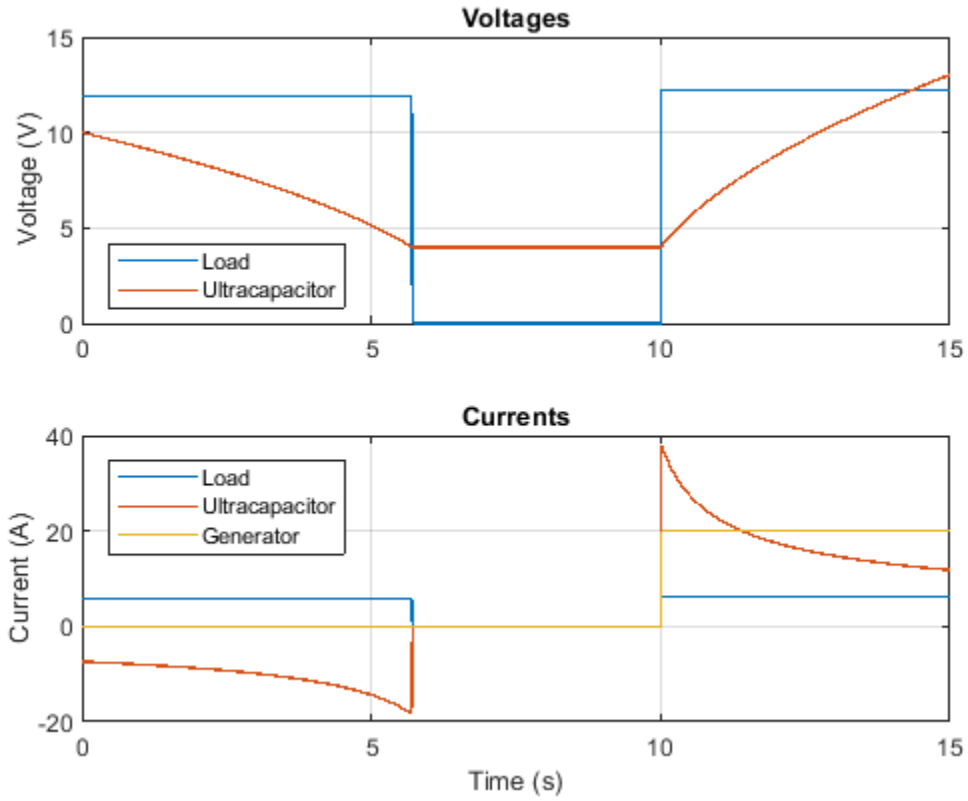
Energy Scavenger

This model shows how the performance of a rotational energy scavenger can be explored using a simple representative model. Electrical energy is produced from an off-center mass attached to the shaft of a DC motor. The mass, geometry, motor and electrical parameters must be matched to the expected mechanical excitation. The generated electrical power is less than the extracted mechanical power primarily due to motor winding losses and viscous damping for the rotor. This example is based on Nunna, K. "Constructive interconnection and damping assignment passivity-based control with applications", Imperial College London (2014). The model here is simplified in that the DC-DC converter is omitted.

Model
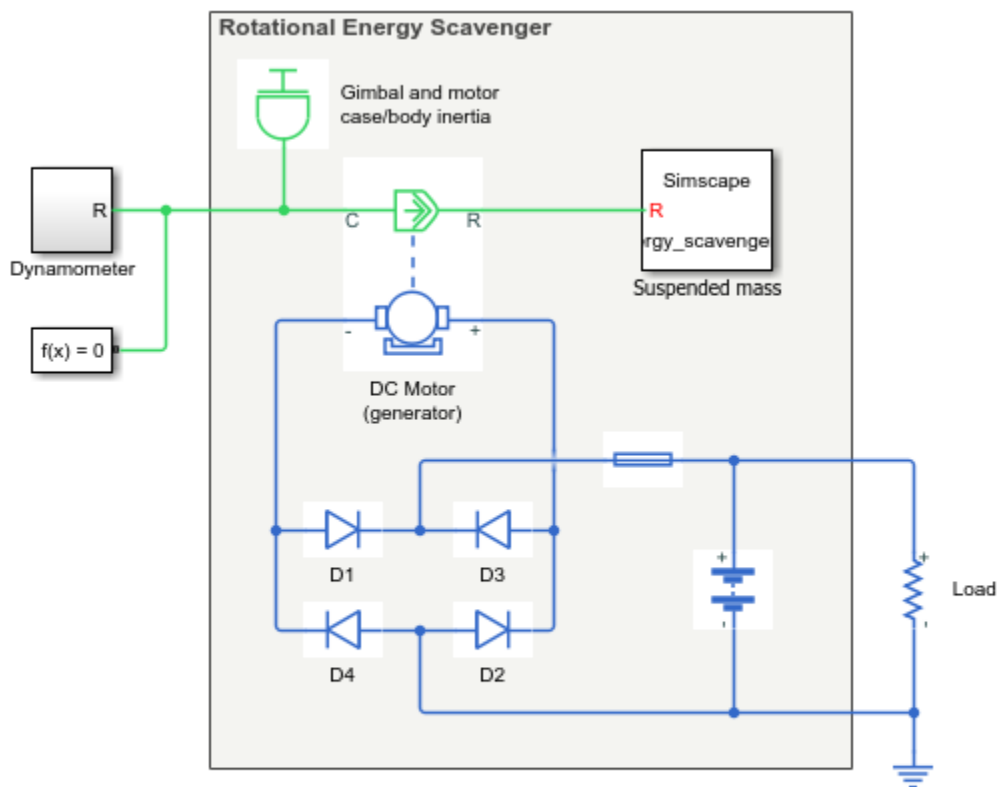
Energy Scavenger

1. Plot speeds and currents (see code)
2. View custom suspended mass component
3. Explore simulation results using sscexplore
4. Learn more about this example

Simulation Results from Simscape Logging

The plot below shows the dynamometer and suspended mass speeds along with generated and load currents plus battery charge.
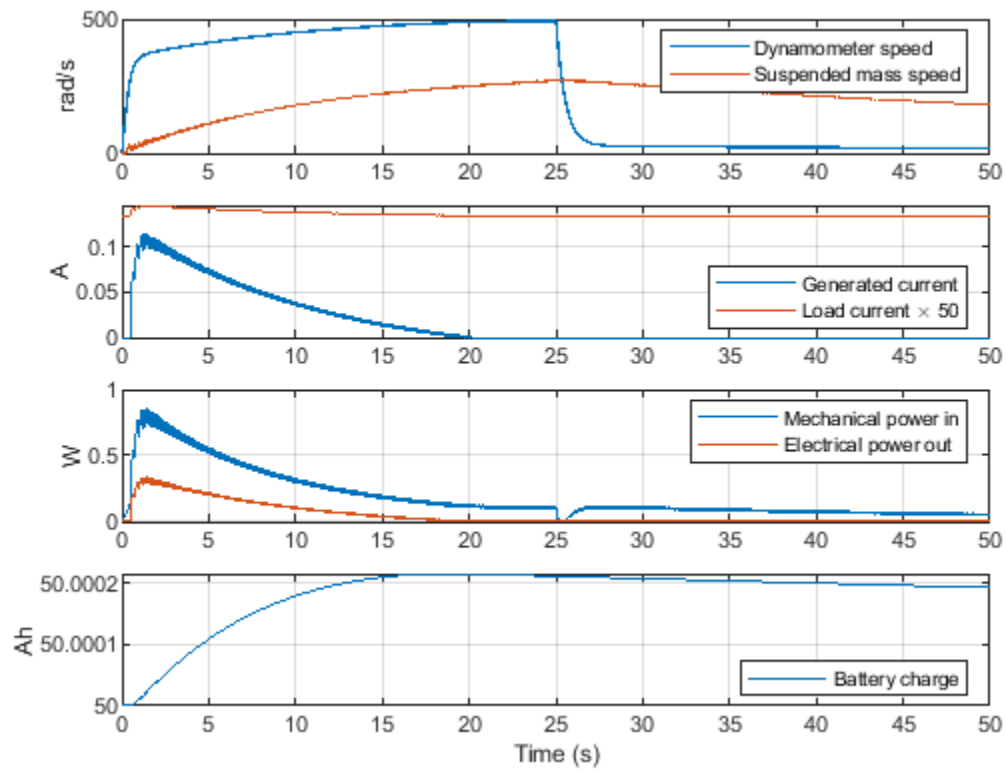
MOSFET Fault in Buck Converter

This model shows how a fault may be applied to a MOSFET in a power converter in order to explore the operation of protection circuitry. After the MOSFET becomes faulted, the crowbar circuitry is activated in order to clamp the output voltage across the load and eventually to cause the fuse to blow.

Model
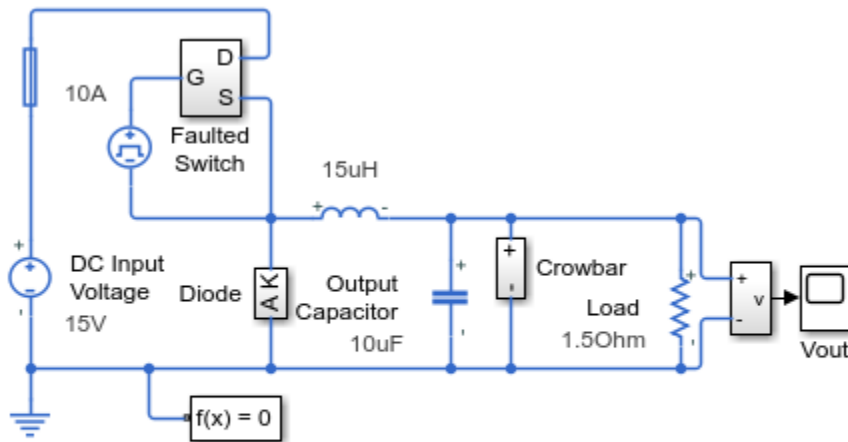
MOSFET Fault in Buck Converter

1. Plot input current and output voltage for buck converter (see code)
2. Explore simulation results using sscexplore
3. Learn more about this example

Faulted Switch Subsystem
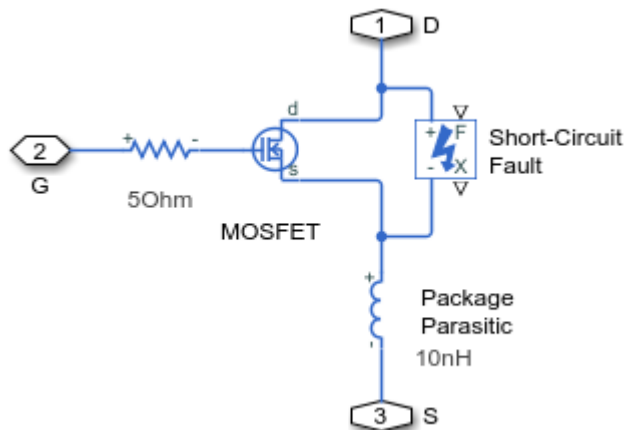
Crowbar Subsystem
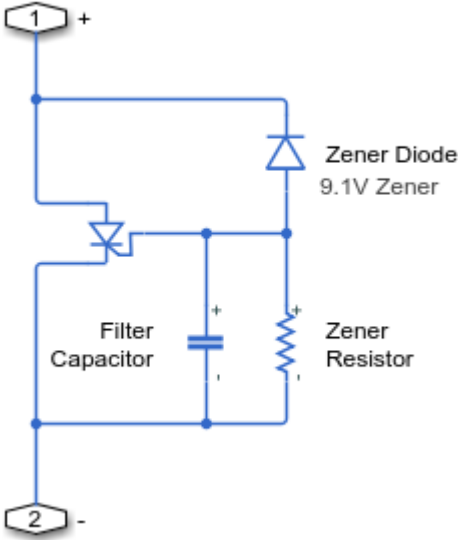
Simulation Results from Simscape Logging

The plot below shows the current passing through the input Fuse and the voltage across the Load resistor.
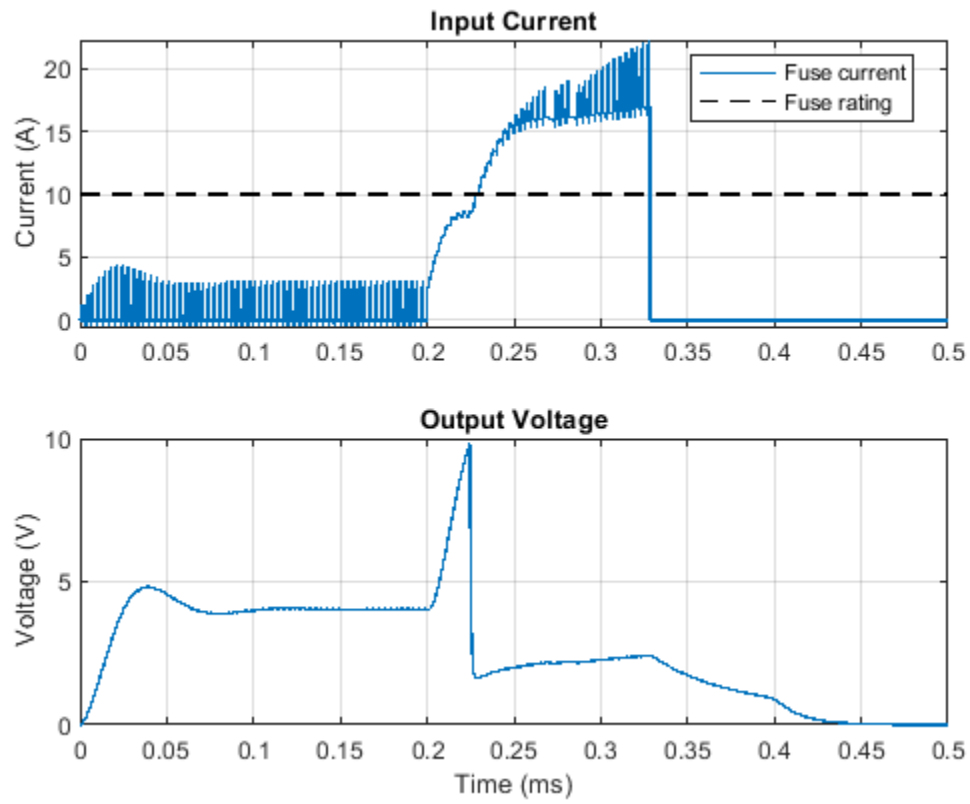
Surge Protection in Buck Converter

This model shows how a varistor may be applied to a buck converter in order to protect the switching MOSFETs from over-voltages due to a differential surge.

Model
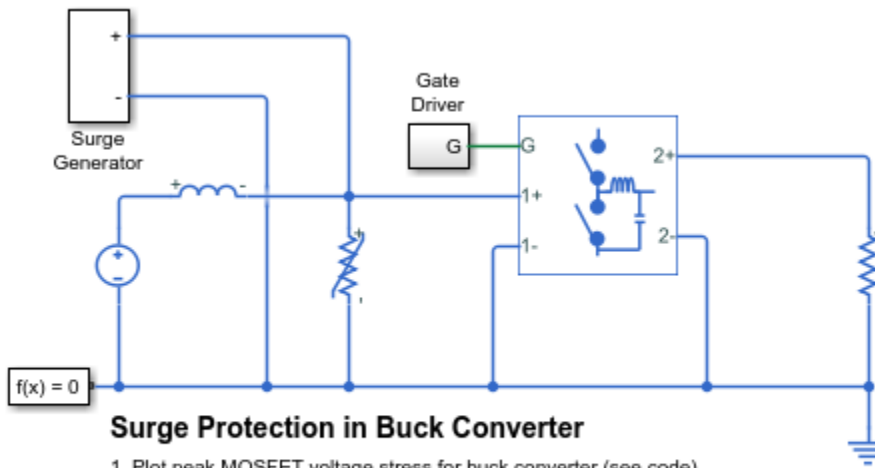
Surge Protection in Buck Converter

1. Plot peak MOSFET voltage stress for buck converter (see code)
2. Surge protection: on, off
3. Explore simulation results using sscexplore
4. Learn more about this example

Simulation Results from Simscape Logging

The plot below shows the peak voltage stress on each MOSFET of the buck converter on a cycle-by-cycle basis.
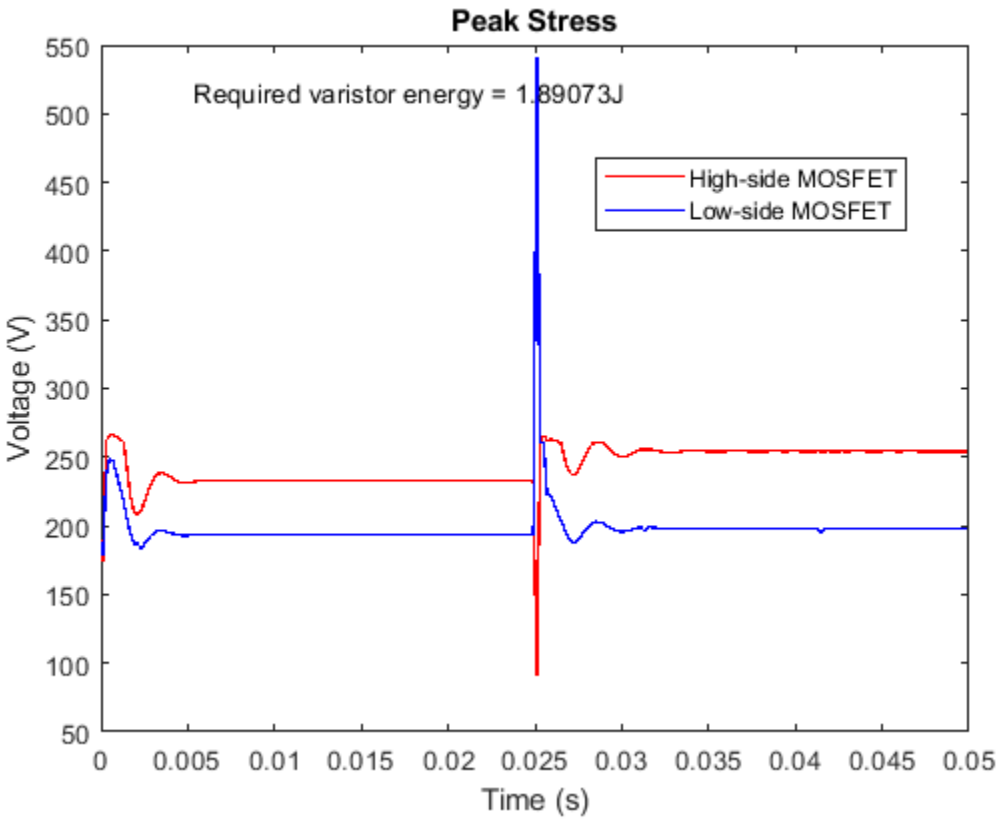
ARINC 429 Communication Link

This example shows a communication link that follows the ARINC 429 specification. Here it is configured for 100kBits per second operation. The ARINC 429 specification is for a simplex broadcast bus with odd parity checking. The cable is a balanced line comprising a twisted pair for data wires A and B, plus an outer shield. This model can be used to check compliance with the ARINC 429 specification, and to assess the impact of different cable lengths, cable parameters and abstracted transmitter and receiver characteristics. It can also be used to check operation with multiple receivers, the ARINC 429 specification allowing for up to 20 receivers to connect to a single transmitter.

Model
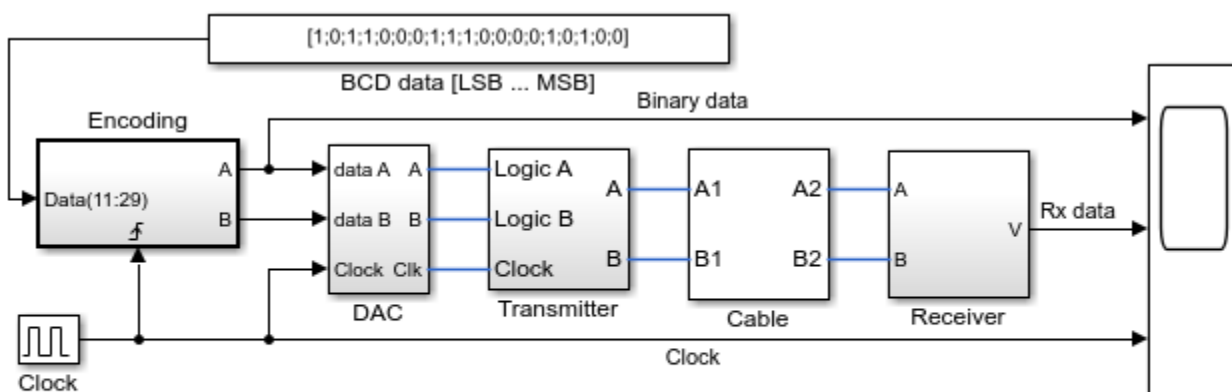
ARINC 429 Communication Link
1. Explore simulation results using sscexplore
2. Learn more about this example

Transmitter Subsystem
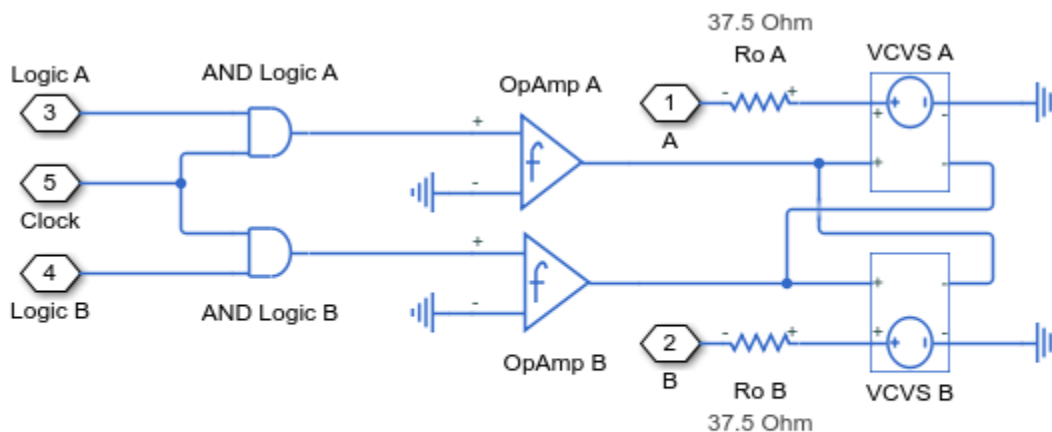
Level and slope control is implemented using a Band-Limited Op-Amp. The maximum slew rate parameter is used to shape the signal to comply with the ARINC 429 specification. As such, this is a simple behavioral model that replicates the input-output characteristics implemented by off-the-shelf chips such as the BD429.

Cable Subsystem
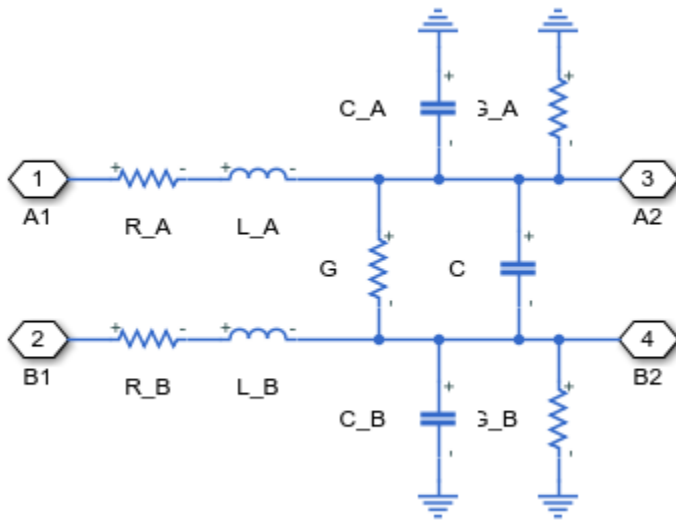
Receiver Subsystem
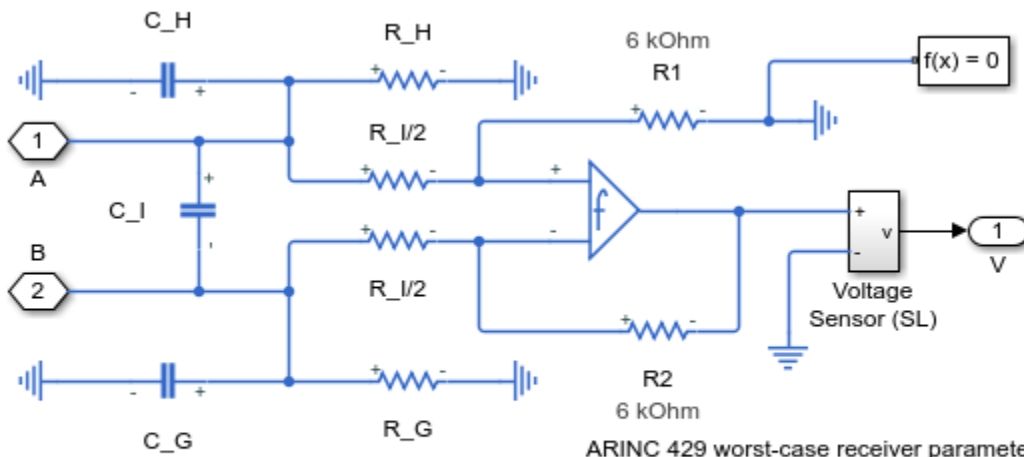
ARINC 429 worst-case receiver parameter values are:
> Differential input resistance R_I = 12K
> Differential input capacitance C_I = 50pF
> Resistance to ground R_H and R_G = 12K
> Capacitance to ground C_H and C_G = 50pF

Simulation Results from Scopes
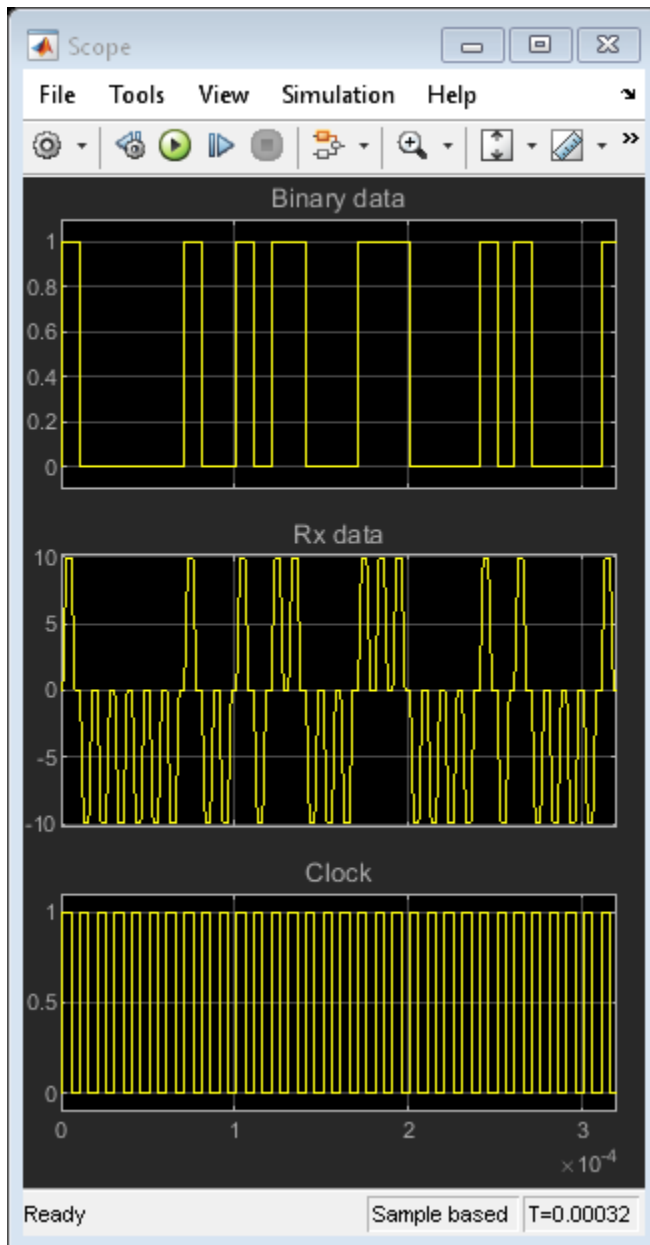
Band-Pass Filter Using Three Mutually-Coupled Inductors

This example shows an implementation of a band-pass filter using three mutually-coupled inductors. The model can be used to validate filter parameters which are chosen to provide a band-pass centered on 100MHz. A band-limited noise source is up-shifted by a 100MHz oscillator and applied to the filter. The response is then down-shifted by the oscillator. The model StopFcn callback takes FFTs of the source and response and estimates the filter frequency response.

Model
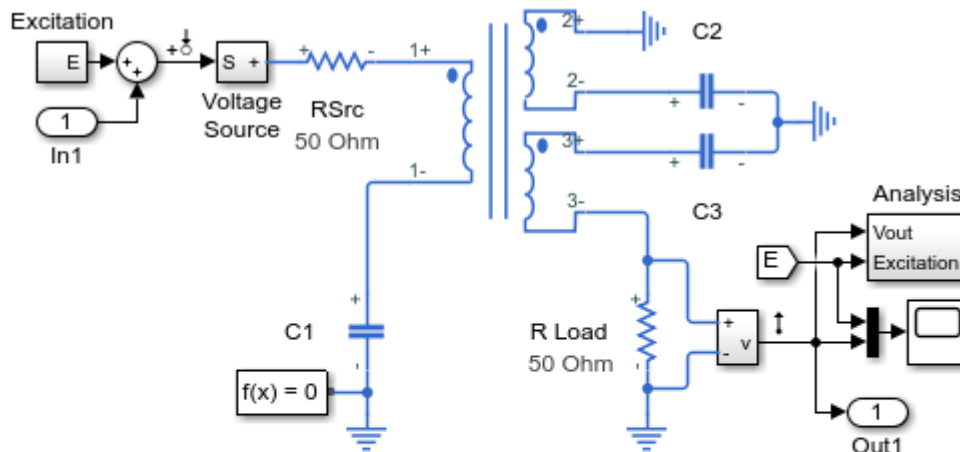
Band-Pass Filter Using Three Mutually-Coupled Inductors
1. Plot voltages from circuit (excitation, response) (see code)
2. Plot frequency response using estimation from
   simulation results and linearized model (see code)
3. Explore simulation results using sscexplore
4. Learn more about this example

Simulation Results from Simscape Logging

The plot below shows the excitation voltage and the response of the bandpass filter circuit. A band-limited noise source is up-shifted by a 100MHz oscillator and applied to the filter. The response is then down-shifted by the oscillator.
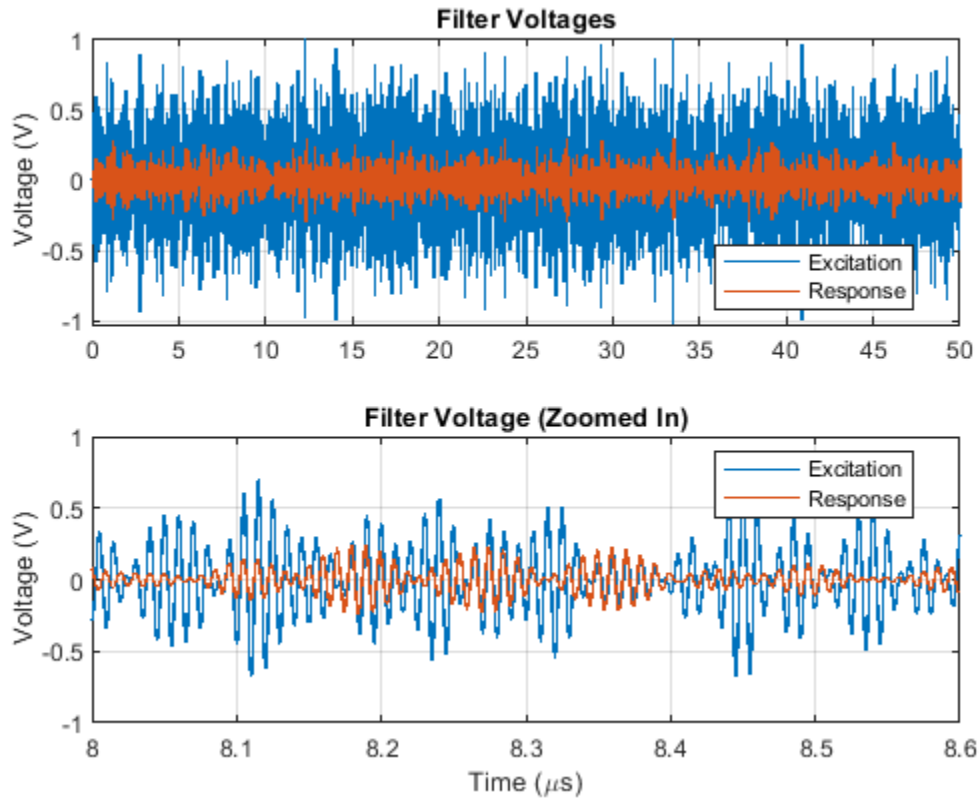
Frequency Response

The plot below shows the frequency response of the circuit obtained using two different methods. MATLAB® is used to apply an FFT to the simulation results to estimate the frequency response. The other method uses the MATLAB command linearize to obtain the frequency response. The two responses match nearly perfectly. The frequency response validates the filter design, showing that it is centered at 100 MHz.

Frequency Response (Bandpass Filter, Coupled Inductors)
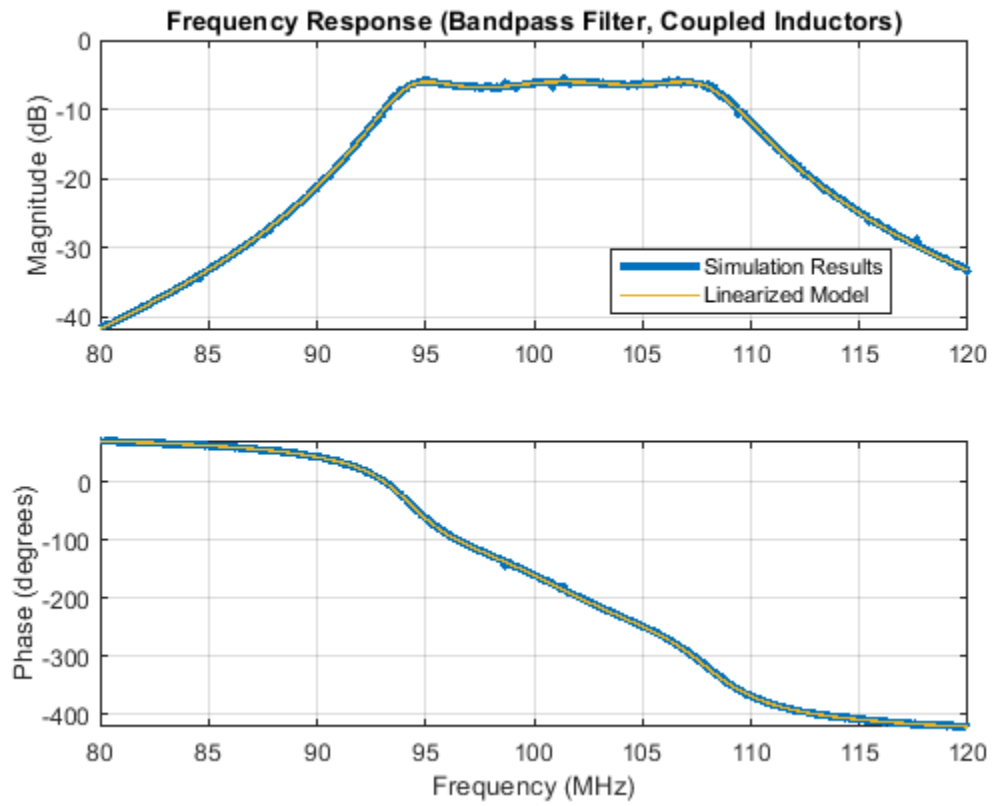
Class-E RF Amplifier

This model shows a class-E RF amplifier with circuit parameters chosen for an 80m wavelength. Class-E amplifiers achieve high efficiency levels as the MOSFETs never have simultaneously high Vds and Ids. The load network is used to shape the voltage and current waveforms. This model can be used to verify correct operation and to support component selection. Correct operation of the circuit is particularly sensitive to source resistance, R_source. The capacitance parameters for the two MOSFETs are representative for an FQA11N90 device.

Model

Class-E RF Amplifier
1. Plot voltages at output and MOSFET A (see code)
2. Explore simulation results using sscexplore
3. Learn more about this example
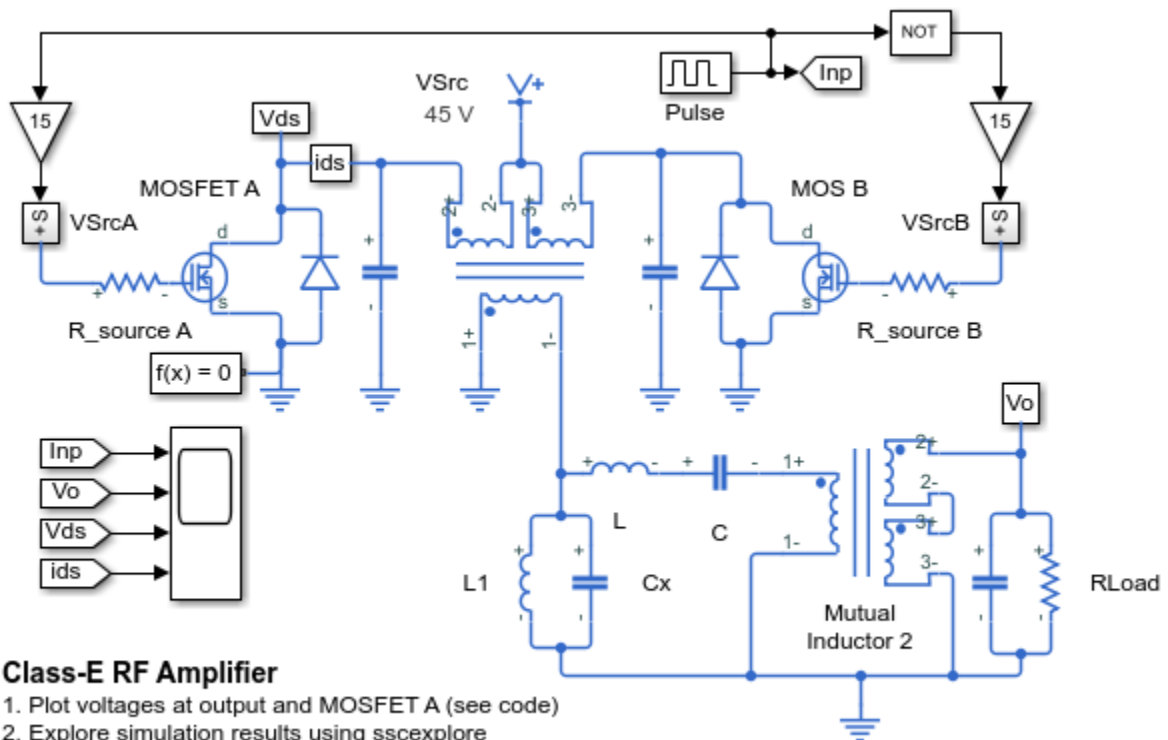
Simulation Results from Simscape Logging

The plot below shows the load voltage and the drain-source voltage and current for MOSFET A.
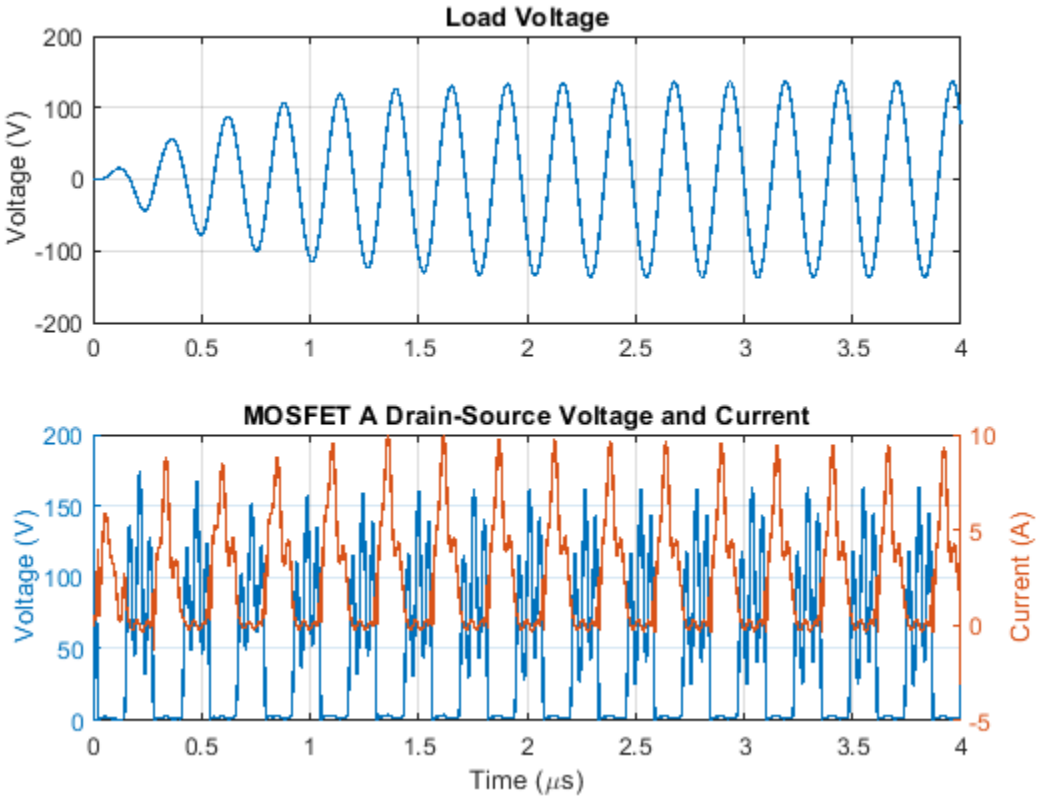
Diode Ring Mixer

This model shows how a diode ring can be used to demodulate a frequency-modulated signal. The RF input has a fixed frequency of 9MHz, and the local oscillator has a fixed frequency of 11MHz. Hence the frequency-modulated signal is a sine wave of 2MHz. This 2MHz component is clearly visible in the IF response. The second component in the IF response is the sum of the RF and local oscillator frequencies i.e. 20MHz.

The spectrum shows that the diode ring mixer introduces some additional unwanted harmonics. The spectrum can be used to determine how to augment the behavioral ring mixer model with additional terms to produce an abstracted mixer model that can be used in a more complex circuit

Model
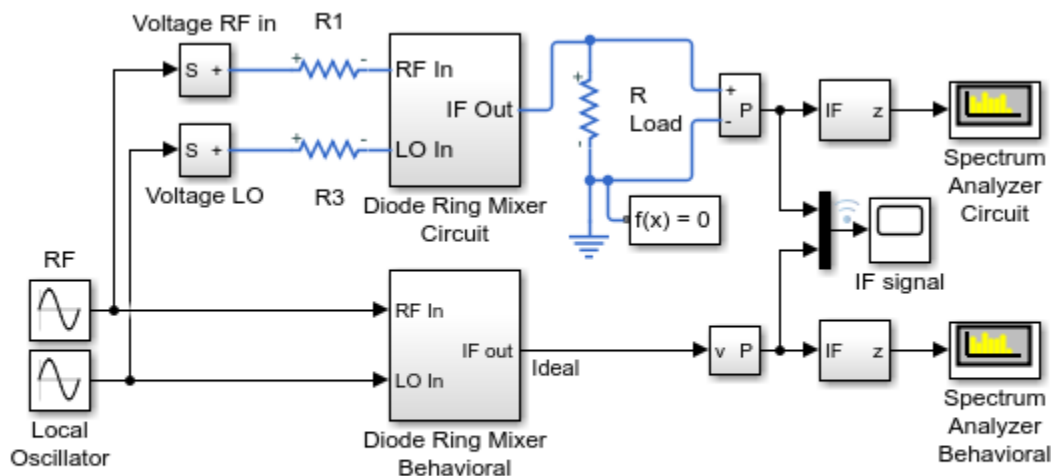
Diode Ring Mixer
1. Plot mixer output from circuit and behavioral models (see code)
2. Explore simulation results using sscexplore
3. Learn more about this example

Diode Ring Mixer Behavioral Subsystem
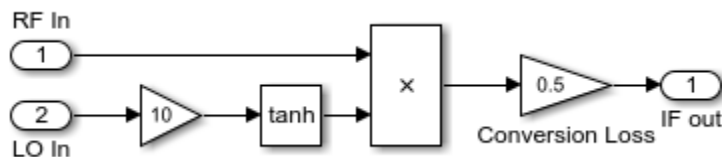
Diode Ring Mixer Circuit Subsystem
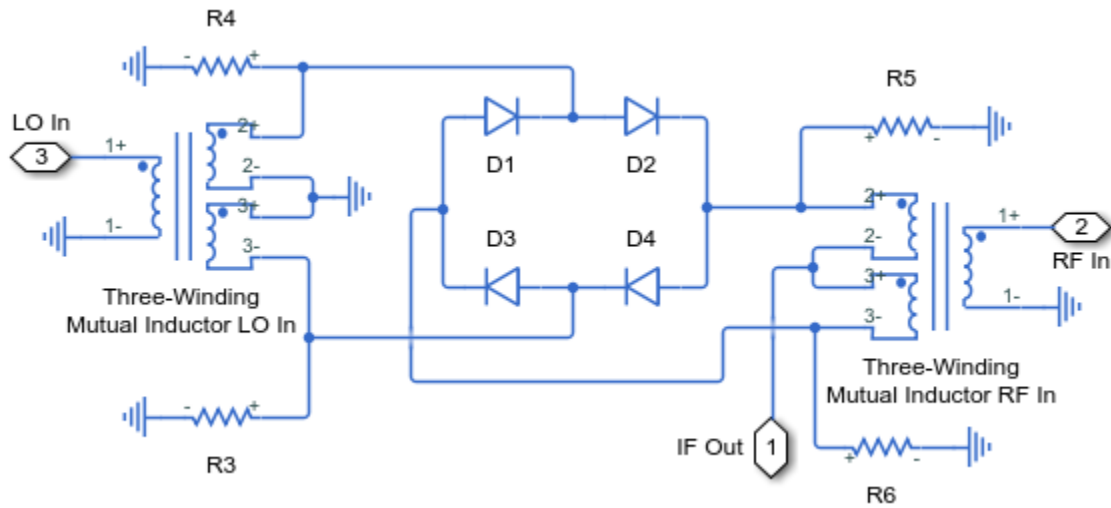
Simulation Results from Simscape Logging

The plot below shows the output of the circuit and behavioral mixer models.
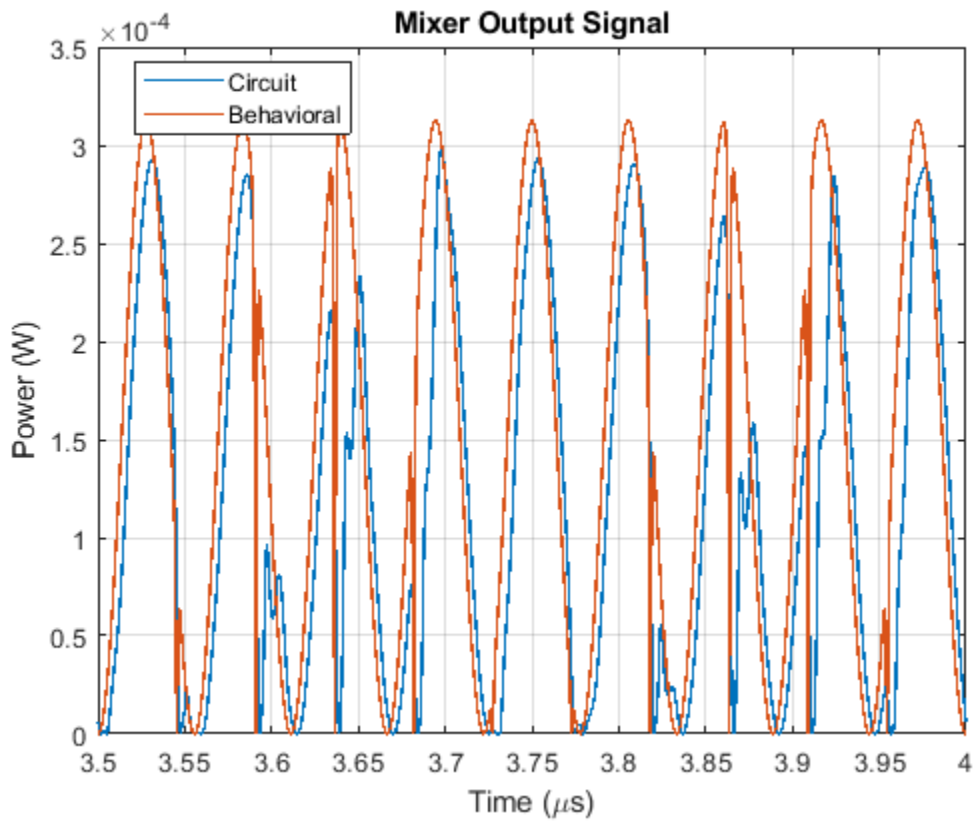
Simulation Results from Spectrum Analyzer

The frequency spectrum plots below show that the behavioral and circuit models produce similar results. The spectrum from the circuit model shows additional unwanted harmonics which are not shown in the ideal behavioral model.
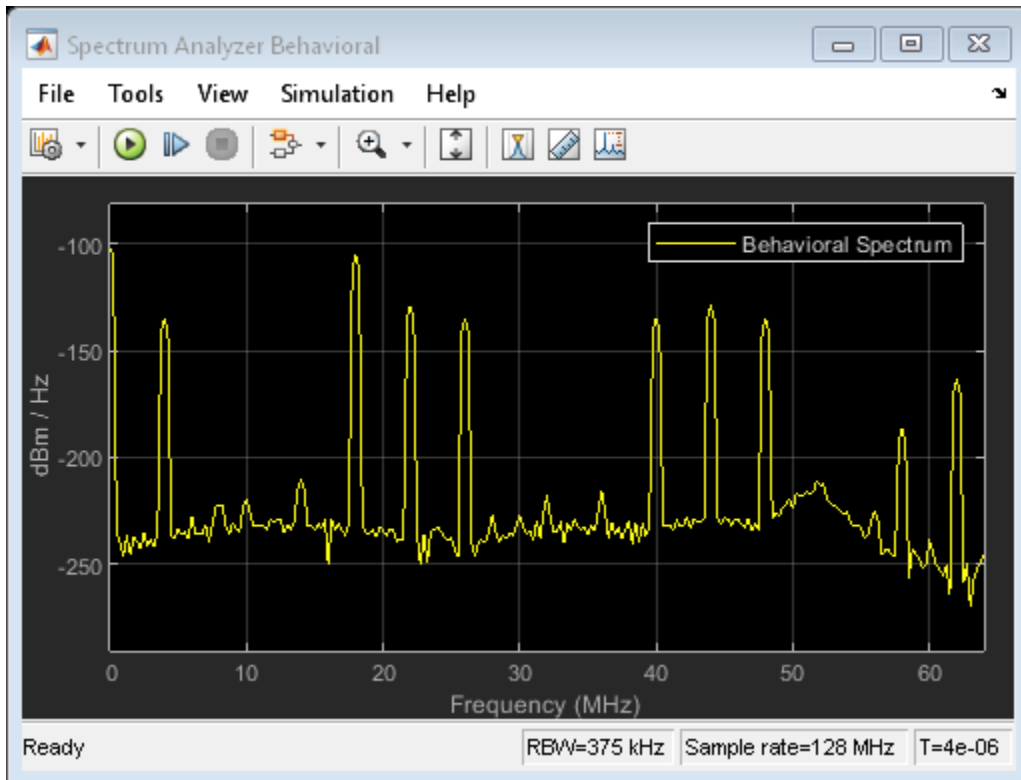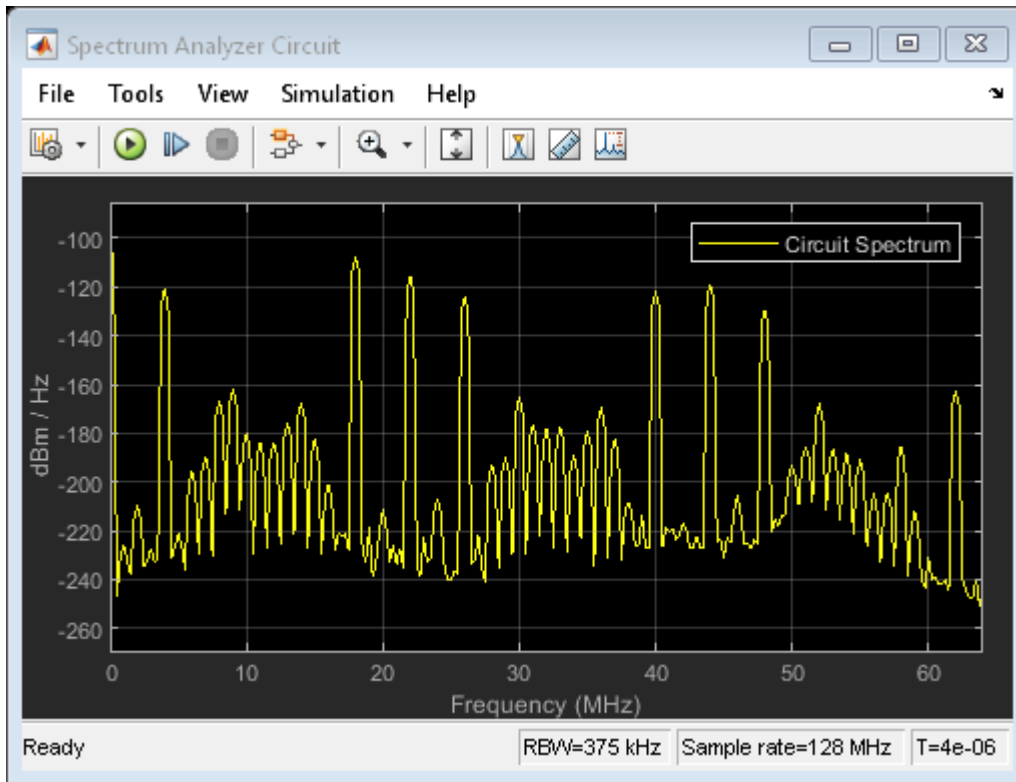
Phase-Locked Loop

This model shows how to model a phase-locked loop. The charge pump and filter are modeled using discrete analog components whereas the oscillator is represented as behavioral component using the Simscape™ Electrical™ Voltage-Controlled Oscillator block. The D-type flip-flops in the phase detector are represented in a simplified form using Simulink® blocks to define the behavior, and electrical components are used just at the interface. Non-zero initial conditions are applied to C1 and C2 in order to start the VCO out of phase and test the tracking ability.

Model
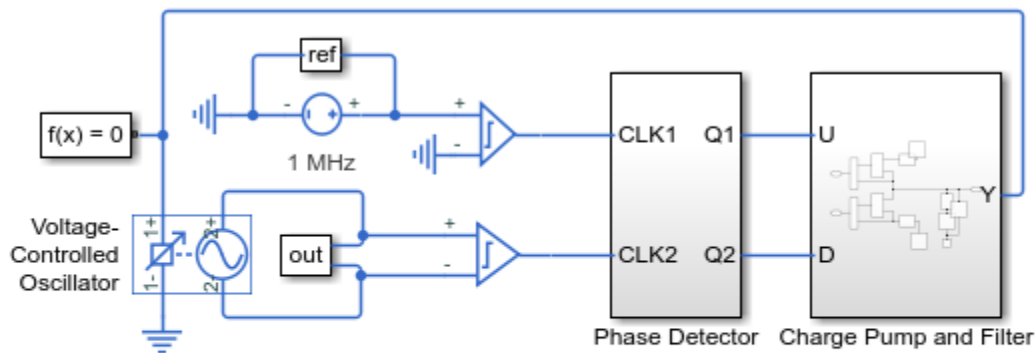
Phase-Locked Loop
1. Plot voltages of voltage controlled oscillator (see code)
2. Explore simulation results using sscexplore
3. Learn more about this example
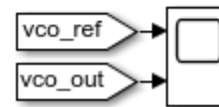
Charge Pump and Filter Subsystem
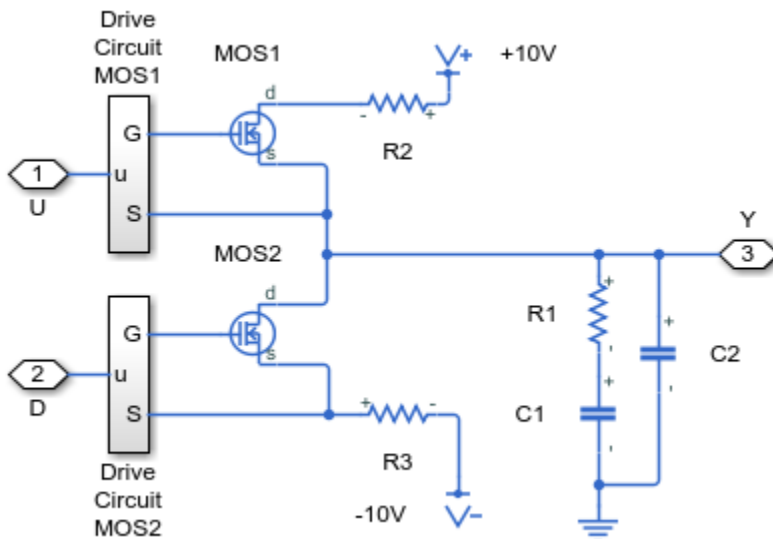
Phase Detector Subsystem
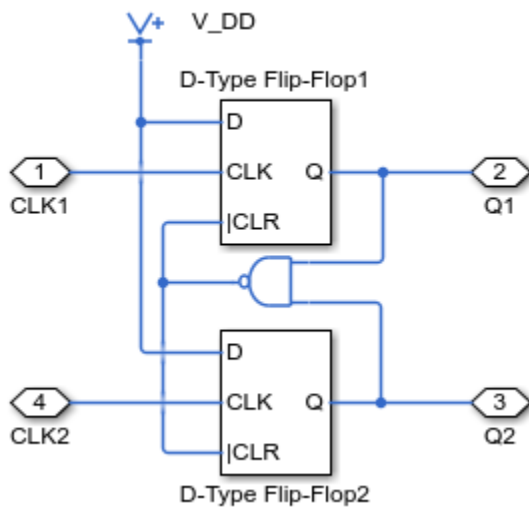
Simulation Results from Simscape Logging

The plot below shows the reference and output voltage for the voltage controlled oscillator within a phase locked loop.

Voltage Controlled Oscillator Output

Voltage Controlled Oscillator Input
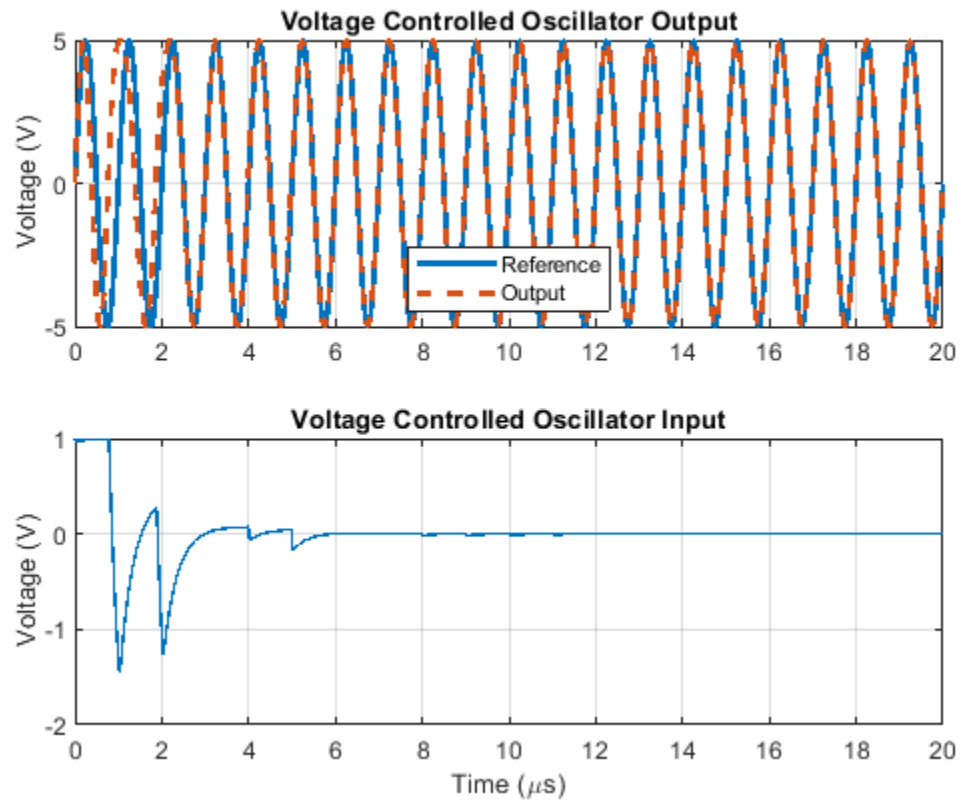
LC Transmission Line and Test Bridge

This model shows a transmission line model and bi-directional test bridge. Reflected and transmitted signals are slightly different if the test direction is changed. This is because the line model is not symmetric. This type of model can be used both to explore the impact of cable choice on transmission characteristics, and also to compare relative fidelity of different transmission line model structures and parameterizations.

This model can be used to obtain the frequency response of the system. MATLAB® command linmod can be used to linearize the model. If you have Simulink® Control Design™, open the model ee_transmission_line_lc. To open the Model Linearizer, on the Apps tab, under Control Systems, click Model Linearizer.

Model
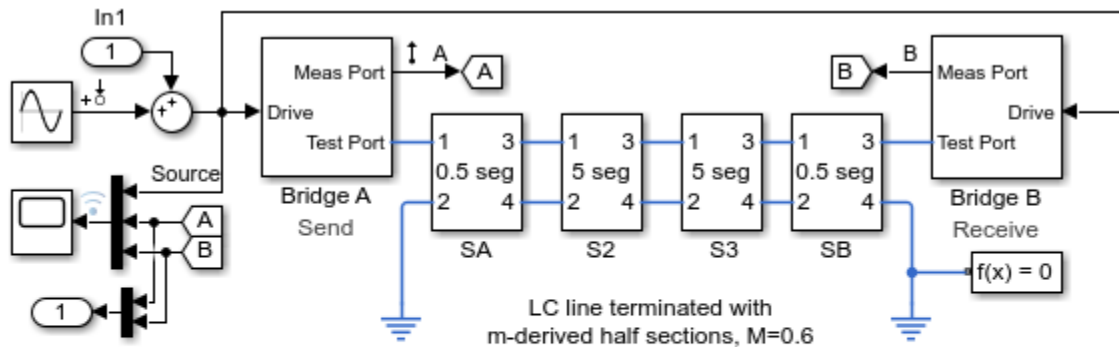
LC Transmission Line and Test Bridge
1. Configure transmission: A to B, B to A
2. Plot signals for A to B and B to A (see code)
3. Linearize circuit to view frequency response (see code)
4. Explore simulation results using sscexplore
5. Learn more about this example

Transmission Line Specs

Impedance Zo   = 50
Trans. delay (ns) = 3.1831
Cutoff freq (GHz) = 1

Bridge A Subsystem
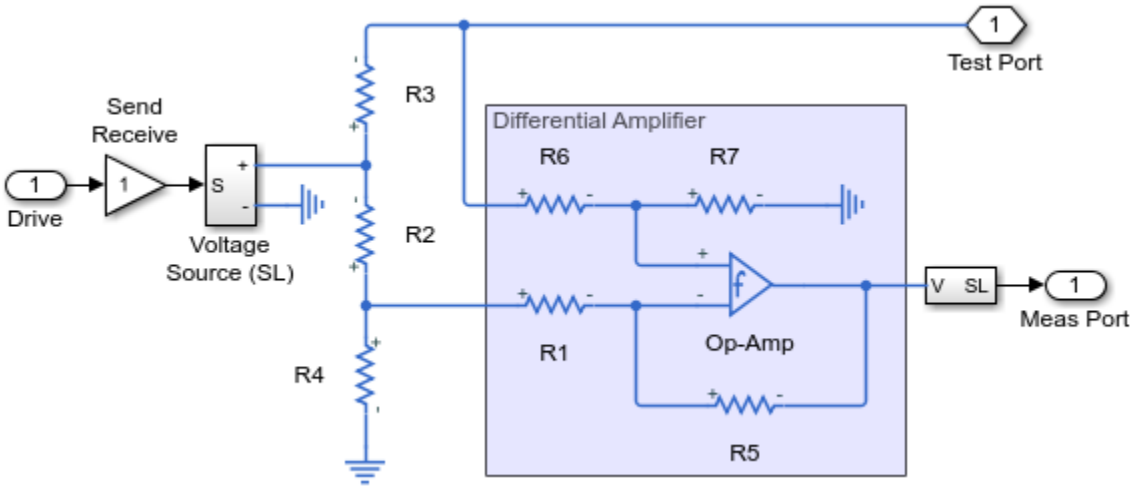
5 Segments (S2 Subsystem)
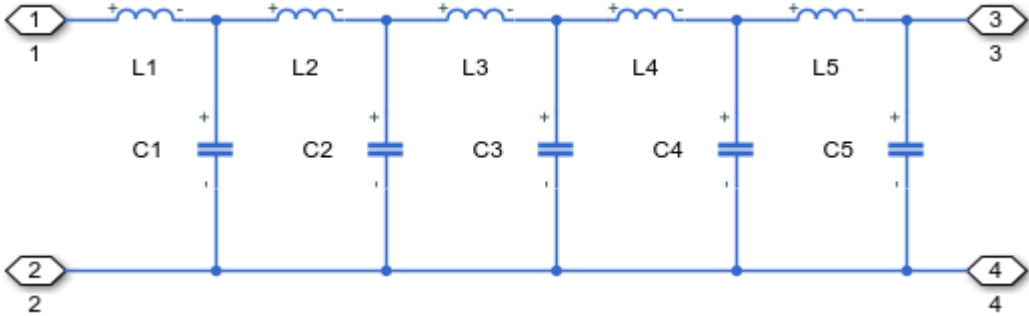
Simulation Results from Simscape Logging

The plots below show the source, transmitted, and reflected signals for transmission tests from A to B and from B to A. Because the transmission line is asymmetric, differences can be seen in the reflected signals.
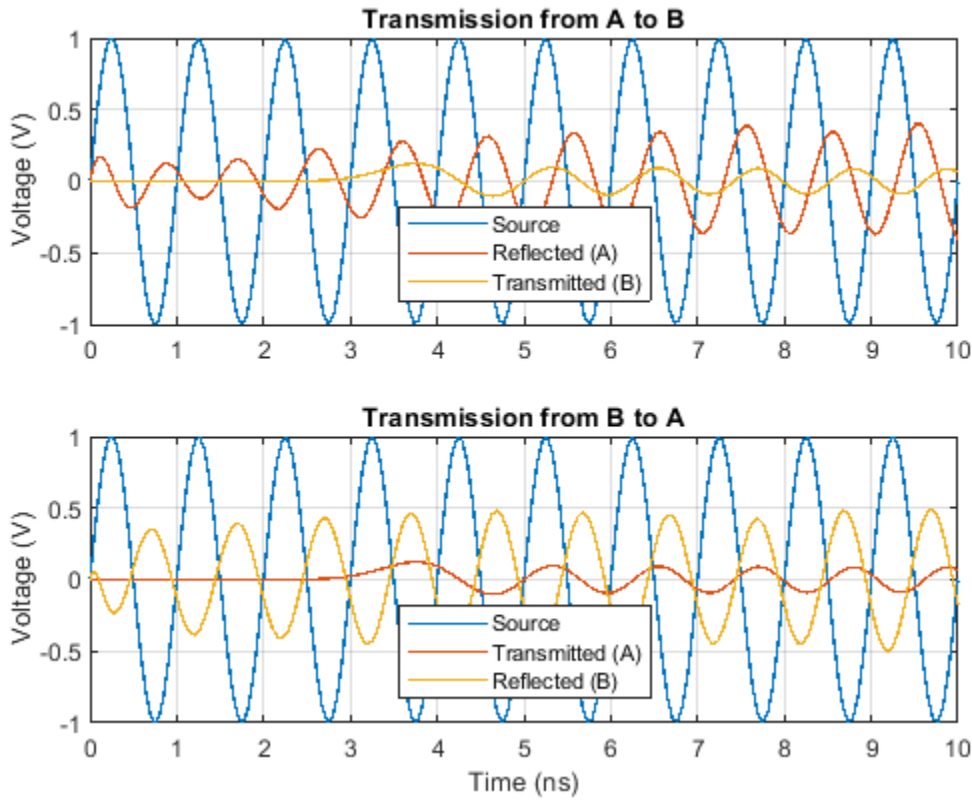
Frequency Response
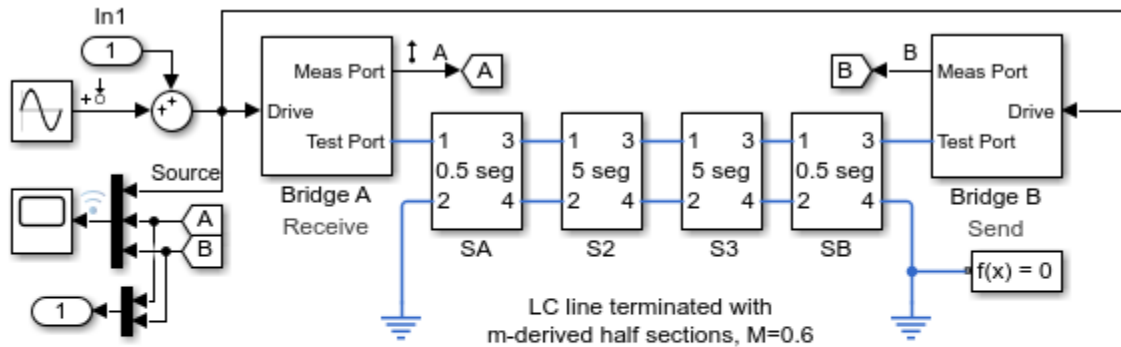
LC line terminated with
m-derived half sections, M=0.6

LC Transmission Line and Test Bridge
1. Configure transmission: A to B, B to A
2. Plot signals for A to B and B to A (see code)
3. Linearize circuit to view frequency response (see code)
4. Explore simulation results using sscexplore
5. Learn more about this example

Transmission Line Specs

Impedance Zo   = 50
Trans. delay (ns) = 3.1831
Cutoff freq (GHz) = 1

Frequency Response (Transmission Line, Source to A)
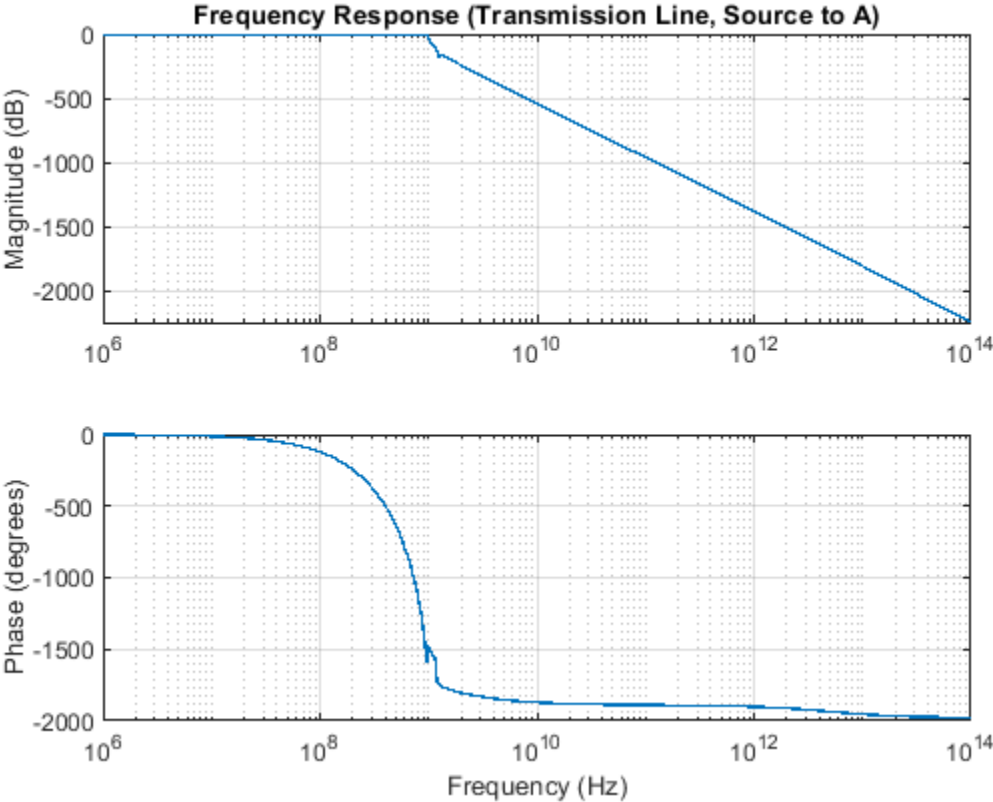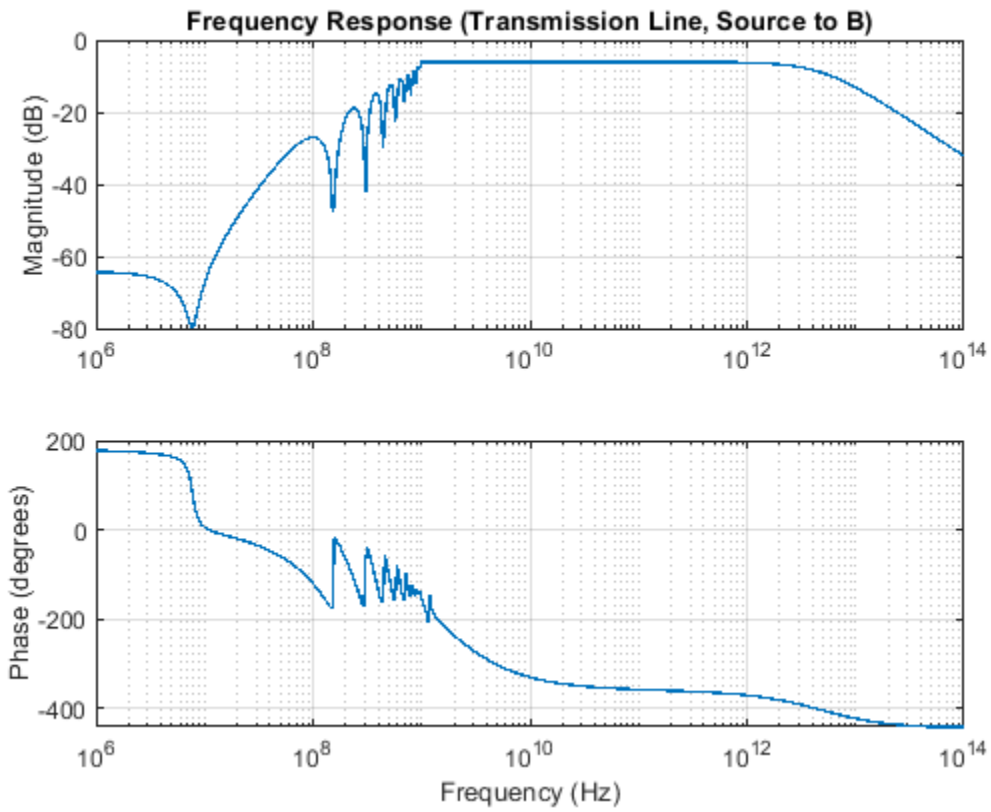
Automotive Electrical System

This example shows a simplified dynamic model of an automotive electrical system. The model contains electrical, mechanical, and thermal systems, and is able to simulate the effect of engine starting on the electrical network.

Model

Automotive Electrical System
1. Plot test results (see code)
2. Explore simulation results using sscexplore
3. Learn more about this example
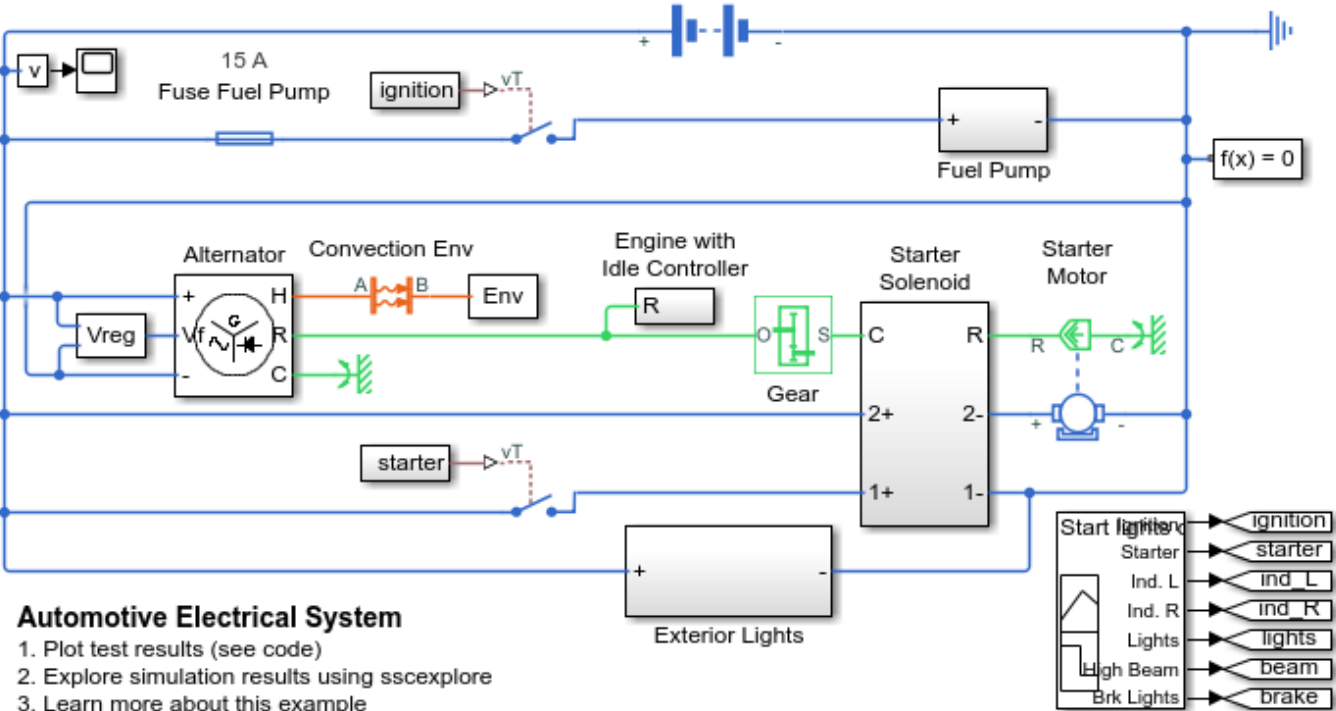
Exterior Lights Subsystem
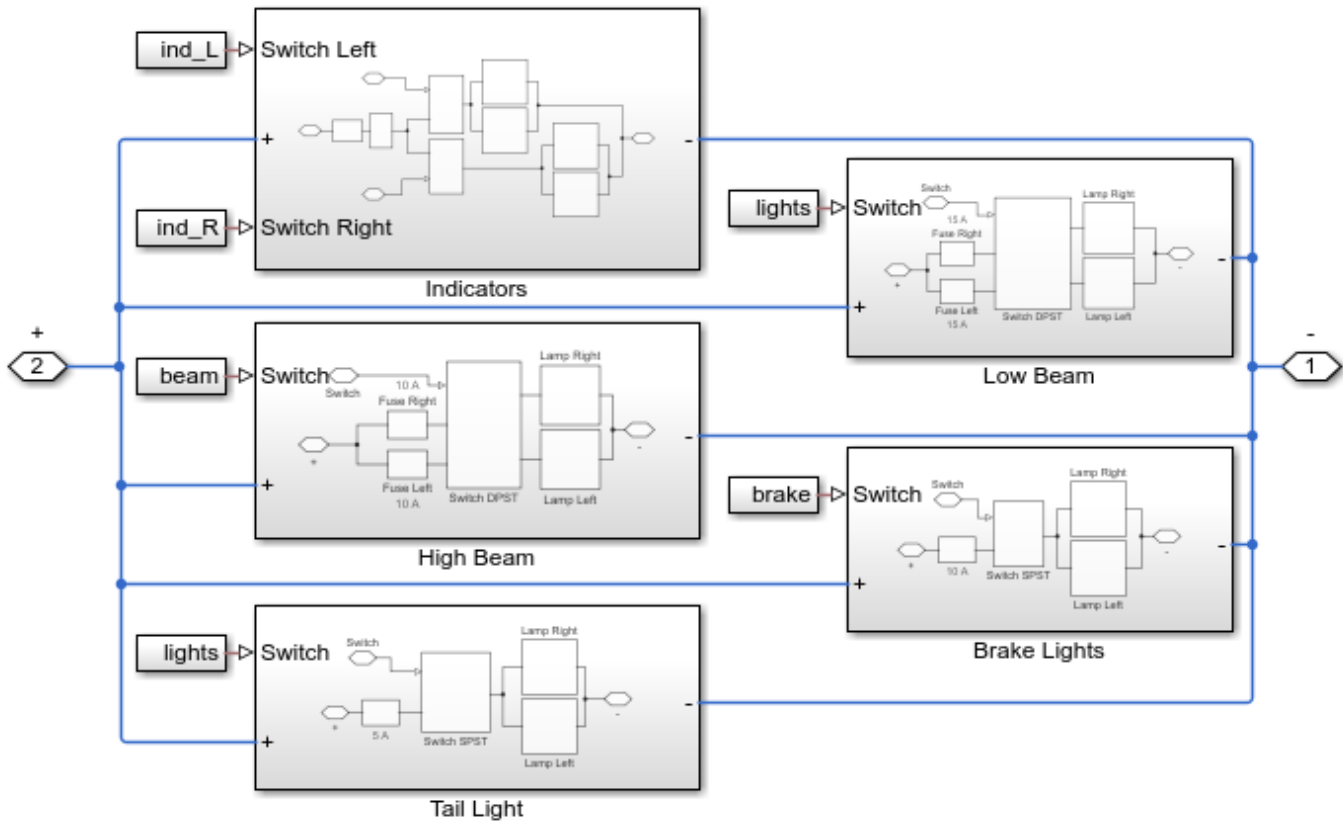
Indicators Subsystem
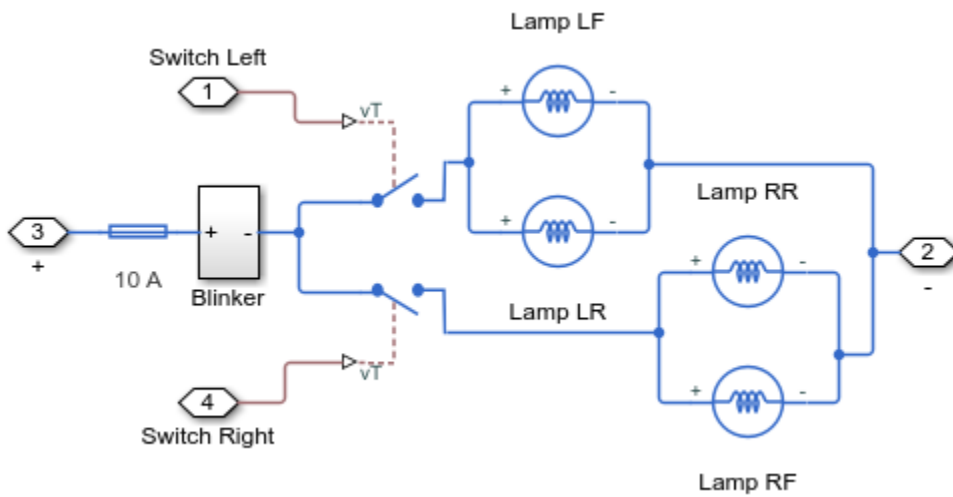
Starter Solenoid Subsystem
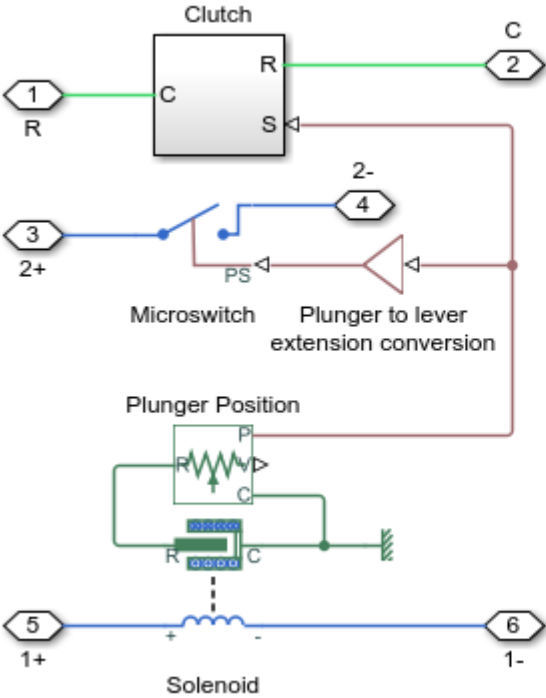
The clutch could be more accurately modeled using the Dog Clutch in Simscape Driveline(TM). Additionally, the Simscape Driveline Unidirectional Clutch could be used to ensure that once the engine catches it does not backdrive the starter motor.

Solenoid plunger has travel of 10mm. When plunger position is 10mm, the clutch is fully disengaged, and when the plunger is anywhere between 5mm and 0mm, the clutch is fully engaged. Switch closes and enables the starter motor only when the plunger is between 0 and 2mm.

Simulation Results from Simscape Logging

The plot below shows a startup sequence for an automotive electrical network. The starter motor is turned on, which draws current from the battery in order to start the engine. As lights and other electrical loads are turned on and off, the current draw from the battery varies.
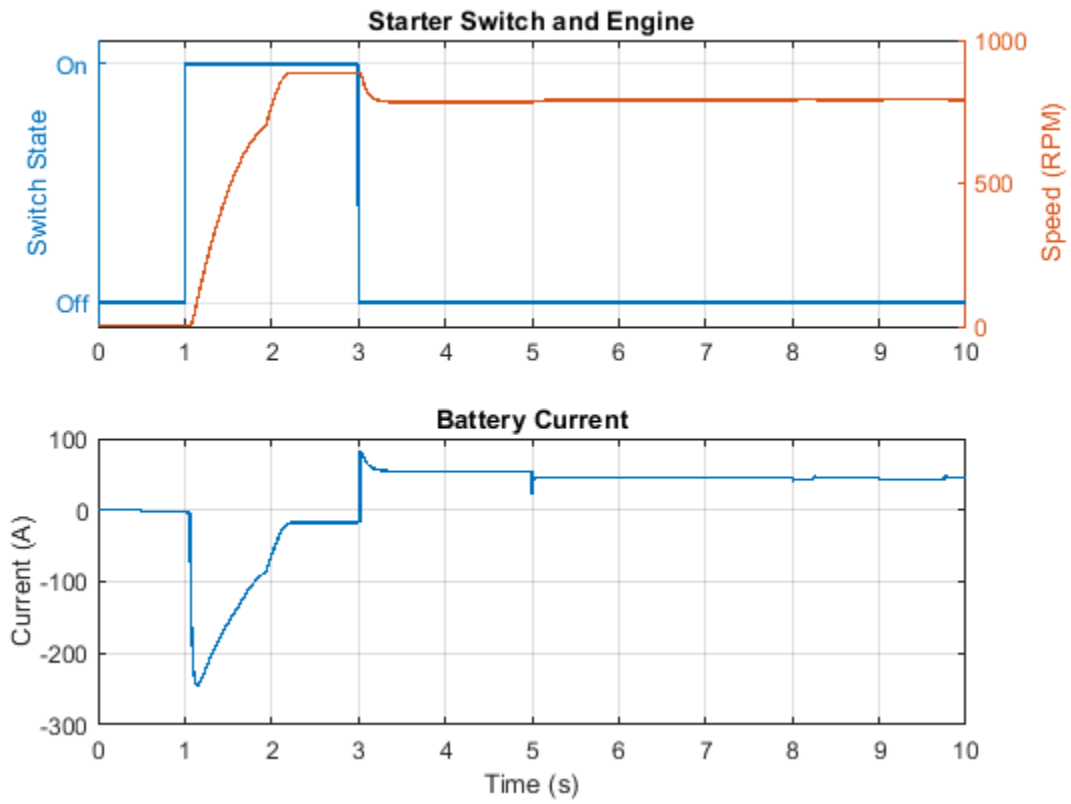
Electric Vehicle Configured for HIL

This example shows an electric vehicle model suitable for Hardware-In-the-Loop (HIL) deployment. Energy-based modeling is used to avoid high-frequency switching, and solvers set for fixed-step simulation.

The test run shows the vehicle accelerating to a steady speed up an incline followed by a period of descent during which electrical power is returned to the battery.

Model
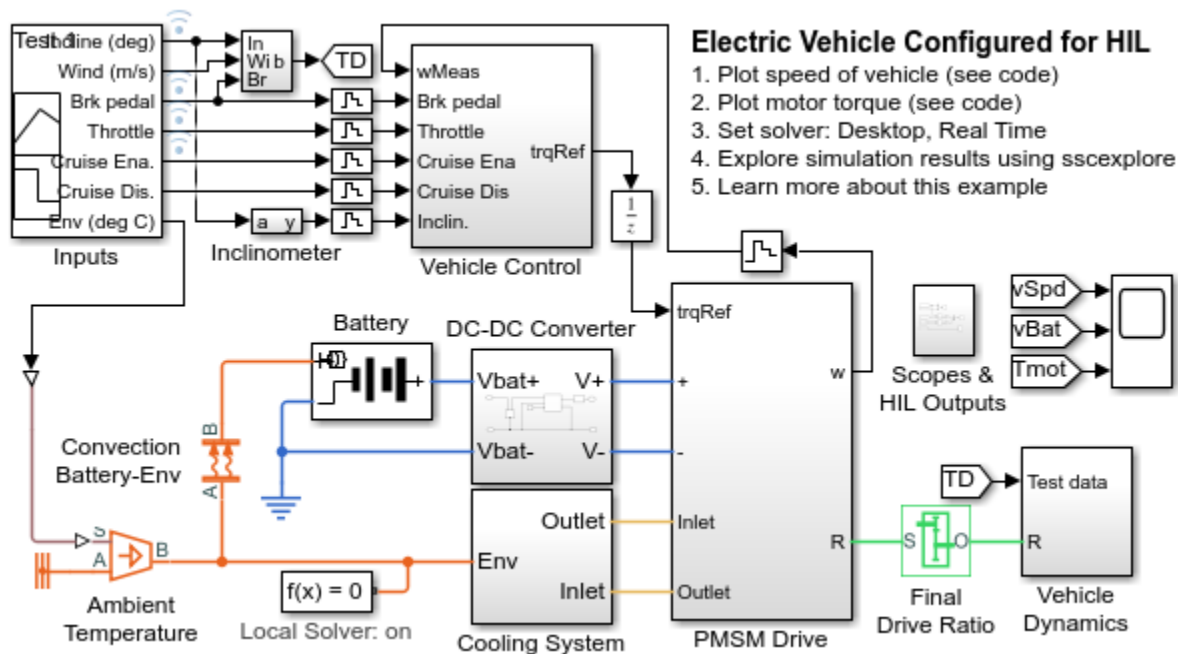
PMSM Drive Subsystem
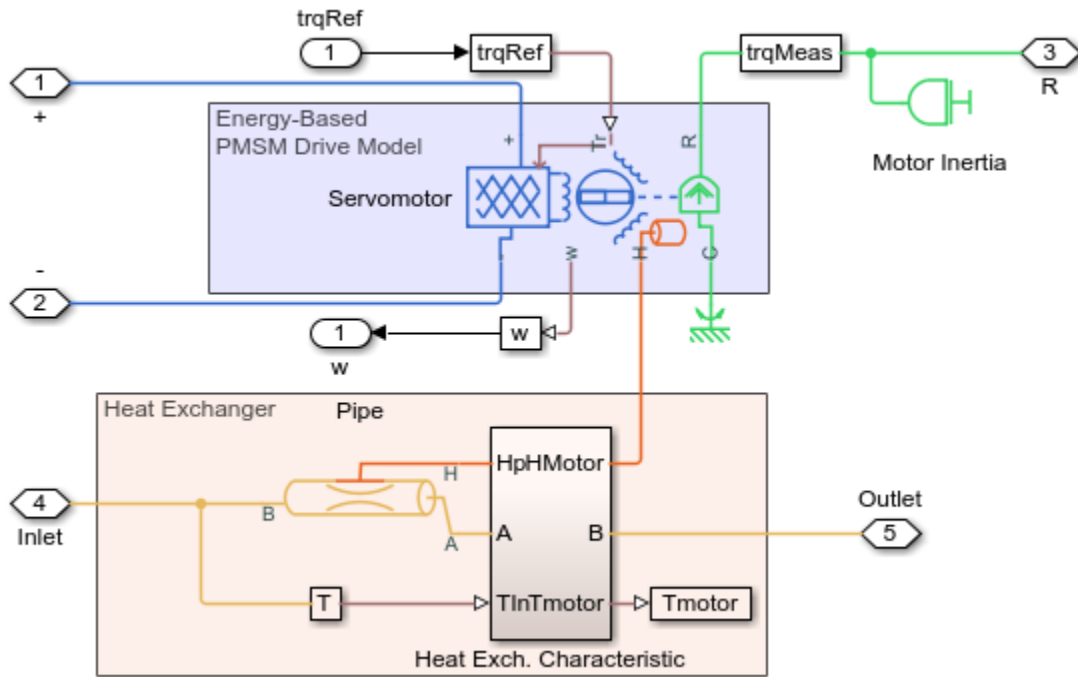
Cooling System Subsystem
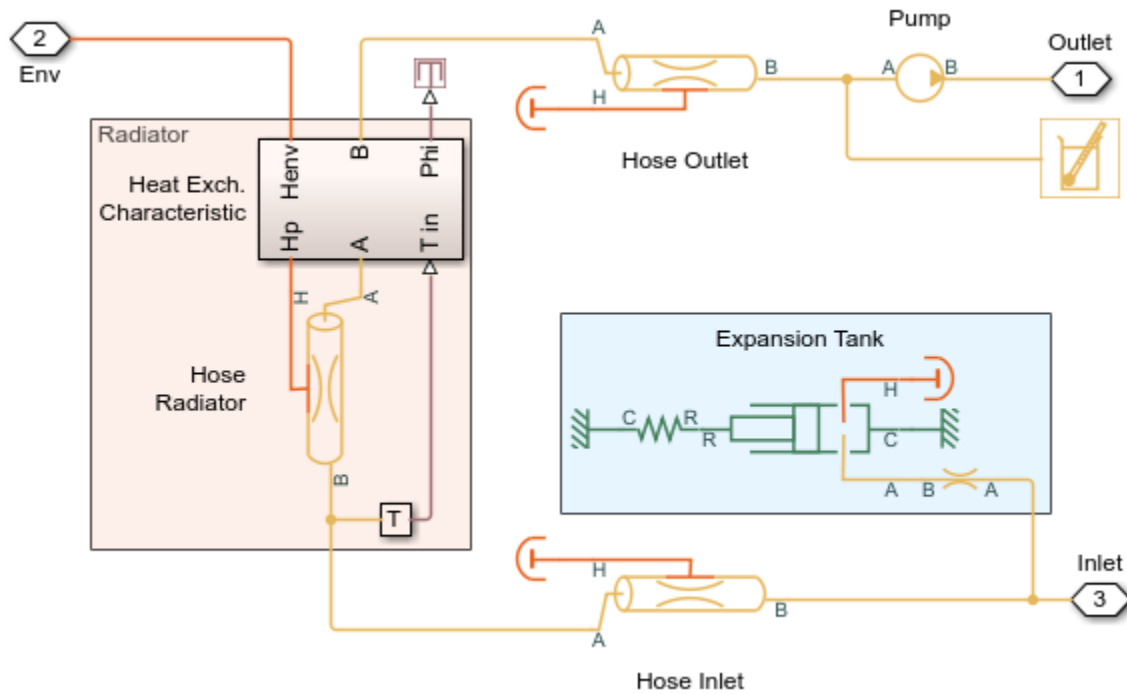
Electric and Thermal Model Subsystem
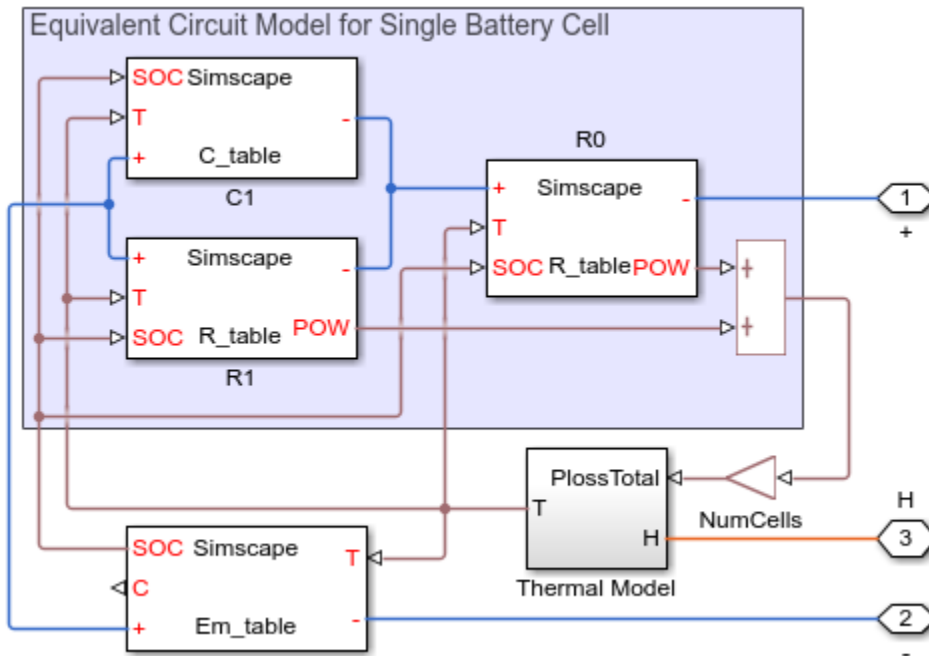
Vehicle Control Subsystem
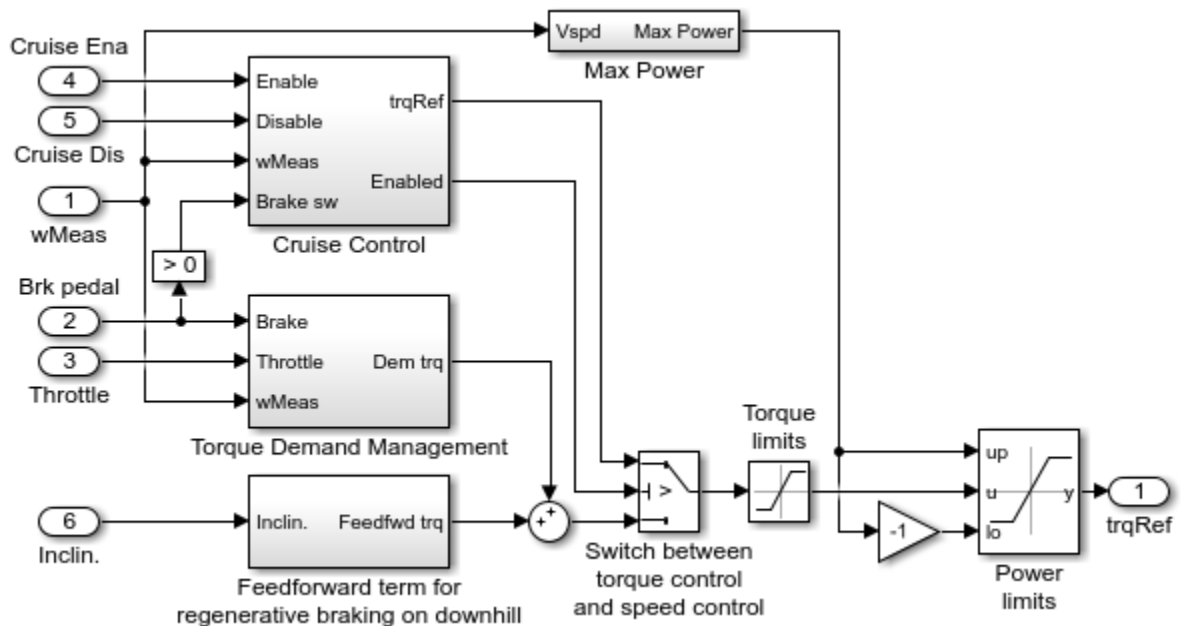
Simulation Results from Simscape Logging

The plot below shows the behavior of an electric vehicle subject to driver inputs and environmental conditions. The vehicle accelerates until the driver enables cruise control. The vehicle maintains

speed even as the grade of the road changes. As the driver applies the brakes, the vehicle slows down to zero speed.

Vehicle Speed

Driver Inputs and Environment
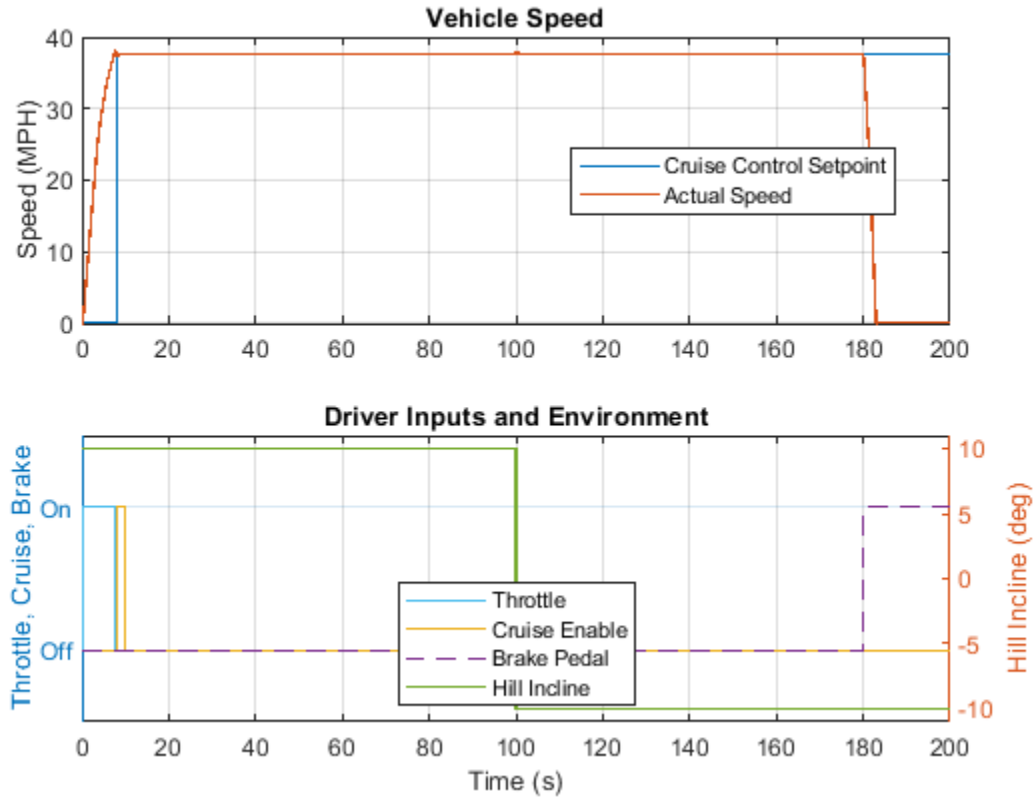
The plots below shows the torque produced by the PMSM motor in the electric vehicle and its temperature. During the first half of the simulation the motor is accelerating the vehicle to the commanded speed and then continuing to apply torque to push the vehicle up a hill. During the second half of the simulation, the motor acts as a generator as shown by the change in sign of motor torque.
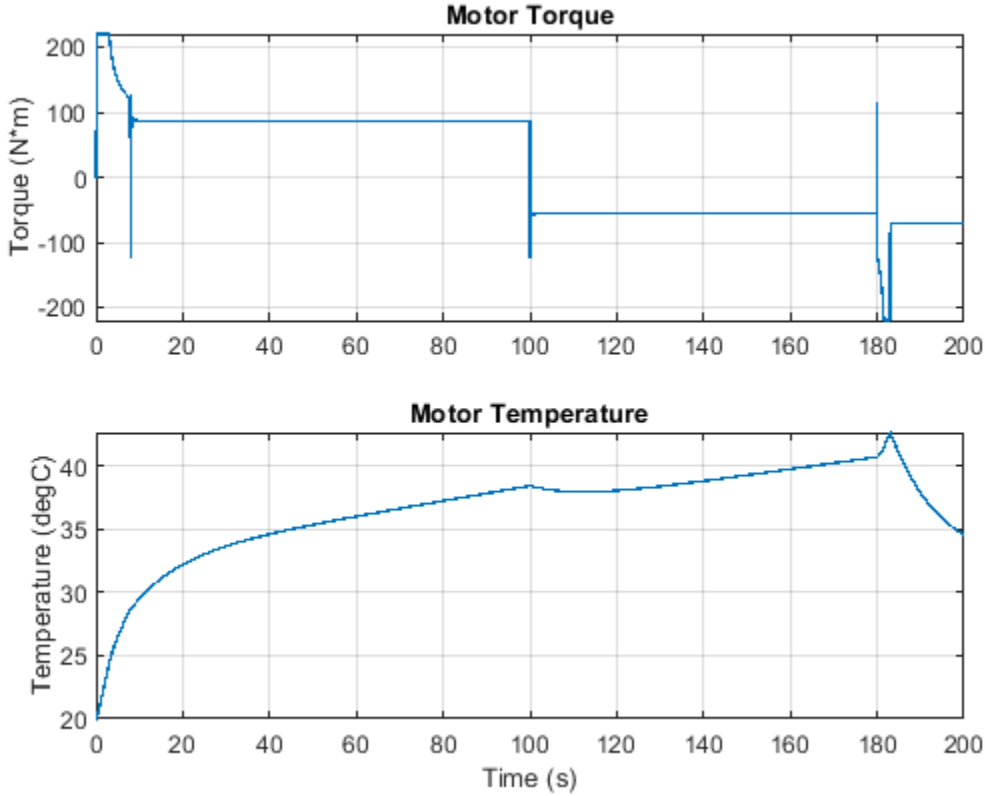
Kinetic Energy Recovery System

This example shows operation of a Kinetic Energy Recovery System (KERS) on a Formula 1 car. The model permits the benefits to be explored. During braking, energy is stored in a lithium-ion battery and ultracapacitor combination. It is assumed that a maximum of 400KJ of energy is to be delivered in one lap at a maximum power of 60KW. Design parameters are the weight of the battery, ultracapacitor and motor-generator. If these parameters are all set to the very small value of 0.01kg, the lap time is 95.0 seconds, this corresponding to a car with no KERS. With default values set here, approximately 1/4 of a second is saved on the lap time when using any available electrical power when not braking. Applying KERS only to selected corners requires a larger ultracapacitor to show any significant benefit.

This model shows how Simscape™ Electrical™ and Simscape can be used to support system-level design. The KERS performance is a complex trade-off between the masses of the three main components (battery, ultracapacitor and motor-generator), plus the energy-management strategy. The KERS system adds mass which reduces acceleration due to the engine. The stored electrical energy from braking must more than compensate for this. Lithium-ion batteries have a very high energy per unit mass but a poor power per unit mass. Conversely an ultracapacitor has relatively low energy per unit mass, but a very high power per unit mass which suits this particular application.

Model
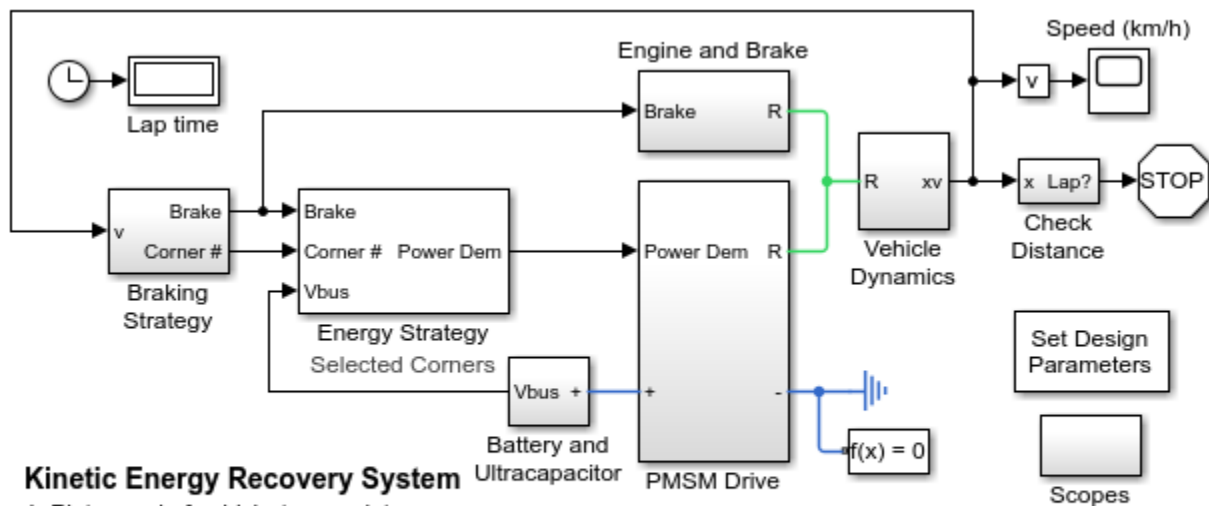
Kinetic Energy Recovery System
1. Plot speed of vehicle (see code)
2. Compare energy strategies for KERS use (see code)
3. Select KERS strategy: All Corners, Selected Corners
4. Explore simulation results using sscexplore
5. Learn more about this example

PMSM Drive Subsystem
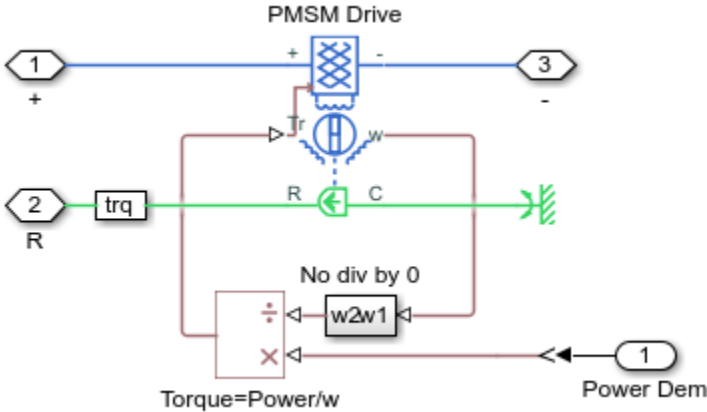
Battery and Ultracapacitor Subsystem
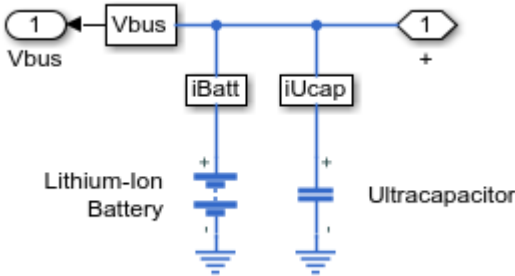
Predict Speed at Corner If Maximum Braking Used Subsystem
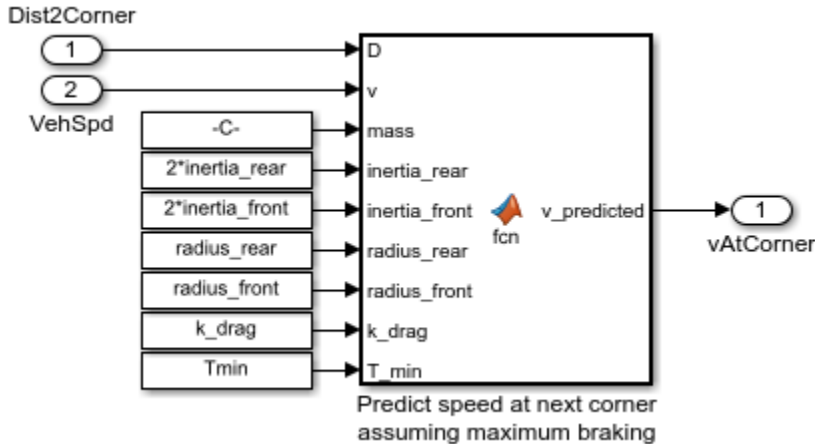
Simulation Results from Simscape Logging

The plot below shows the vehicle speed during a single lap. The driver knows the maximum speed the vehicle should be travelling at the corners on the track and applies the brakes to achieve that speed in the corner.
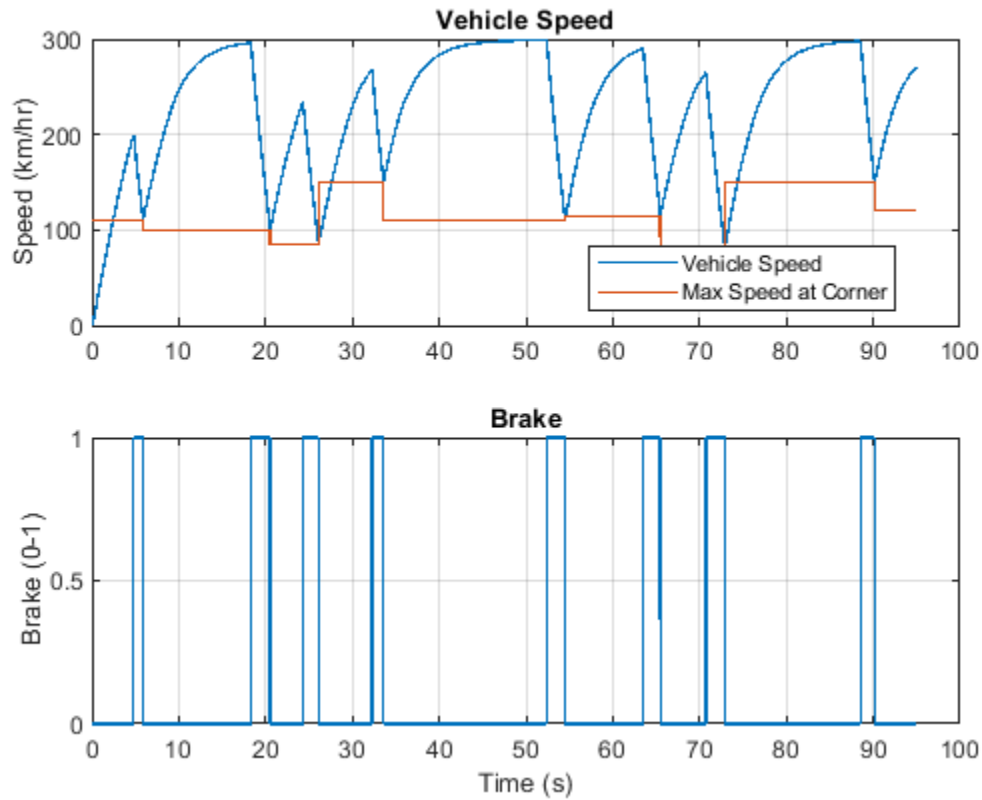
The plot below compares the two strategies for using the electrical drive during acceleration. One strategy uses the electrical powertrain during all corners, the other uses it only on selected corners. The difference is easiest to see on the plot of motor torque, where the selected corners strategy shows zero torque from the motor during a number of the corners in the lap (rapid deceleration and acceleration).
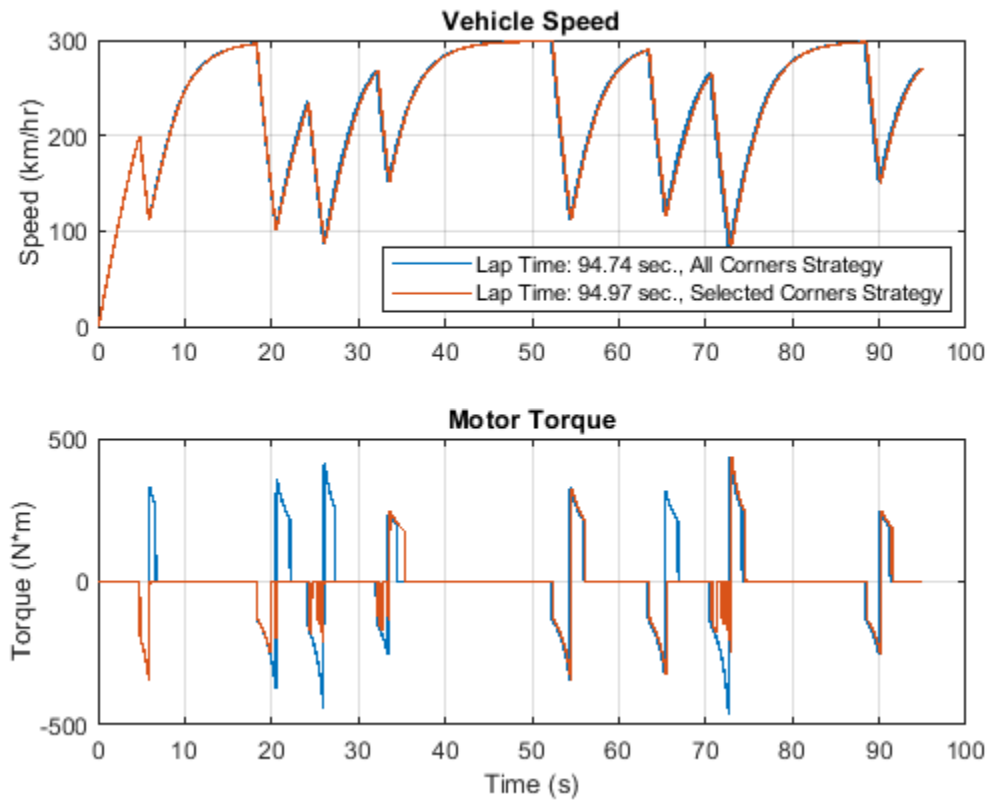
Power Split Hybrid Vehicle Electrical Network

This example shows the basic architecture of a power-split hybrid transmission. The planetary gear, along with the motor and generator, acts like a variable ratio gear. In this test, the vehicle accelerates from 15 m/s to 20 m/s, and then decelerates back to 15 m/s. The power management strategy uses just electrical power to perform the maneuver.

The motor, generator, and DC-DC converter are modeled using Simscape™ Electrical™ library blocks. These blocks use energy-based system-level equations that result in an efficient simulation whilst still capturing conversion losses. As such, the model is suitable for supporting design of the power management strategy. This example can be directly compared with the Simscape Driveline™ sdl_hybrid_power_split example which also includes more accurate representations of the engine, tires and mechanical gears.

Model
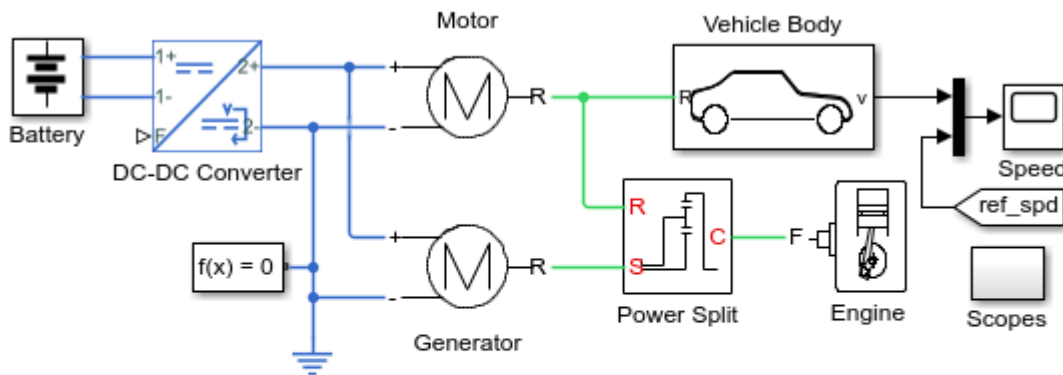
Power Split Hybrid Vehicle Electrical Network
1. Plot power of powertrain components (see code)
2. Plot electrical losses of components (see code)
3. Explore simulation results using sscexplore
4. Learn more about this example
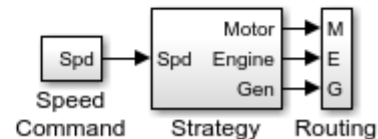
Motor Subsystem
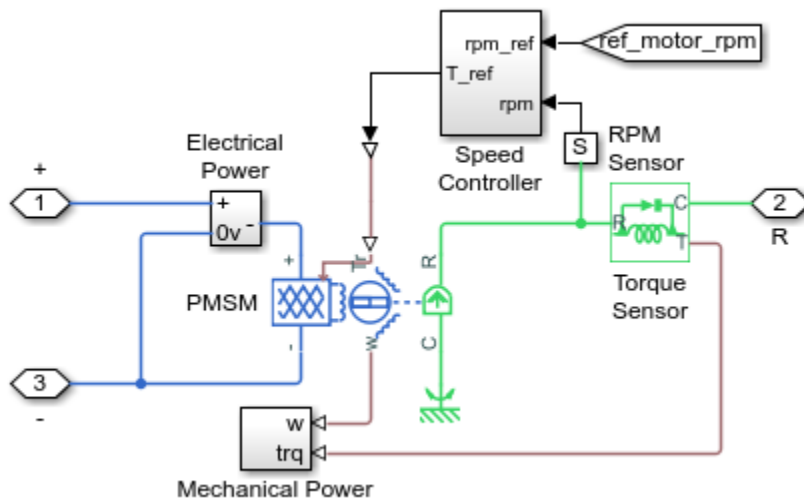
Simulation Results from Simscape Logging

The plot below shows the flow of power from the engine, motor, and generator as the vehicle accelerates and decelerates. The generator supplies the DC network with a constant flow of power drawn from the engine. The motor draws power from the battery to accelerate the vehicle and then uses regenerative braking to feed that power back to the battery.

Power and Vehicle Speed
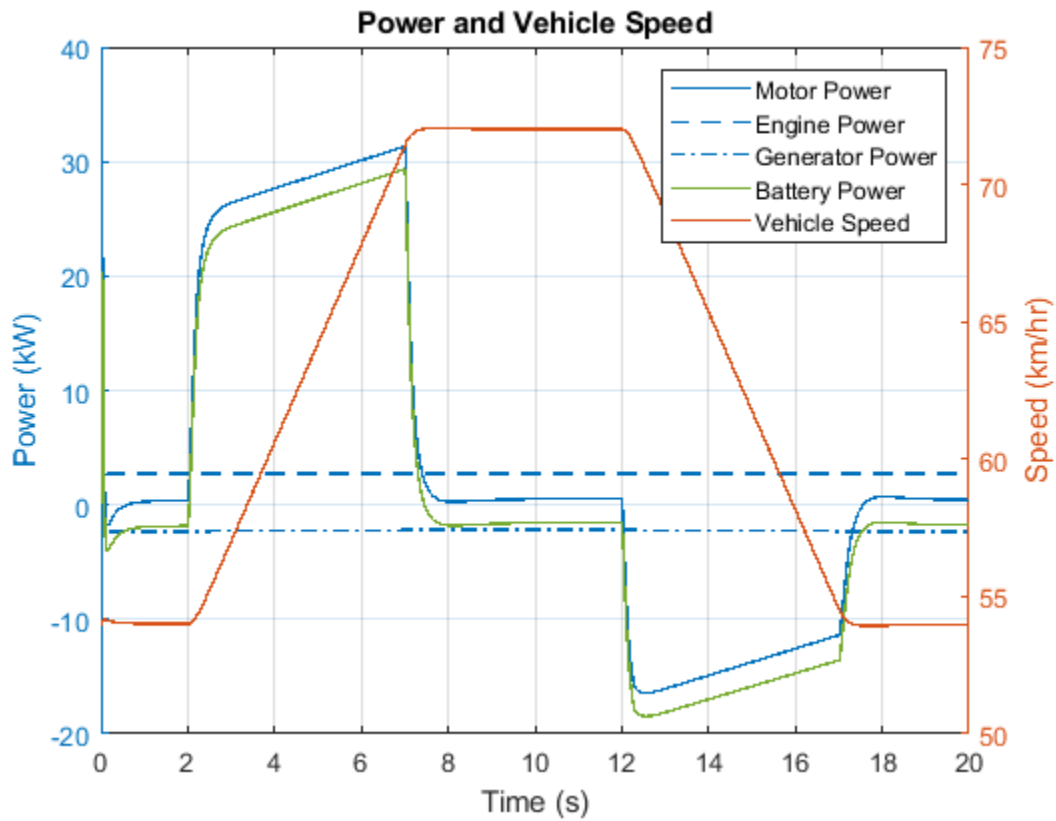
The plot below shows the electrical losses from the motor, generator, and battery as the vehicle accelerates and decelerates. The largest losses come from the motor and the battery.

Electrical Losses and Vehicle Speed
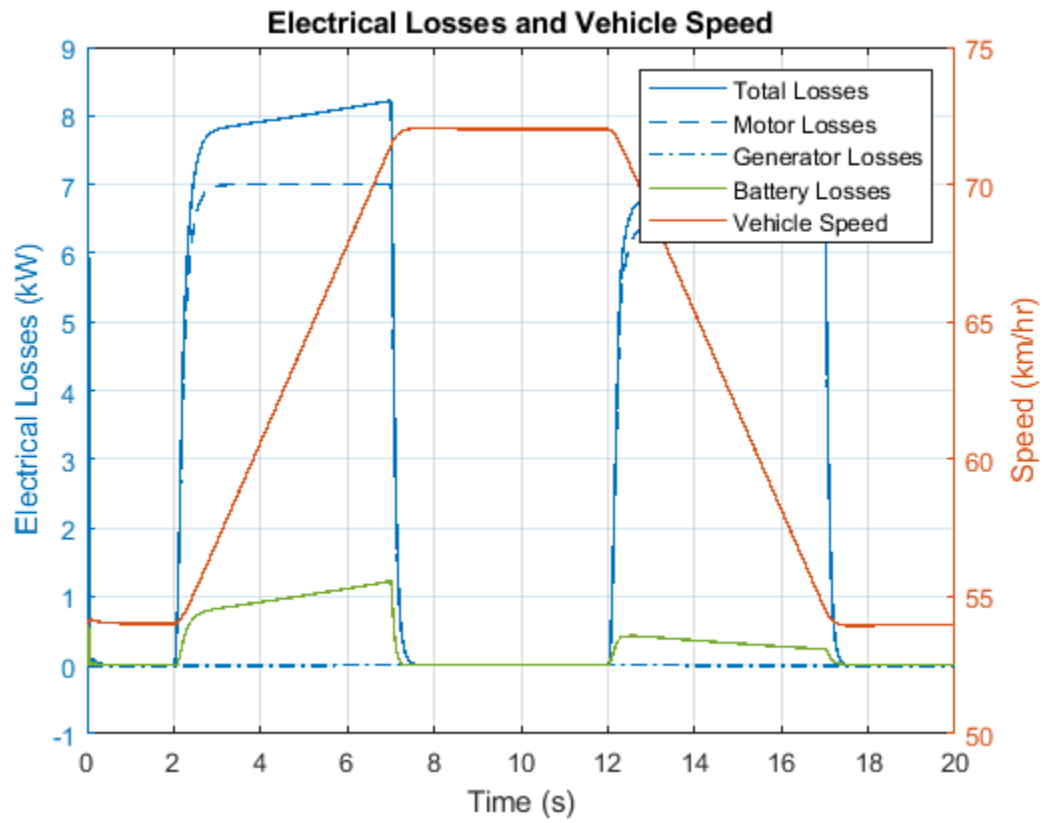
Import IPMSM Flux Linkage Data from Motor-CAD

This example shows how you can import a motor design from Motor-CAD into a Simscape™ simulation.

Flux Linkage Data

The data file defining the motor flux linkage is saved in ee_import_fem_motorcad_mat.mat. The script ee_import_fem_motorcad_data.m shows how this data was originally exported from Motor-CAD into the MATLAB® workspace.

Model
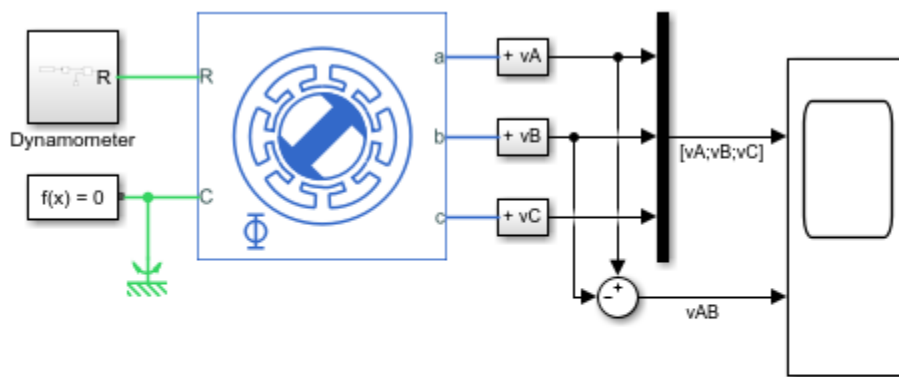
Import IPMSM Flux Linkage Data from Motor-CAD

1. Motor data: Open MATLAB script
2. Plot voltages in motor windings (see code)
3. Explore simulation results using sscexplore
4. Learn more about this example

Simulation Results from Simscape Logging

The plot below shows the simulated open-circuit voltages. Notice the nonlinear back EMF profile that would not be captured if using a simplified PMSM model with fixed back EMF constant.
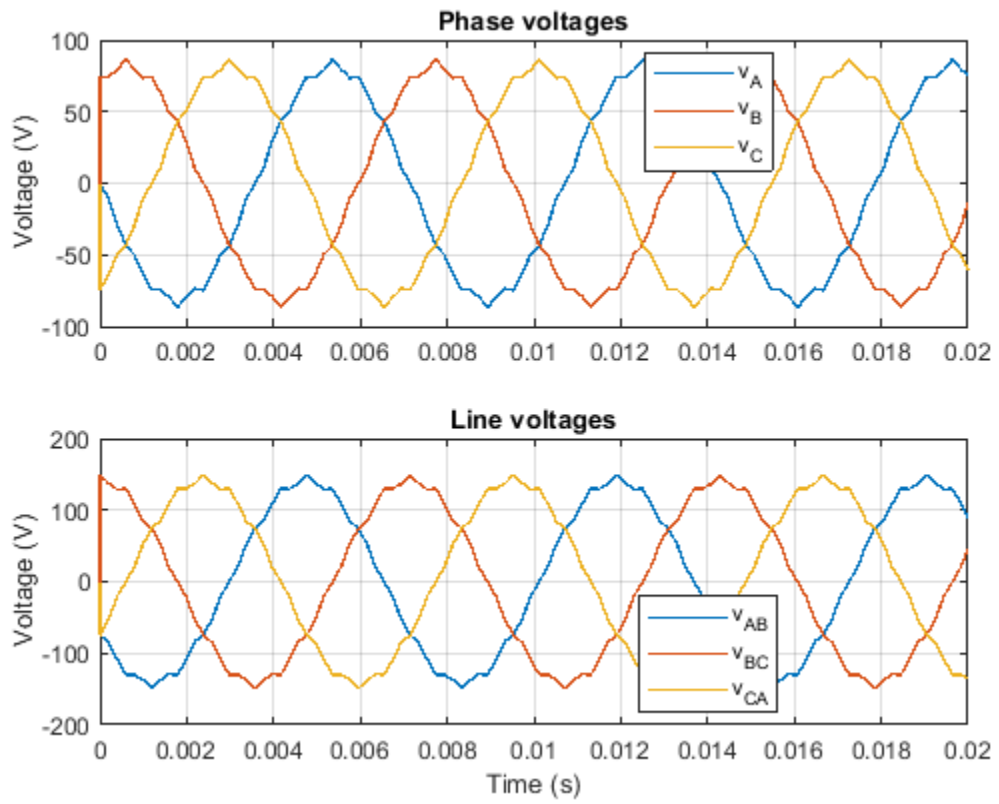
Three-Phase PMSM Traction Drive

This example shows how to control the rotor speed in a permanent magnet synchronous machine (PMSM) based electrical-traction drive. A high-voltage battery feeds the FEM-Parameterized PMSM block through a controlled three-phase converter. A Rotational Friction block provides the load. The position and speed information are obtained by using a high-fidelity resolver. The PMSM controller subsystem includes a cascade control structure which has an outer speed-control loop and two inner current-control loops. During the 0.3 seconds simulation, the rotor speed demand is ramped-up from 0 to 1000 rpm.

Model

Simulation Results from Simscape Logging

The plot below shows the requested and measured speed for the test, as well as the phase currents in the electric drive.
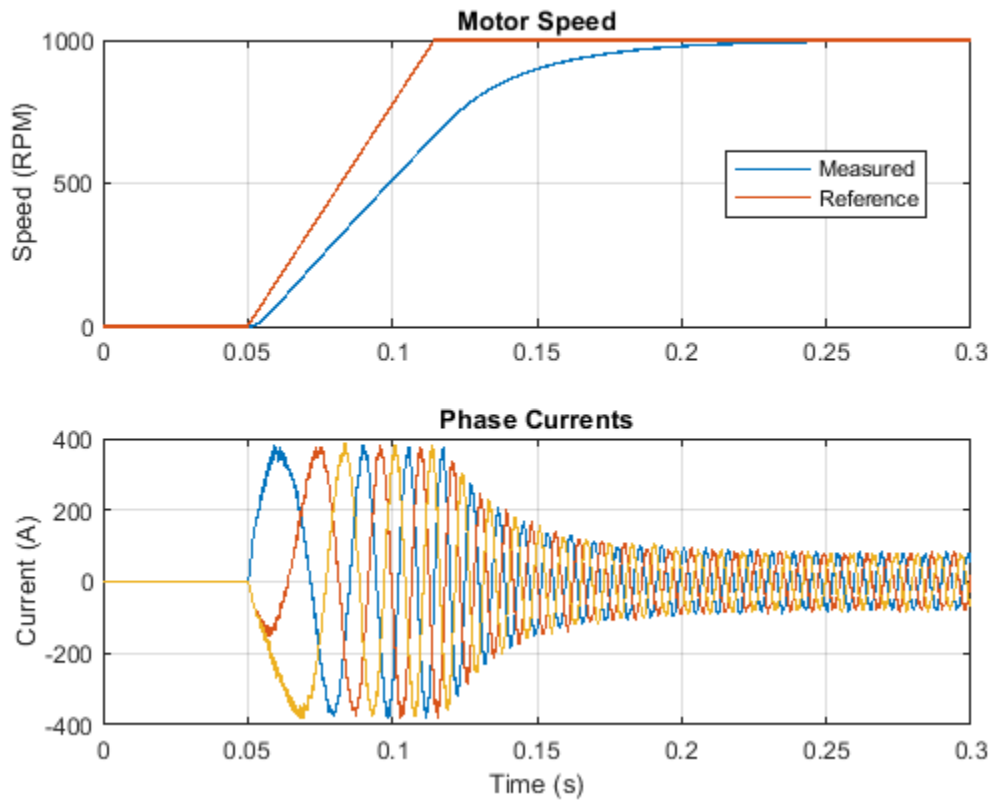
Solar Panel Parameterization Validation

This example shows how to model a solar panel using information from a manufacturer datasheet. The data is imported and used to generate current-voltage and power-voltage curves for the solar panel. The power-voltage curve is useful for designing an inverter because it helps to identify the peak power for a given irradiance level and panel cell temperature

Model
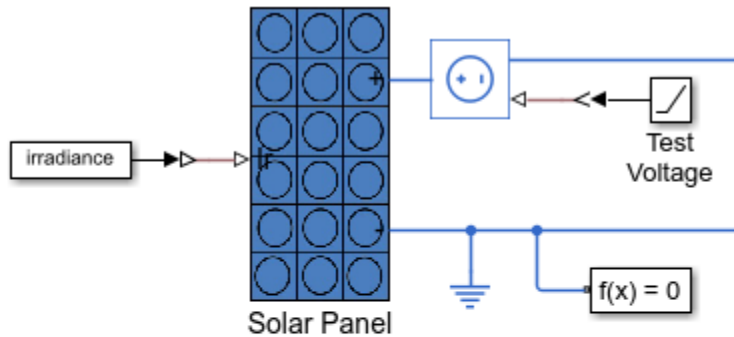
Solar Panel Parameterization Validation

1 Open script that inputs values from the datasheet
2. Plot current, power curves at varying conditions (see code)
3. Explore simulation results using sscexplore
4. Learn more about this example

Solar Panel Subsystem

The Solar Panel subsystem models a solar panel that contains parallel-connected strings of series-connected solar cells. The solar cell strings are modeled by a single Solar Cell block from the Simscape™ Electrical™ library. The number of parallel strings is defined by the workspace variable *Np_cell* and the number series-connected cells in each string is defined by workspace variable *Ns_cell*.

Connecting multiple solar cell strings in parallel can slow simulation because it increases the number of elements in a model. To avoid a reduction in performance, a controlled-current source scales up the current to the fulfill the demand of the requisite number of parallel paths. The current source is modeled by a Current-Controlled Current Source block from the Simscape™ Foundation Library.

The implicit assumptions are that:

• All of the solar cells are identical.

• Temperature and irradiance levels are the same for each cell.
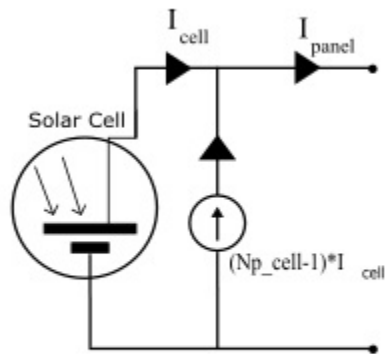
Simulation Results

The plot shows current and power as a function of voltage for various irradiance and cell temperatures. The table shows the peak power values extracted from the plot. If the datasheet includes a plot like this, then it can be used to validate correct parameterization of the Solar Cell block.

```
Maximum power operating points
    PanelIrradiance    CellTemperature    MaximumCurrent    MaximumVoltage    MaximumPower
    _____    _____    _____    _____    _____

         200                25                1.5067             27.11            40.847
         500                25                3.7887            29.066           110.12
        1000                25                7.6059            30.557           232.41
         200                50                1.4954             23.88            35.712
         500                50                3.7665            25.992            97.899
        1000                50                7.5762            27.576           208.92
```
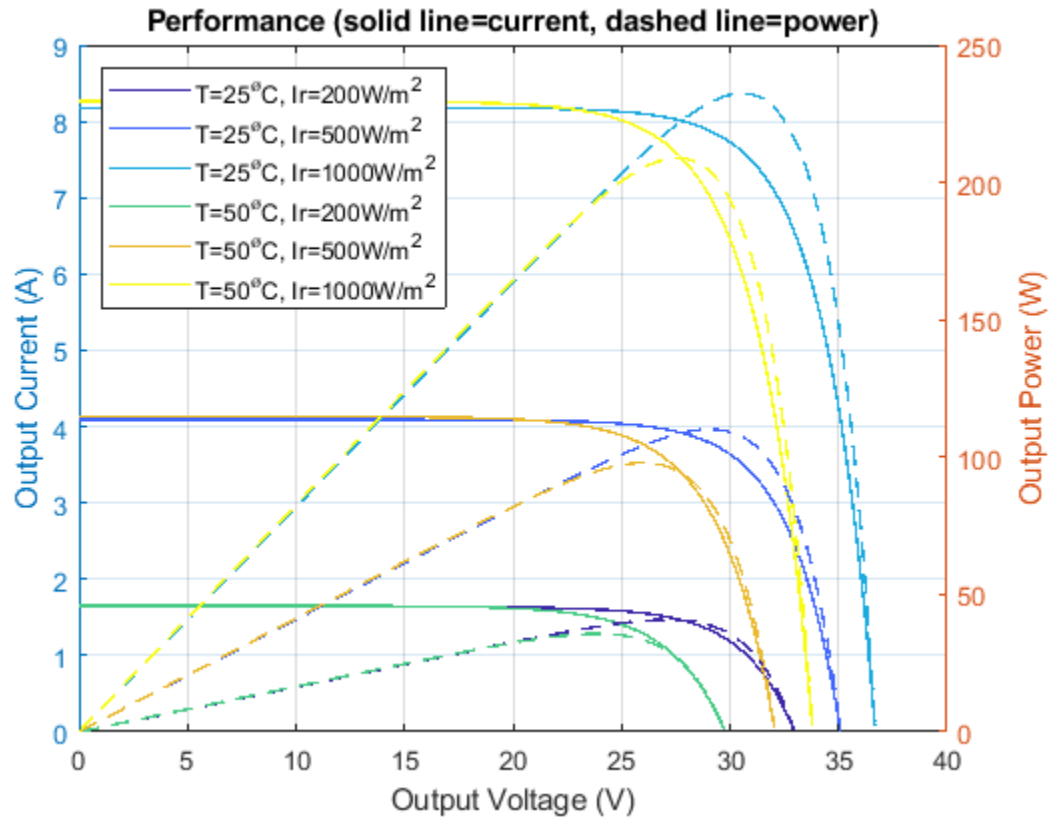
Energy Balance in a 48V Starter Generator

This example shows an interior permanent magnet synchronous machine (IPMSM) used as a starter/generator in a simplified 48V automotive system. The system contains a 48V electric network and a 12V electric network. The internal combustion engine (ICE) is represented by basic mechanical blocks. The IPMSM operates as a motor until the ICE reaches the idle speed and then it operates as a generator. The IPMSM supplies power to the 48V network, which contains the R3 power consumer. The 48V network supplies power to the 12V network which has two consumers: R1 and R2. The total simulation time (t) is 0.5 seconds. At t = 0.05 seconds, the ICE turns on. At t = 0.1 seconds, R3 switches on. At t = 0.3 seconds, R2 switches on and increases the load on the 12V electric network. The EM Controller subsystem includes a multi-rate PI-based cascade control structure which has an outer voltage-control loop and two inner current-control loops. The task scheduling in the Control subsystem is implemented as a Stateflow® state machine. The DCDC Controller subsystem implements a simple PI controller for the DC-DC Buck converter, which feeds the 12V network. The Scopes subsystem contains scopes that allow you to see the simulation results.

Model

Energy Balance in a 48V Starter Generator

1. Plot DC voltages in network (see code)
2. Explore simulation results using sscexplore
3. Modify model parameters
4. Learn more about this example
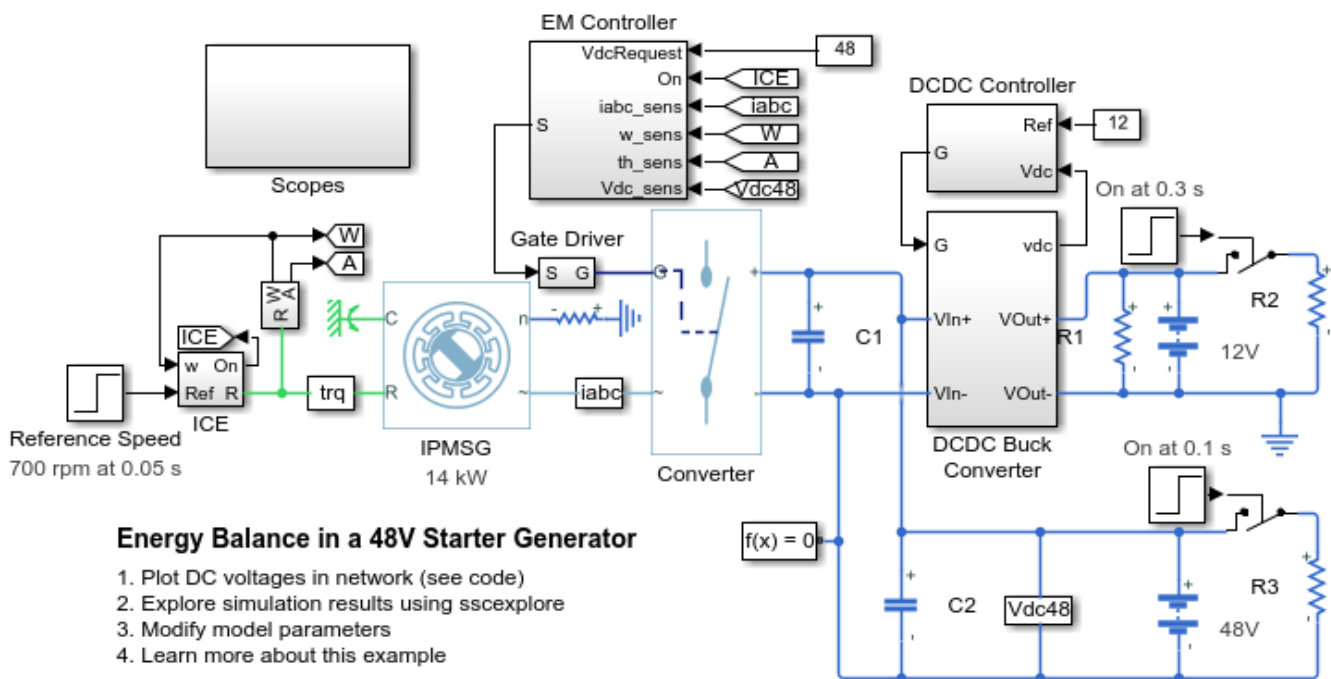
DCDC Buck Converter Subsystem
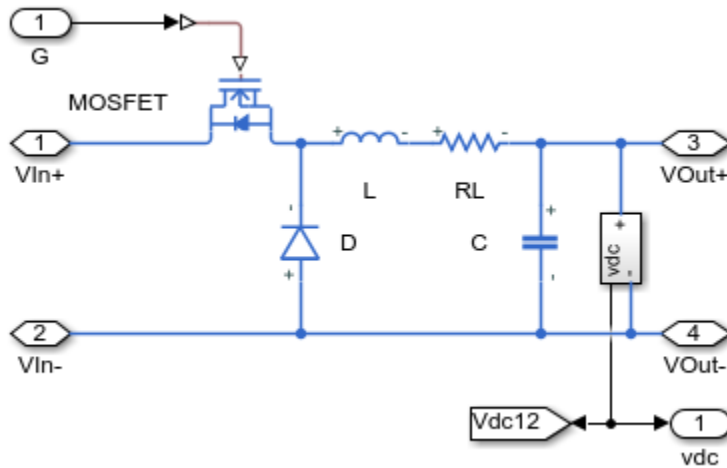
Simulation Results from Simscape Logging

The plots below show how the system maintains voltages on the 48V and 12V DC busses as loads in the network change.
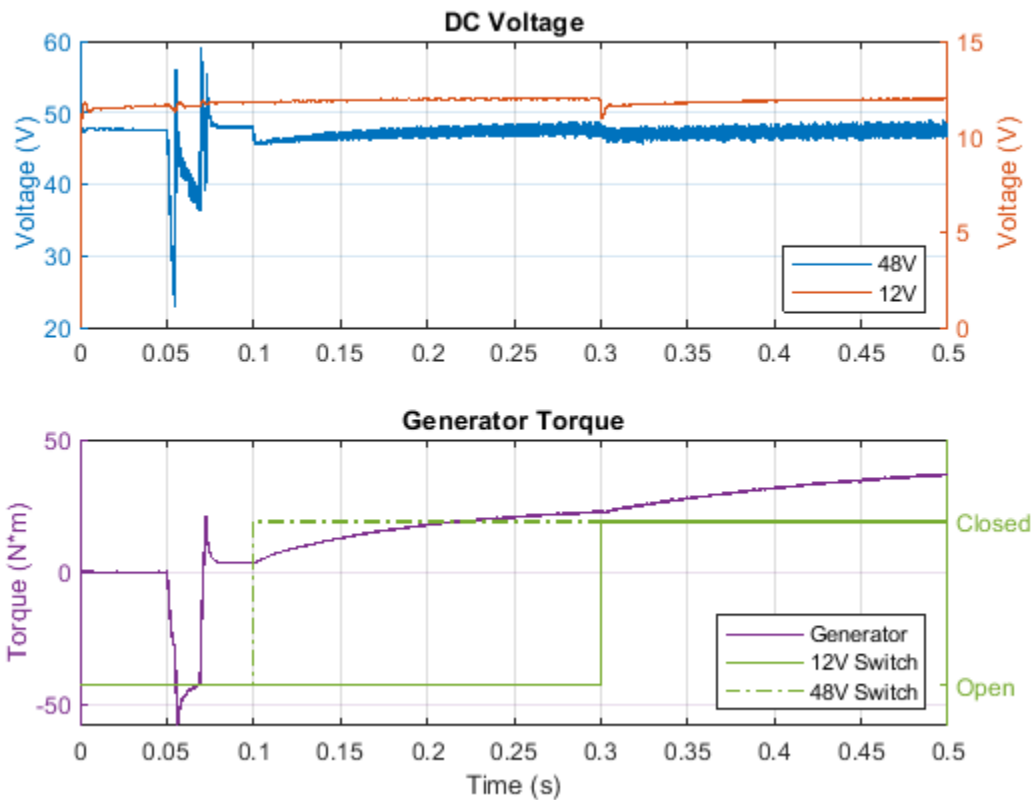
Three-Phase Asynchronous Direct Online Motor Connected to Hydraulic Pump

In this example, an asynchronous motor is connected to an ideal pump. The diameter of the valve on the hydraulic system affects the speed and current drawn by the motor.

At 0 seconds, the motor is at zero speed. It starts up and at 2.5 seconds, it reaches rated speed. At 9 seconds, the diameter of the valve reduces to 4cm and the motor stalls.

Model
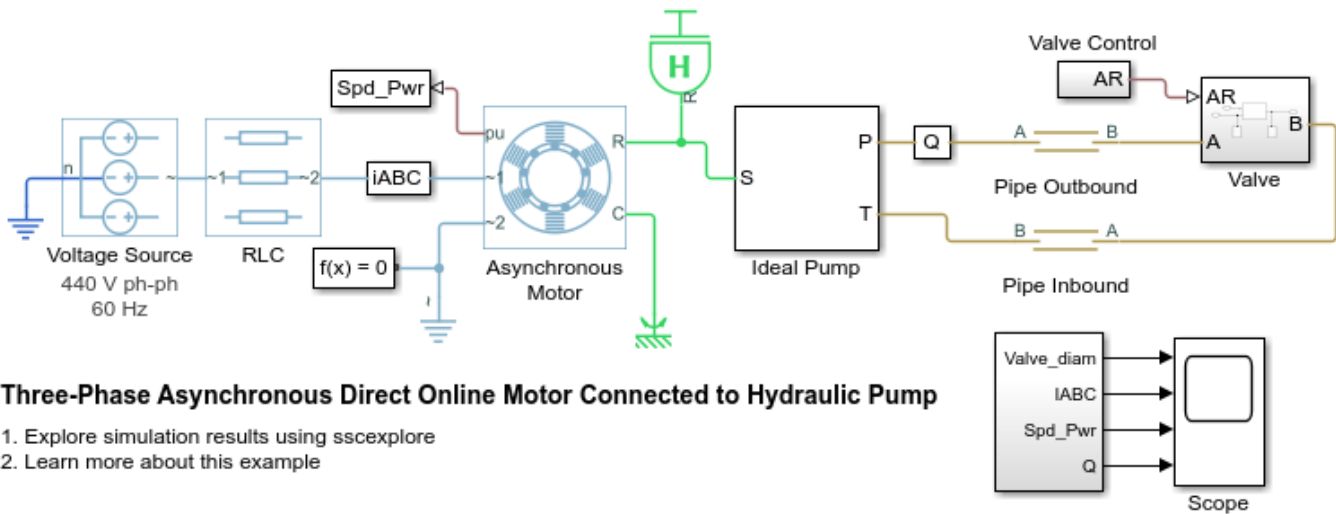
Three-Phase Asynchronous Direct Online Motor Connected to Hydraulic Pump

1. Explore simulation results using sscexplore
2. Learn more about this example

Three-Phase Asynchronous Drive with Sensorless Control

This example shows how to control and analyze the operation of an Asynchronous Machine (ASM) using sensorless rotor field-oriented control. The model shows the main electrical circuit, with three additional subsystems containing the controls, measurements, and scopes. The Controls subsystem contains two controllers: one for the Grid-Side Converter (AC/DC) and one for the Machine-Side Converter (DC/AC). The Scopes subsystem contains two time scopes: one for the Grid-Side Converter and one for the ASM. When the model is executed, a Spectrum Analyzer opens and displays frequency data for the A-Phase Supply Current.

Model

Three-Phase Asynchronous Drive with Sensorless Control

1. Modify model parameters
2. Explore simulation results using sscexplore
3. Learn more about this example

Three-Phase Asynchronous Drive with Sensor Control

This example shows how to control and analyze the operation of an Asynchronous Machine (ASM) using sensored rotor field-oriented control. The model shows the main electrical circuit, with three additional subsystems containing the controls, measurements, and scopes. The Controls subsystem contains two controllers: one for the Grid-Side Converter (AC/DC) and one for the Machine-Side Converter (DC/AC). The Scopes subsystem contains two time scopes: one for the Grid-Side Converter and one for the ASM. When the model is executed, a Spectrum Analyzer opens and displays frequency data for the A-Phase Supply Current.
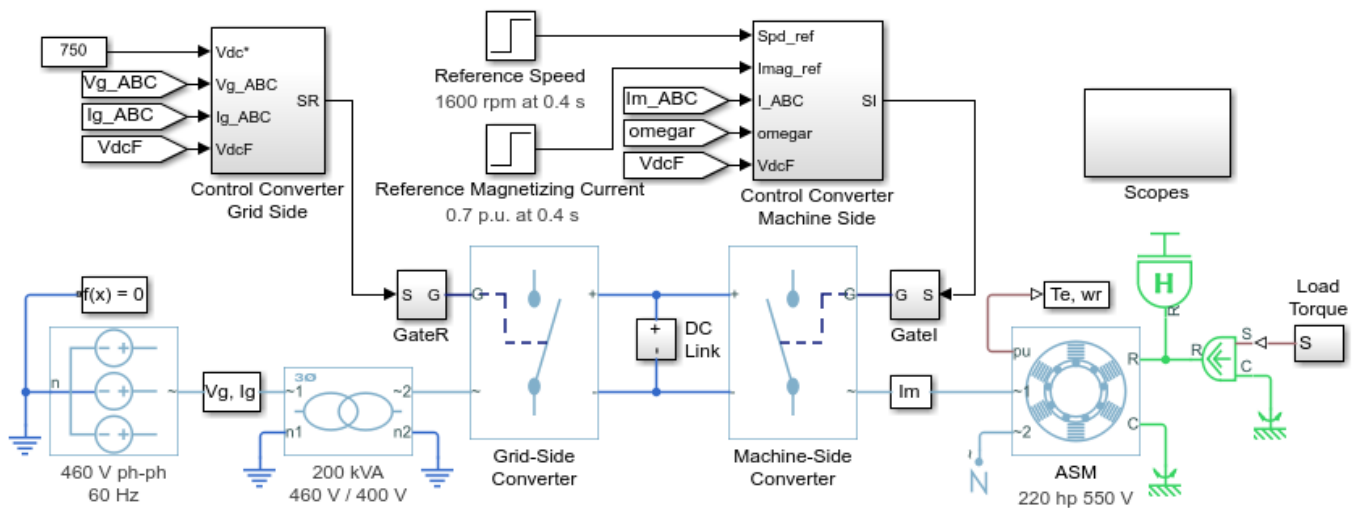
Model

Three-Phase Asynchronous Drive with Sensor Control

1. Modify model parameters
2. Explore simulation results using sscexplore
3. Learn more about this example

Asynchronous Machine Direct Torque Control

This example shows how to control an asynchronous machine (ASM) using the direct-torque control method. A PI-based speed controller supplies the torque reference. The direct-torque controller generates the inverter pulses.

Model
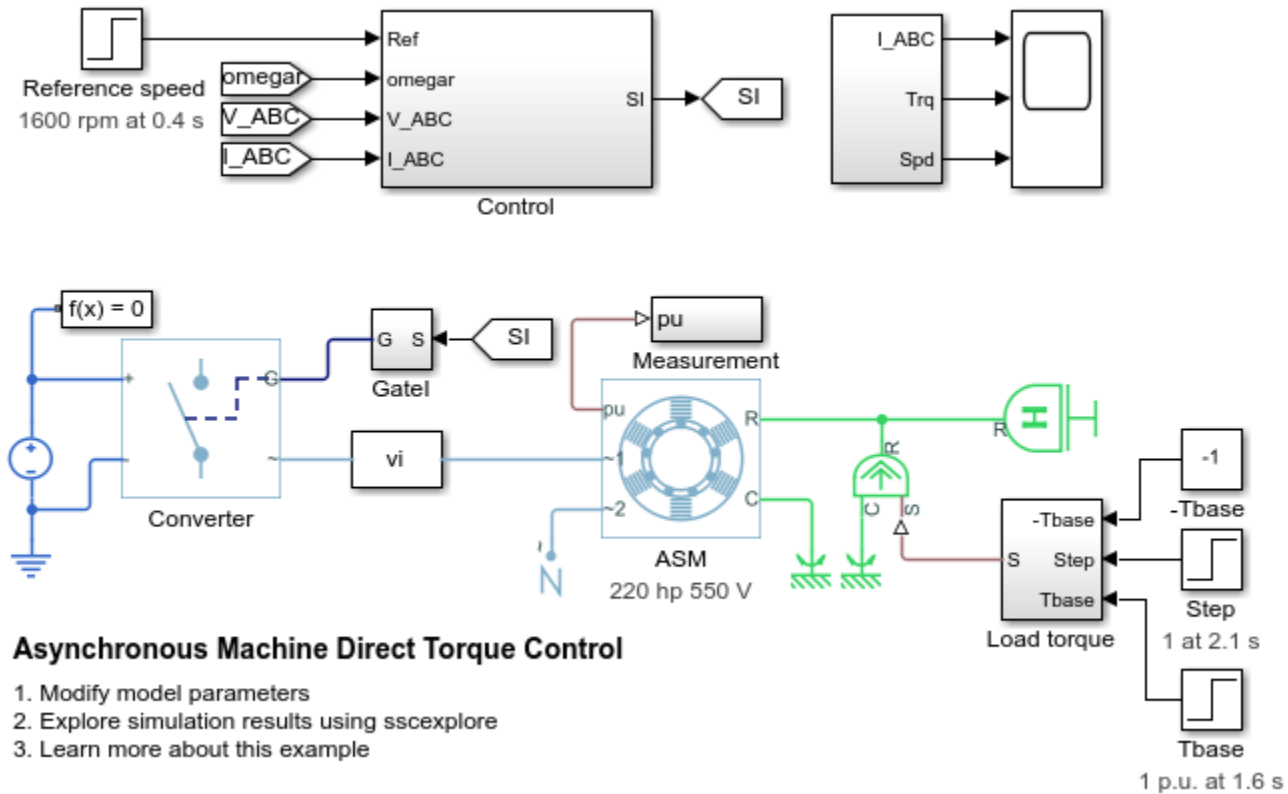
Asynchronous Machine Direct Torque Control

1. Modify model parameters
2. Explore simulation results using sscexplore
3. Learn more about this example

Asynchronous Machine Direct Torque Control with Space Vector Modulator

This example shows how to control an asynchronous machine (ASM) using the direct-torque control method with space vector modulator. A PI-based speed controller supplies the torque reference. The direct-torque controller generates the reference voltages required by the space vector modulator. A DC voltage source feeds the ASM through a controlled average-value voltage source converter.

Model
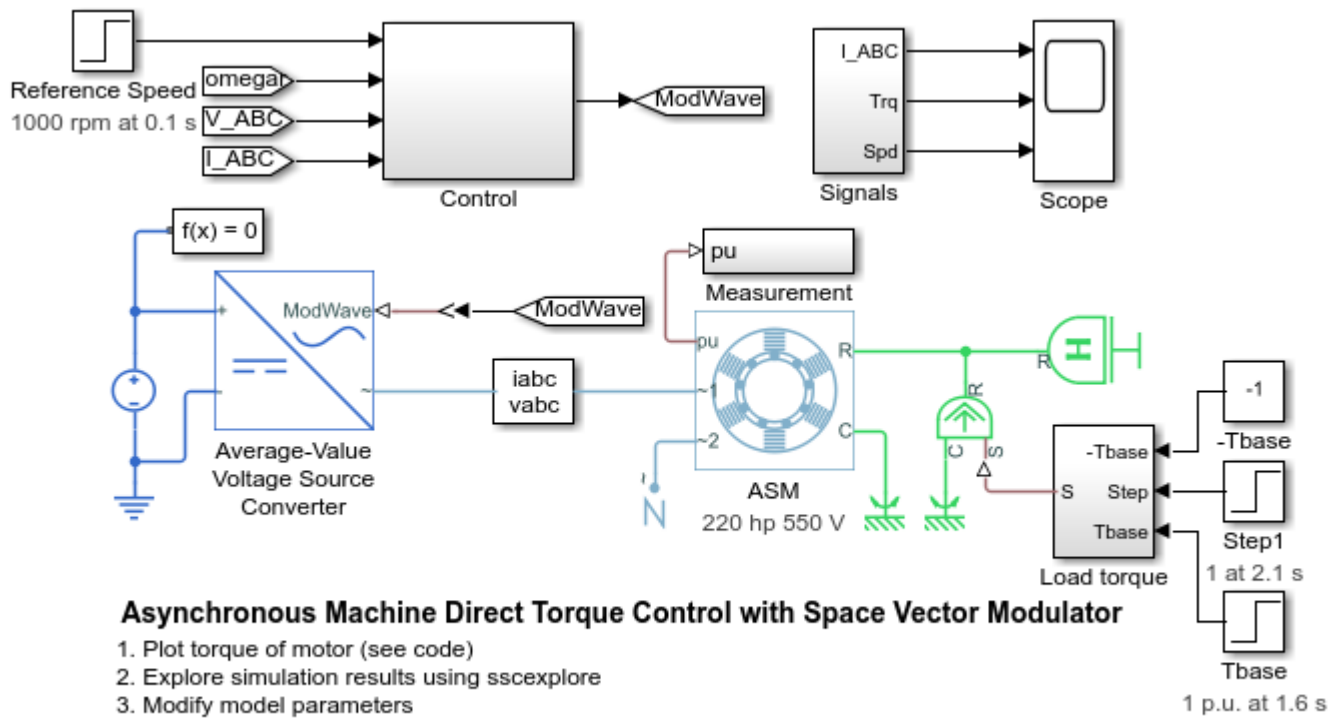
Asynchronous Machine Direct Torque Control with Space Vector Modulator
1. Plot torque of motor (see code)
2. Explore simulation results using sscexplore
3. Modify model parameters
4. Learn more about this example

Simulation Results from Simscape Logging

The plot below shows the requested and measured speed for the test, as well as the phase currents in the electric drive.
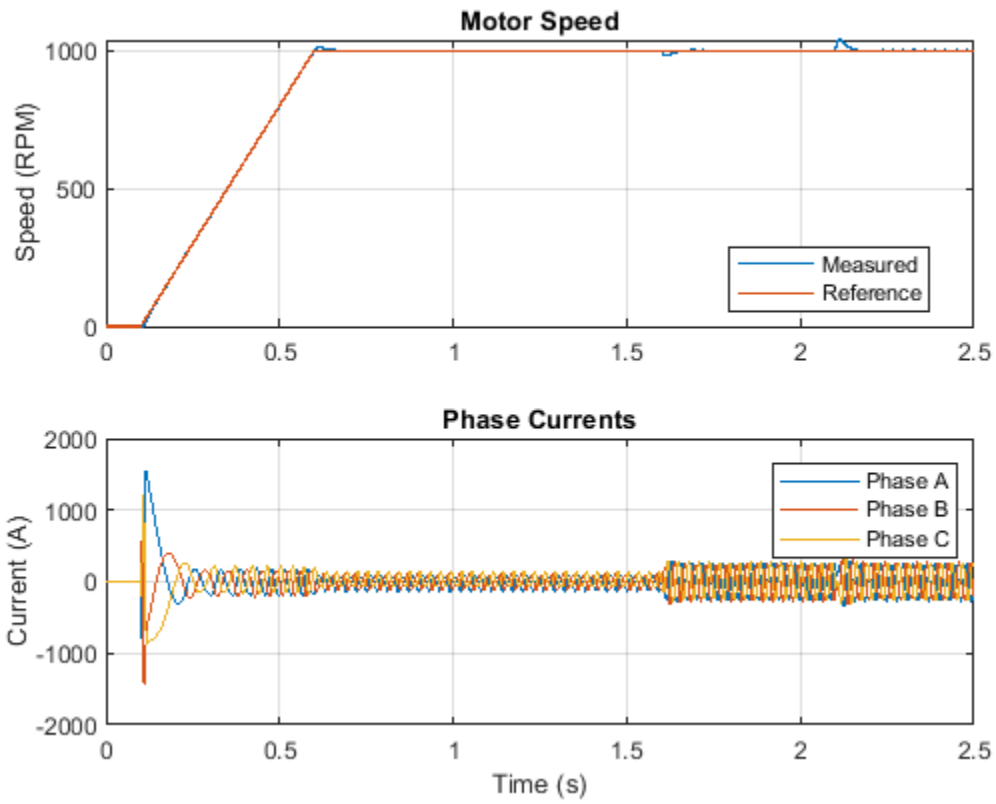
Three-Phase Asynchronous Wind Turbine Generator

This example shows an induction machine used as a wind turbine generator. The Simple Turbine block converts wind speed to turbine output power by a simple output power versus wind speed characteristic.

When the wind speed is below the cut-in speed or above the cut-out speed, the machine generates zero real power. The machine always consumes reactive power. The Reactive Compensation block offsets the machine's reactive power requirement.

The local load consumes 75kW. The infeed from the power grid meets any wind turbine generation shortfall. When the generator produces more than 75kW, excess power is exported to the grid.

The reactive power compensator is dimensioned to supply 90 kvar when a 440 V phase-to-phase voltage is applied across its terminals.

Model

Three-Phase Asynchronous Wind Turbine Generator

1. Plot wind speed and output power of Simple turbine (see code)
2. Explore simulation results using sscexplore
3. Learn more about this example
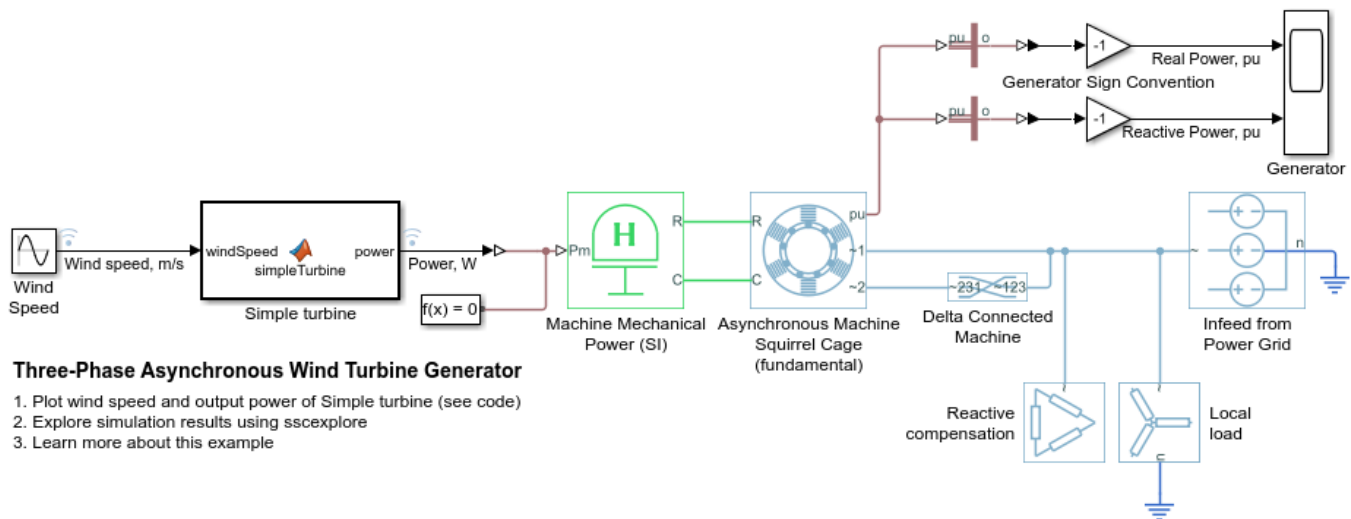
Simulation Results from Simscape Logging

The plot below shows the input wind speed and output power of the Simple turbine block.
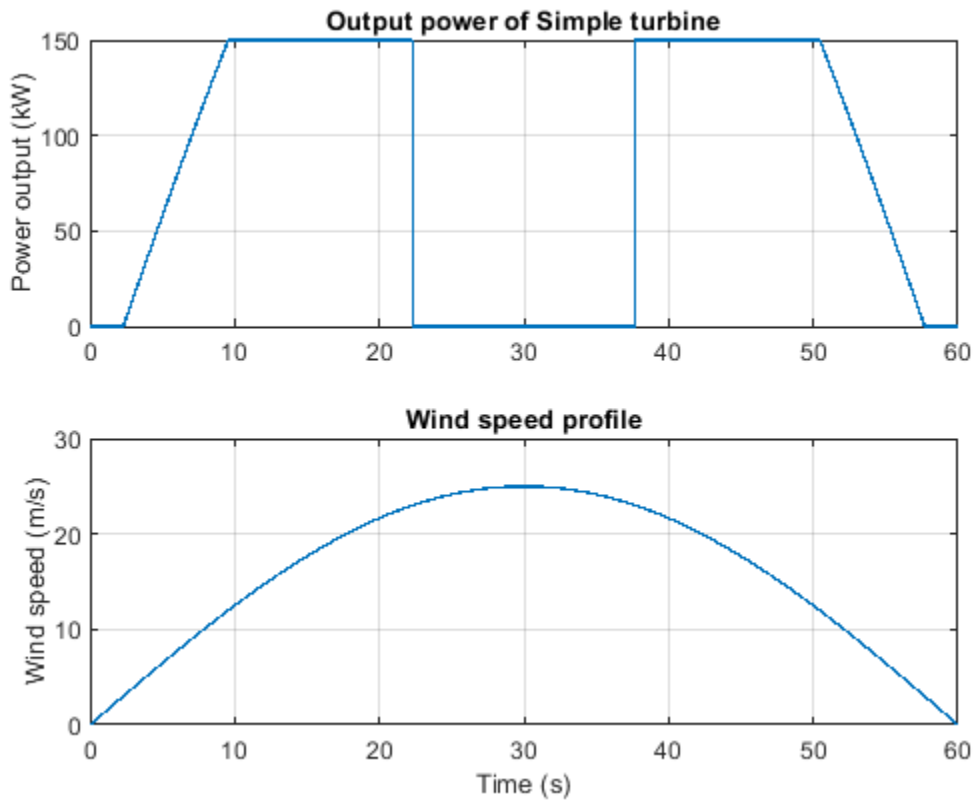
Asynchronous Machine Scalar Control

This example shows how to control the rotor speed in an asynchronous machine (ASM) drive using the scalar V/f control method. The converter transforms a reference speed to a reference electrical frequency. The controller generates reference voltages from the reference frequency by maintaining a constant voltage-to-frequency ratio through scalar V/f control.

Model

Asynchronous Machine Scalar Control

1. Modify model parameters
2. Explore simulation results using sscexplore
3. Learn more about this example
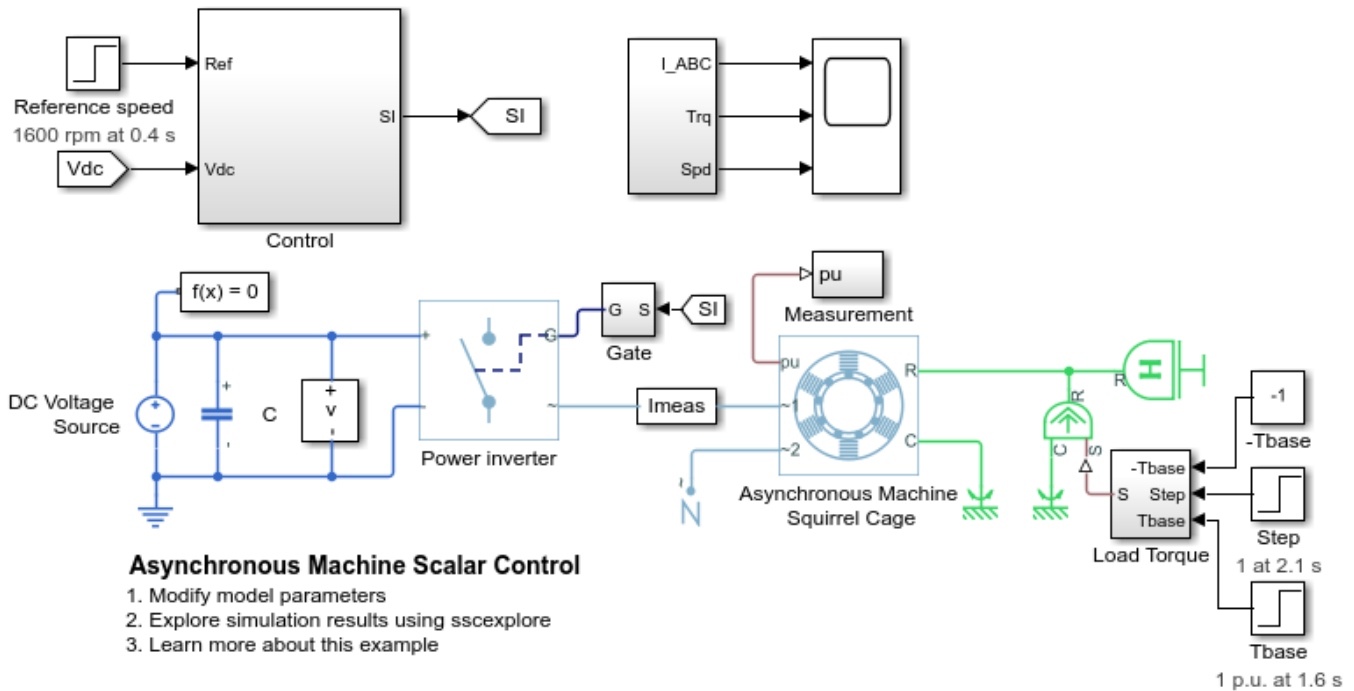
Three-Phase Asynchronous Machine Starting

This example shows how to model a wye-delta starting circuit for an induction machine. When the supply is connected to the machine via switch S1, switch S2 is initially off resulting in the machine being connected in a wye configuration. Once the machine is close to synchronous speed, switch S2 is operated thereby reconnecting the machine in a delta configuration. The higher impedance seen by the supply when the motor is in wye configuration reduces the starting current, and causes less disruption to other connected loads.

Model
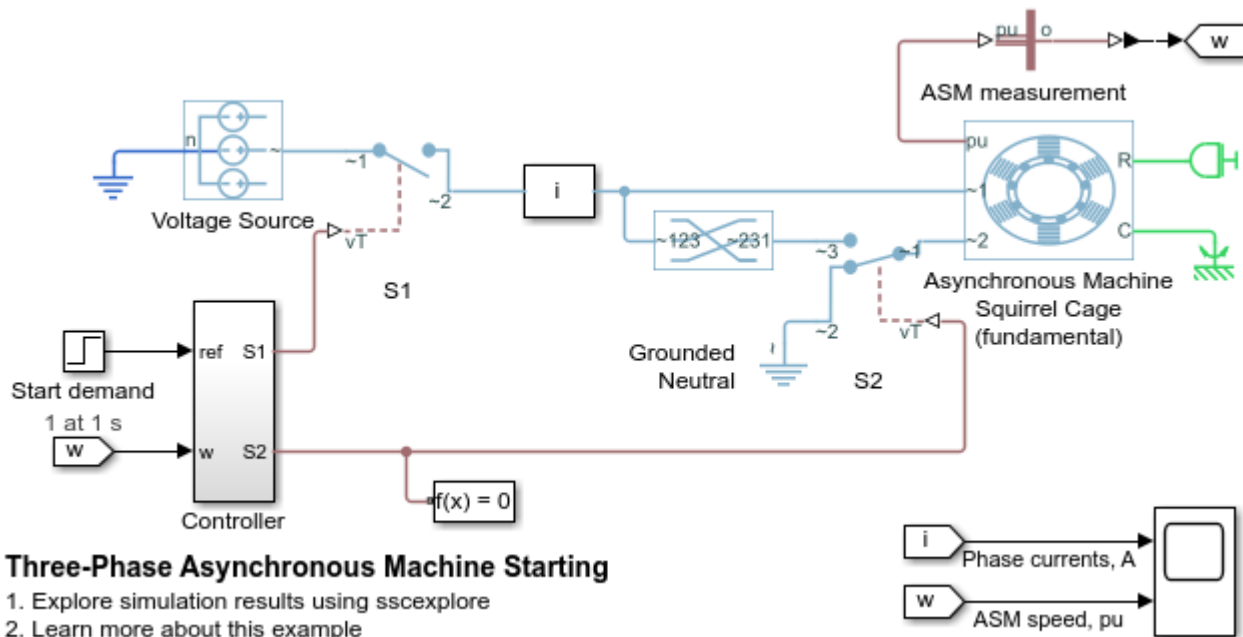
Three-Phase Asynchronous Machine Starting
1. Explore simulation results using sscexplore
2. Learn more about this example

Torque Control in Three-Level Converter-Fed Asynchronous Machine Drive

This example shows how to control the torque in an asynchronous machine (ASM) based electrical-traction drive. A high-voltage battery feeds the ASM through a three-phase three-level neutral-point clamped controlled converter. The ASM operates in both motoring and generating modes. An ideal angular velocity source provides the load. The Control subsystem uses the field-oriented control strategy to control the flux and torque. The current control is PI-based. A proportional controller regulates the neutral point voltage. The simulation uses several torque steps in both motor and generator modes. The Scopes subsystem contains scopes that allow you to see the simulation results.
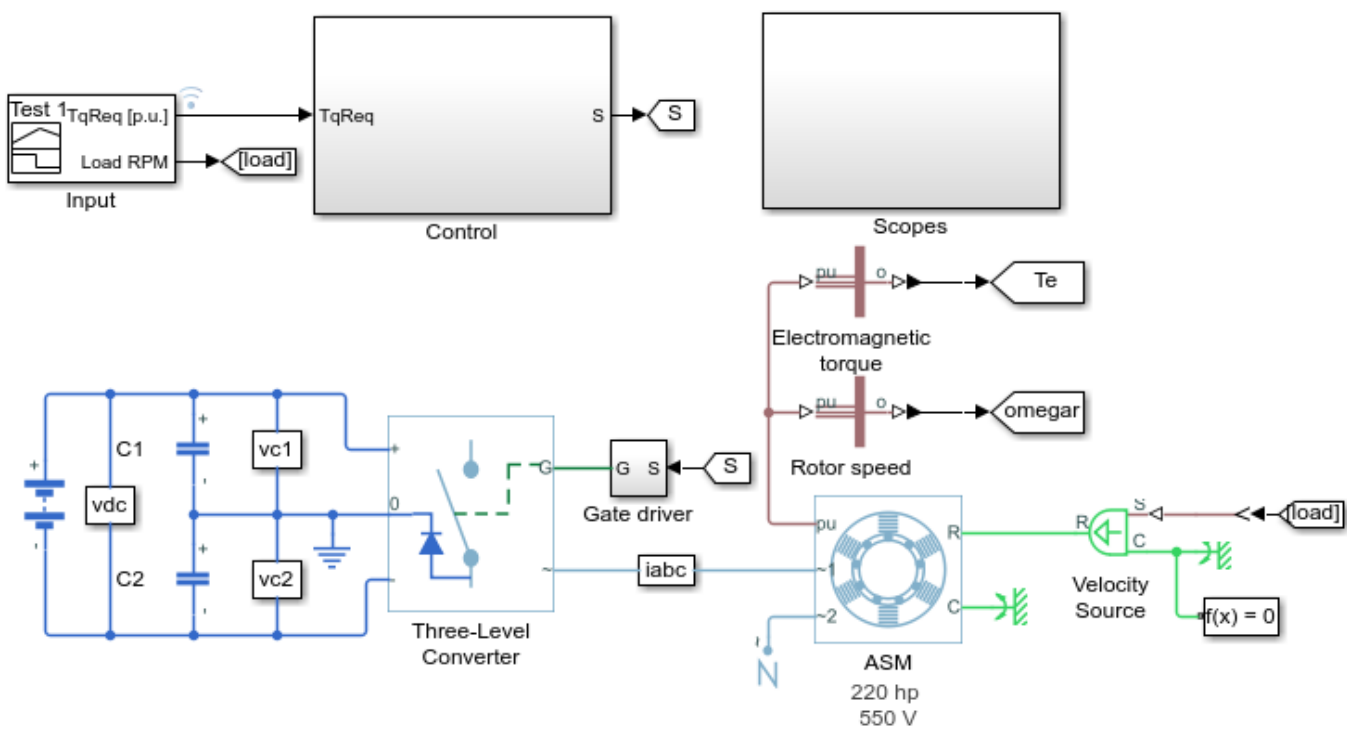
Model

Torque Control in Three-Level Converter-Fed Asynchronous Machine Drive

1. Plot torque of motor (see code)
2. Explore simulation results using sscexplore
3. Modify model parameters
4. Learn more about this example

Simulation Results from Simscape Logging

The plot below shows the requested and measured torque for the test, as well as the phase currents in the electric drive.
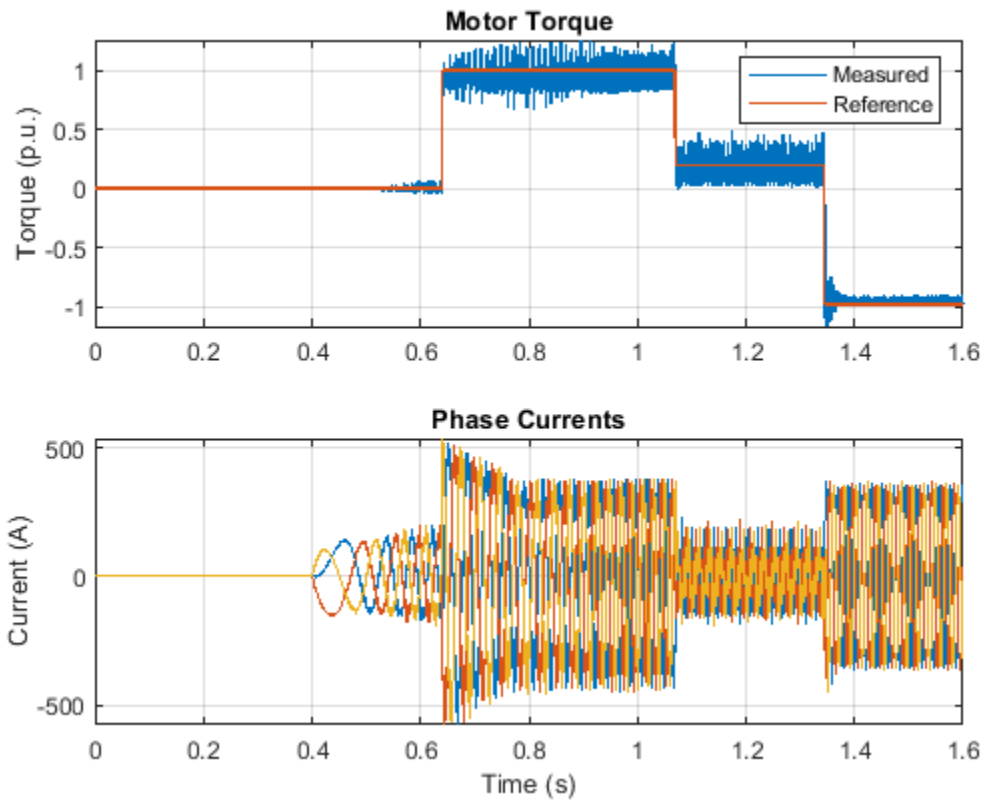
Average-Value Chopper Control

This example shows how to control a four-quadrant chopper. The Control subsystem implements a simple PI-based control algorithm for controlling the output current. An average-value Chopper model is used to speed up the simulation. The simulation uses both positive and negative references. The total simulation time (t) is 1 s. At t = 0.5 s, the polarity of the load DC source E changes.

Model
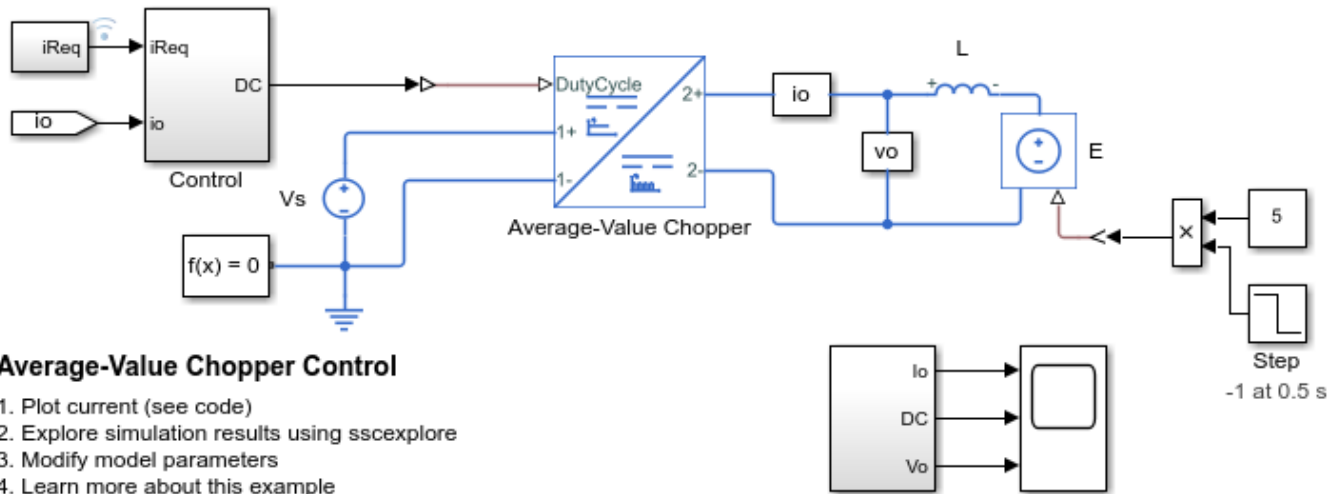
Average-Value Chopper Control

1. Plot current (see code)
2. Explore simulation results using sscexplore
3. Modify model parameters
4. Learn more about this example

Simulation Results from Simscape Logging

The plot below shows the requested and measured current for the test and the output voltage in the circuit.
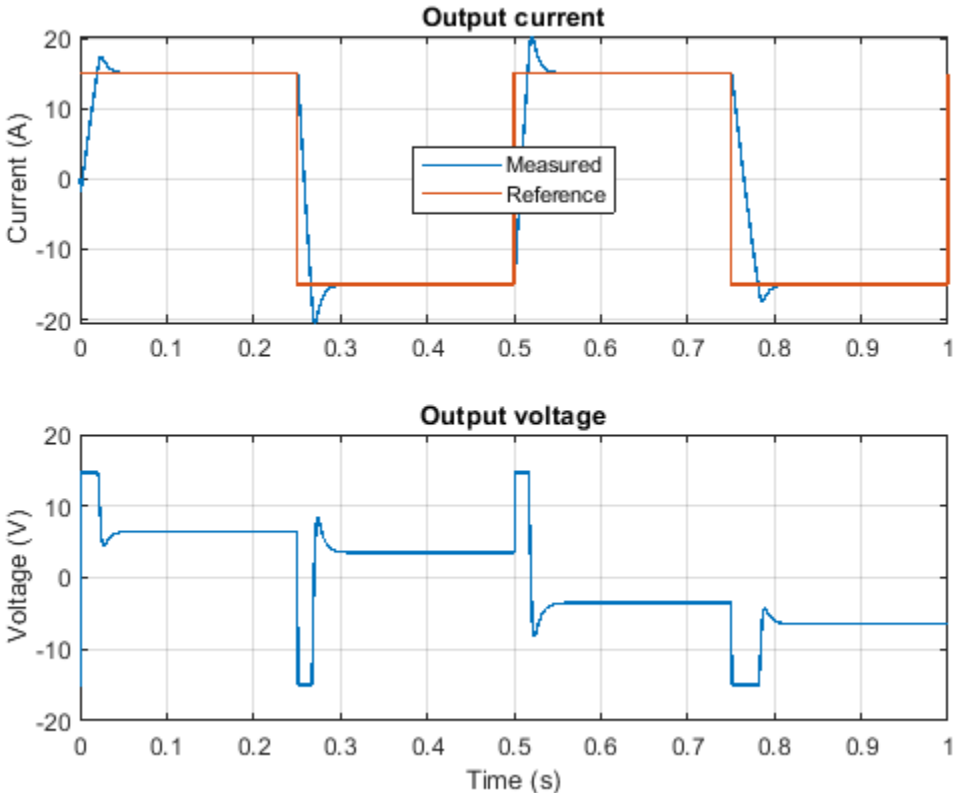
Average-Value DC-DC Converter Control

This example shows how to control the output voltage of a buck-boost converter. To adjust the duty cycle, the Control subsystem uses a PI-based control algorithm. An average-value DC-DC converter model is used to speed up the simulation. The input voltage and the system load are held constant throughout the simulation. The total simulation time (t) is 0.25 s. At t = 0.15 s, the voltage reference changes and the system switches from buck mode to boost mode.
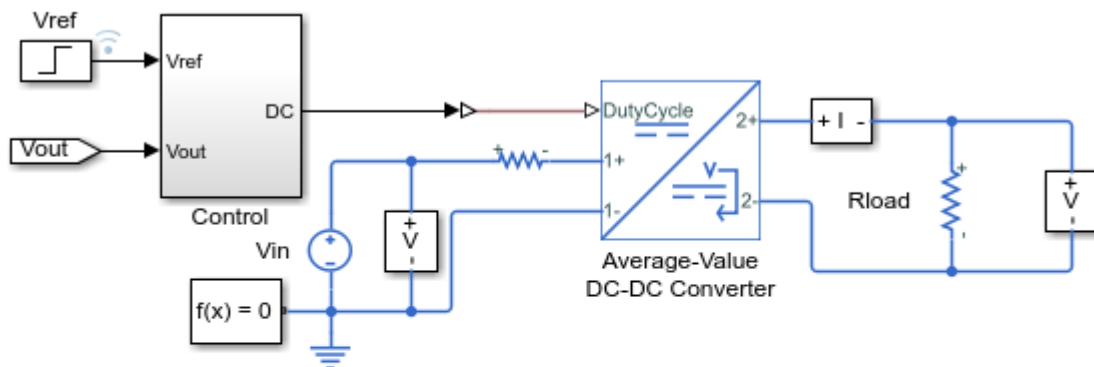
Model

Average-Value DC-DC Converter Control

1. Plot voltage (see code)
2. Explore simulation results using sscexplore
3. Modify model parameters
4. Learn more about this example
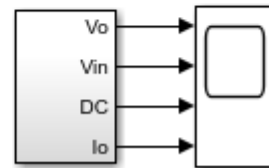
Simulation Results from Simscape Logging

The plot below shows the requested and measured voltage for the test and the input voltage in the circuit.
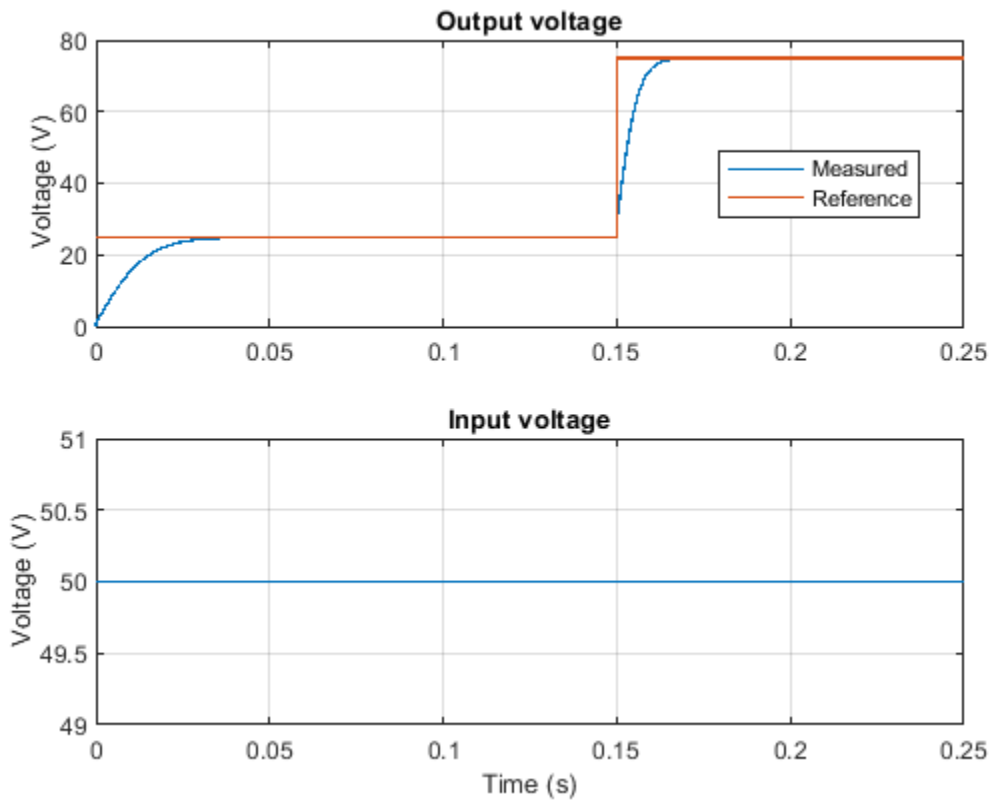
BLDC Hysteresis Current Control

This example shows how to control the currents in a BLDC based electrical drive using hysteresis controllers. A DC voltage source feeds the BLDC through a controlled three-phase inverter. A ramp of current request is provided to the motor controller. The load torque is quadratically dependent on the rotor speed. The Control subsystem implements the hysteresis-based current control strategy. The Scopes subsystem contains scopes that allow you to see the simulation results.
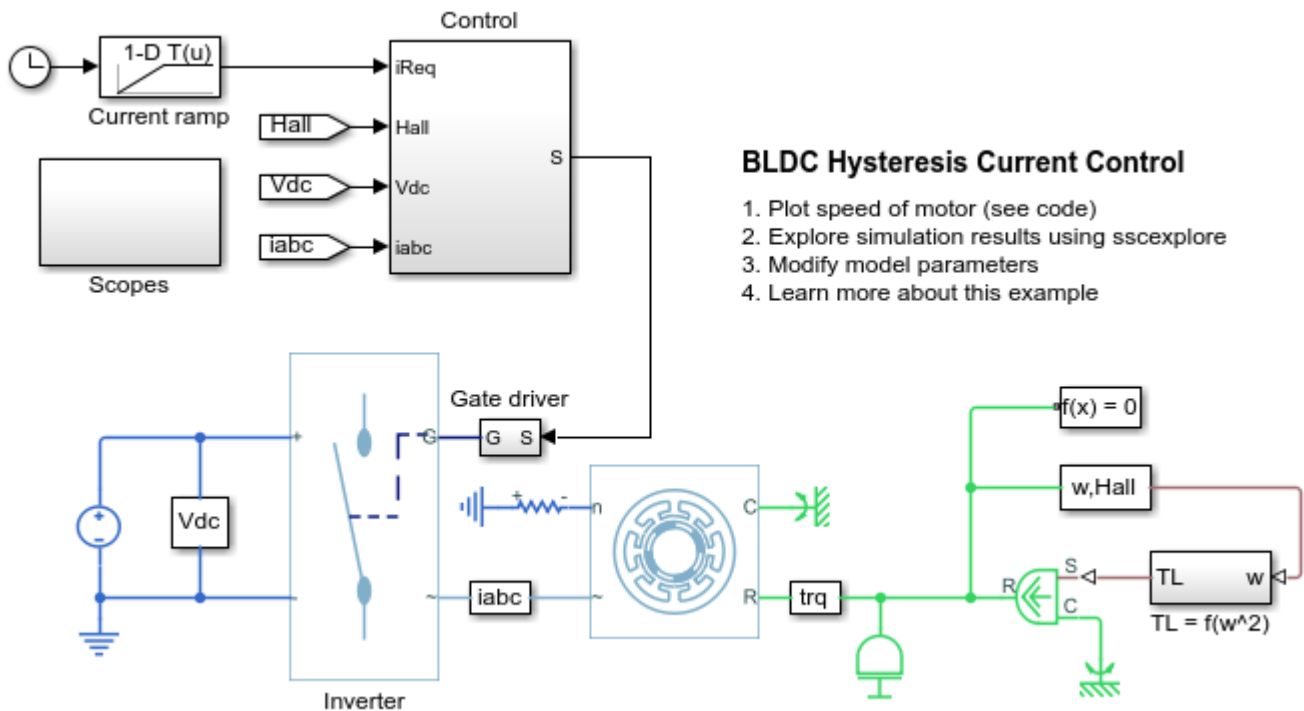
Model

BLDC Hysteresis Current Control

1. Plot speed of motor (see code)
2. Explore simulation results using sscexplore
3. Modify model parameters
4. Learn more about this example

Simulation Results from Simscape Logging

The plot below shows the requested and measured current on phase A for the test and the rotor speed in the electric drive.
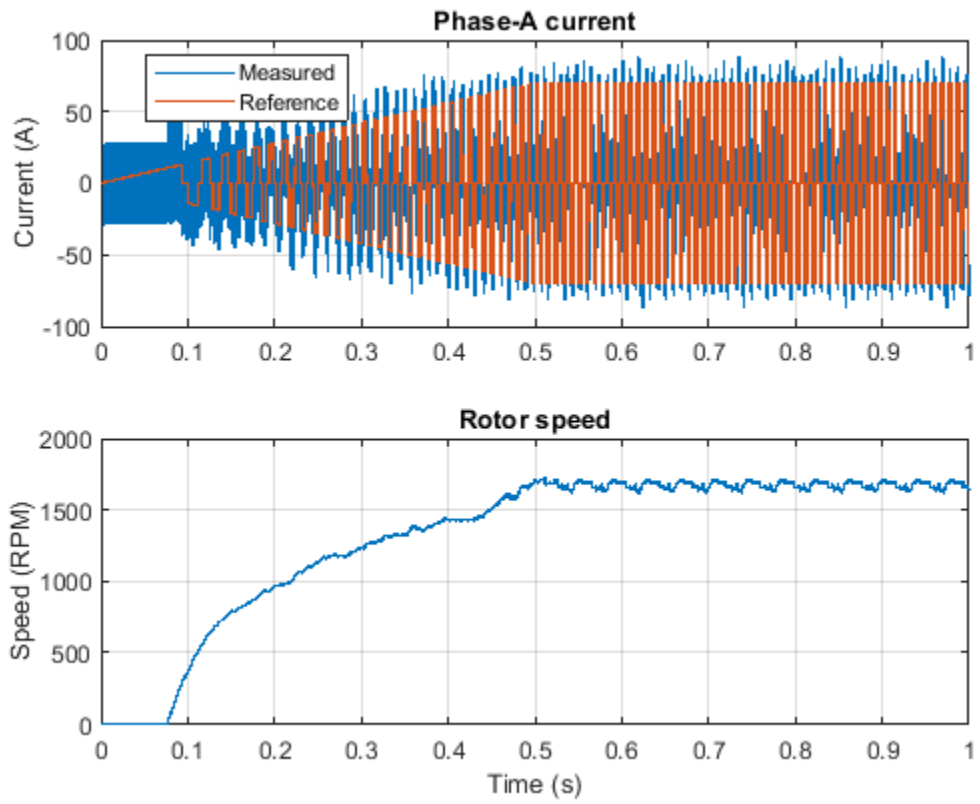
BLDC Position Control

This example shows how to control the rotor angle in a BLDC based electrical drive. An ideal torque source provides the load. The Control subsystem uses a PI-based cascade control structure with three control loops, an outer position control loop, a speed control loop and an inner current control loop. The BLDC is fed by a controlled three-phase inverter. The gate signals for the inverter are obtained from hall signals. The simulation uses step references. The Scopes subsystem contains scopes that allow you to see the simulation results.
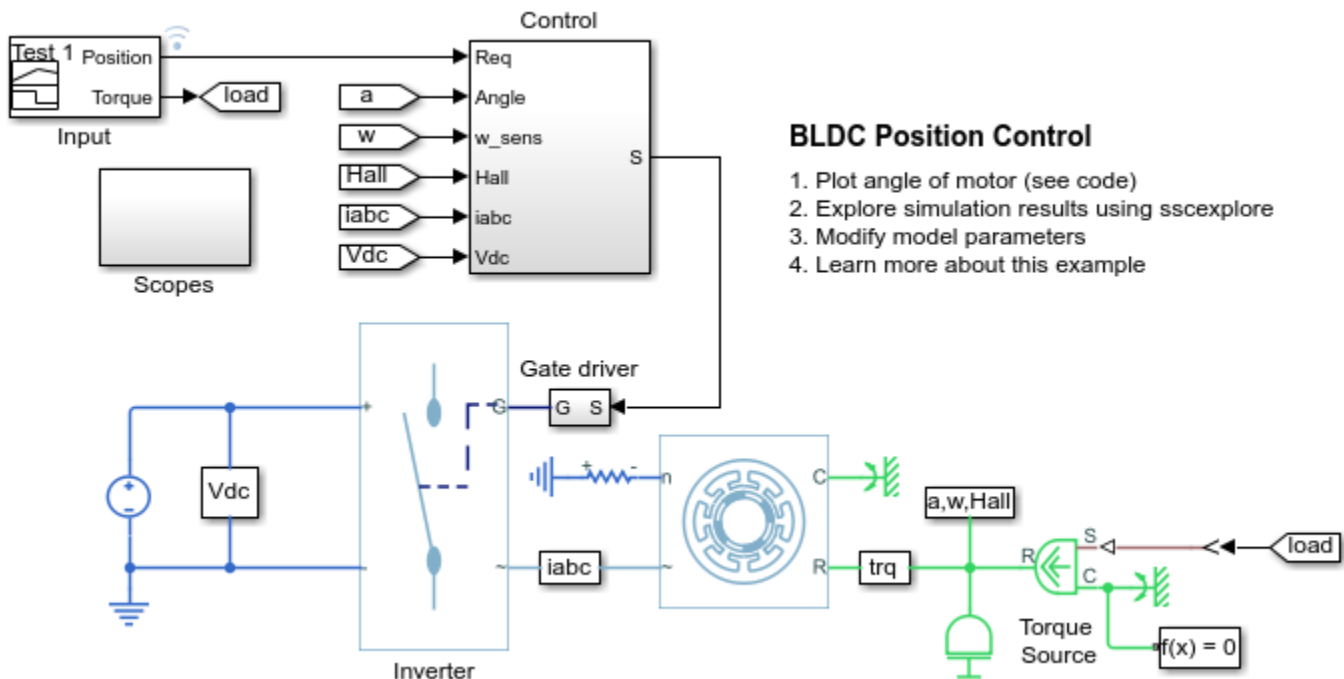
Model

BLDC Position Control

1. Plot angle of motor (see code)
2. Explore simulation results using sscexplore
3. Modify model parameters
4. Learn more about this example

Simulation Results from Simscape Logging

The plot below shows the requested and measured angle for the test and the rotor speed in the electric drive.
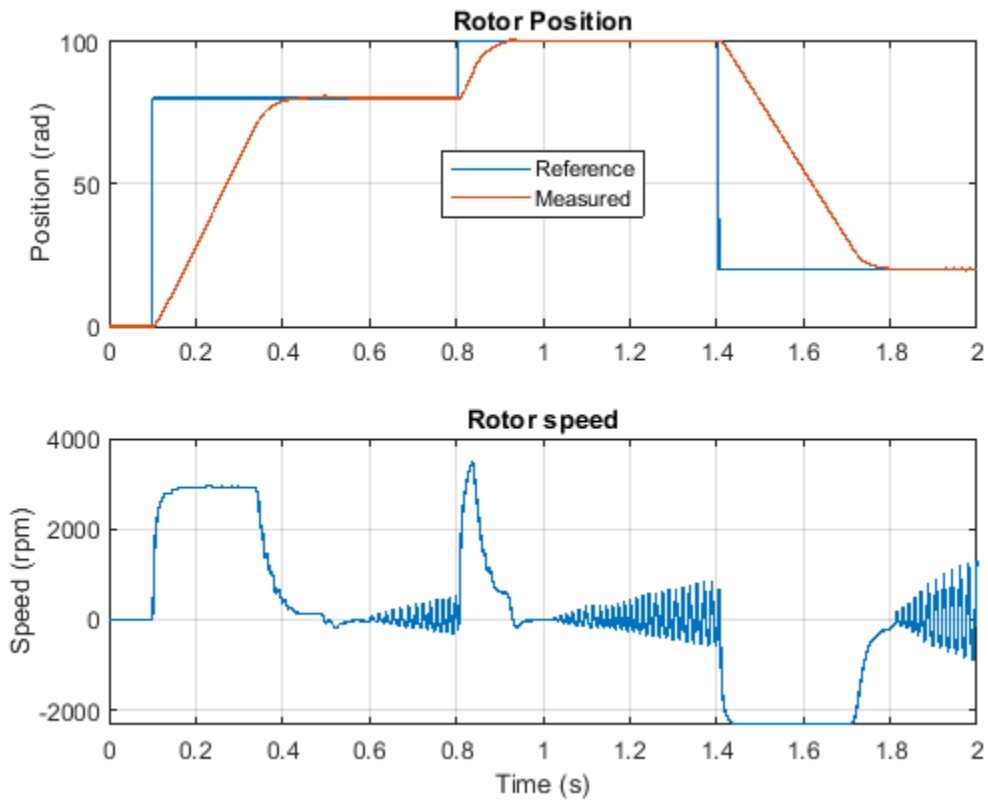
BLDC Speed Control

This example shows how to control the rotor speed in a BLDC based electrical drive. An ideal torque source provides the load. The Control subsystem uses a PI-based cascade control structure with an outer speed control loop and an inner dc-link voltage control loop. The dc-link voltage is adjusted through a DC-DC buck converter. The BLDC is fed by a controlled three-phase inverter. The gate signals for the inverter are obtained from hall signals. The simulation uses speed steps. The Scopes subsystem contains scopes that allow you to see the simulation results.
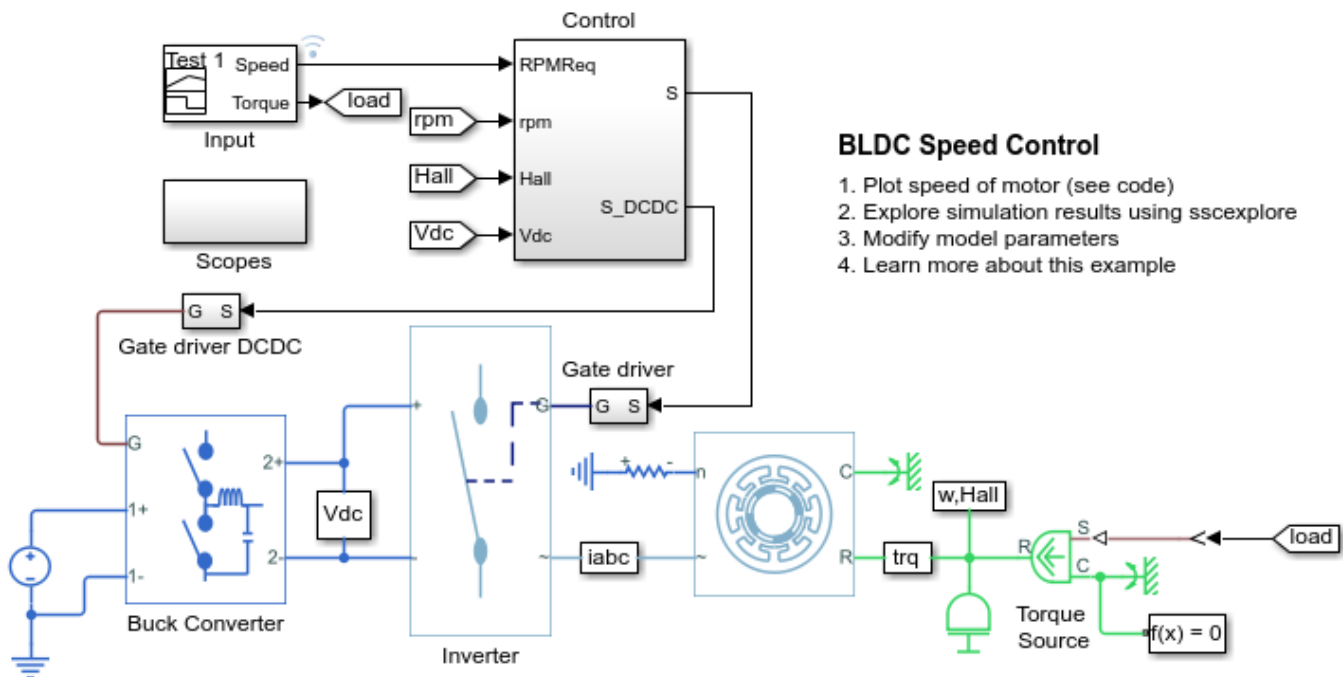
Model

BLDC Speed Control

1. Plot speed of motor (see code)
2. Explore simulation results using sscexplore
3. Modify model parameters
4. Learn more about this example

Simulation Results from Simscape Logging

The plot below shows the requested and measured speed for the test and the phase currents in the electric drive.
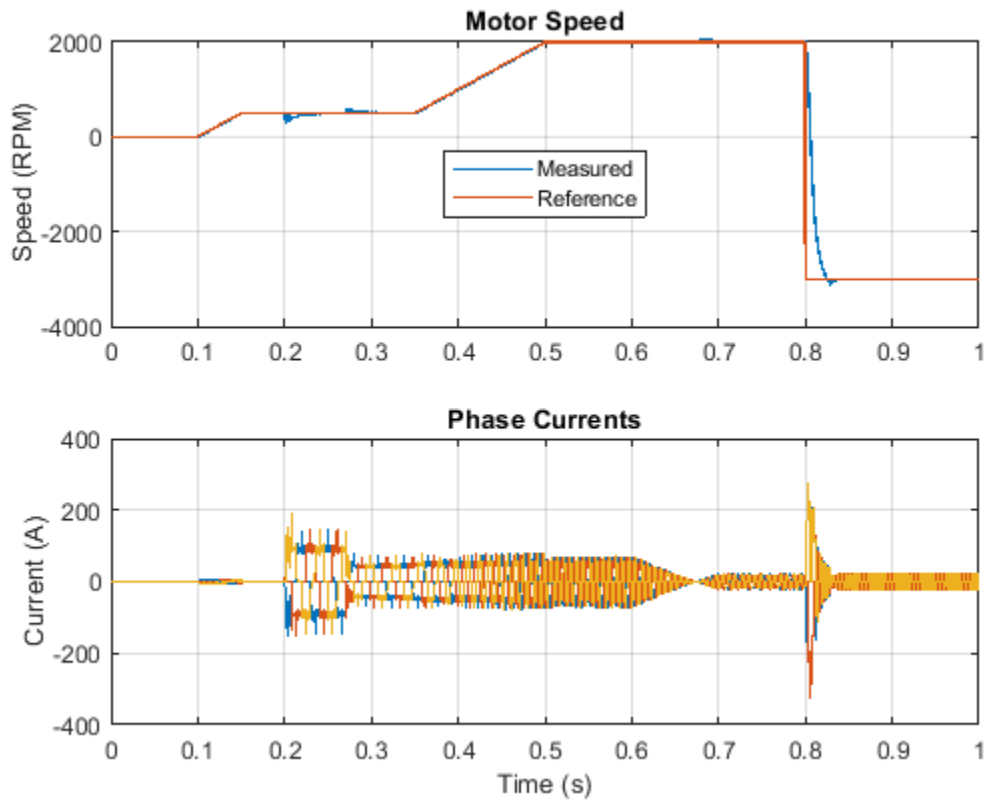
Boost Converter Voltage Control

This example shows how to control the output voltage of a boost converter. To adjust the duty cycle, the Control subsystem uses a PI-based control algorithm. The input voltage is considered constant throughout the simulation. A variable resistor provides the load for the system. The total simulation time (t) is 0.25 seconds. At t = 0.15 seconds, the load changes.
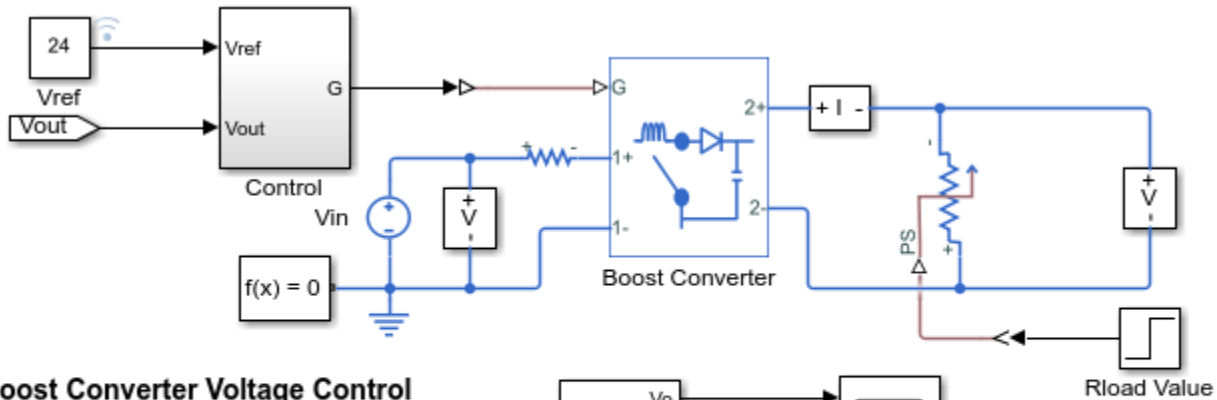
Model

Boost Converter Voltage Control

1. Plot voltage (see code)
2. Explore simulation results using sscexplore
3. Modify model parameters
4. Learn more about this example
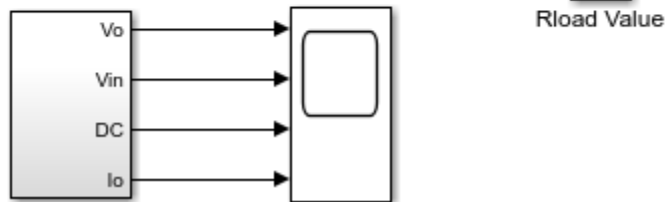
Simulation Results from Simscape Logging

The plot below shows the requested and measured voltage for the test and the input voltage in the circuit.
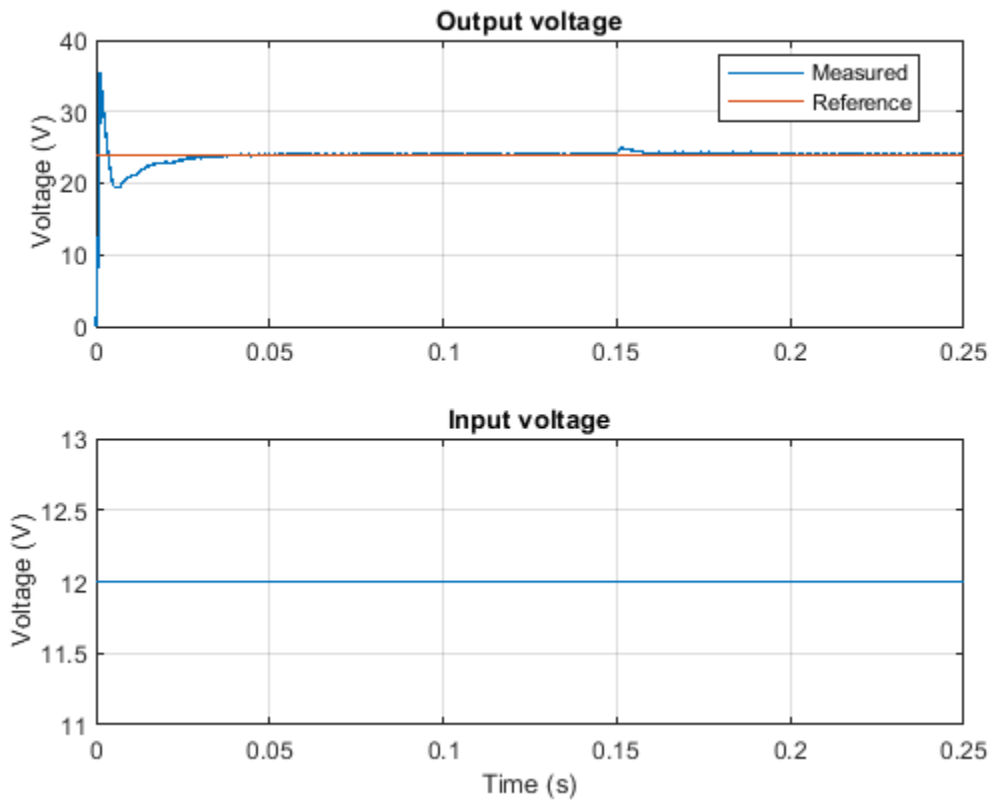
Buck Converter Voltage Control

This example shows how to control the output voltage of a buck converter. To adjust the duty cycle, the Control subsystem uses a PI-based control algorithm. The input voltage is considered constant throughout the simulation. A variable resistor provides the load for the system. The total simulation time (t) is 0.25 seconds. At t = 0.15 seconds, the load changes.
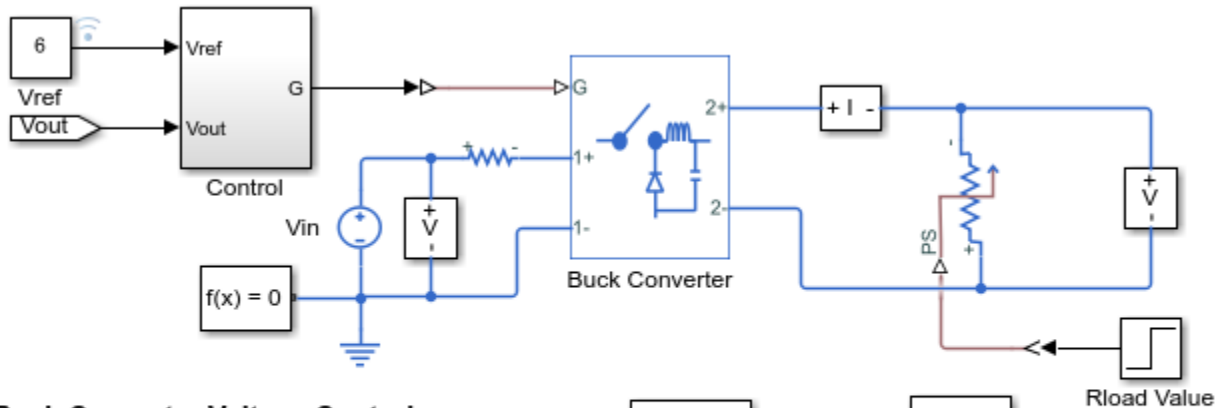
Model

Buck Converter Voltage Control

1. Plot voltage (see code)
2. Explore simulation results using sscexplore
3. Modify model parameters
4. Learn more about this example
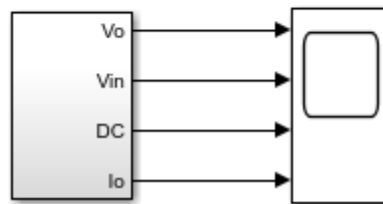
Simulation Results from Simscape Logging

The plot below shows the requested and measured voltage for the test and the input voltage in the circuit.
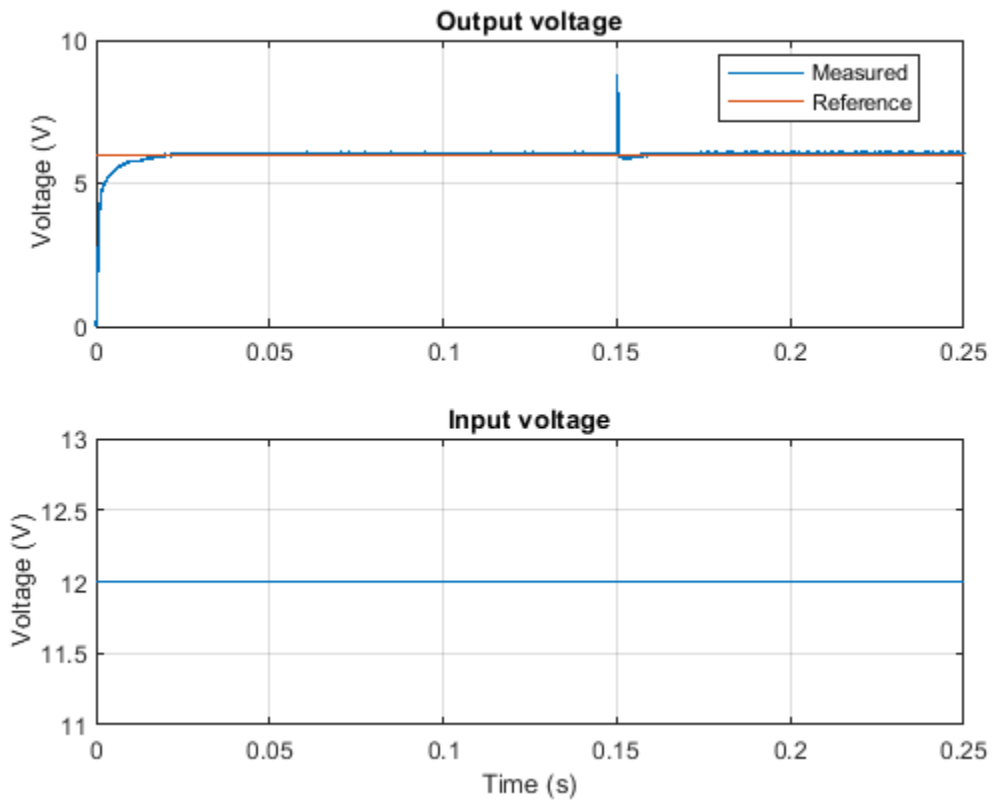
Buck Converter Polynomial Voltage Control

This example shows how to control the output voltage of a buck converter using a polynomial RST controller. The RST controller adjusts the duty cycle. The input voltage is considered constant throughout the simulation. A variable resistor provides the load for the system. The total simulation time (t) is 0.25 seconds. At t = 0.15 seconds, the load is changed. At t = 0.2 seconds, the voltage reference is changed from 6V to 4V.

Model
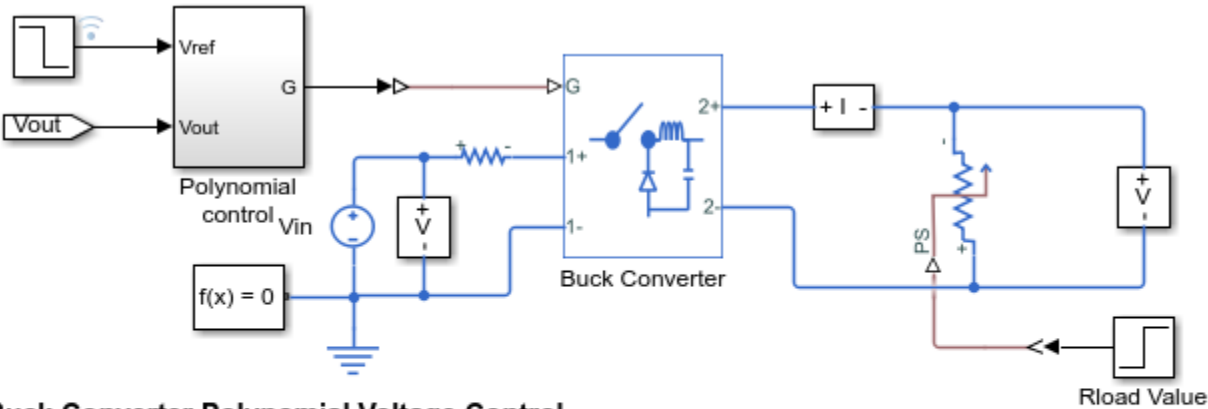
Buck Converter Polynomial Voltage Control

1. Plot voltage (see code)
2. Explore simulation results using sscexplore
3. Modify model parameters
4. Learn more about this example
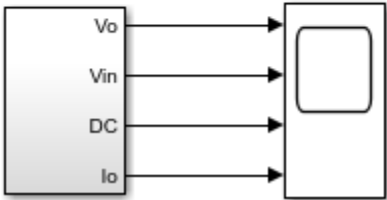
Simulation Results from Simscape Logging

The plot below shows the requested and measured voltage for the test and the input voltage in the circuit.
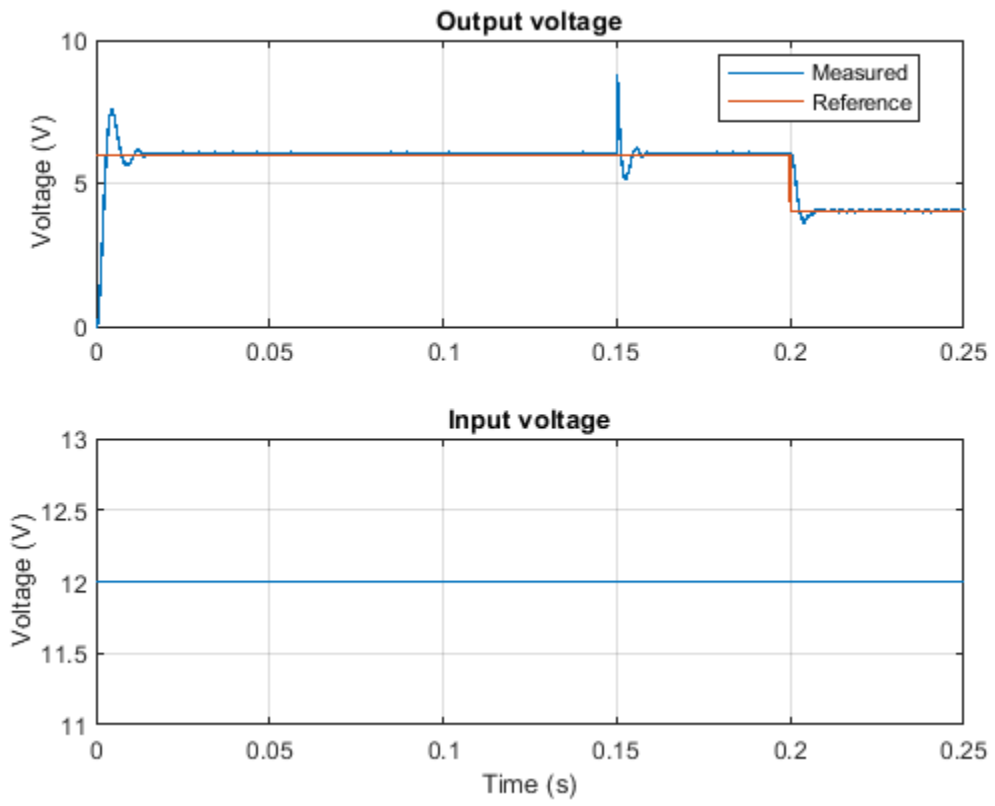
AC Cable with Bonded Sheaths

This example shows a three-phase cable model comprised of multiple pi-sections. Each phase is enclosed in a conductive sheath. The conductive sheath is connected to ground at either end of the cable through a simple resistance. A high-voltage source provides power to an unbalanced resistive load through the power cable. You can configure the sheath to be either series-bonded or cross-bonded. You can also configure the number of pi-sections. Increasing the number of pi-sections improves the accuracy but slows down the simulation. To facilitate convergence, the voltage source includes an internal impedance.

Model
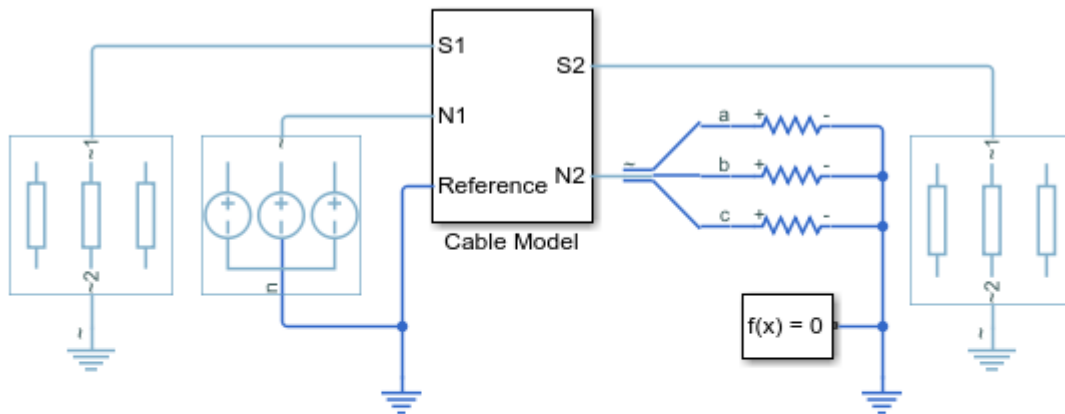
AC Cable with Bonded Sheaths

1. Plot sheath voltage per pi-section of cable model (see code)
2. Adjust sheath bonding: Series-bonding, Cross-bonding (open mask, see code)
3. Look under mask to see pi-section connections
4. Compare sheath bonding methods: simulate (see code), plot results (see code)
5. Explore simulation results using sscexplore
6. Learn more about this example

Cable Model: Parameters

AC Cable (Multiple Pi-sections) (mask)

This block represents an AC cable model using multiple pi-sections. You can configure the sheath to be either series-bonded or cross-bonded. You can also configure the number of pi-sections. Increasing the number of pi-sections improves the accuracy but slows down the simulation.

Parameters

Sheath bonding	Cross-bonded
Number of sections	22
Total length (km)	120
Line radius (mm)	15
Sheath radius (mm)	22.55
Pitch (mm)	500
Line resistance (Ohm/km)	5e-2
Sheath resistance (Ohm/km)	2e-1
Insulation relative permittivity	2.4
Environment relative permittivity	6
Frequency for return path calculation (Hz)	60
Resistivity for return path calculation (Ohm*m)	100

OK   Cancel   Help   Apply

Cable Model: Series-bonded

The figure shows the cable model comprised of three pi-sections with series-bonded sheaths.
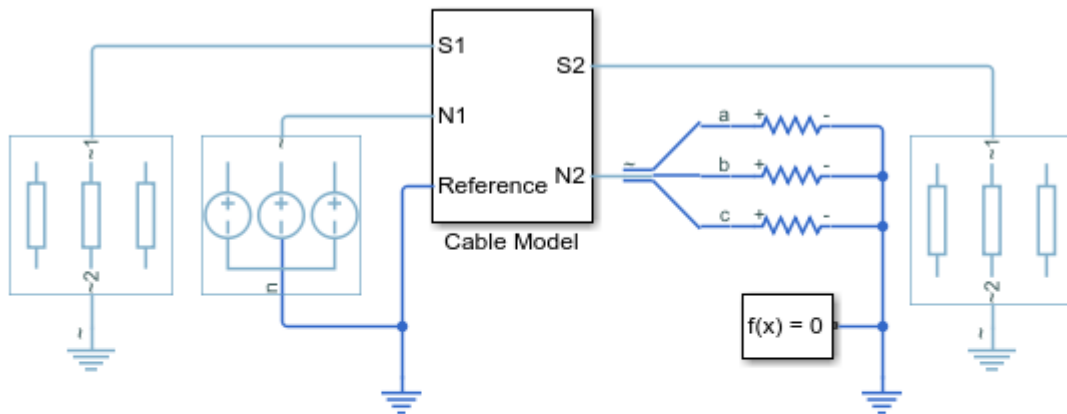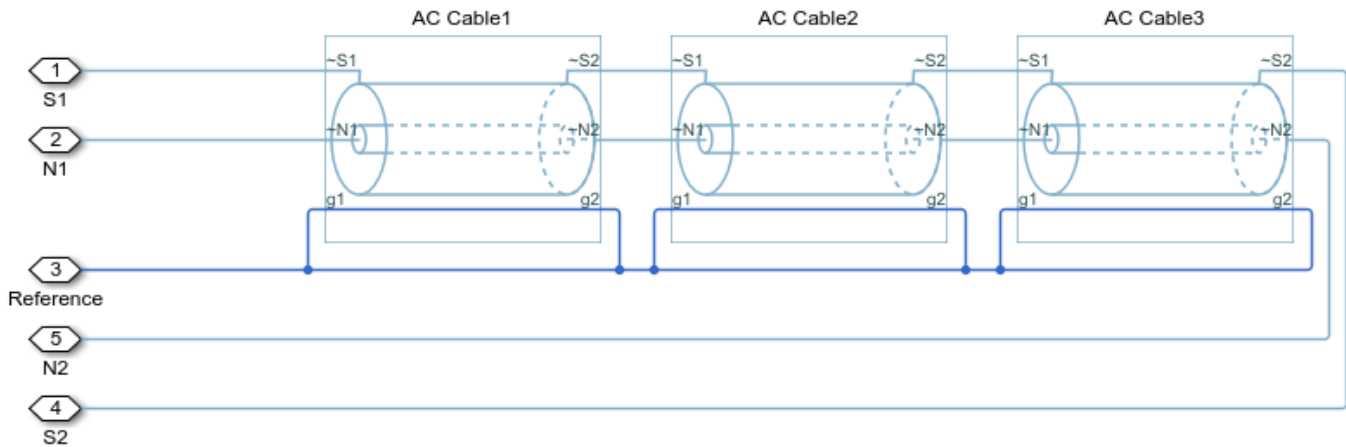
AC Cable with Bonded Sheaths

1. Plot sheath voltage per pi-section of cable model (see code)
2. Adjust sheath bonding: Series-bonding, Cross-bonding (open mask, see code)
3. Look under mask to see pi-section connections
4. Compare sheath bonding methods: simulate (see code), plot results (see code)
5. Explore simulation results using sscexplore
6. Learn more about this example

Cable Model: Cross-bonded

The figure shows the cable model comprised of three pi-sections with cross-bonded sheaths. The sheath cross-bonding is implemented using Phase Permute blocks.
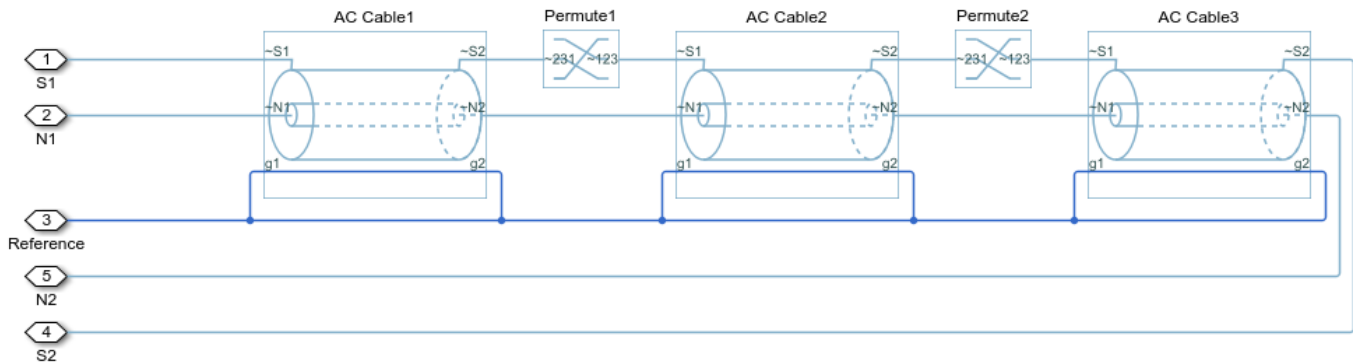
Simulation Results from Simscape Logging

The plots show the behavior of the sheath bonding methods for a cable model comprised of 22 pi-sections. The efficiency with cross-bonded sheaths is higher than the efficiency with series-bonded sheaths.
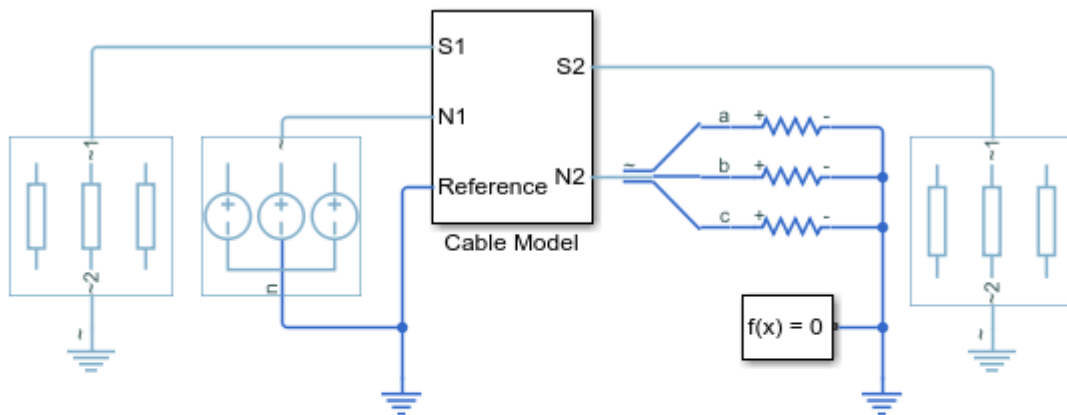
AC Cable with Bonded Sheaths

1. Plot sheath voltage per pi-section of cable model (see code)
2. Adjust sheath bonding: Series-bonding, Cross-bonding (open mask, see code)
3. Look under mask to see pi-section connections
4. Compare sheath bonding methods: simulate (see code), plot results (see code)
5. Explore simulation results using sscexplore
6. Learn more about this example
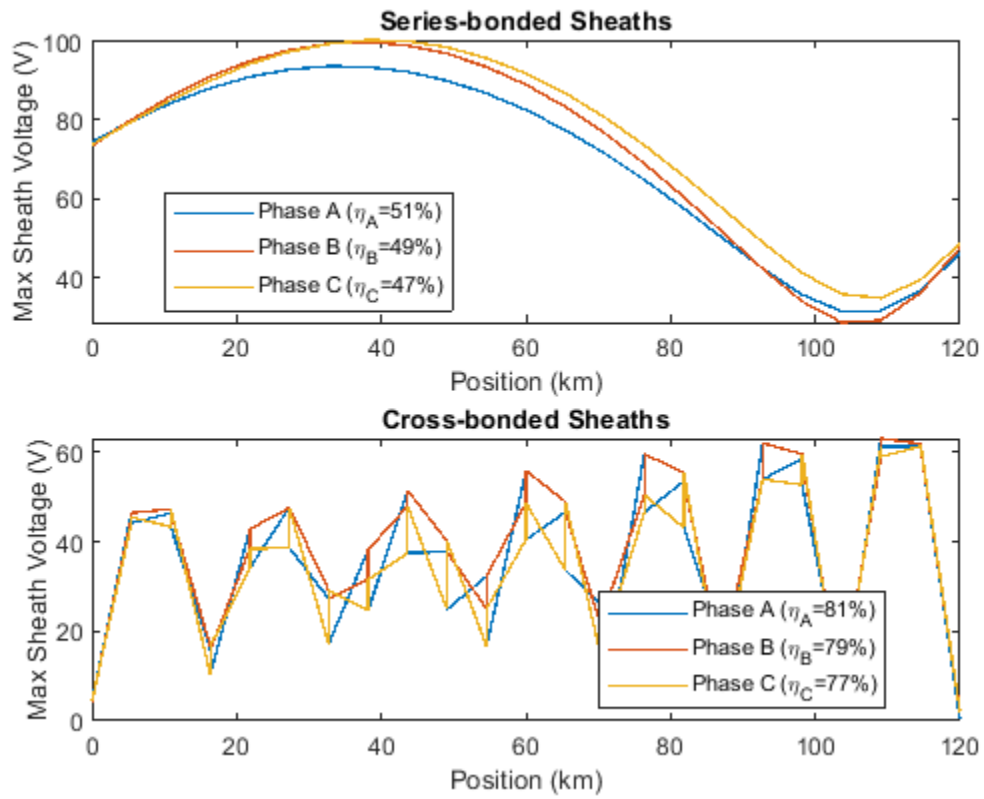
Composite Three-Phase Rectifier

This is the base model for analysis workflow examples.

Model
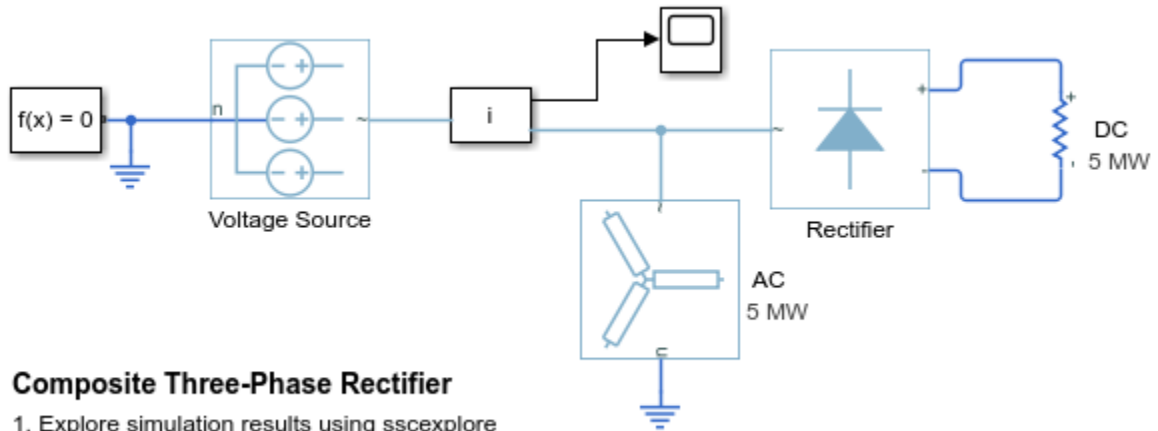
Composite Three-Phase Rectifier

1. Explore simulation results using sscexplore
2. Learn more about this example

Custom Inductor (B-H Curve)

This example shows a comparison in behavior of a linear and nonlinear inductor. Starting with fundamental parameter values, the parameters for linear and nonlinear representations are derived. These parameters are then used in a Simscape™ model and the simulation outputs compared.

Open Model

```
modelName = 'ee_custom_inductor';
open_system( modelName );
```
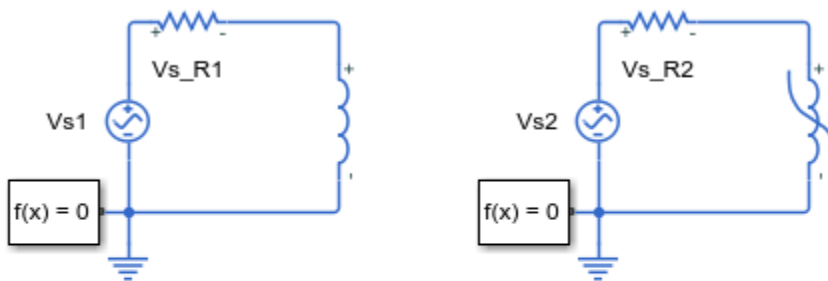

Custom Inductor (B-H Curve)

1. Plot comparison graphs (see code)
2. Modify model parameters
3. Explore simulation results using sscexplore
4. Learn more about this example

Specification of Parameters

Fundamental parameter values used as the basis for subsequent calculations:

- Permeability of free space, $\mu_0, \mathrm{H/m}$
- Relative permeability of core, μ_r
- Number of winding turns, N_w
- Effective magnetic core length, l_e, m
- Effective magnetic core cross-sectional area, $A_e, \mathrm{m^2}$
- Core saturation begins, $B_{sat_{begin}}, \mathrm{T}$
- Core fully saturated, B_{sat}, T

```
mu_0 = pi*4e-7;
mu_r = 3000;
Nw = 100;
le = 0.02;
Ae = 1e-05;
B_sat_begin = 0.75;
B_sat = 1.5;
```

Calculate Magnetic Flux Density and Magnetic Field Strength Data

Where:

- Magnetic flux density, B, T

- Magnetic field strength, $H, \mathrm{A/m}$

Linear representation:

- $B = \mu_0 \mu_r H$

Nonlinear representation (including coefficient, a):

- $B = B_{sat} \tanh(a.H)$

```matlab
% Use linear representation to find value of H corresponding to B_sat_begin
H_sat_begin = B_sat_begin/(mu_0*mu_r);
% Rearrange nonlinear representation to calculate coefficient, a
a = atanh( B_sat_begin/B_sat )/H_sat_begin;

% Linear representation
H_linear = [-500 500];
B_linear = mu_0*mu_r*H_linear;

% Nonlinear representation
H_nonlinear = -5*H_sat_begin:H_sat_begin:5*H_sat_begin;
B_nonlinear = B_sat*tanh(a*H_nonlinear);
```

Display Magnetic Flux Density Versus Magnetic Field Strength

The linear and nonlinear representations can be overlaid.

```matlab
if ~exist('h1_ee_custom_inductor', 'var') || ...
        ~isgraphics(h1_ee_custom_inductor, 'figure')
    h1_ee_custom_inductor = figure('Name', 'ee_custom_inductor');
end
figure(h1_ee_custom_inductor)
clf(h1_ee_custom_inductor)

plot( H_linear, B_linear, H_nonlinear, B_nonlinear );
grid( 'on' );
title( 'Magnetic flux density, B, versus Magnetic field strength, H' );
xlabel( 'Magnetic field strength, H (A/m)' );
ylabel( 'Magnetic flux density, B (T)' );
legend( 'B=\mu_0 \mu_r H', 'B-H curve', 'Location', 'NorthWest' )
```
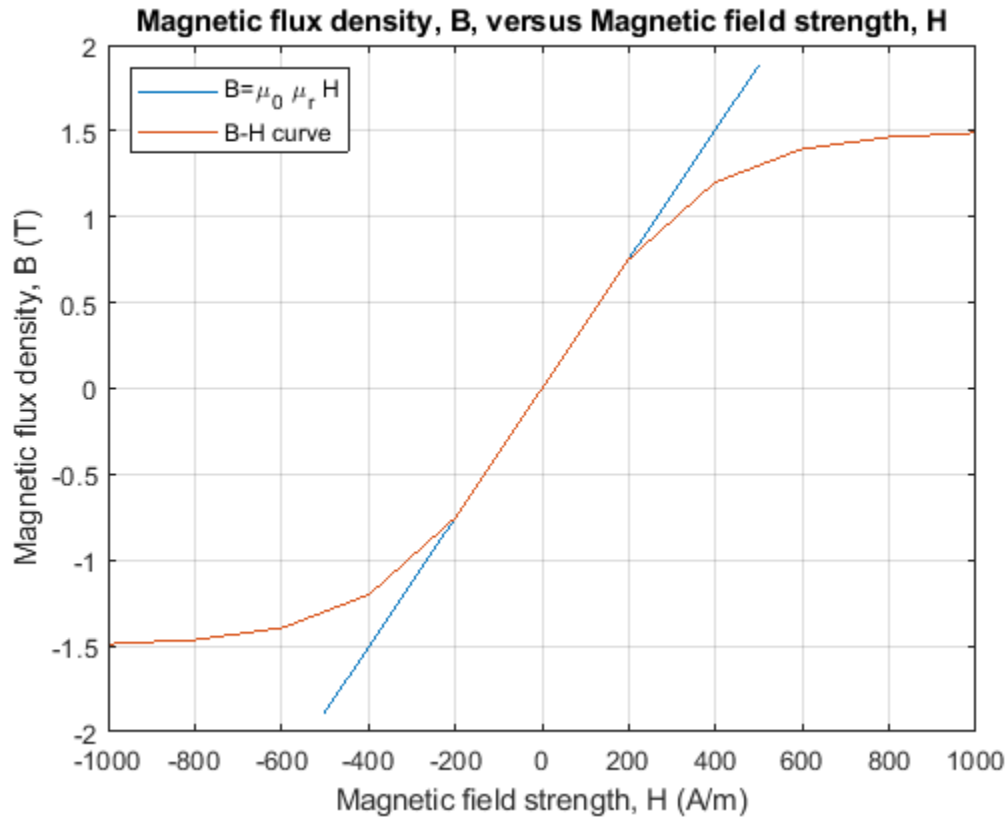
Calculate Magnetic Flux and Current Data

Where:

- Magnetic flux, ϕ, Wb
- Current, I, A

Linear representation:

- $L = \mu_0 \mu_r A_e N_w^2 / l_e$

- $\phi = IL/N_w$

Nonlinear representation:

- $I = Hl_e/N_w$

- $\phi = BA_e$

```
% Linear inductance
L_linear = mu_0*mu_r*Ae*Nw^2/le;

% Linear representation
I_linear = [-1.5 1.5];
phi_linear = I_linear.*L_linear/Nw;
```

```
% Nonlinear representation
I_nonlinear = le.*H_nonlinear./Nw;
phi_nonlinear = B_nonlinear.*Ae;
```

Display Magnetic Flux Versus Current

The linear and nonlinear representations can be overlaid.

```
if ~exist('h2_ee_custom_inductor', 'var') || ...
        ~isgraphics(h2_ee_custom_inductor, 'figure')
    h2_ee_custom_inductor = figure('Name', 'ee_custom_inductor');
end
figure(h2_ee_custom_inductor)
clf(h2_ee_custom_inductor)

plot( I_linear, phi_linear, I_nonlinear, phi_nonlinear );
grid( 'on' );
title( 'Magnetic flux, \phi, versus current, I' );
xlabel( 'Current, I (A)' );
ylabel( 'Magnetic flux, \phi (Wb)' );
xlim([-0.2 0.2]);
ylim([-2e-5 2e-5]);
legend( '\phi=I L/N_w', '\phi-I curve', 'Location', 'NorthWest' );
```
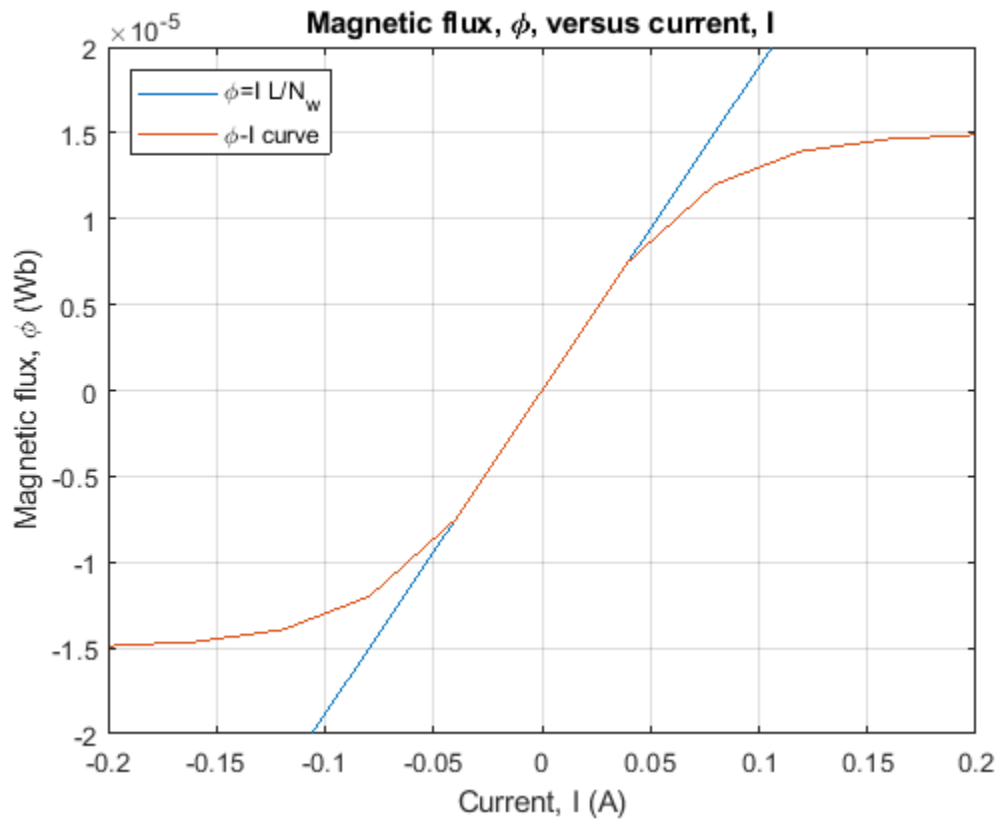
Use Parameters in Simscape Model

The parameters calculated can now be used in a Simscape model. Once simulated, the model is set to create a Simscape logging variable, simlog_ee_custom_inductor.

```
sim( modelName );
```

Conclusion

The state variable for both representations is magnetic flux, ϕ. Current, I, and magnetic flux, ϕ, can be obtained from the Simscape logging variable, simlog_ee_custom_inductor, for each representation. Overlaying the simulation results from the representations permits direct comparison.

```
if ~exist('h3_ee_custom_inductor', 'var') || ...
        ~isgraphics(h3_ee_custom_inductor, 'figure')
    h3_ee_custom_inductor = figure('Name', 'ee_custom_inductor');
end
figure(h3_ee_custom_inductor)
clf(h3_ee_custom_inductor)

plot( ...
    simlog_ee_custom_inductor.Inductor_linear_magnetic_flux.i.series.values,...
    simlog_ee_custom_inductor.Inductor_linear_magnetic_flux.phi.series.values,...
    simlog_ee_custom_inductor.Inductor_B_H_curve.i.series.values,...
    simlog_ee_custom_inductor.Inductor_B_H_curve.phi.series.values);
grid( 'on' );
title( 'Magnetic flux, \phi, versus current, I' );
xlabel( 'Current, I (A)' );
ylabel( 'Magnetic flux, \phi (Wb)' );
xlim([-0.2 0.2]);
ylim([-2e-5 2e-5]);
legend( 'Linear (single inductance)', 'B-H characteristic', 'Location', 'NorthWest' );
```
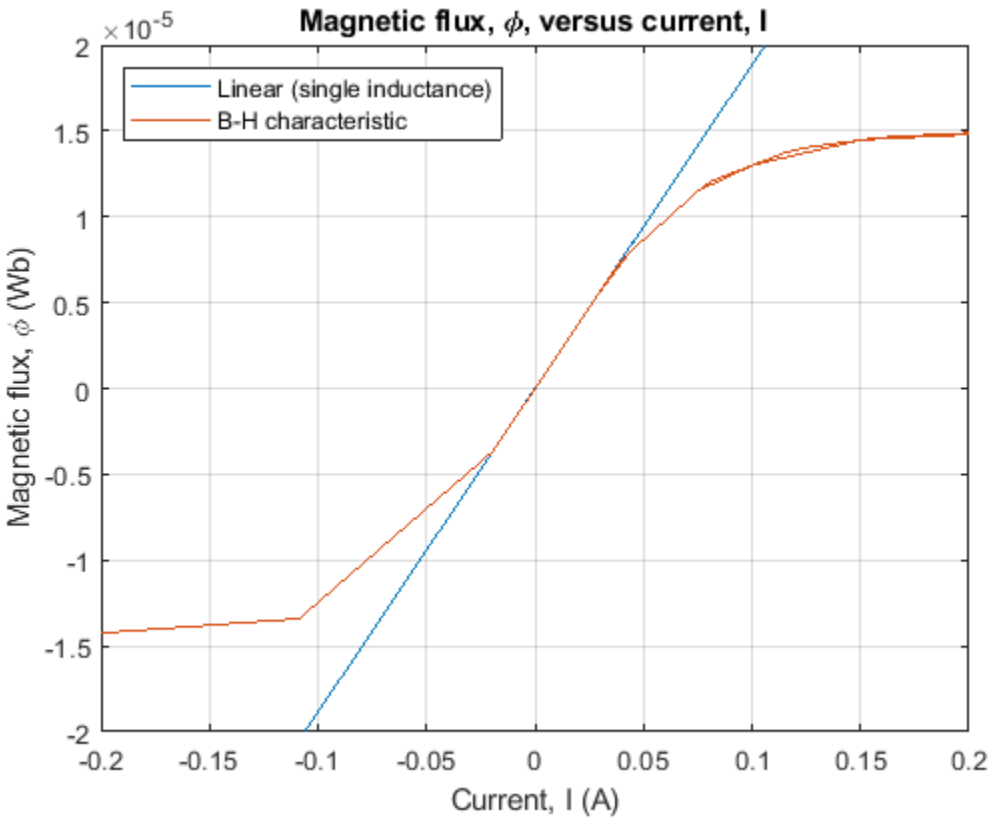
Three-Phase Custom PMSM

This example shows a custom Simscape™ implementation of a Permanent Magnet Synchronous Machine (PMSM). To view the PMSM source code, double-click on the motor block and then click on the hyperlink 'Source code'. This test circuit shows the PMSM being used as a generator, the rectifier block converting the induced AC back EMFs to a DC voltage which is in turn applied to a resistive load.

Model
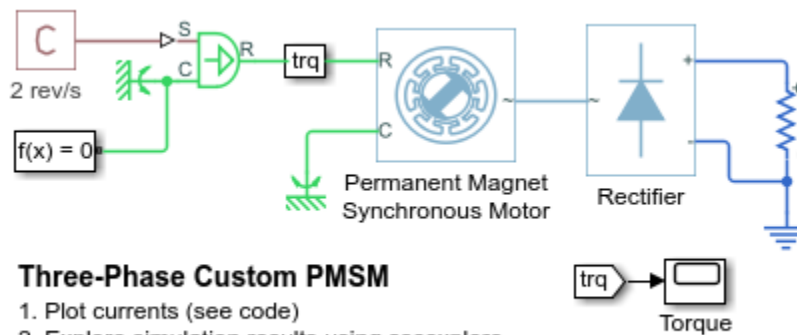
Three-Phase Custom PMSM
1. Plot currents (see code)
2. Explore simulation results using sscexplore
3. Learn more about this example

Simulation Results from Simscape Logging

The first subplot shows the AC currents generated by the Permanent Magnet Synchronous Machine (PMSM). The second subplot shows the DC current through the resistive load.
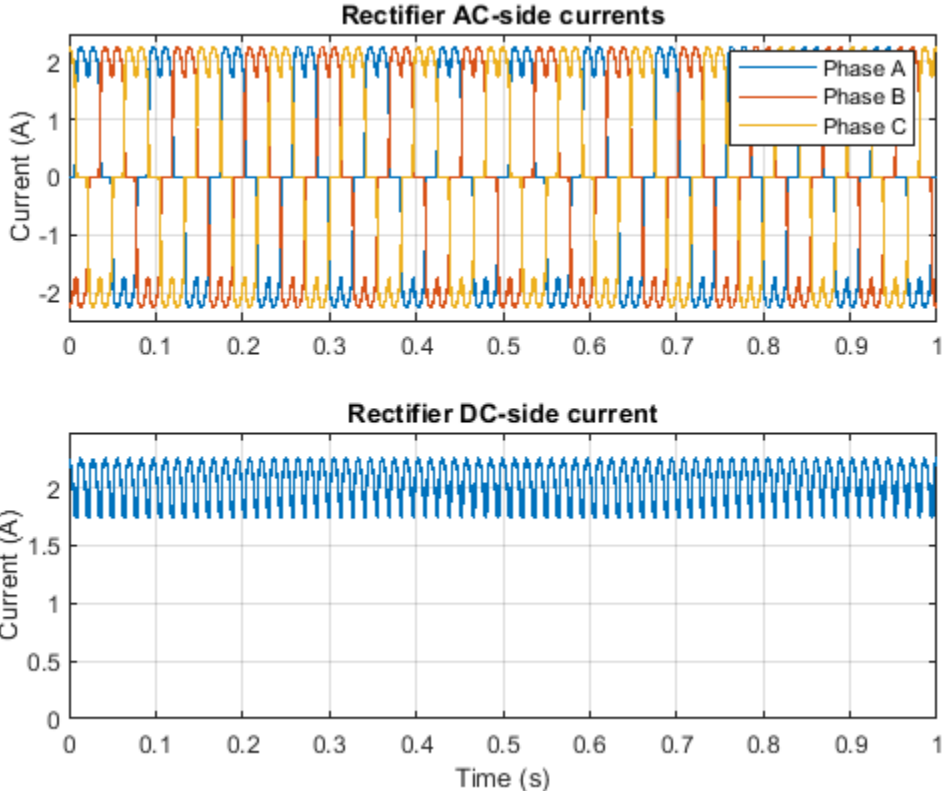
Three-Phase Custom Synchronous Machine

This example shows a custom Simscape™ implementation of a Synchronous Machine (SM). To view the SM source code, double-click on the Synchronous Machine block and then click on the hyperlink 'Source code'.

This test circuit shows the SM being used as a generator, the field has constant voltage applied, and the rotor is turned at a constant speed. The overall model has been initialized to start in periodic steady state to supply the 500MW load.

Model
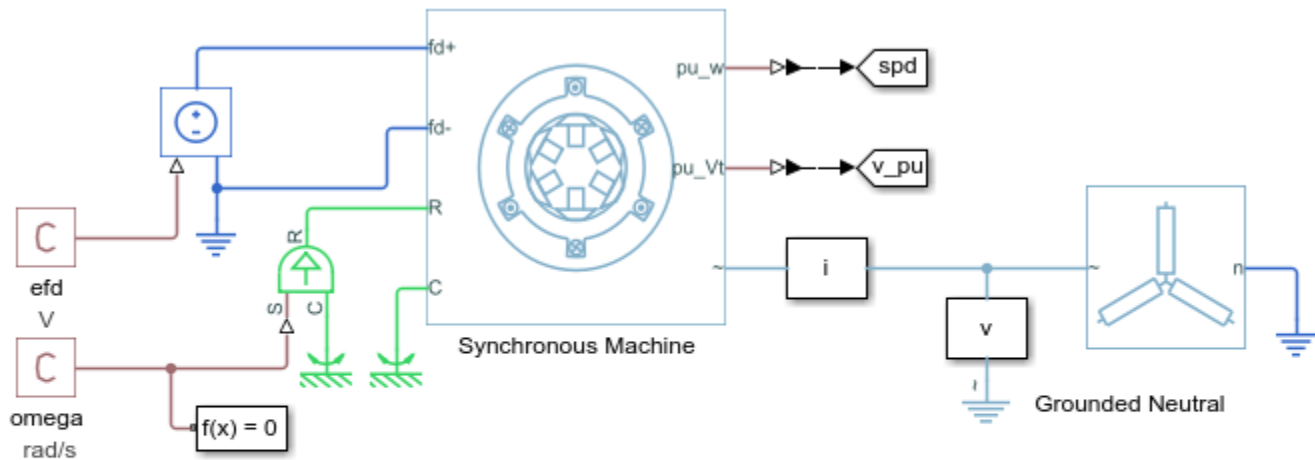
Three-Phase Custom Synchronous Machine

1. Explore simulation results using sscexplore
2. Learn more about this example
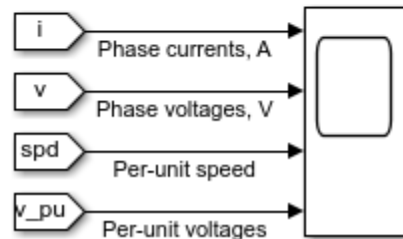
Three-Phase Custom Simplified Synchronous Machine

This example shows how simplify the custom Simscape™ synchronous machine model.

Detailed Machine Equations

Detailed per-unit stator voltage equations include the effect of rate of change of magnetic flux linkage, and the effect of speed variation:

$$e_d = \frac{1}{\omega_{base}} \frac{d\Psi_d}{dt} - \Psi_q \omega_r - R_a i_d$$

$$e_q = \frac{1}{\omega_{base}} \frac{d\Psi_q}{dt} + \Psi_d \omega_r - R_a i_q$$

Simplified Machine Equations

Large-scale studies are simulations that include more than 10 generators or motors. To reduce computational requirements of large-scale studies, simplifications are necessary. Simplifications for large-scale studies, include neglecting the following:

- The transformer voltage terms, $\frac{d\Psi_d}{dt}$ and $\frac{d\Psi_q}{dt}$
- The effect of speed variations. ω_r is set to 1.

These simplifications yield the following per-unit stator voltage equations:

$$e_d = -\Psi_q - R_a i_d$$

$$e_q = \Psi_d - R_a i_q$$

Simscape Component Comparison

You can use the Comparison Tool to see the differences between the detailed and simplified Simscape components. Type visdiff('ThreePhaseExamples.sm', 'ThreePhaseExamples.sm_simplified') at the MATLAB® command line to open the Comparison Tool to see the detailed differences on lines 176 and 177 of the files. The detailed Synchronous Machine component is on the left side, and the simplified Synchronous Machine component is on the right side.

Open Model

Open the model.

```
open_system( 'ee_custom_sm_simplified' );
```

Three-Phase Custom Simplified Synchronous Machine

1. Explore simulation results using sscexplore
2. Learn more about this example
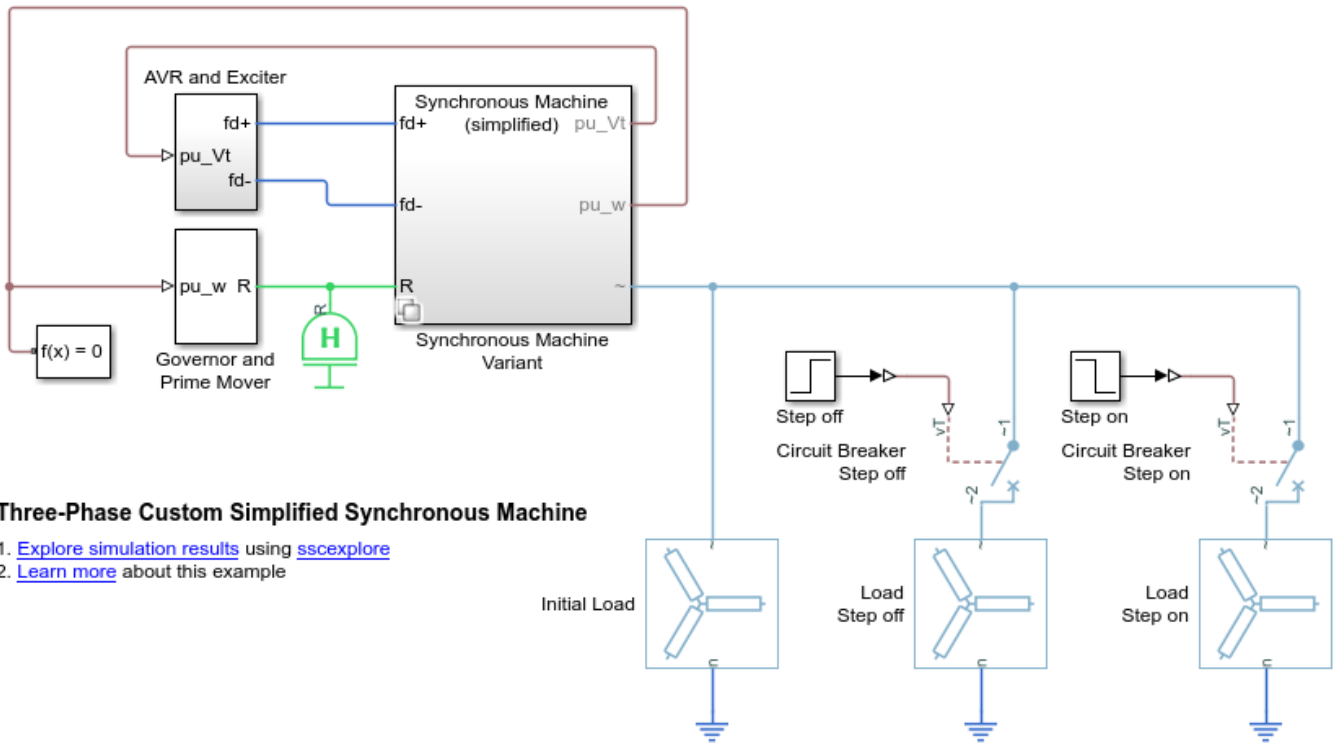
Define Initial Conditions

Specify values of initial conditions.

```
pu_psid0 = 0.7850;
pu_psiq0 = -0.6216;
pu_psifd0 = 0.9553;
pu_psi1d0 = 0.8269;
pu_psi1q0 = -0.5686;
pu_psi2q0 = -0.5686;
Efd0 = 1.2803;
torque0 = 0.4610;
```

Define Variants

The example model, ee_custom_sm_simplified, has been configured using Simulink® Variants. To execute the detailed model, set the variable isSimplified to false. To execute the simplified model, set the variable isSimplified to true.

Execute Simulations and Collect Results

Simulate detailed model.

```
isSimplified = false; %#ok<NASGU>
sim( 'ee_custom_sm_simplified' );
t1 = simlog_ee_custom_sm_simplified.Synchronous_Machine_Variant.Synchronous_Machine.Synchronous_N
v1 = simlog_ee_custom_sm_simplified.Synchronous_Machine_Variant.Synchronous_Machine.Synchronous_N
w1 = simlog_ee_custom_sm_simplified.Synchronous_Machine_Variant.Synchronous_Machine.Synchronous_N
```

Simulate simplified model.

```
isSimplified = true;
sim( 'ee_custom_sm_simplified' );
t2 = simlog_ee_custom_sm_simplified.Synchronous_Machine_Variant.Synchronous_Machine_simplified.Sy
v2 = simlog_ee_custom_sm_simplified.Synchronous_Machine_Variant.Synchronous_Machine_simplified.Sy
w2 = simlog_ee_custom_sm_simplified.Synchronous_Machine_Variant.Synchronous_Machine_simplified.Sy
```

Plot Results

The voltage and rotor velocity traces can be overlaid to directly compare differences.

```
figure;
subplot(2,1,1);
plot( t1, v1, t2, v2 );
ylim( [ 0.95 1.05 ] );
legend( 'Detailed', 'Simplified' );
title( 'Terminal voltage, pu' );
subplot(2,1,2);
plot( t1, w1, t2, w2 );
ylim( [ 0.95 1.05 ] );
title( 'Rotor velocity, pu' );
```
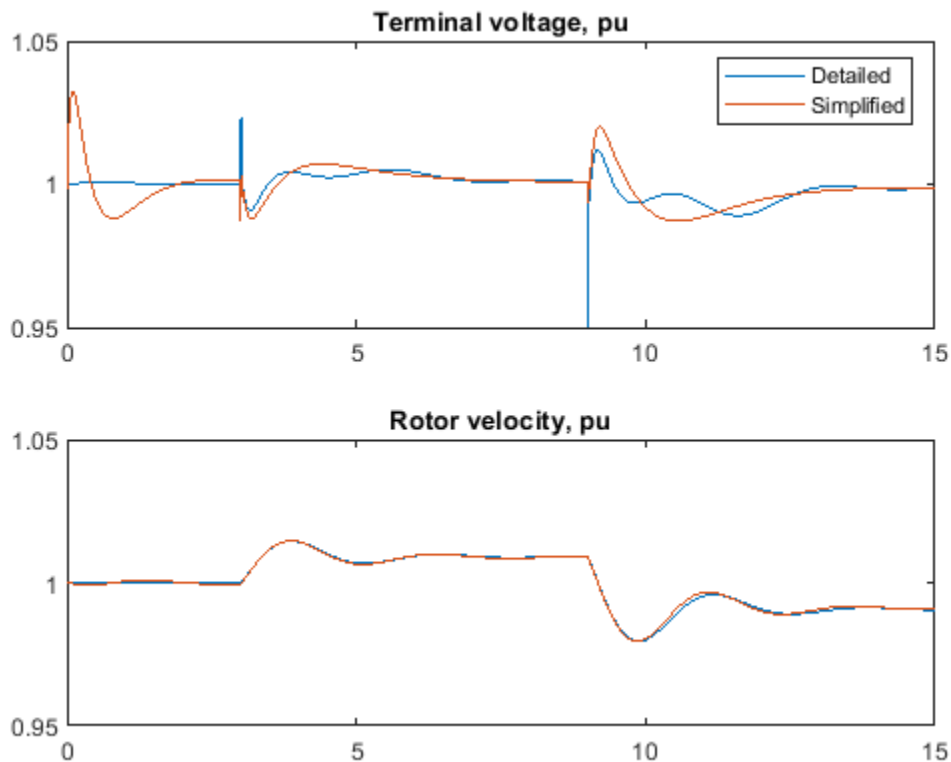

Conclusion

This example shows:

- How to simplify the custom synchronous machine model
- A comparison of Simscape component source code

- A comparison of model simulation outputs

Custom Transformer (B-H Curve)

This example shows calculation and confirmation of a nonlinear transformer core magnetization characteristic. Starting with fundamental parameter values, the core characteristic is derived. This is then used in a Simscape™ model of an example test circuit which can be used to plot the core magnetization characteristic on an oscilloscope. Model outputs are then compared to the known values.

Open Model

```
modelName = 'ee_custom_transformer';
open_system( modelName );
```
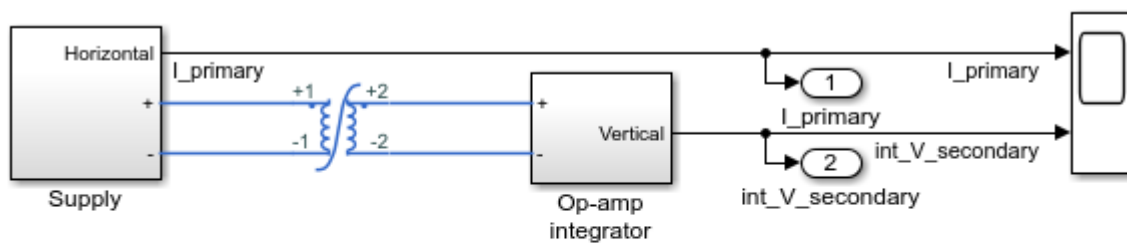

Custom Transformer (B-H Curve)

1. Plot comparison graph (see code)
2. Modify model parameters
3. Explore simulation results using sscexplore
4. Learn more about this example

Specification of Parameters

Fundamental parameter values used as the basis for subsequent calculations:

- Permeability of free space, $\mu_0, \mathrm{H/m}$
- Relative permeability of core, μ_r
- Number of primary turns, N_1
- Number of secondary turns, N_2
- Effective magnetic core length, l_e, m
- Effective magnetic core cross-sectional area, $A_e, \mathrm{m^2}$
- Core saturation begins, $B_{sat_{begin}}, \mathrm{T}$
- Core fully saturated, B_{sat}, T

```
mu_0 = pi*4e-7;
mu_r = 3000;
N1 = 100;
N2 = 200;
le = 0.1;
Ae = 1e-4;
B_sat_begin = 0.6;
B_sat = 1.2;
```

Calculate Magnetic Flux Density Versus Magnetic Field Strength Characteristic

Where:

- Magnetic flux density, B, T
- Magnetic field strength, $H, \mathrm{A/m}$

Linear representation:

- $B = \mu_0 \mu_r H$

Nonlinear representation (including coefficient, a):

- $B = B_{sat} \tanh(a.H)$

```
% Use linear representation to find value of H corresponding to B_sat_begin
H_sat_begin = B_sat_begin/(mu_0*mu_r);
% Rearrange nonlinear representation to calculate coefficient, a
a = atanh( B_sat_begin/B_sat )/H_sat_begin;

% Nonlinear representation
H_nonlinear = -750:25:750;
B_nonlinear = B_sat*tanh(a*H_nonlinear);
```

Use Parameters in Simscape Model

The parameters calculated can now be used in a Simscape model. Once simulated, the model is set to output a Simscape logging variable, simlog_ee_custom_transformer, and some signals using output ports, yout. Circuit parameters are:

- Voltage source magnitude, $V_s = 10\mathrm{V}$
- Voltage source frequency, $Freq_{Hz} = 60\mathrm{Hz}$
- Voltage source resistance, $R_{Vs} = 10\Omega$
- Operational amplifier input resistance, $R_1 = 1\mathrm{k}\Omega$
- Operational amplifier feedback resistance, $R_2 = 1\mathrm{M}\Omega$
- Operational amplifier feedback capacitance, $C_2 = 1\mu\mathrm{F}$

```
% Circuit parameters
Vs = 10;
Freq_Hz = 60;
R_Vs = 10;
R_1 = 1e3;
R_2 = 1e6;
C_2 = 1e-6;

% Simulate model
simOut = sim( modelName );
yout = get(simOut, 'yout');
simlog = get(simOut, 'simlog_ee_custom_transformer');

% Collect internal Simscape logging data for comparison
I_simscape = simlog.Transformer_B_H_curve.L_m.i.series.values;
phi_simscape = simlog.Transformer_B_H_curve.L_m.phi.series.values;
```

```
% Collect model output data for comparison (as used for oscilloscope)
I_primary = yout(:,1);
int_V_secondary = yout(:,2);
```

Calculations on Logging and Output Data

The data needs to be processed to provide the Magnetic field strength and Magnetic flux density data for comparison. Where:

- Magnetomotive force, F_m, A

- Magnetic flux, ϕ, Wb

- Operational amplifier input voltage, V_{in}, V

- Operational amplifier output voltage, V_{out}, V

Equations to be used are as follows:

- $F_m = I.N_1$

- $H = F_m/l_e$

- $B = \phi/A_e$

- $V_{out} = \frac{1}{R_1 C_2} \int V_{in}.dt + c$

- $\phi = \frac{1}{N_2} \int v.dt$

```
% Internal logging data
H_simscape = I_simscape.*N1./le;
B_simscape = phi_simscape./Ae;

% Oscilloscope scaling and model output data
H_measured = I_primary.*N1./le;
phi_measured = (int_V_secondary.*R_1.*C_2)./N2;
B_measured = phi_measured./Ae;
```

Conclusion

The three characteristics can now be overlaid:

- Defined characteristic: calculated from fundamental parameters
- Characteristic from logging: calculated from internal Simscape logging data
- Characteristic from measurement: obtained by measurement and calculation using electronic test circuit

Due to leakage and parasitic parameters, the characteristic obtained from the electronic test circuit differs to the defined characteristic. However, the test circuit and its parameterization is shown to find the characteristic for the given transformer within suitable tolerances.

```
if ~exist('h1_ee_custom_transformer', 'var') || ...
        ~isgraphics(h1_ee_custom_transformer, 'figure')
    h1_ee_custom_transformer = figure('Name', 'ee_custom_transformer');
end
figure(h1_ee_custom_transformer)
clf(h1_ee_custom_transformer)
```

```
plot( ...
    H_nonlinear,...
    B_nonlinear,...
    'o',...
    H_simscape,...
    B_simscape,...
    H_measured,...
    B_measured,...
    '-',...
    'LineWidth',2);
grid( 'on' );
title( 'Transformer B-H Curve' );
xlabel( 'Magnetic Field Strength, H (A/m)' );
ylabel( 'Magnetic Flux Density, B (T)' );
legend( 'Calculated', 'Simulated',...
    'From Sensor', 'Location', 'NorthWest' );
```
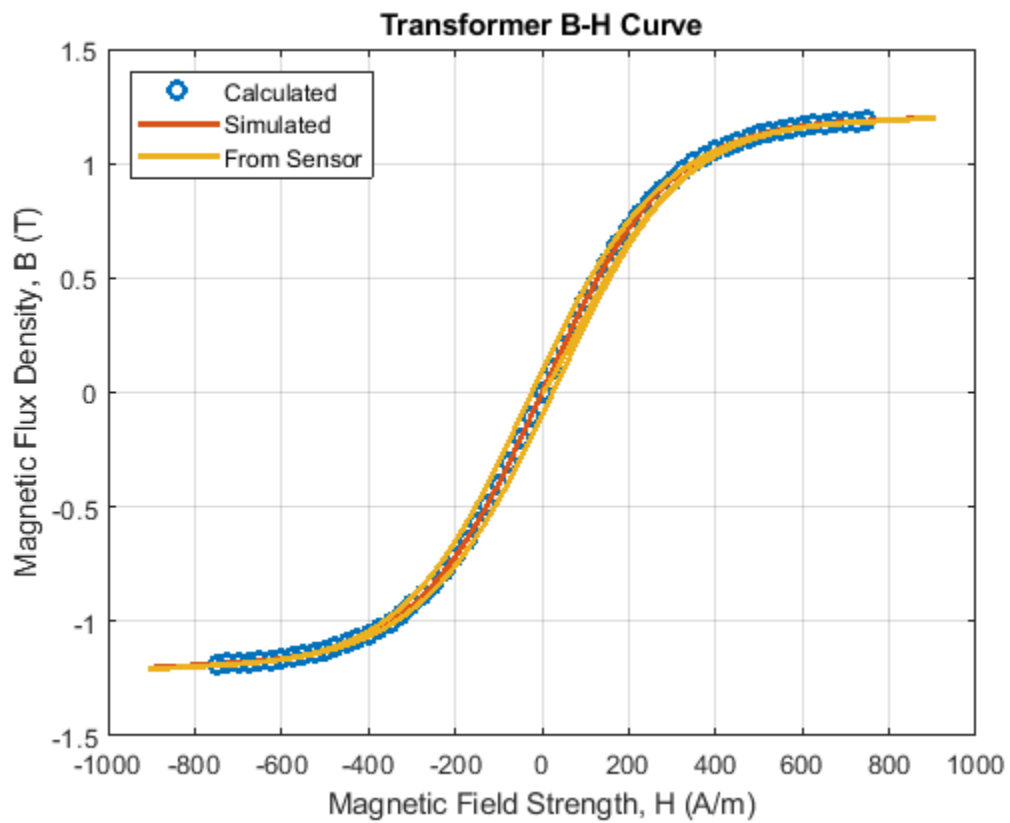
Three-Phase Custom Transforms

This example shows a comparison of three-phase transforms.

There are four transforms:

- Park transform with rotating reference frame
- Park transform with stationary reference frame
- Park transform with synchronous reference frame
- Clarke transform

For more information on each, double-click the block of interest and read the block mask.

```
open_system('ee_custom_transforms');
set_param(find_system('ee_custom_transforms','FindAll', 'on','type','annotation','Tag','ModelFea
```
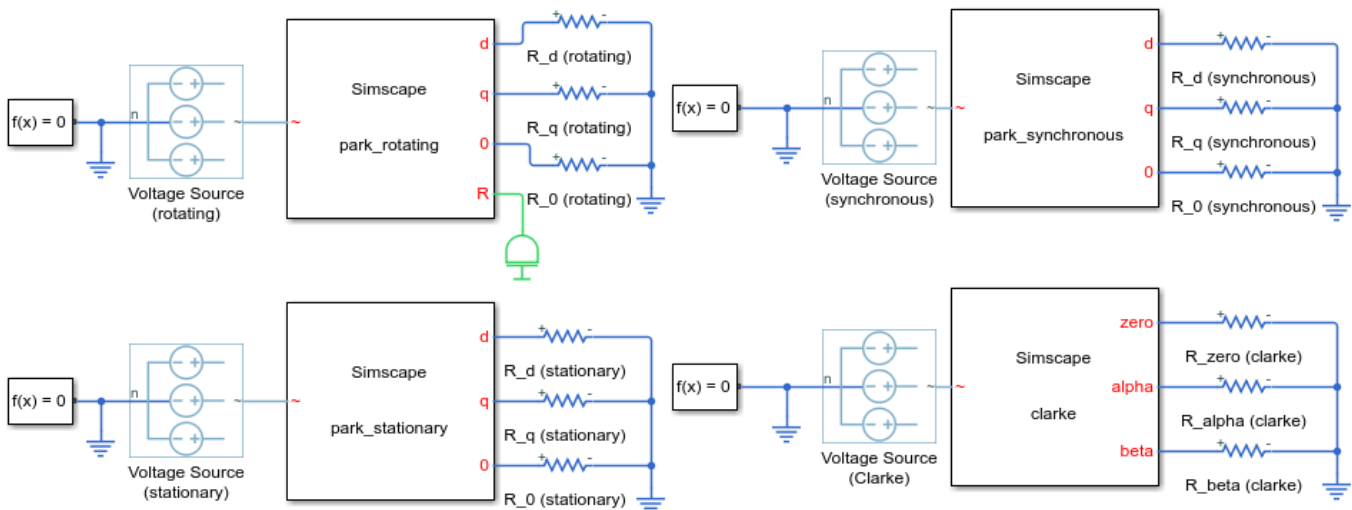

Three-Phase Custom Transforms
1. Explore simulation results using sscexplore
2. Learn more about this example

To view full results, run the simulation, and enter at the MATLAB® command prompt:

```
sscexplore(ee_custom_transforms_simlog)
```

Three-Phase Custom Zigzag Transformer

This example shows two different ways to create a custom transformer component. The first uses built-in library blocks to lay out the magnetic circuit as a masked Simulink® subsystem. The second builds a custom Simscape™ component using the Simscape language.

To view the Simscape language source code, double-click on the Custom Zigzag transformer (Language) block and then click on the hyperlink 'Source code'.

The transformer is implemented with a zigzag primary, 1 o'clock delta secondary and wye secondary winding. The phase offset between the zigzag and wye windings can be specified via a block mask parameter.

Model
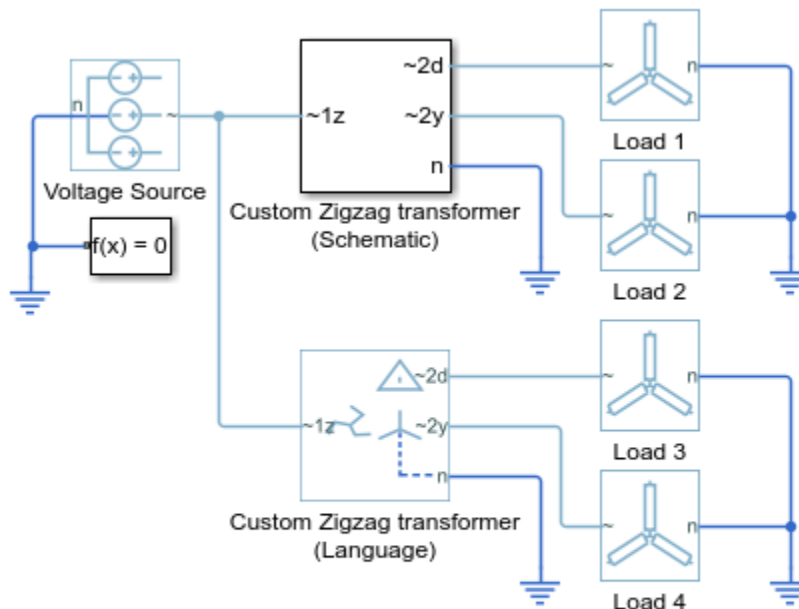
Three-Phase Custom Zigzag Transformer

1. Plot results (see code)
2. Explore simulation results using sscexplore
3. Learn more about this example
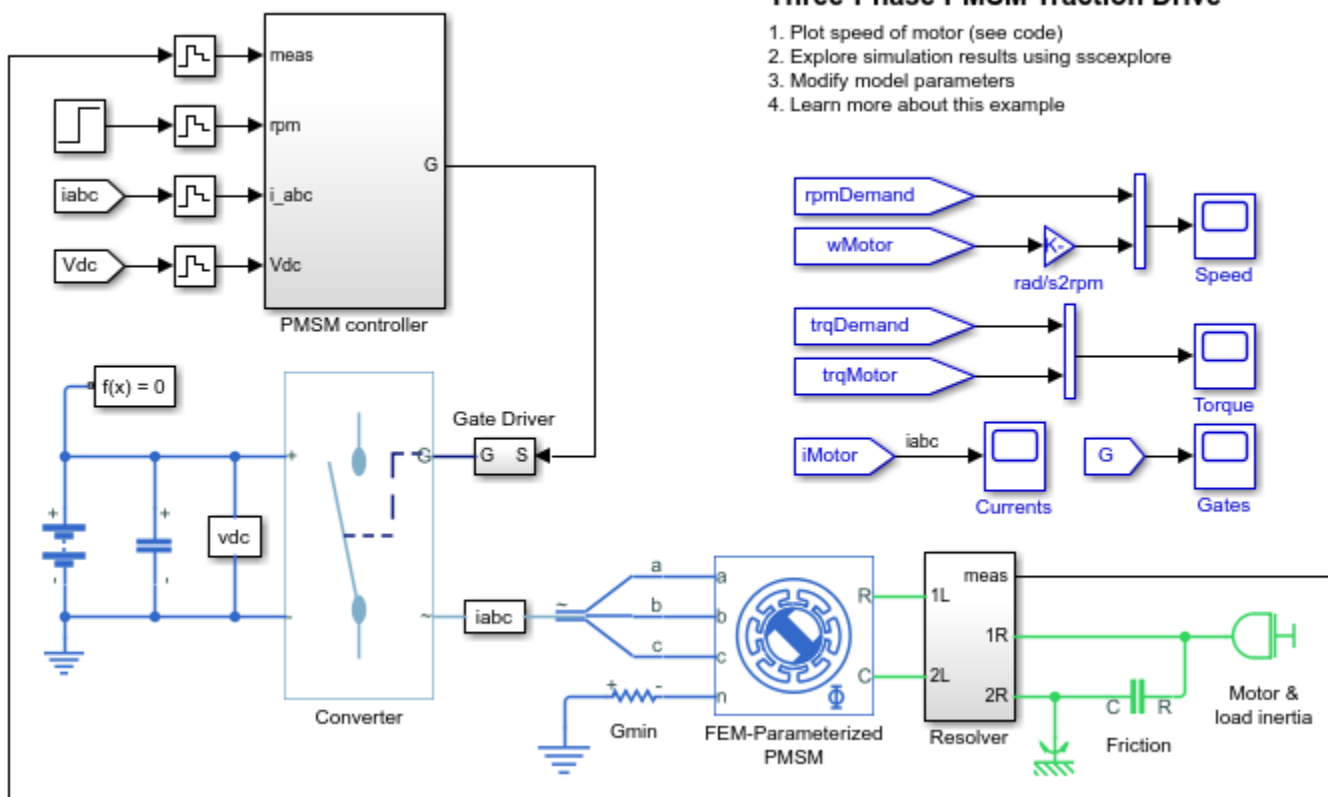
Simulation Results from Simscape Logging

The plots below show the results for two different ways to create a custom transformer component. One using built-in library blocks to lay out the magnetic circuit as a masked Simulink® subsystem and other creating a Simscape™ component in Simscape language.
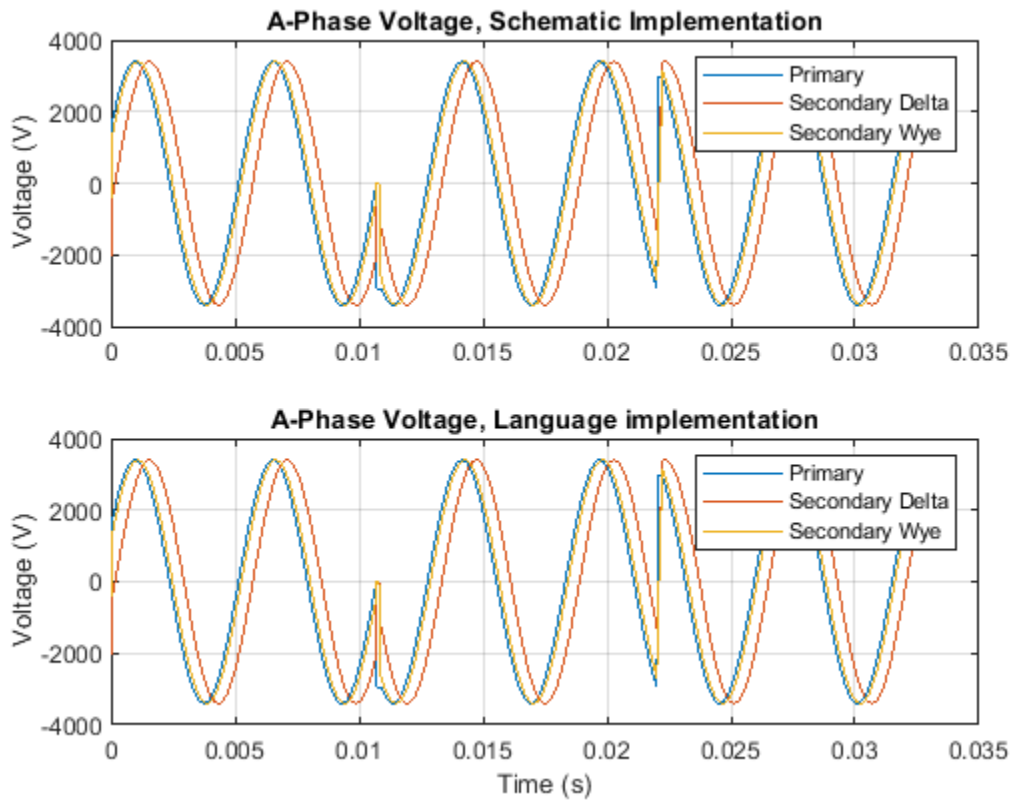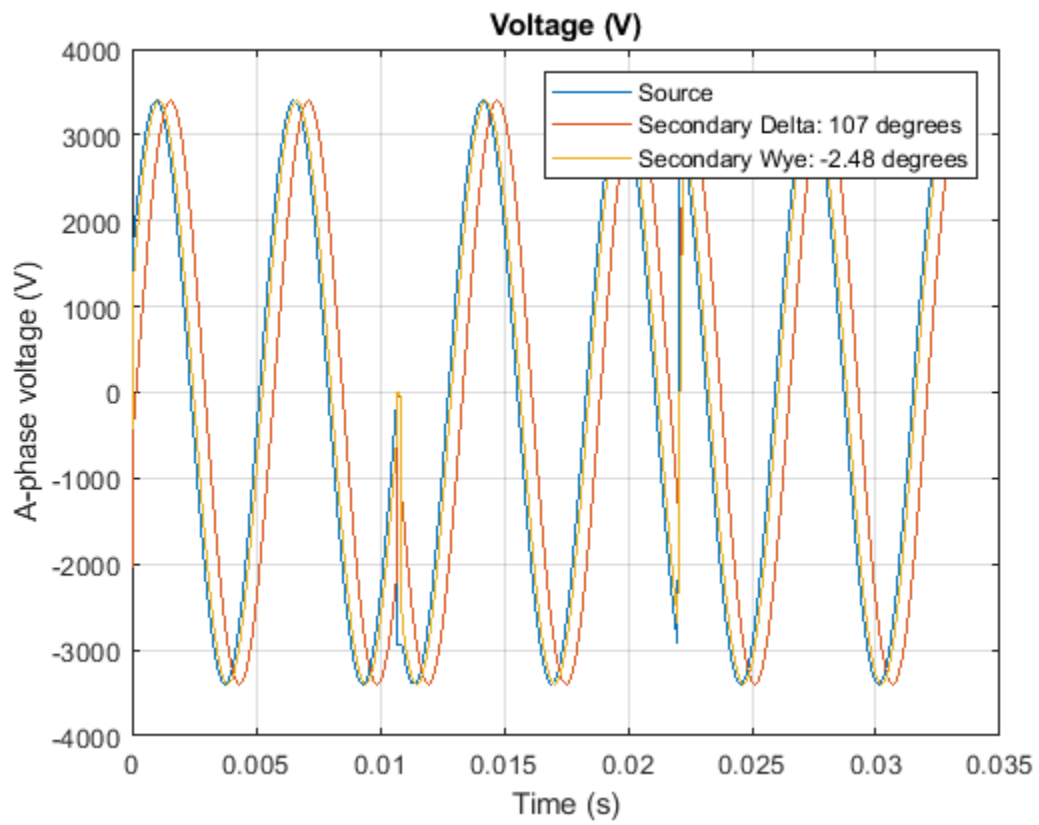
DC Motor Control

This example shows a cascade speed-control structure for a DC motor. A PWM controlled four-quadrant Chopper is used to feed the DC motor. The Control subsystem includes the outer speed-control loop, the inner current-control loop, and the PWM generation. The total simulation time (t) is 4 seconds. At t = 1.5 seconds, the load torque increases. At t = 2.5 seconds, the reference speed is changed from 1000 rpm to 2000 rpm.
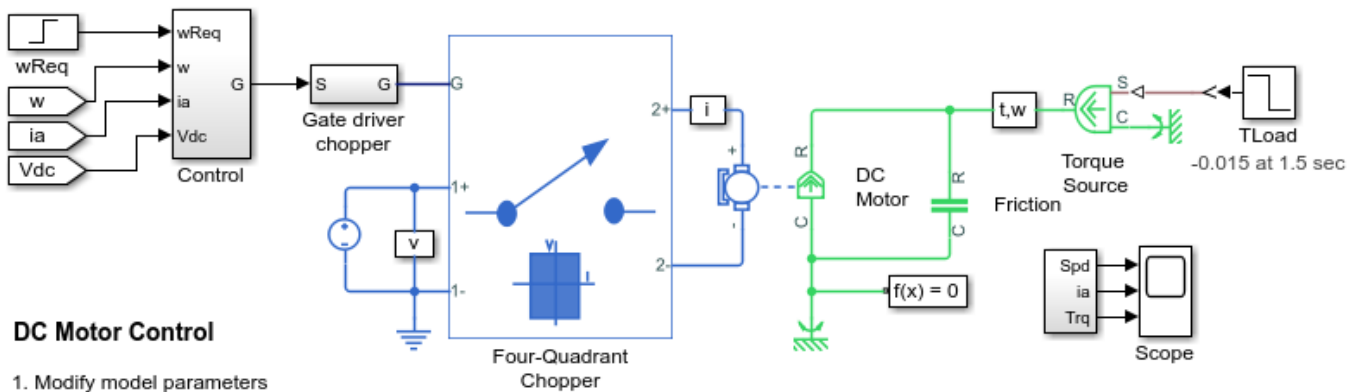
Model

DC Motor Control

1. Modify model parameters
2. Explore simulation results using sscexplore
3. Learn more about this example

DC Motor Control (Lead-Lag)

This example shows a lead-lag speed-control structure for a DC motor. A PWM controlled four-quadrant Chopper is used to feed the DC motor. The Control subsystem includes a lead-lag controller, a constant gain, and the PWM generation. The total simulation time (t) is 4 seconds. At t = 1.5 seconds, the load torque increases. At t = 2.5 seconds, the reference speed is changed from 1000 rpm to 2000 rpm.

Model

DC Motor Control (Lead-Lag)

1. Modify model parameters
2. Explore simulation results using sscexplore
3. Learn more about this example
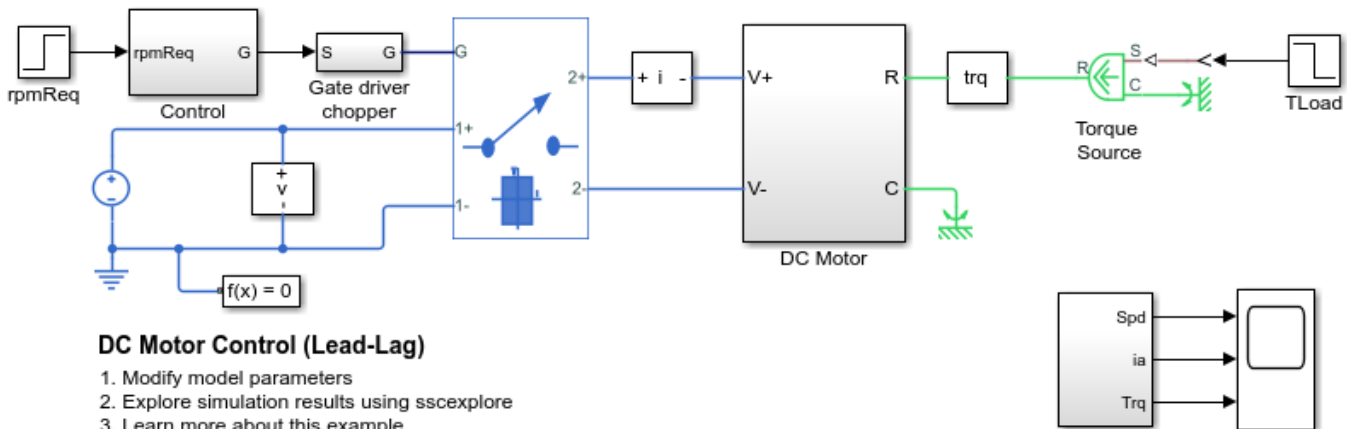
DC Motor Control (RST)

This example shows an RST speed-control structure for a DC motor. A PWM controlled four-quadrant Chopper is used to feed the DC motor. The Control subsystem includes the RST controller with control horizon of 30, and the PWM generation. A sensor measures the rotor speed with a delay of 5ms. The total simulation time (t) is 4 seconds. At t = 1.5 seconds, the load torque increases. At t = 2.5 seconds, the reference speed is changed from 1000 rpm to 2000 rpm.

Model

DC Motor Control (RST)
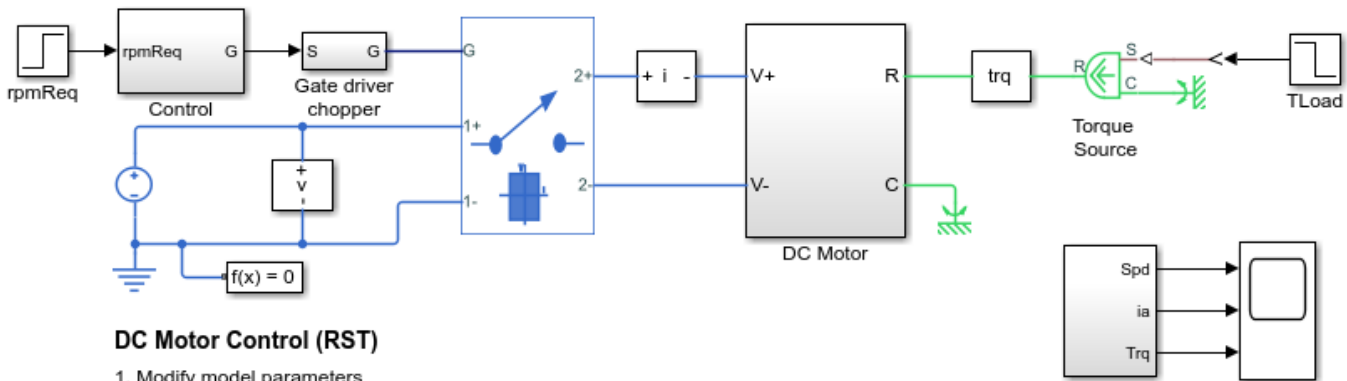
1. Modify model parameters
2. Explore simulation results using sscexplore
3. Learn more about this example

DC Motor Control (Smith Predictor)

This example shows a Smith Predictor speed-control structure for a DC motor. A PWM controlled four-quadrant Chopper is used to feed the DC motor. The Control subsystem includes the Smith predictor controller, and the PWM generation. A sensor measures the rotor speed with a delay of 5ms. The total simulation time (t) is 4 seconds. At t = 1.5 seconds, the load torque increases. At t = 2.5 seconds, the reference speed is changed from 1000 rpm to 2000 rpm.

Model

DC Motor Control (Smith Predictor)

1. Modify model parameters
2. Explore simulation results using sscexplore
3. Learn more about this example
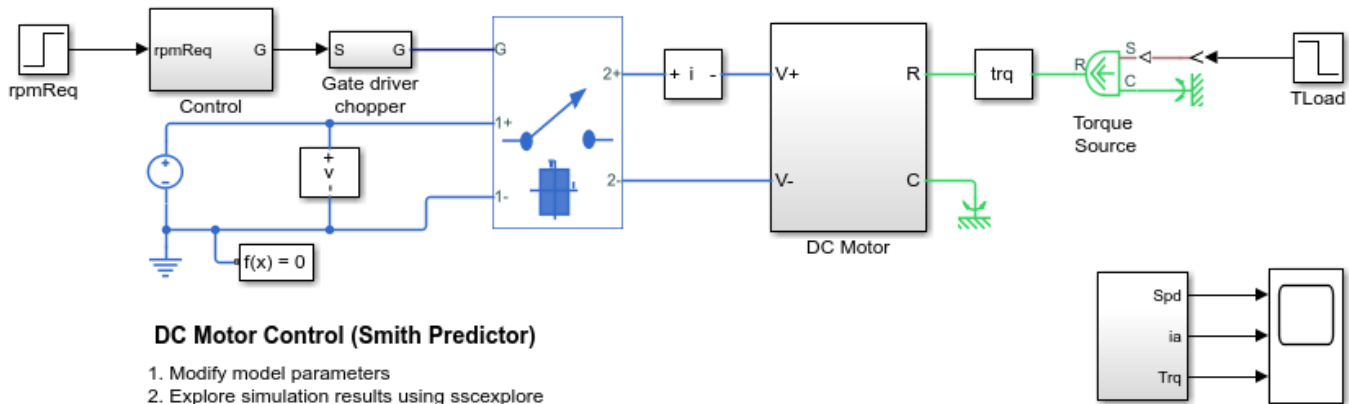
DC Motor Control (State-Feedback and Observer)

This example shows a state-feedback speed-control structure for a DC motor. A PWM controlled four-quadrant Chopper is used to feed the DC motor. The Control subsystem includes the state-feedback control loop, and the PWM generation. The state vector includes the rotor speed which is measured, and the dc motor current, which is estimated using an observer. Both the observer and state-feedback controller are synthesized by pole placement using the state-space model of the system. The total simulation time (t) is 4 seconds. At t = 1.5 seconds, the load torque increases. At t = 2.5 seconds, the reference speed is changed from 1000 rpm to 2000 rpm.
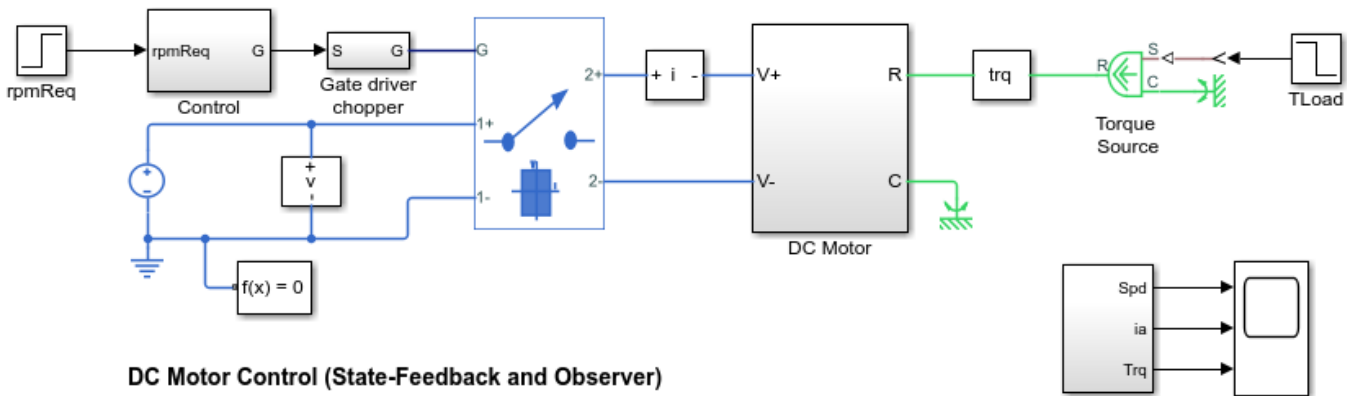
Model

DC Motor Control (State-Feedback and Observer)
1. Modify model parameters
2. Explore simulation results using sscexplore
3. Learn more about this example

Electric Engine Dyno

This example shows how to model an electric vehicle dynamometer test. The test environment contains an asynchronous machine (ASM) and an interior permanent magnet synchronous machine (IPMSM) connected back-to-back through a mechanical shaft. Both machines are fed by high-voltage batteries through controlled three-phase converters. The 164 kW ASM produces the load torque. The 35 kW IPMSM is the electric machine under test. The Control Machine Under Test (IPMSM) subsystem controls the torque of the IPMSM. The controller includes a multi-rate PI-based control structure. The rate of the open-loop torque control is slower than the rate of the closed-loop current control. The task scheduling for the controller is implemented as a Stateflow® state machine. The Control Load Machine (ASM) subsystem uses a single rate to control the speed of the ASM. The Visualization subsystem contains scopes that allow you to see the simulation results.
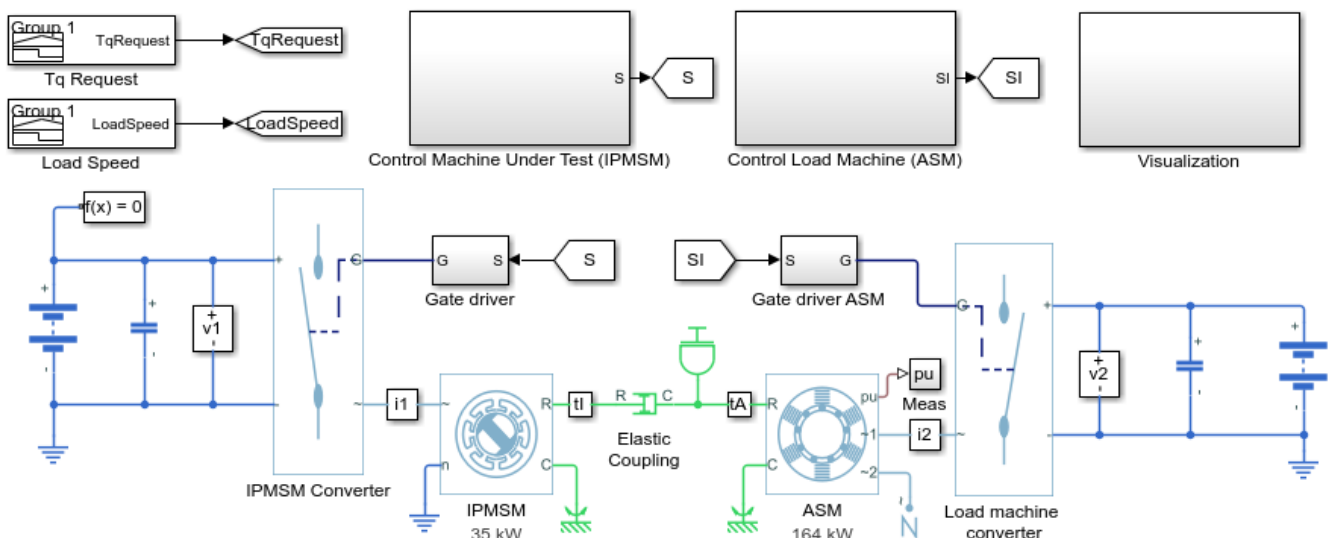
Model

Electric Engine Dyno

1. Modify model parameters
2. Explore simulation results using sscexplore
3. Learn more about this example

Electric Power Assisted Steering

This example shows how to use a Permanent Magnet Synchronous Motor (PMSM) to amplify driver-applied force in a power-assisted automobile steering system.

The steering wheel angular velocity represents the input from the driver. The inputs to the PMSM controller are the angular difference from one end of the steering column to the other and the velocity and angular displacement of the steering load.

The PMSM Inverter is idealized. You can see voltage and current waveforms on the Inverter Voltages & Currents Scope.

Within the PMSM Driver, the PMSM Controller is a Model Reference block. If you have a Simulink® Coder™ or Embedded Coder® license, you can generate code directly from the PMSM Controller referenced model.

Model
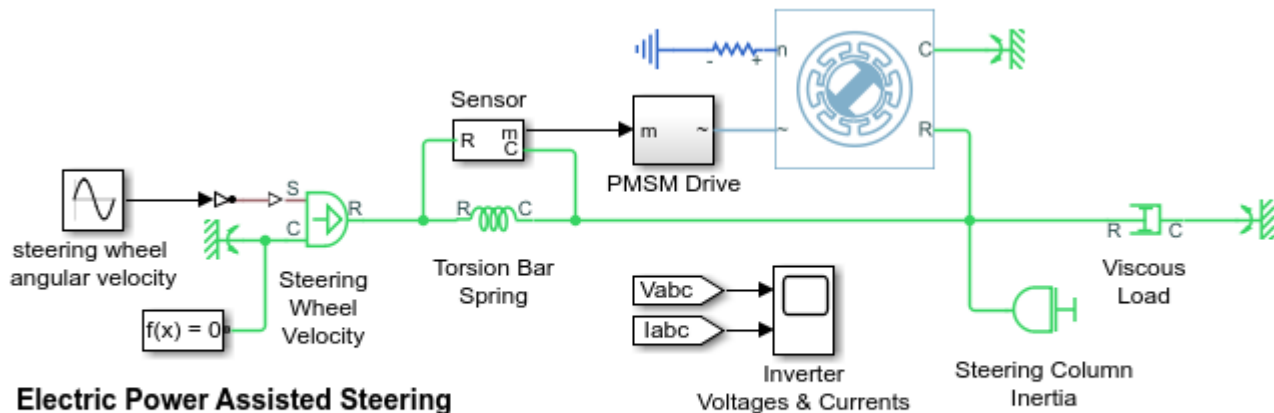
Electric Power Assisted Steering

1. Plot torque waveforms (see code)
2. Explore simulation results using sscexplore
3. Learn more about this example

Simulation Results from Simscape Logging

The plot below shows the driver, power assist, and total torque waveforms.
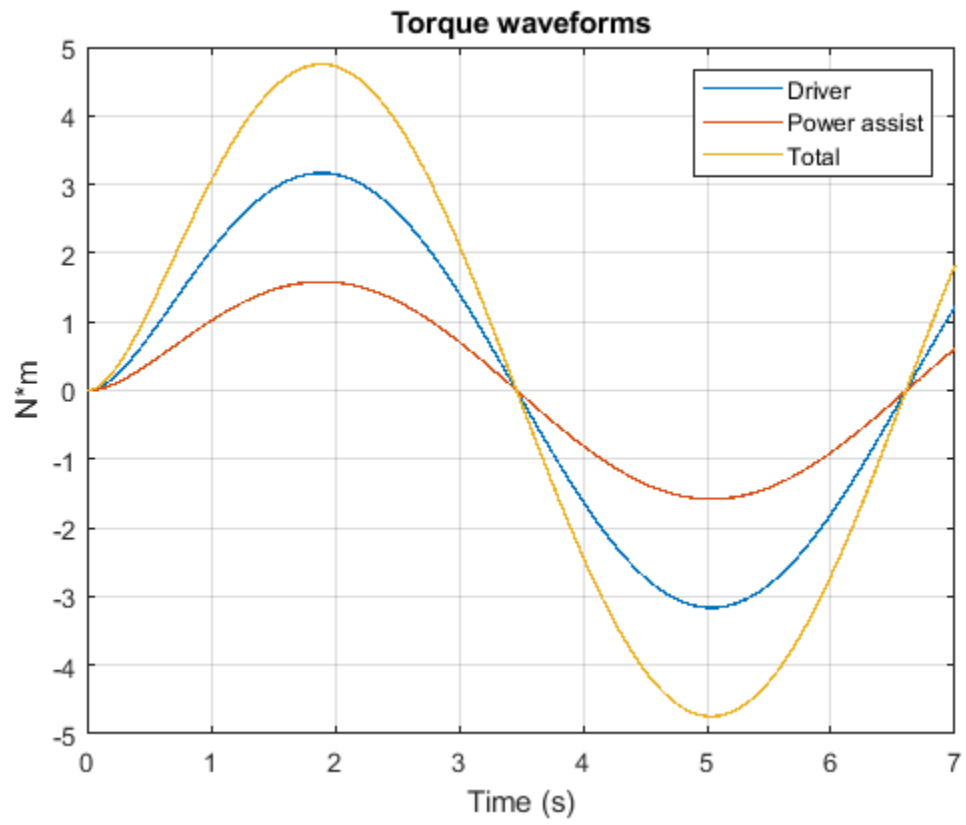
Five-Phase Switched Reluctance Machine Control

This example shows how to control the rotor speed in a five-phase switched reluctance machine (SRM) based electrical drive. A DC voltage source feeds the SRM through a controlled five-arm bridge. The converter turn-on and turn-off angles are held constant.
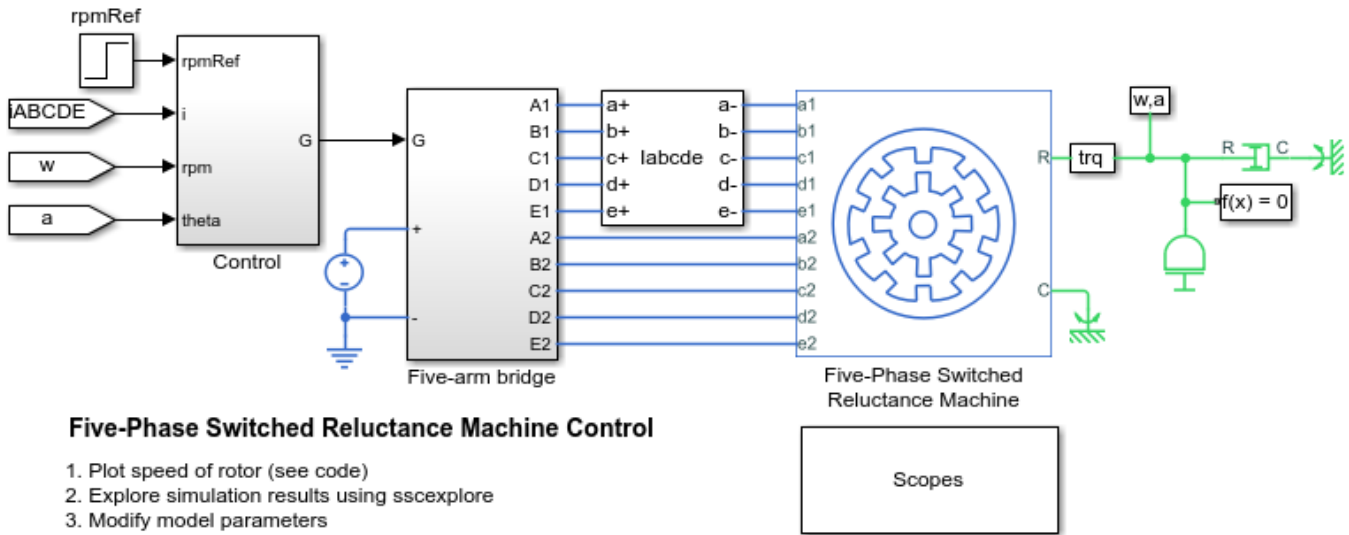
Model

Five-Phase Switched Reluctance Machine Control

1. Plot speed of rotor (see code)
2. Explore simulation results using sscexplore
3. Modify model parameters
4. Learn more about this example

Simulation Results from Simscape Logging

The plot below shows the requested and measured speed for the test, as well as the phase currents in the electric drive.
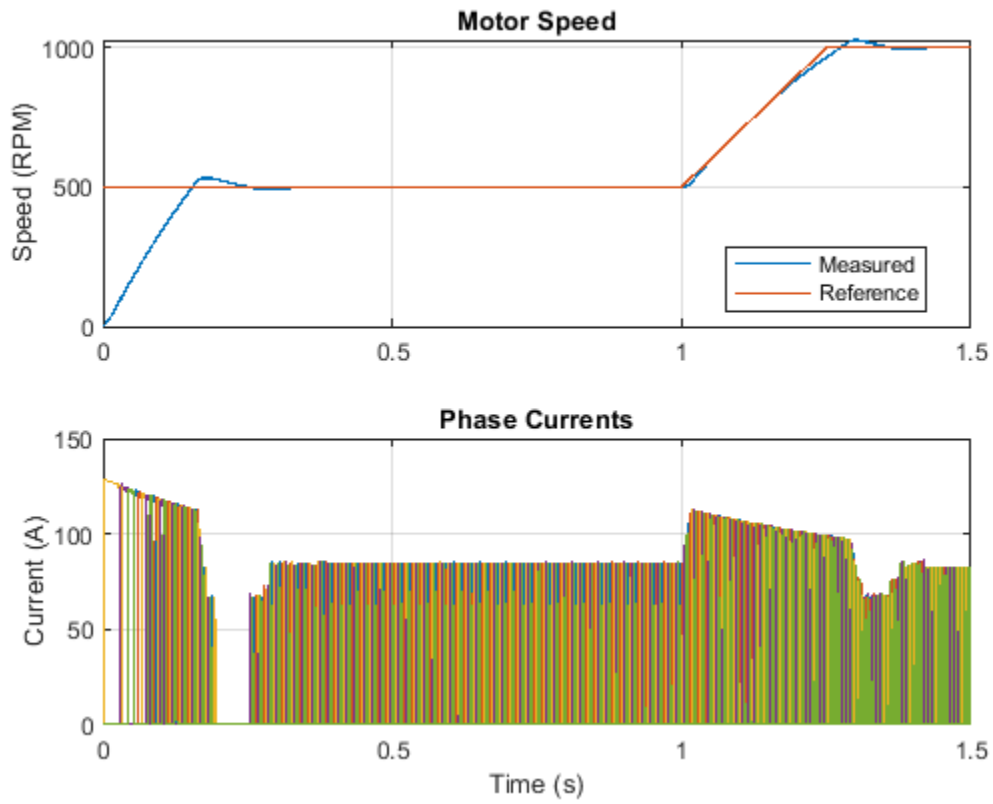
Four-Phase Switched Reluctance Machine Control

This example shows how to control the rotor speed in a four-phase switched reluctance machine (SRM) based electrical drive. A DC voltage source feeds the SRM through a controlled four-arm bridge. The converter turn-on and turn-off angles are held constant.
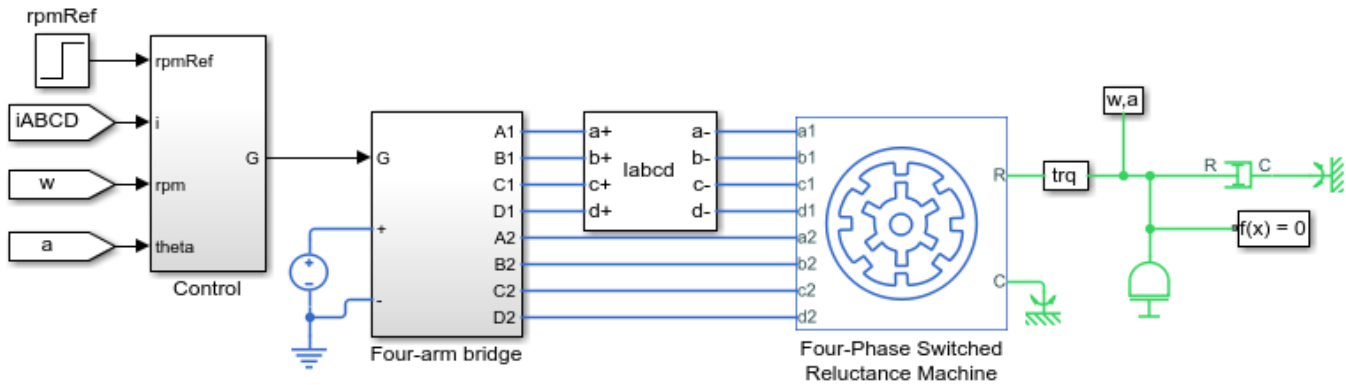
Model

Four-Phase Switched Reluctance Machine Control

1. Plot speed of rotor (see code)
2. Explore simulation results using sscexplore
3. Modify model parameters
4. Learn more about this example
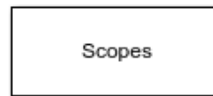
Simulation Results from Simscape Logging

The plot below shows the requested and measured speed for the test, as well as the phase currents in the electric drive.
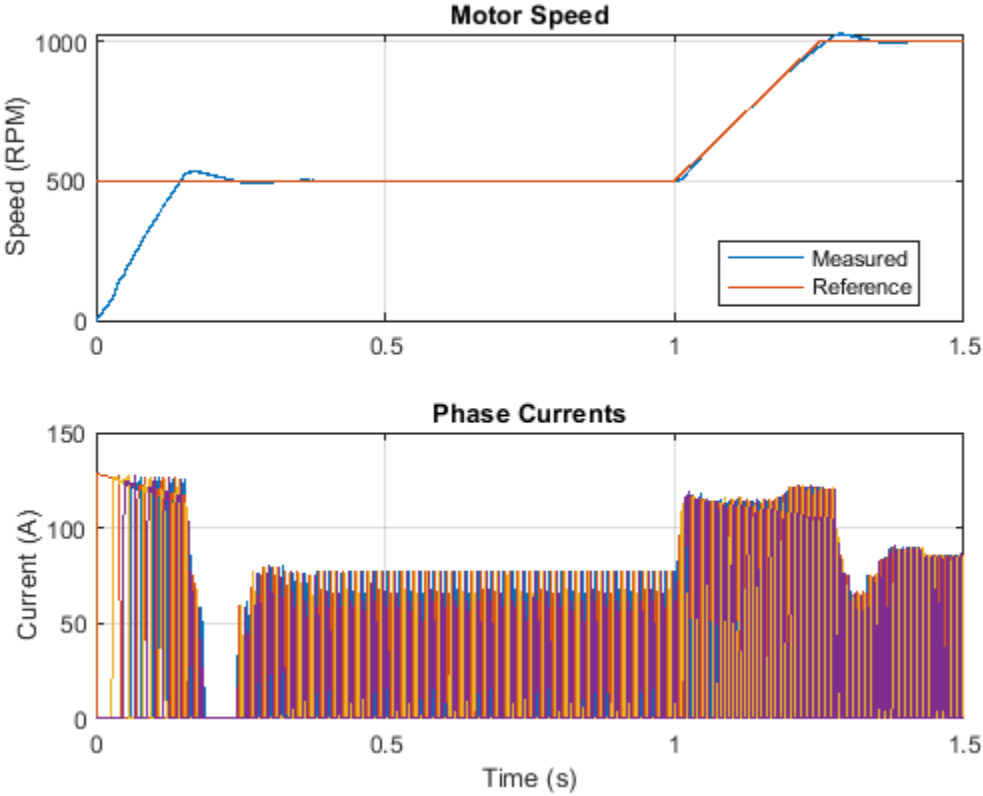
Four-Quadrant Chopper Control

This example shows how to control a four-quadrant chopper. The Control subsystem implements a simple PI-based control algorithm for controlling the output current. The simulation uses both positive and negative references. The total simulation time (t) is 1 second. At t = 0.5 seconds, the polarity of the load DC source E is changed.
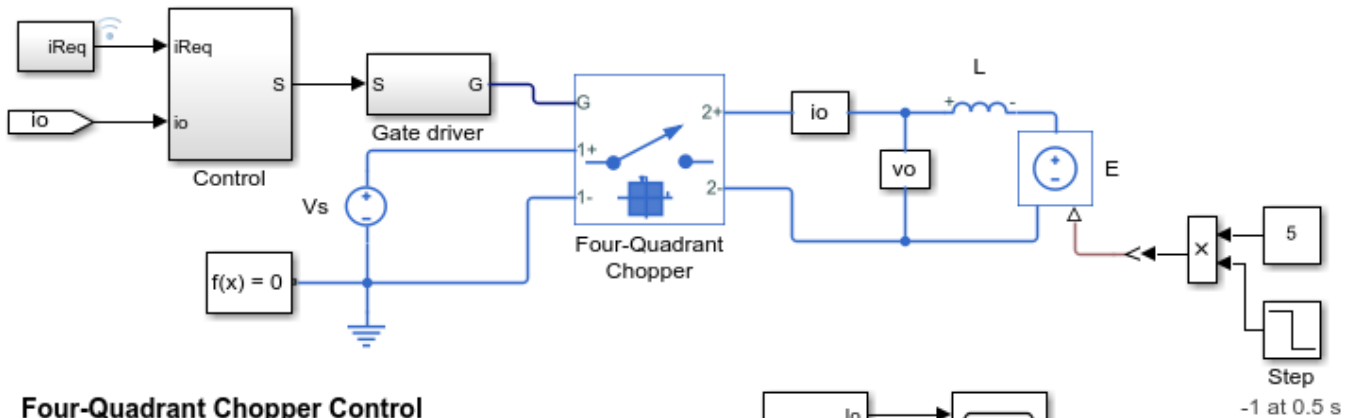
Model

Four-Quadrant Chopper Control

1. Plot current (see code)
2. Explore simulation results using sscexplore
3. Modify model parameters
4. Learn more about this example
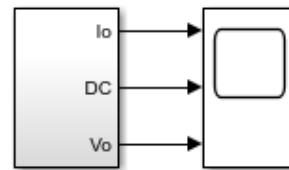
Simulation Results from Simscape Logging

The plot below shows the requested and measured current for the test and the output voltage in the circuit.
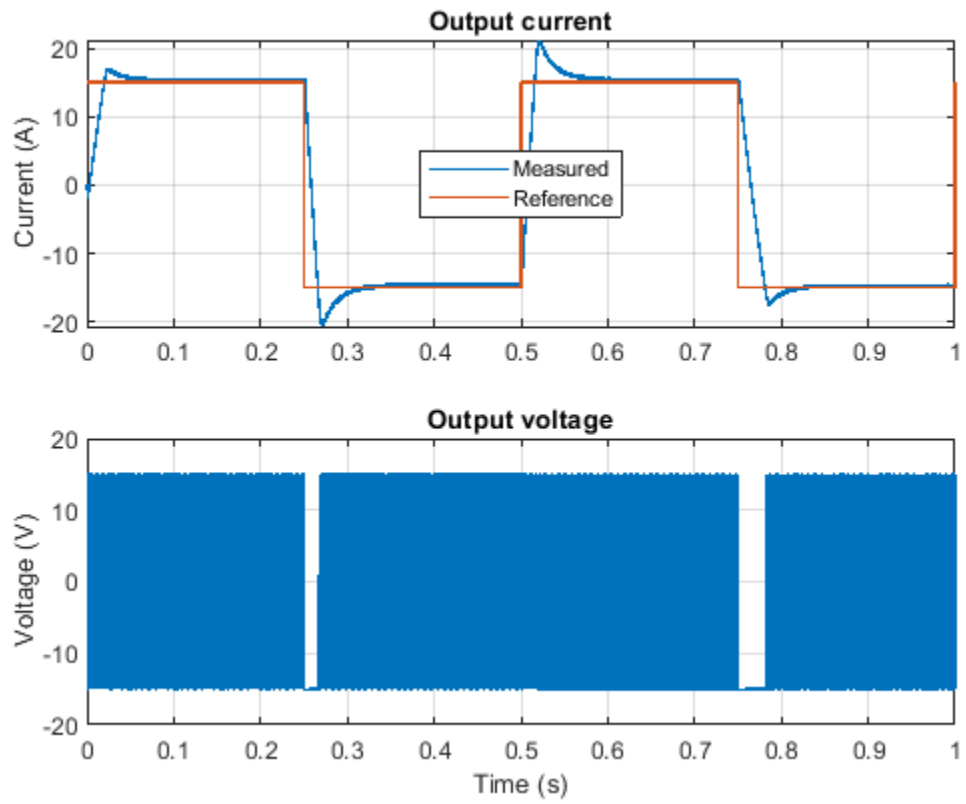
Four-Switch Buck-Boost Converter Control

This example shows how to control the output voltage of a four-switch buck-boost converter. To adjust the duty cycle, the Control subsystem uses a PI-based control algorithm. In both the boost and buck modes, one switch controls the duty cycle, one is operated inversely and the other two are kept in fix positions. The input voltage and the system load are considered constant throughout the simulation. The total simulation time (t) is 0.25 seconds. At t = 0.15 seconds, the voltage reference changes and the system switches from buck mode to boost mode.
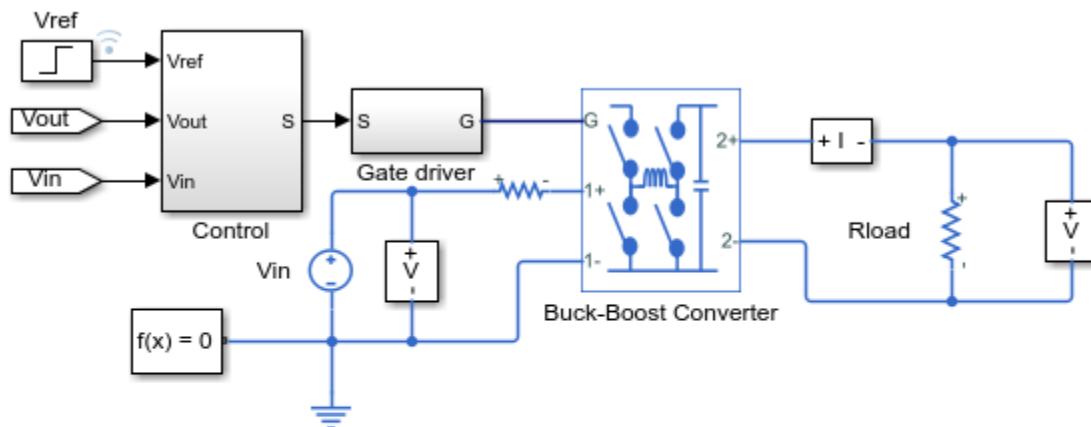
Model

Four-Switch Buck-Boost Converter Control

1. Plot voltage (see code)
2. Explore simulation results using sscexplore
3. Modify model parameters
4. Learn more about this example
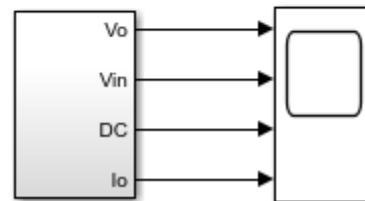
Simulation Results from Simscape Logging

The plot below shows the requested and measured voltage for the test and the input voltage in the circuit.
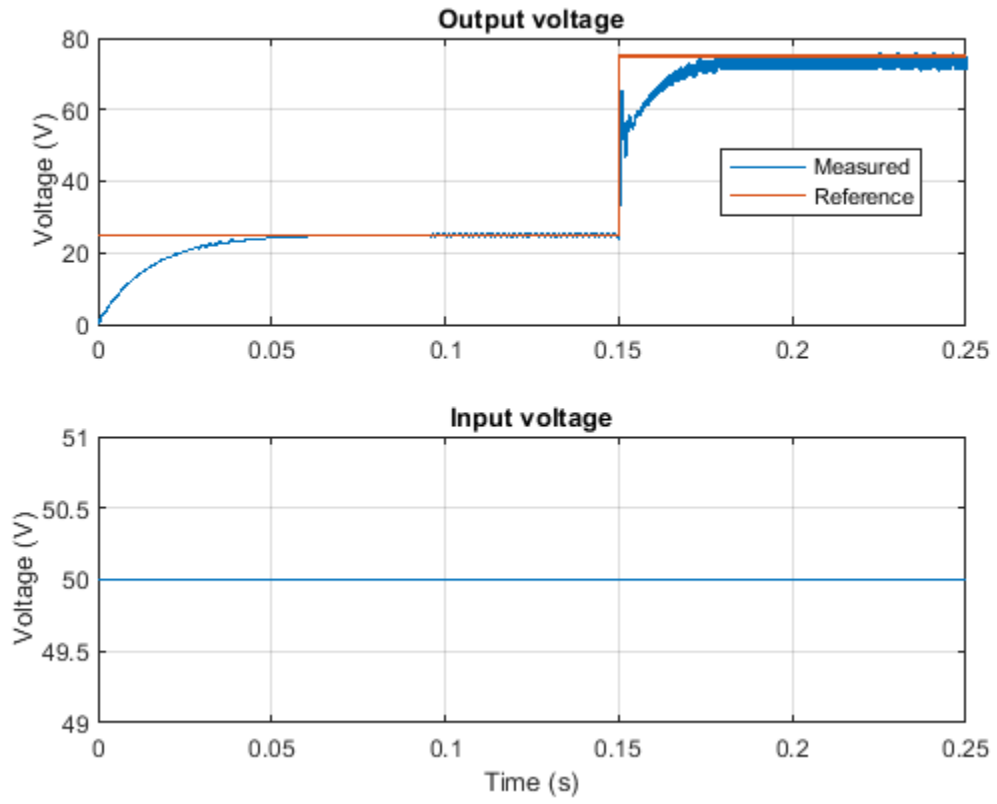
Harmonic Analysis of a Three-Phase Rectifier

This example shows how to use functions which analyze Simscape™ logging data to get harmonic magnitudes, calculate total harmonic distortion percentage and plot harmonic magnitudes. The model to which this analysis is applied is of a three-phase rectifier. The functions demonstrated are:

- ee_getHarmonics
- ee_calculateThdPercent
- ee_plotHarmonics

Open Model

Open the model.

```
open_system( 'ee_harmonics_rectifier' );
```
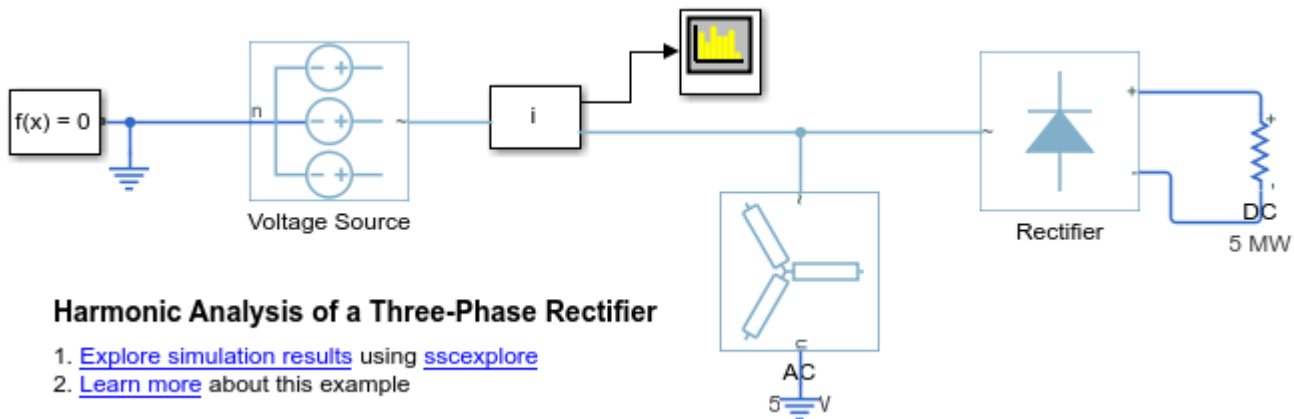

Harmonic Analysis of a Three-Phase Rectifier

1. Explore simulation results using sscexplore
2. Learn more about this example

Specification of Parameters

Where:

- Rated AC voltage, V_{Rated}V
- Rated AC frequency, F_{Rated}Hz
- AC real power load, P_{AC}W
- DC real power load, P_{DC}W
- Total apparent power, S_{Rated}VA

For the test circuit, the AC load is set to consume 5MW, and the DC load is set to consume approximately 5MW.

```
V_Rated = 4160;
F_Rated = 60;
P_AC = 5e6;
P_DC = 5e6;
S_Rated = P_AC + P_DC;
```

Calculate Source Impedance

Where:

- X/R Ratio, XR
- Per-unit impedance, Z_{pu}
- Per-unit base impedance, Z_{base}
- Per-unit base inductance, L_{base}
- Source series resistance, $R_{series}\Omega$
- Source series inductance, $L_{series}\mathrm{H}$

```
XR = 15;
Z_pu = 0.01;
Z_base = ((V_Rated/sqrt(3))^2)/(S_Rated/3);
L_base = Z_base/(2*pi*F_Rated);
R_series = cos(atan(XR))*Z_pu*Z_base;
L_series = sin(atan(XR))*Z_pu*L_base;
```

Calculate DC Resistance

Where:

- Average DC voltage calculated neglecting losses, $V_{DC}\mathrm{V}$
- DC resistance, $R_{DC}\Omega$

(Consult appendix for derivation of equations)

```
V_DC = 3*sqrt(2)*V_Rated/pi;
R_DC = V_DC^2/P_DC;
disp( [ 'DC resistance required to draw ', num2str( P_DC ), ' W on DC side = ', num2str( R_DC ),
```

```
DC resistance required to draw 5000000 W on DC side = 6.3123 Ohm
```

Use Parameters in Simscape Model

The parameters calculated can now be used in a Simscape model, ee_harmonics_rectifier. Once simulated, the model is set to create a Simscape logging variable, simlog_ee_harmonics_rectifier.

```
sim( 'ee_harmonics_rectifier' );
Voltage_Source_Currents = simlog_ee_harmonics_rectifier.Voltage_Source.I;
```
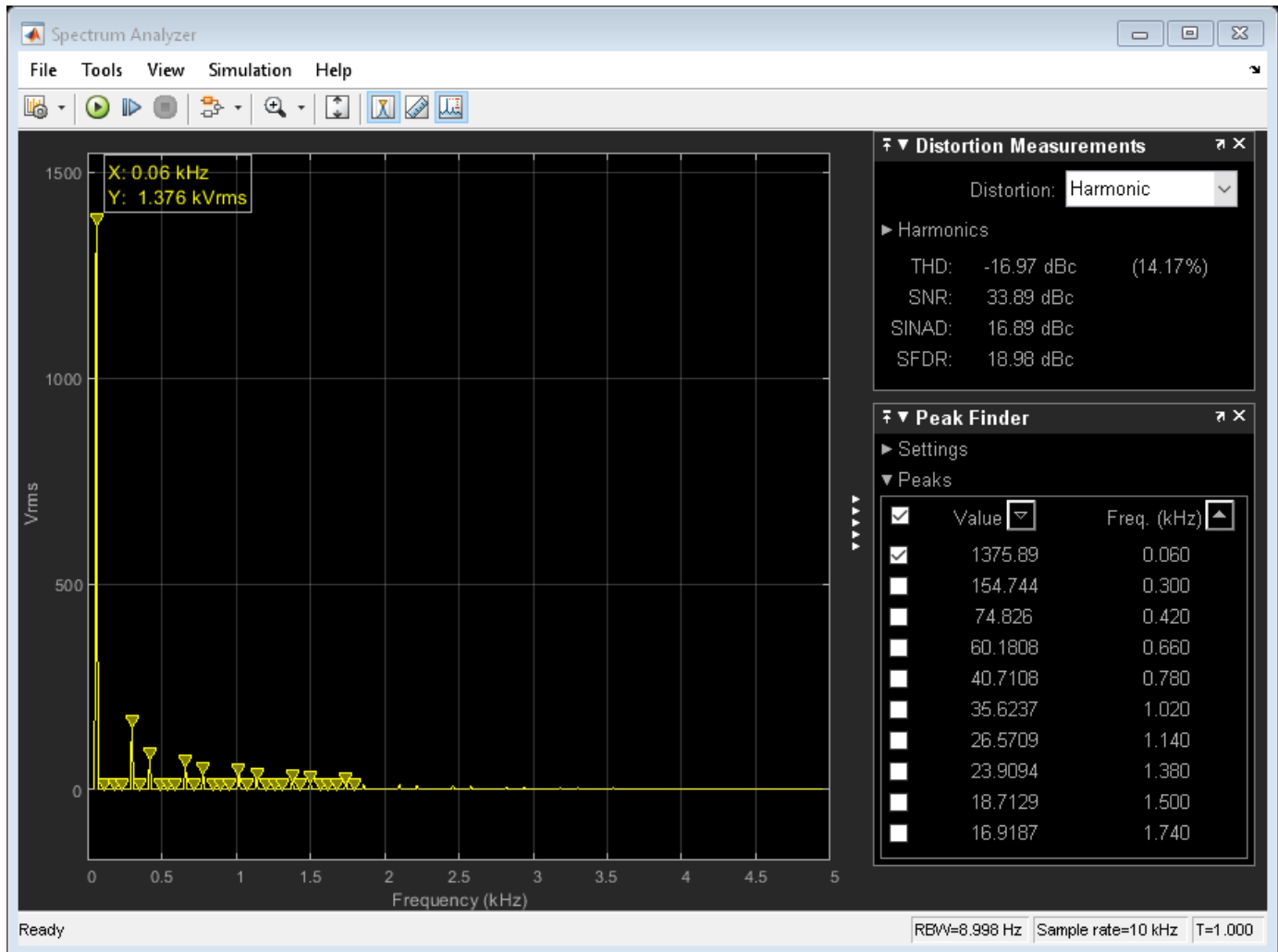
Obtain Harmonic Data

Details of harmonic order, harmonic magnitude and fundamental frequency can be obtained from a Simscape logging variable using the ee_getHarmonics function.

```
[ harmonicOrder, harmonicMagnitude, fundamentalFrequency ] = ee_getHarmonics( Voltage_Source_Curr
```

Calculate Peak Fundamental Value

The peak value of fundamental can be extracted.

```
fundamentalPeak = harmonicMagnitude( harmonicOrder==1 );
disp( [ 'Peak value of fundamental = ', num2str( fundamentalPeak ), ' A' ] );
```

Peak value of fundamental = 1945.806 A

Remove Small Harmonics

Find and keep harmonics which are greater than one thousandth of fundamental.

```
threshold = fundamentalPeak ./ 1e3;
aboveThresold = harmonicMagnitude > threshold;
```

```
harmonicOrder = harmonicOrder( aboveThresold )';
harmonicMagnitude = harmonicMagnitude( aboveThresold )';
```

Display Tabular Harmonic Data

Harmonic data can be contained in a MATLAB® table.

```
harmonicRms = harmonicMagnitude./sqrt(2);
harmonicPercentage = 100.*harmonicMagnitude./harmonicMagnitude( harmonicOrder==1 );
harmonicTable = table( harmonicOrder,...
    harmonicMagnitude,...
    harmonicRms,...
    harmonicPercentage,...
    'VariableNames', {'Order','Magnitude','RMS','Percentage'});
display( harmonicTable );
```

```
harmonicTable =

  10x4 table

    Order    Magnitude     RMS      Percentage
    _____    _____    _____    _____

      1       1945.8      1375.9        100
      5       218.86      154.75      11.248
      7       105.83      74.835       5.439
     11       85.135       60.2       4.3753
     13       57.599      40.729      2.9602
     17       50.417       35.65      2.5911
     19       37.612      26.596       1.933
     23       33.859      23.942      1.7401
     25       26.507      18.743      1.3622
     29       23.979      16.955      1.2323
```

Total Harmonic Distortion

Calculate Total Harmonic Distortion (THD) percentage from harmonic data using the ee_calculate_ThdPercent function.

```
thdPercent = ee_calculateThdPercent( harmonicOrder, harmonicMagnitude );
disp( [ 'Total Harmonic Distortion percentage = ' num2str( thdPercent ), ' %' ] );
```

```
Total Harmonic Distortion percentage = 14.1721 %
```

Plot Harmonics

The harmonic data could be plotted from the harmonic data using the MATLAB bar function. For convenience the ee_plotHarmonics function plots a bar chart directly from the Simscape logging variable.

```
ee_plotHarmonics( Voltage_Source_Currents );
h1_ee_rectifier_thd = gcf;
```

Fundamental frequency 60 Hz = 1945.806. THD = 14.1721%.
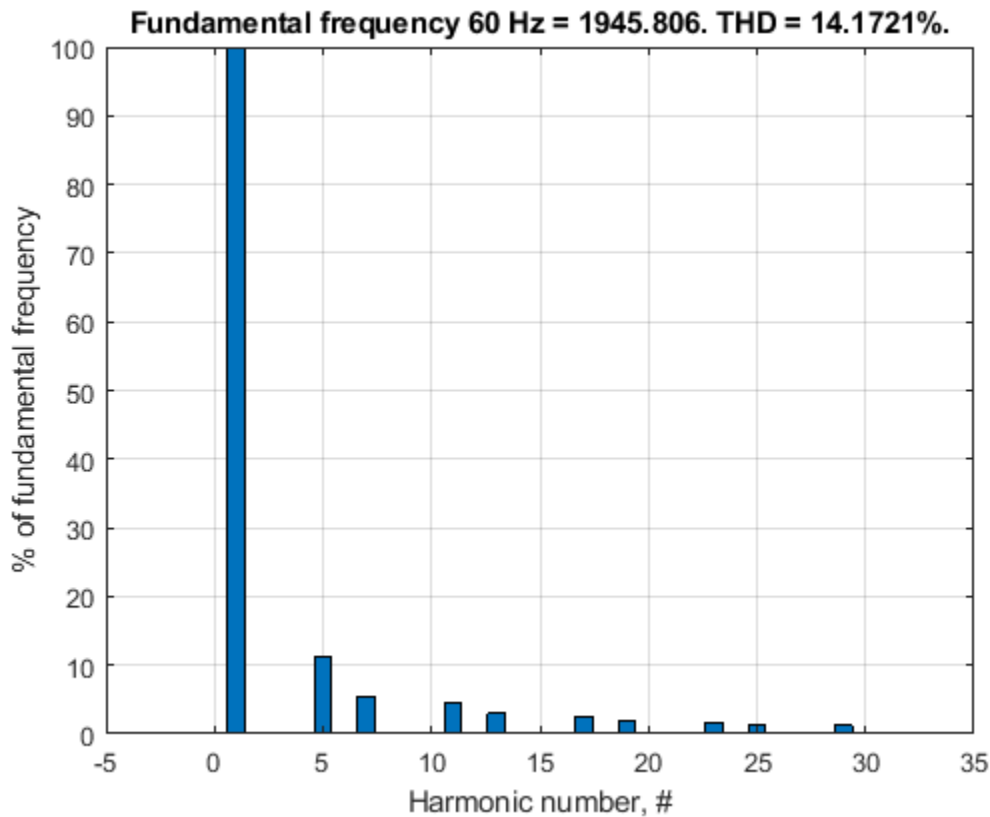
Conclusion

This example shows how to use three functions which are callable from the MATLAB command line. The functions analyze Simscape logging data to get harmonic magnitudes, calculate total harmonic distortion percentage and plot harmonic magnitudes.

Appendix - Equations for Calculation of DC Resistance Value

The relationship between peak AC input, V_P, and average DC output, V_{DC}, of a three-phase rectifier, neglecting losses, can be calculated as follows:

$$V_{DC} = \tfrac{1}{2\pi/6} \int_{2\pi/3}^{\pi/3} \sqrt{3} V_P \sin(\omega t)\, \mathrm{d}(\omega t)$$

$$V_{DC} = \tfrac{3\sqrt{3}}{\pi} V_P$$

As the relationship between the rated voltage (line-line RMS), V_{Rated}, and peak phase voltage, V_P, is:

$$V_P = \tfrac{\sqrt{2}}{\sqrt{3}} V_{Rated}$$

The relationship between rated AC voltage and average DC voltage is:

$$V_{DC} = \tfrac{3\sqrt{2}}{\pi} V_{Rated}$$

The resistance, R required to draw a particular power, P, is:

$$R = \frac{V_{DC}^2}{P}$$

HESM Torque Control

This example shows how to control the torque in a hybrid excitation synchronous machine (HESM) based electrical-traction drive. Permanent magnets and an excitation winding excite the HESM. A high-voltage battery feeds the SM through a controlled three-phase converter for the stator windings and through a controlled four quadrant chopper for the rotor winding. An ideal angular velocity source provides the load. The Control subsystem uses an open-loop approach to control the torque and a closed-loop approach to control the current. At each sample instant, the torque request is converted to relevant current references. The current control is PI-based. The simulation uses several torque steps in both the motor and generator modes. The Visualization subsystem contains scopes that allow you to see the simulation results.

Model

HESM Torque Control

1. Modify model parameters
2. Explore simulation results using sscexplore
3. Learn more about this example

HESM Velocity Control

This example shows how to control the rotor angular velocity in a hybrid excitation synchronous machine (HESM) based electrical-traction drive. Permanent magnets and an excitation winding excite the HESM. A high-voltage battery feeds the HESM through a controlled three-phase converter for the stator windings and through a controlled four quadrant chopper for the rotor winding. An ideal torque source provides the load. The Control subsystem includes a multi-rate PI-based cascade control structure. The control structure has an outer angular-velocity-control loop and three inner current-control loops. The Visualization subsystem contains scopes that allow you to see the simulation results.

Model

HESM Velocity Control

1. Modify model parameters
2. Explore simulation results using sscexplore
3. Learn more about this example
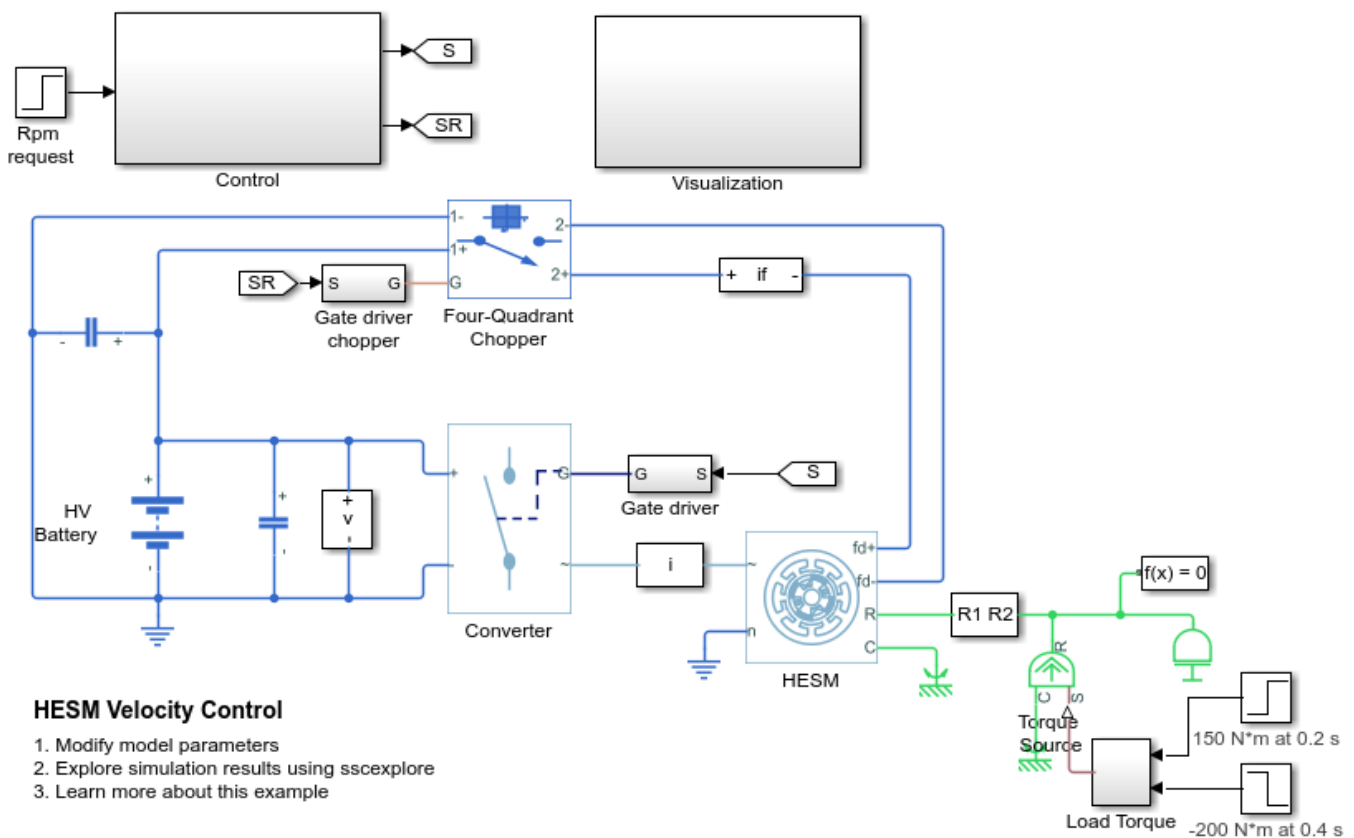
HV Battery Charge/Discharge

This example shows a high-voltage battery like those used in hybrid electric vehicles. The model uses a realistic DC-link current profile, which originates from a dynamic driving cycle. The total simulation time is 3600 seconds.

Model
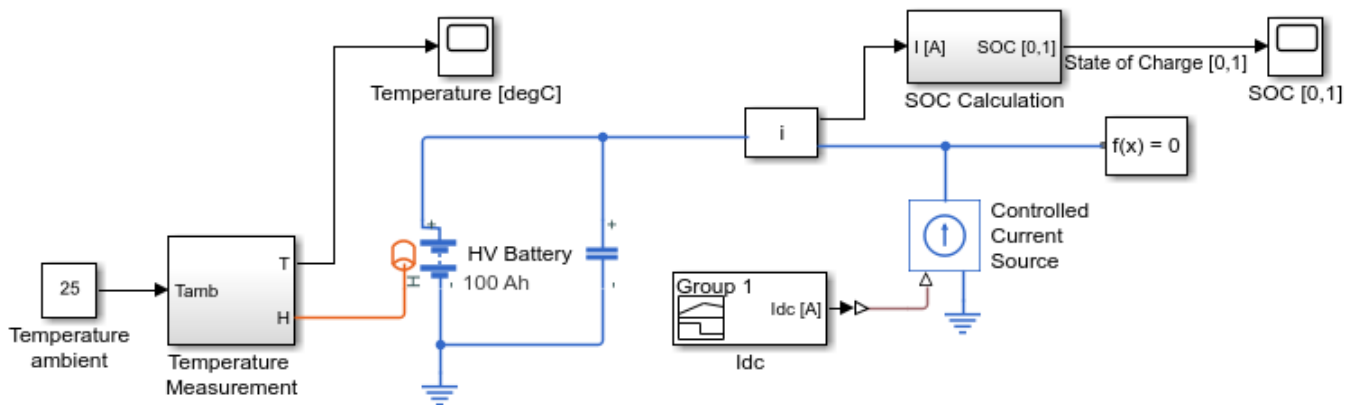
HV Battery Charge/Discharge
1. Plot DC-link voltage and current (see code)
2. Modify model parameters
3. Explore simulation results using sscexplore
4. Learn more about this example

Simulation Results from Simscape Logging

The plot below shows the battery voltage and output current.
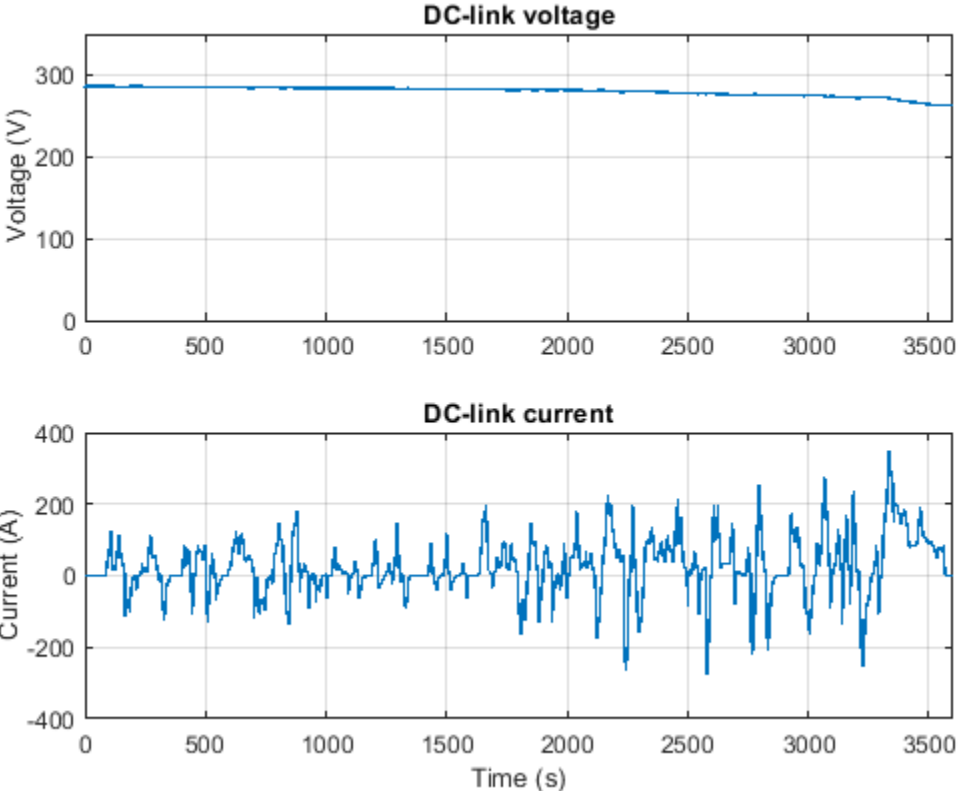
Quantifying IGBT Thermal Losses

This example shows the generation of a temperature profile based on switching and conduction losses in an insulated-gate bipolar transistor (IGBT). There are two buck converters. For one converter, the IGBT attaches to a Foster thermal model. For the other converter, the IGBT attaches to a Cauer thermal model. The parameters for the thermal models are tuned to give roughly equivalent results. At a simulation time of 50ms, the driving frequency changes from 40kHz to 20kHz, which increases the conduction losses and decreases the switching losses. The change in the losses results in a corresponding change in the temperature of the IGBT.

Model
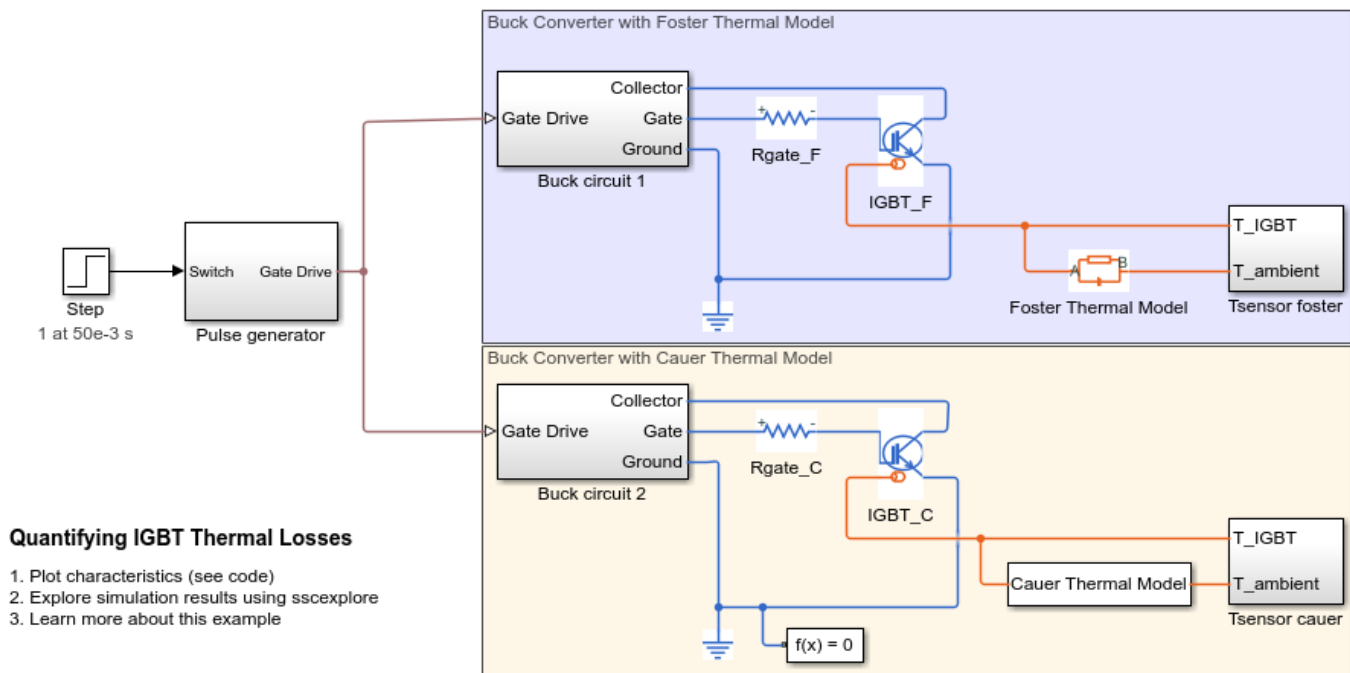
Simulation Results from Simscape Logging

The plot below shows the IGBT temperature and energy loss as functions of time for both the Foster and Cauer thermal models. The power loss is the slope of the energy-loss curve.
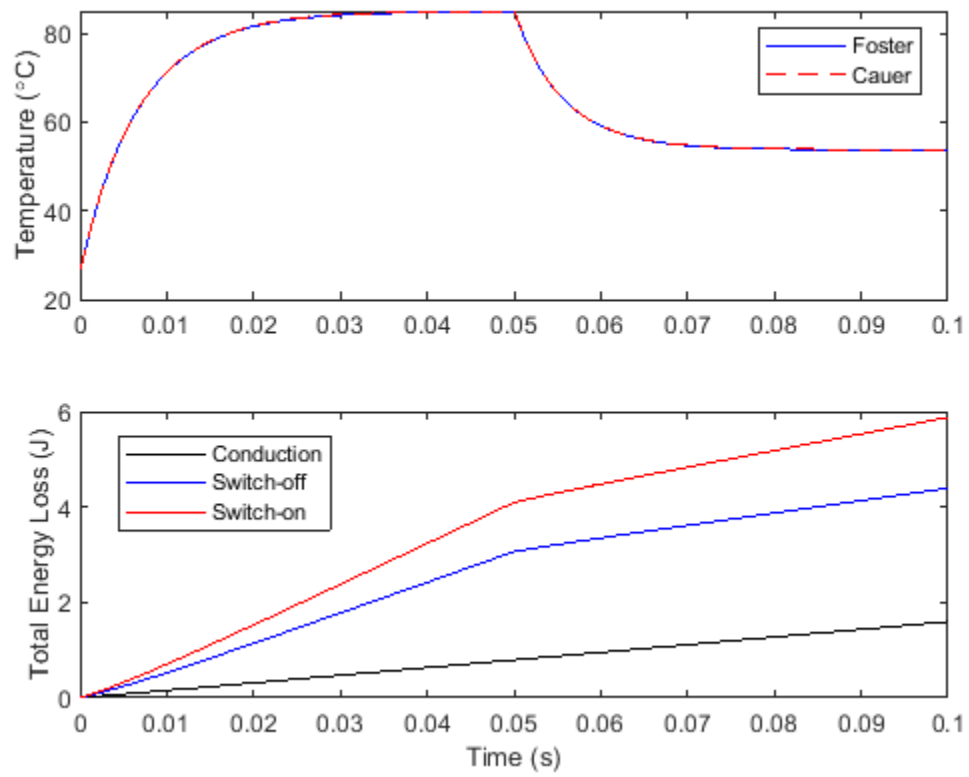
Inverting Topology Buck-Boost Converter Control

This example shows how to control the output voltage of an inverting topology buck-boost converter. The inverting topology buck-boost converter uses only a single switch and the output voltage is of the opposite polarity than the input. To adjust the duty cycle, the Control subsystem uses a PI-based control algorithm. The input voltage and the system load are considered constant throughout the simulation. The total simulation time (t) is 0.25 seconds. At t = 0.15 seconds, the voltage reference changes and the system switches from buck mode to boost mode.
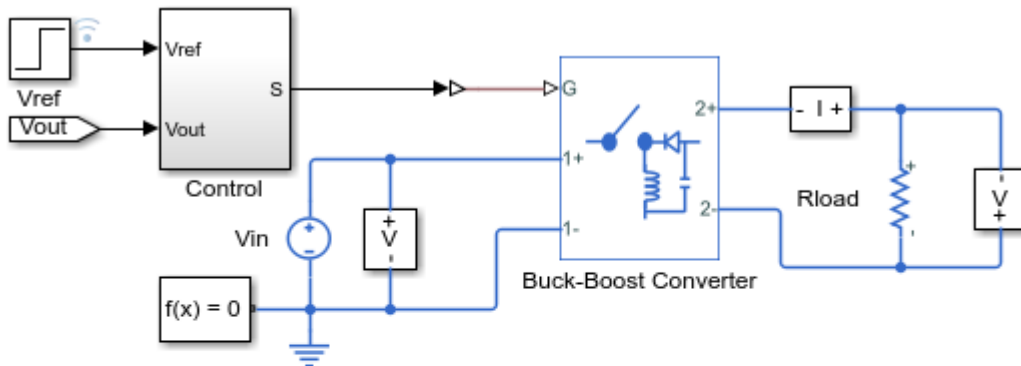
Model

Inverting Topology Buck-Boost Converter Control

1. Plot voltage (see code)
2. Explore simulation results using sscexplore
3. Modify model parameters
4. Learn more about this example
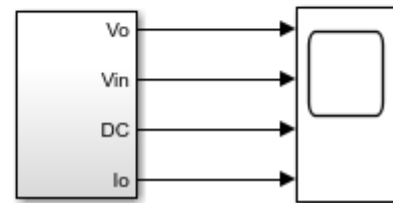
Simulation Results from Simscape Logging

The plot below shows the requested and measured voltage for the test and the input voltage in the circuit.
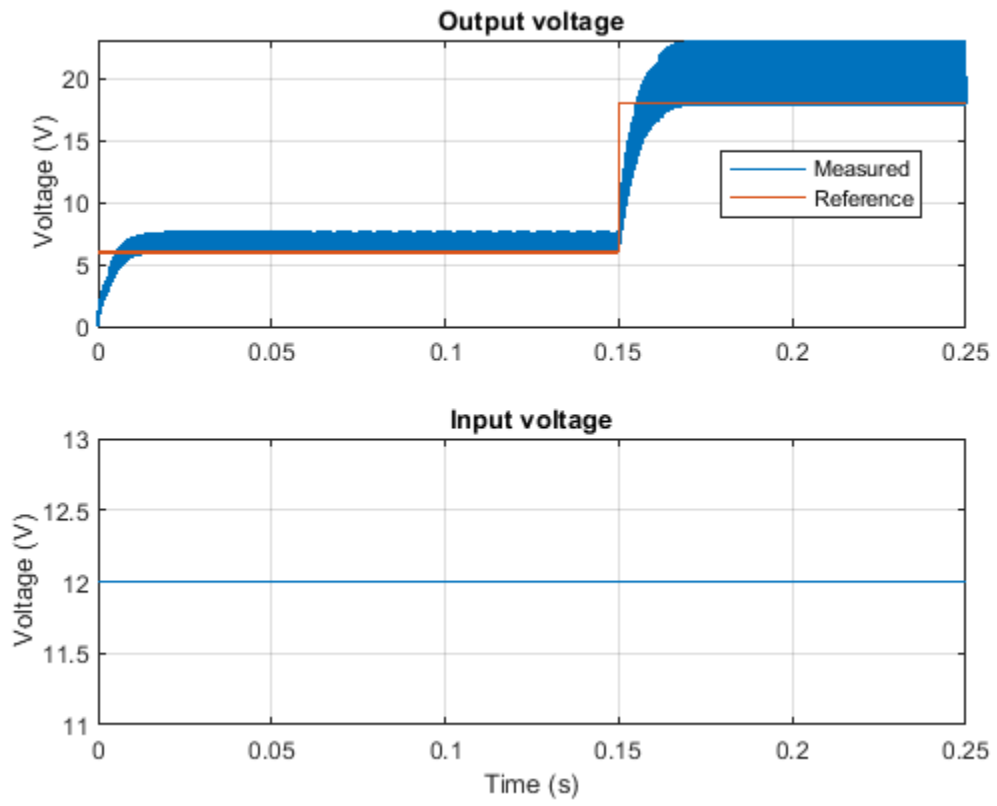
IPMSM Outer Loop Controller Evaluation

This example shows how to control the rotor angular velocity in an interior permanent magnet synchronous machine (IPMSM) based electrical-traction drive. For evaluation of the outer loop velocity controller, the inner loop current controller is replaced by a three-phase controlled current source. An ideal torque source provides the load. The Scopes subsystem contains scopes that allow you to see the simulation results. The Control subsystem includes a PI-based outer loop controller. During the three-seconds simulation, the angular velocity demand is -1000, 2000, 3000, 4000, and then 5000 rpm. Above 1630 rpm, the IPMSM enters the field weakening mode.

Model

IPMSM Outer Loop Controller Evaluation

1. Plot speed of motor (see code)
2. Explore simulation results using sscexplore
3. Modify model parameters
4. Learn more about this example

Simulation Results from Simscape Logging

The plot below shows the requested and measured speed for the test, as well as the phase currents in the electric drive.
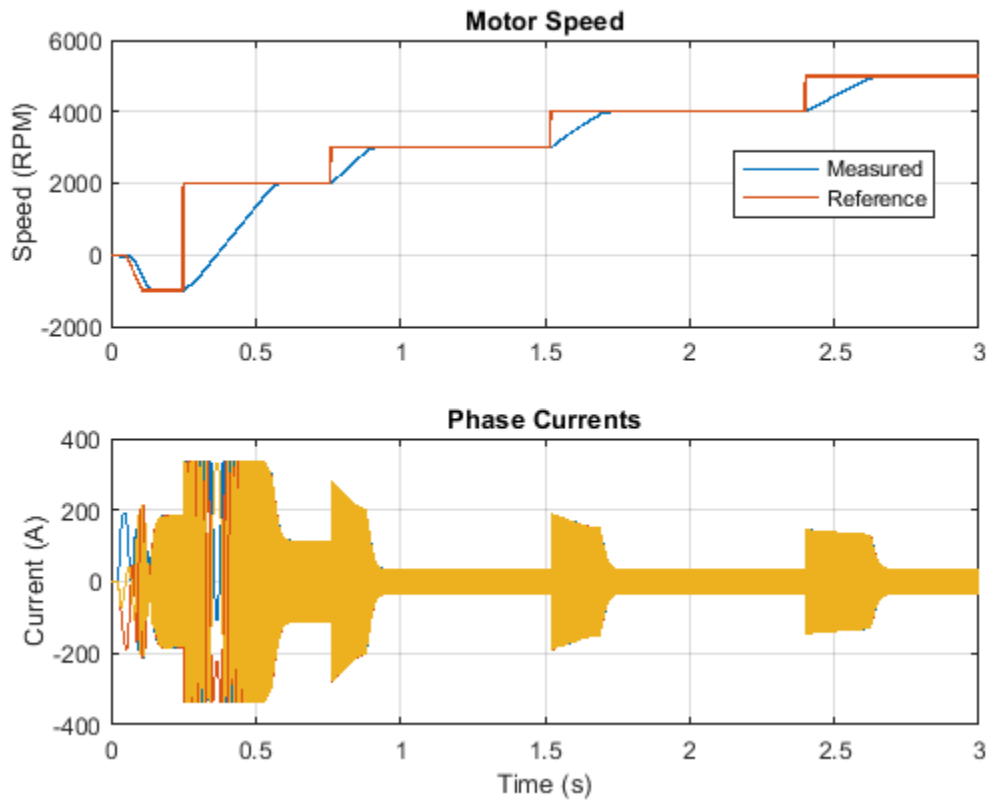
IPMSM Torque-Based Load Control

This example shows how to control the torque in an interior permanent magnet synchronous motor (IPMSM) based drive. A high-voltage battery feeds the IPMSM through a controlled three-phase inverter. A ramp of torque request is provided to the motor controller. The load torque is quadratically dependent on the rotor speed. The Control subsystem uses an open-loop approach to control the IPMSM torque and a closed-loop approach to control the current. At each sample instant, the torque request is converted to relevant current references. The current control is PI-based and uses a sample rate that is faster than the rate that is used for torque control. The task scheduling is designed in Stateflow®. The Scopes subsystem contains scopes that allow you to see the simulation results.

Model
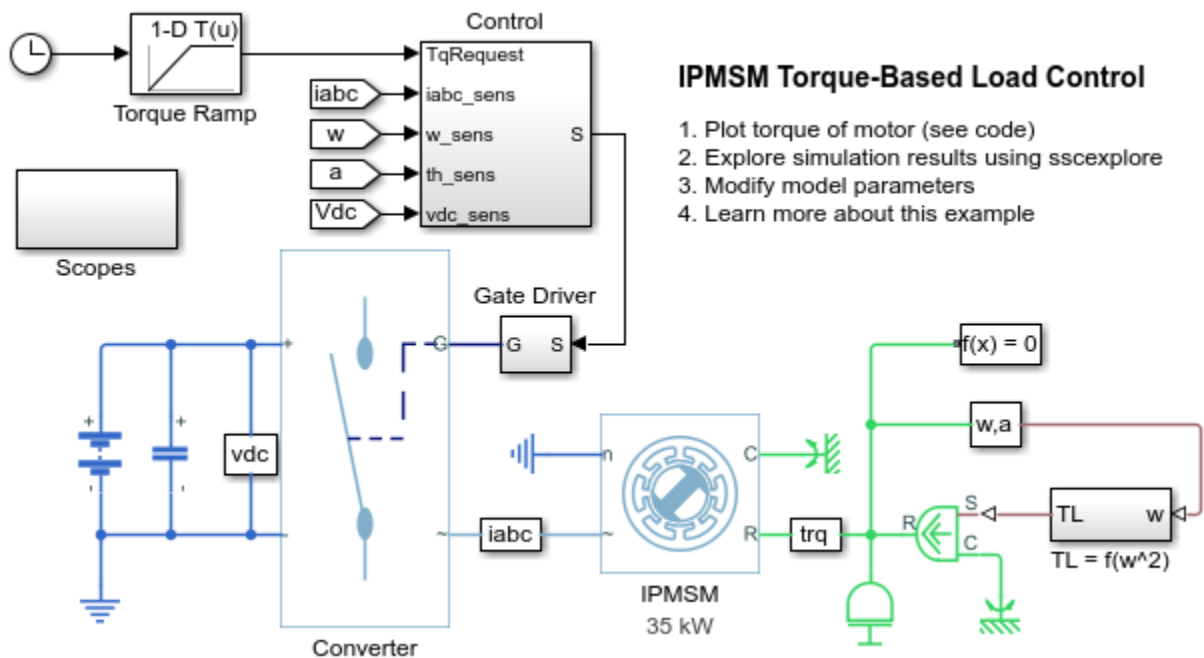
Simulation Results from Simscape Logging

The plot below shows the requested and measured torque for the test, as well as the rotor speed in the electric drive.

IPMSM Torque Control

This example shows how to control the torque in an interior permanent magnet synchronous machine (IPMSM) based automotive electrical-traction drive. A high-voltage battery feeds the IPMSM through a controlled three- phase converter. The IPMSM operates in both motoring and generating modes according to the load. An ideal angular velocity source provides the load. The Control subsystem uses an open-loop approach to control the IPMSM torque and a closed-loop approach to control the current. At each sample instant, the torque request is converted to relevant current references. The current control is PI-based and uses a sample rate that is faster than the rate that is used for torque control. The simulation uses several torque steps in both motor and generator modes. The task scheduling is designed in Stateflow®. The Scopes subsystem contains scopes that allow you to see the simulation results.

Model
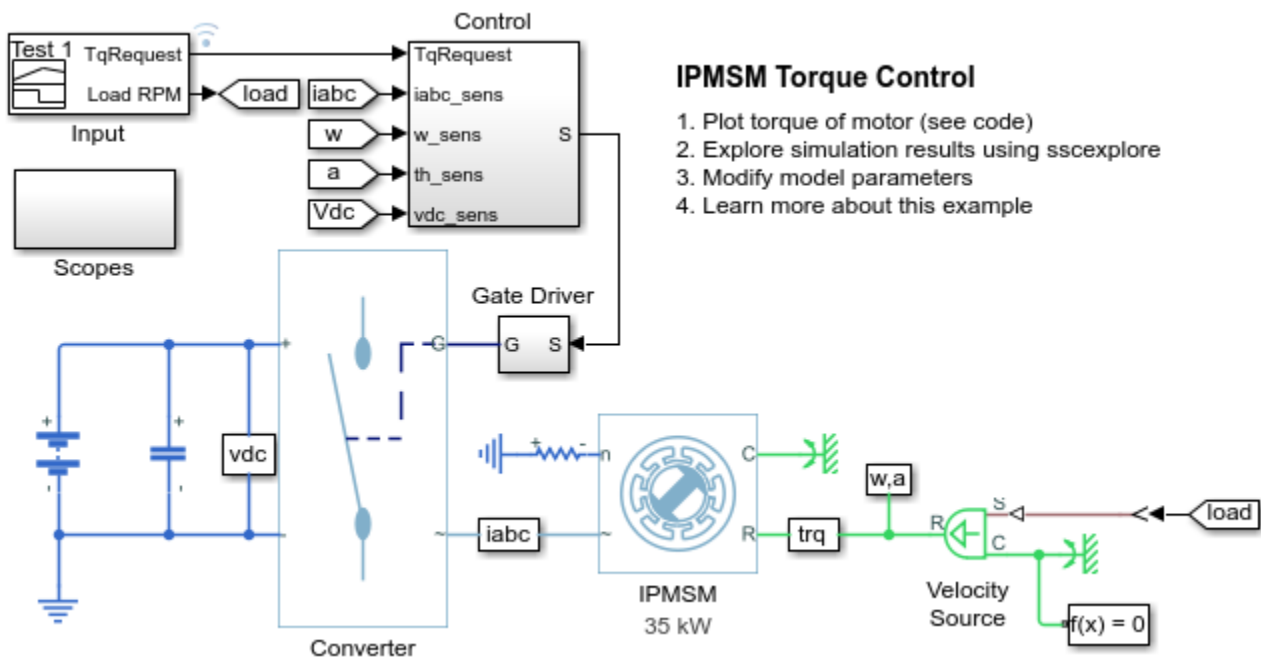
Simulation Results from Simscape Logging

The plot below shows the requested and measured torque for the test, as well as the phase currents in the electric drive.
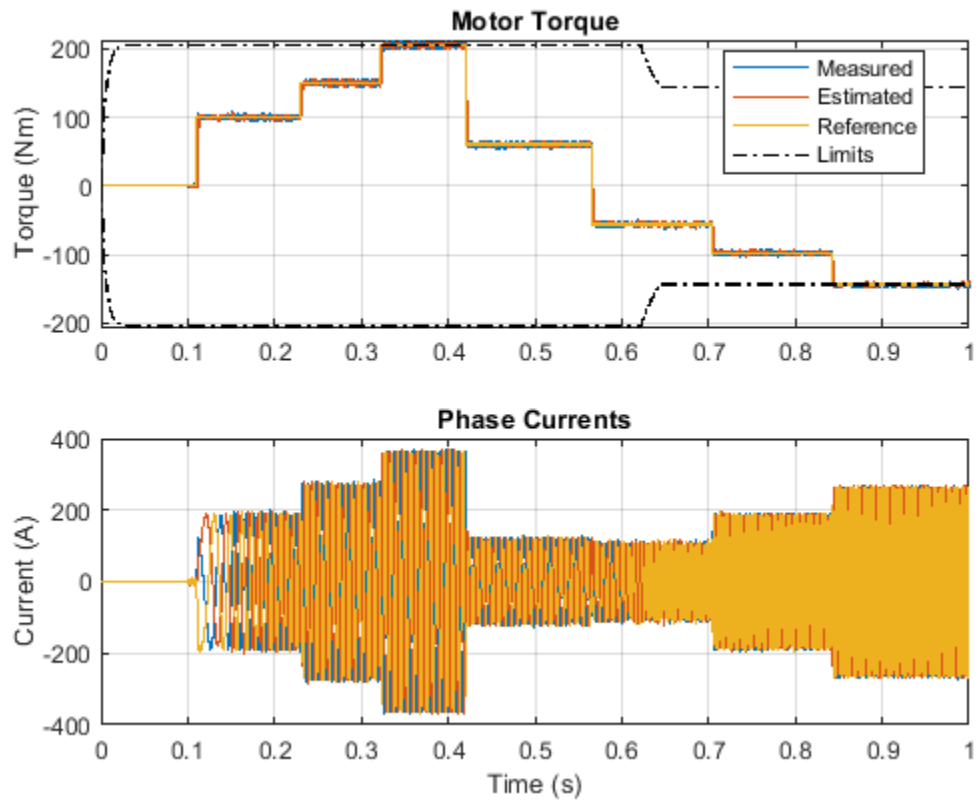
IPMSM Torque Control in an Axle-Drive EV

This example shows an interior permanent magnet synchronous machine (IPMSM) propelling a simplified axle-drive electric vehicle. A high-voltage battery feeds the IPMSM through a controlled three-phase converter. The IPMSM operates in both motoring and generating modes. The vehicle transmission and differential are implemented using a fixed-ratio gear reduction model. The Vehicle Controller subsystem converts the driver inputs into a relevant torque command. The Drive Controller subsystem controls the torque of the IPMSM. The controller includes a multi-rate PI-based control structure. The rate of the open-loop torque control is slower than the rate of the closed-loop current control. The task scheduling for the controller is implemented as a Stateflow® state machine. The Scopes subsystem contains scopes that allow you to see the simulation results.
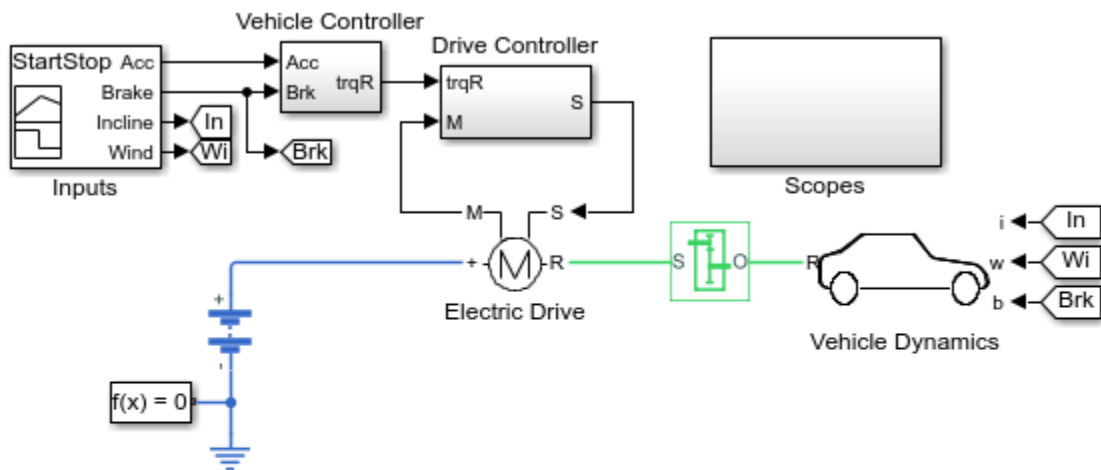
Model

IPMSM Torque Control in an Axle-Drive EV

1. Plot torque of electric drive (see code)
2. Explore simulation results using sscexplore
3. Modify model parameters
4. Learn more about this example

Electric Drive Subsystem
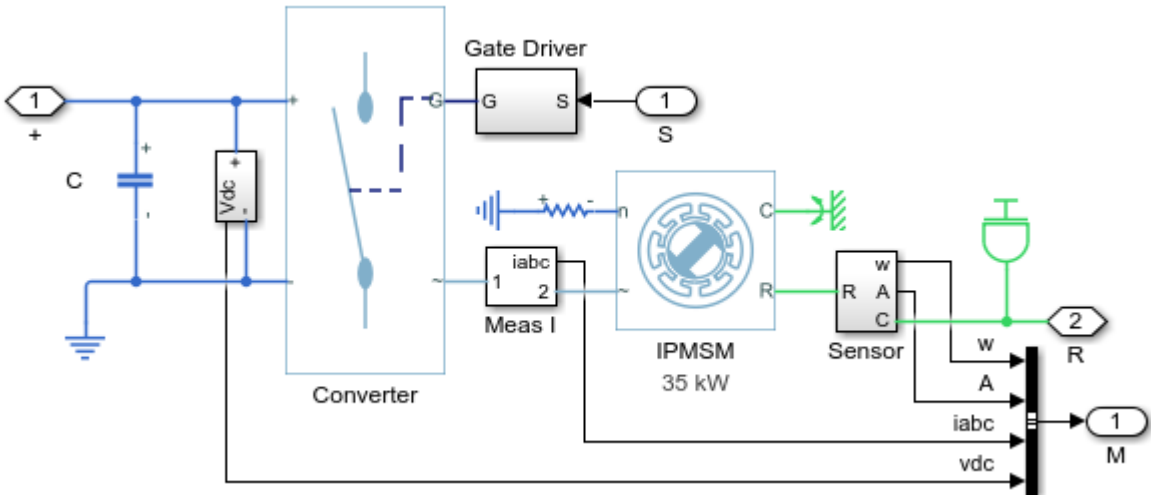
Simulation Results from Simscape Logging

The plot below shows the requested and measured torque for the test, as well as the phase currents in the electric drive.

IPMSM Torque Control in a Series HEV

This example shows an interior permanent magnet synchronous machine (IPMSM) propelling a simplified series hybrid electric vehicle (HEV). An ideal DCDC converter, connected to a high-voltage battery, feeds the IPMSM through a controlled three-phase converter. A combustion engine driven generator charges the high-voltage battery. The vehicle transmission and differential are implemented using a fixed-ratio gear-reduction model. The Vehicle Controller subsystem converts the driver inputs into relevant commands for the IPMSM and generator. The Drive Controller subsystem controls the torque of the IPMSM. The controller includes a multi-rate PI-based control structure. The rate of the open-loop torque control is slower than the rate of the closed-loop current control. The task scheduling for the controller is implemented as a Stateflow® state machine. The Scopes subsystem contains scopes that allow you to see the simulation results.

Model

IPMSM Torque Control in a Series HEV

1. Plot torque of electric drive (see code)
2. Explore simulation results using sscexplore
3. Modfiy model parameters
4. Learn more about this example

Electric Drive Subsystem
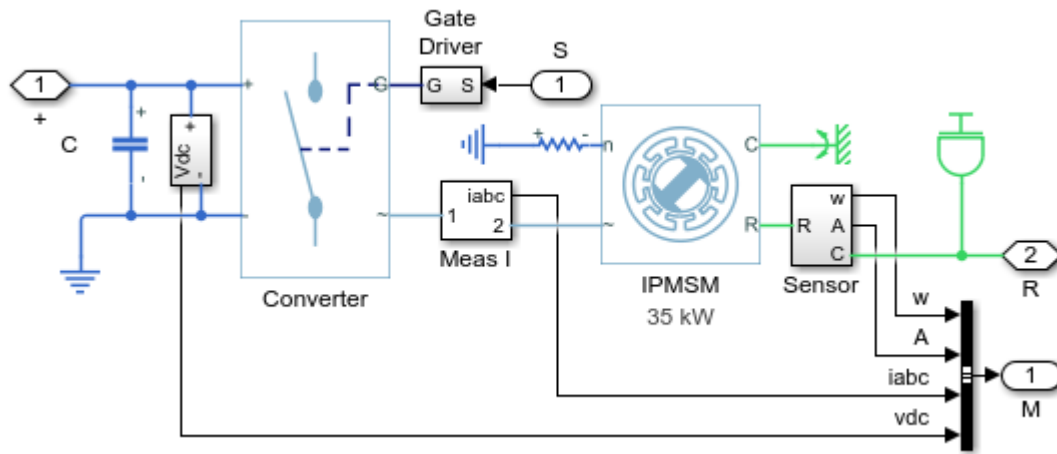
Simulation Results from Simscape Logging

The plot below shows the requested and measured torque for the test, as well as the phase currents in the electric drive.
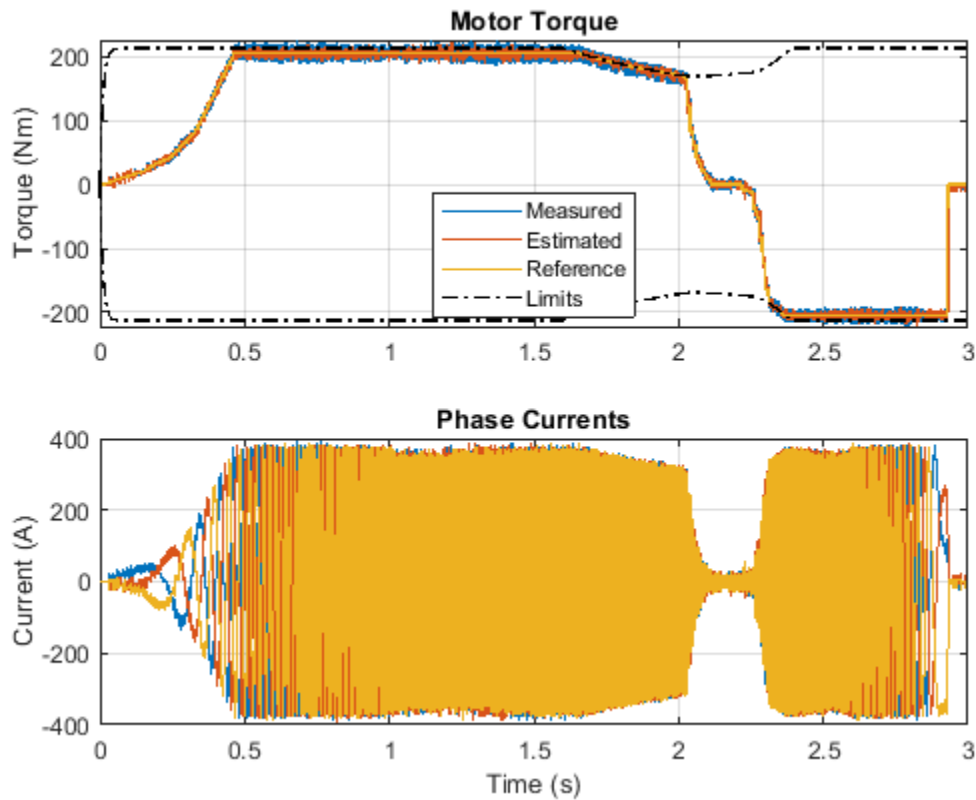
IPMSM Torque Control in a Parallel HEV

This example shows a simplified parallel hybrid electric vehicle (HEV). An interior permanent magnet synchronous machine (IPMSM) and an internal combustion engine (ICE) provide the vehicle propulsion. The IPMSM operates in both motoring and generating modes. The vehicle transmission and differential are implemented using a fixed-ratio gear-reduction model. The Vehicle Controller subsystem converts the driver inputs into torque commands. The vehicle control strategy is implemented as a Stateflow® state machine. The ICE Controller subsystem controls the torque of the combustion engine. The Drive Controller subsystem controls the torque of the IPMSM. The Scopes subsystem contains scopes that allow you to see the simulation results.

Model

IPMSM Torque Control in a Parallel HEV

1. Plot torque of electric drive (see code)
2. Explore simulation results using sscexplore
3. Modfiy model parameters
4. Learn more about this example

Electric Drive Subsystem
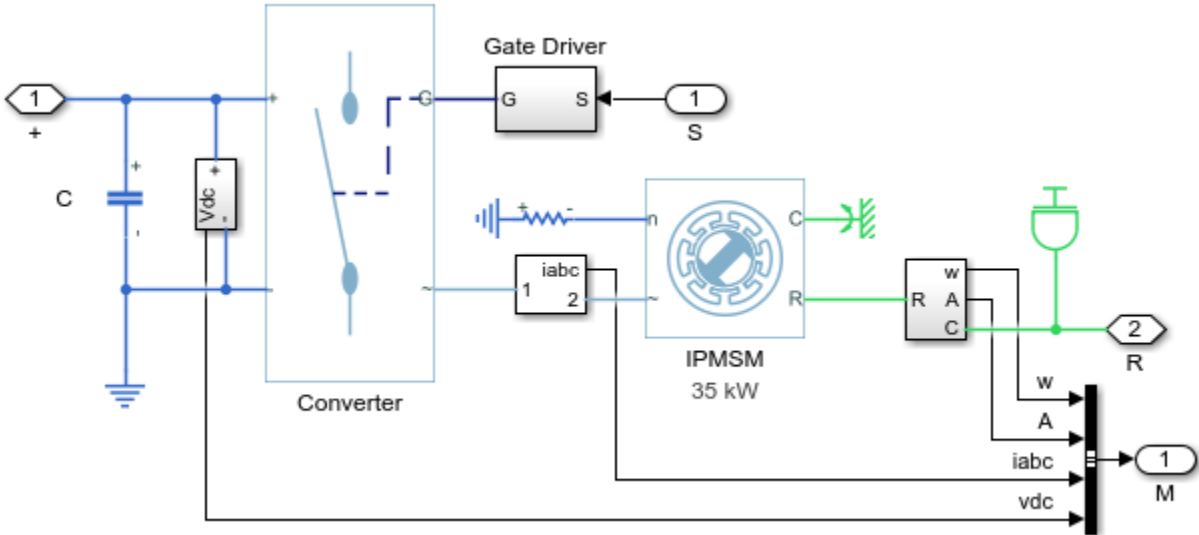
Simulation Results from Simscape Logging

The plot below shows the requested and measured torque for the test, as well as the phase currents in the electric drive.
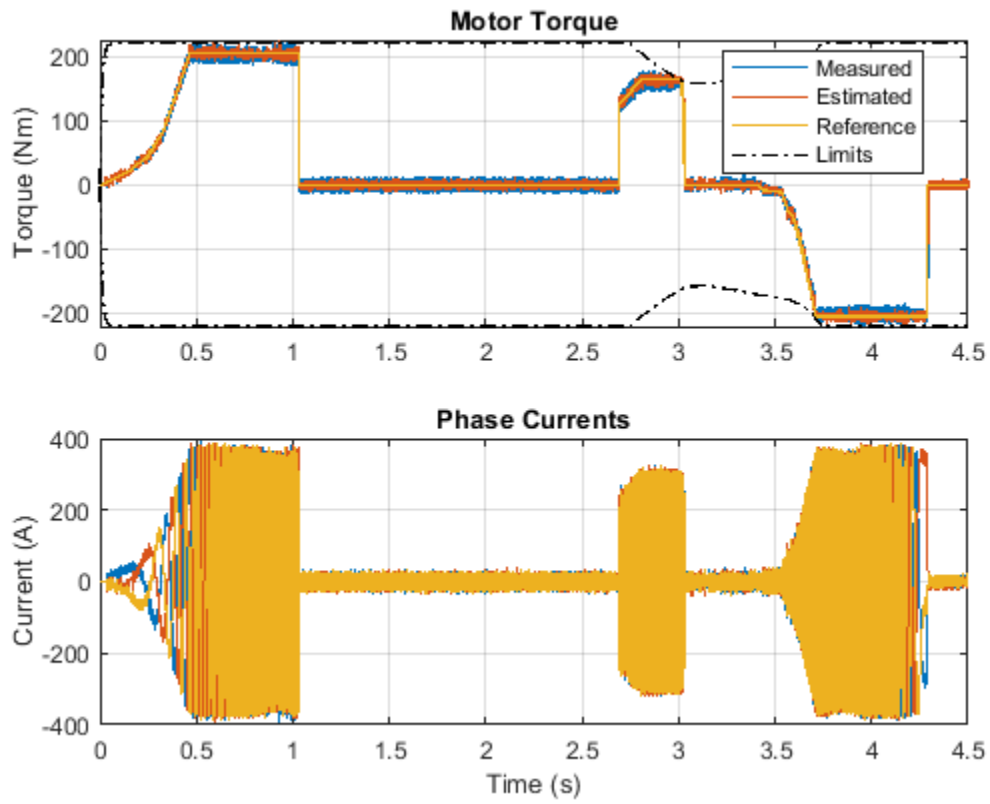
IPMSM Torque Control in a Series-Parallel HEV

This example shows a simplified series-parallel hybrid electric vehicle (HEV). An interior permanent magnet synchronous machine (IPMSM) and an internal combustion engine (ICE) provide the vehicle propulsion. The ICE also uses electric generator to recharge the high-voltage battery during driving. The vehicle transmission and differential are implemented using a fixed-ratio gear-reduction model. The Vehicle Controller subsystem converts the driver inputs into torque commands. The vehicle control strategy is implemented as a Stateflow® state machine. The ICE Controller subsystem controls the torque of the combustion engine. The Generator Controller subsystem controls the torque of the electric generator. The Drive Controller subsystem controls the torque of the IPMSM. The Scopes subsystem contains scopes that allow you to see the simulation results.

Model

IPMSM Torque Control in a Series-Parallel HEV

1. Plot torque of electric drive (see code)
2. Explore simulation results using sscexplore
3. Modfiy model parameters
4. Learn more about this example

Electric Drive Subsystem
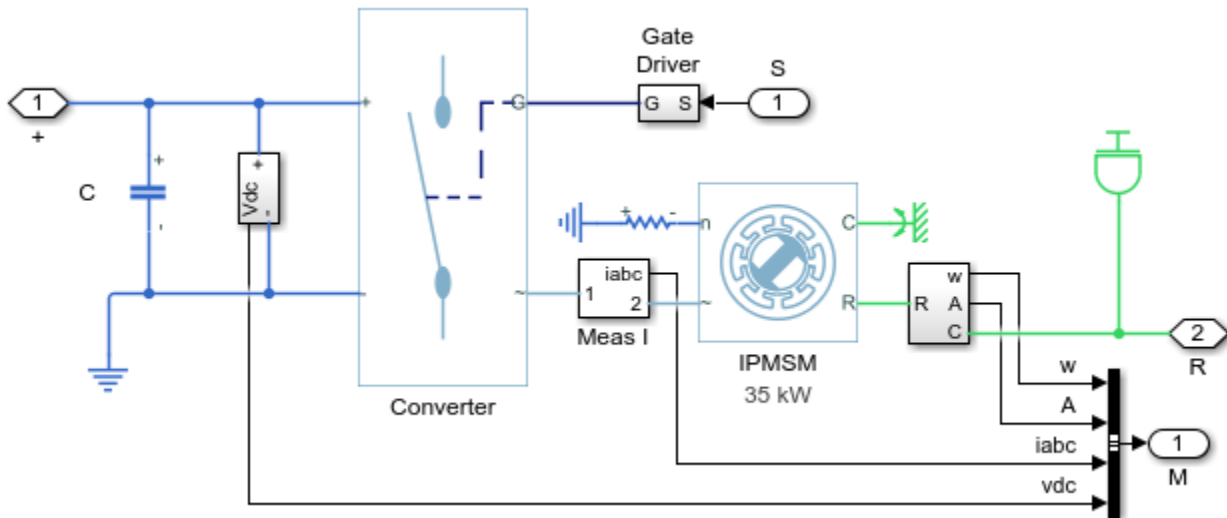
Simulation Results from Simscape Logging

The plot below shows the requested and measured torque for the test, as well as the phase currents in the electric drive.

IPMSM Velocity Control

This example shows how to control the rotor angular velocity in an interior permanent magnet synchronous machine (IPMSM) based automotive electrical-traction drive. A high-voltage battery feeds the IPMSM through a controlled three-phase converter. The IPMSM operates in both motoring and generating modes according to the load. An ideal torque source provides the load. The Scopes subsystem contains scopes that allow you to see the simulation results. The Control subsystem includes a multi-rate PI-based cascade control structure which has an outer angular-velocity-control loop and two inner current-control loops. The task scheduling in the Control subsystem is implemented as a Stateflow® state machine. During the one-second simulation, the angular velocity demand is 0 rpm, 500 rpm, 2000 rpm, and then 3000 rpm. Above 1630 rpm, the IPMSM enters in field weakening mode.
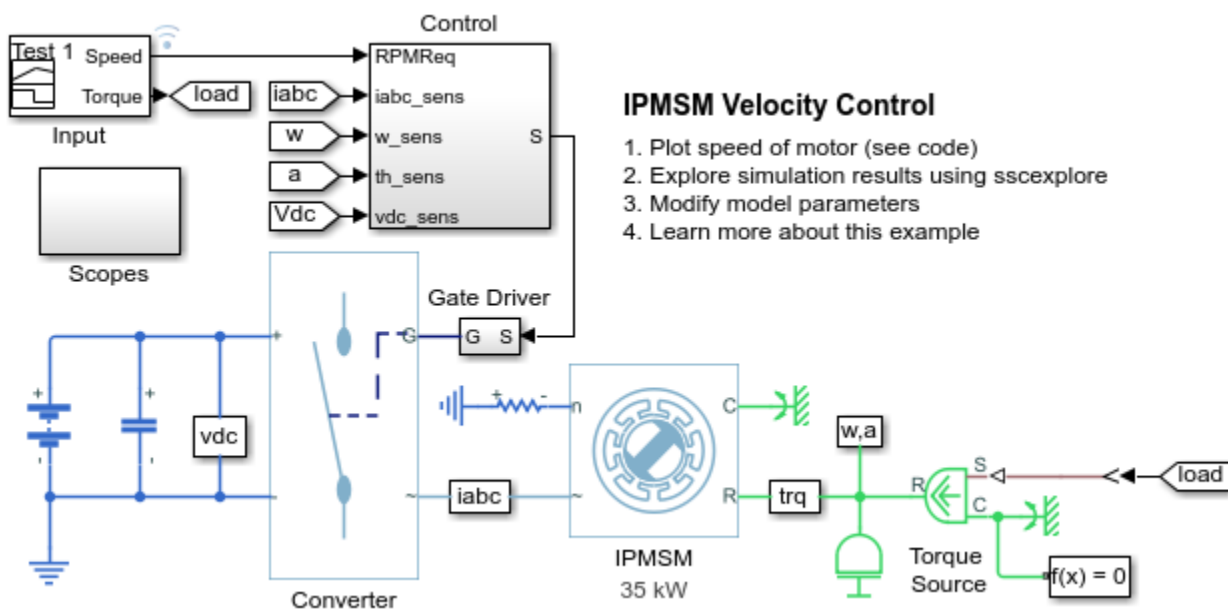
Model

Simulation Results from Simscape Logging

The plot below shows the requested and measured speed for the test, as well as the phase currents in the electric drive.
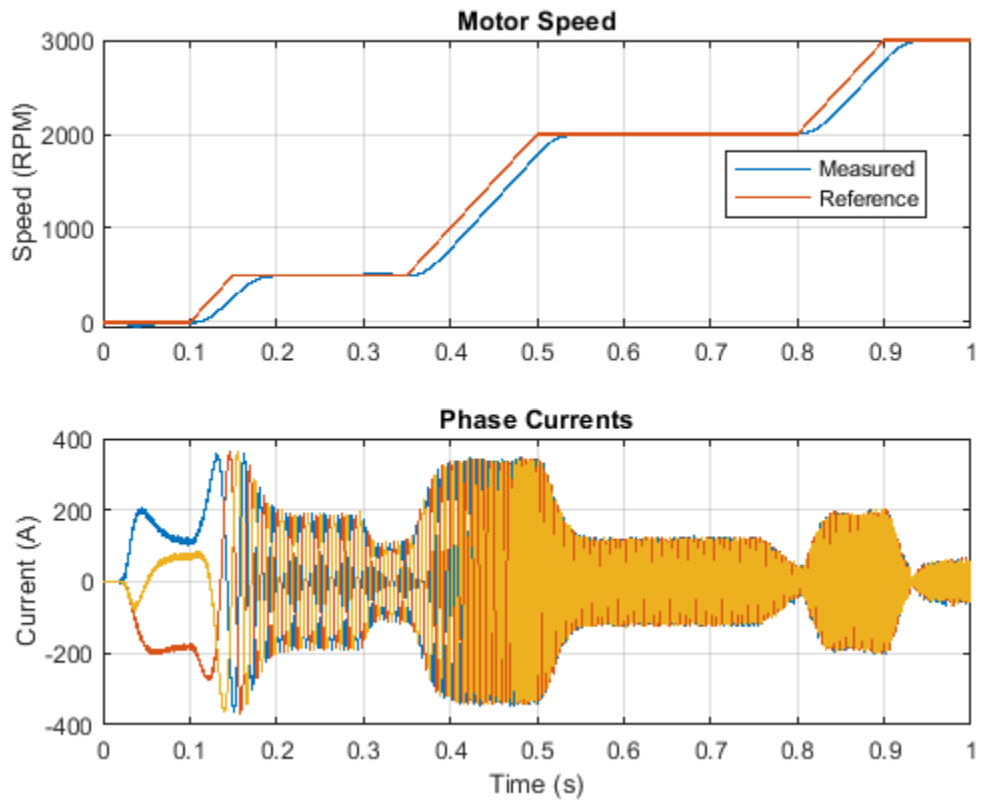
IPMSG Voltage Stabilization

This example shows how to control an Interior Permanent Magnet Synchronous Generator (IPMSG) based low voltage generator system for a hybrid electric vehicle (HEV). The Control subsystem includes a multi-rate PI-based cascade control structure which has an outer voltage-control loop and two inner current-control loops. The task scheduling in the Control subsystem is implemented as a Stateflow® state machine. The Scopes subsystem contains scopes that allow you to see the simulation results. An ideal angular velocity source, which represents a combustion engine, drives the IPMSG. The IPMSG supplies low-voltage power to loads R1 and R2. At t = 0.4 seconds, the switch closes, increasing the load.

Model
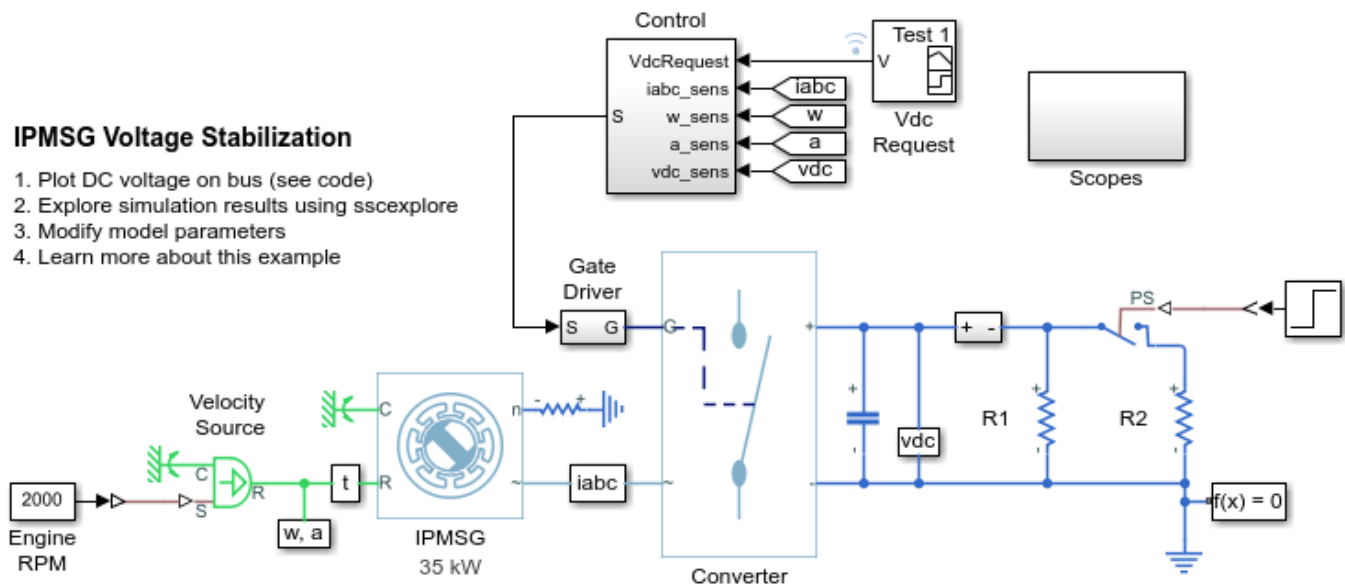
Simulation Results from Simscape Logging

The plot below shows how well the system maintains the requested DC voltage as the load changes.
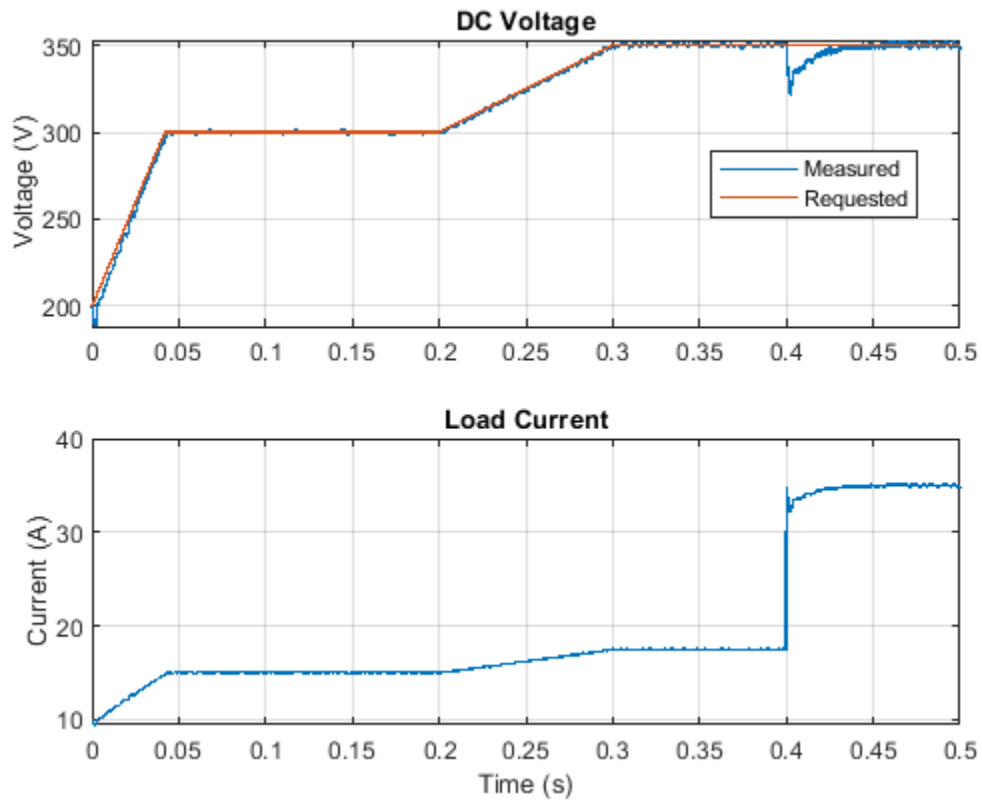
Marine Full Electric Propulsion Power System

This example shows a representative marine half-ship electrical power system with base load, hotel load, bow thrusters and electric propulsion.

The principal components of the system are:

- 30 MVA Gas Turbine, Round Rotor Alternator
- 5 MVA Diesel Generator, Salient Pole Alternator
- 11.5 MVA Base Load
- 6 MVA Switched Hotel Load
- 20 MVA Average-Value Propulsion Rectifier
- 1 MVA Direct Online Start, Squirrel Cage Bow Thrusters

The sequence of events during the simulation is:

- 10 seconds - Hotel Load disconnected
- 20 seconds - Propulsion begins ramping up
- 30 seconds - Full power ahead
- 40 seconds - Propulsion begins ramping down
- 50 seconds - Bow Thrusters start up
- 60 seconds - Hotel Load connected

Model
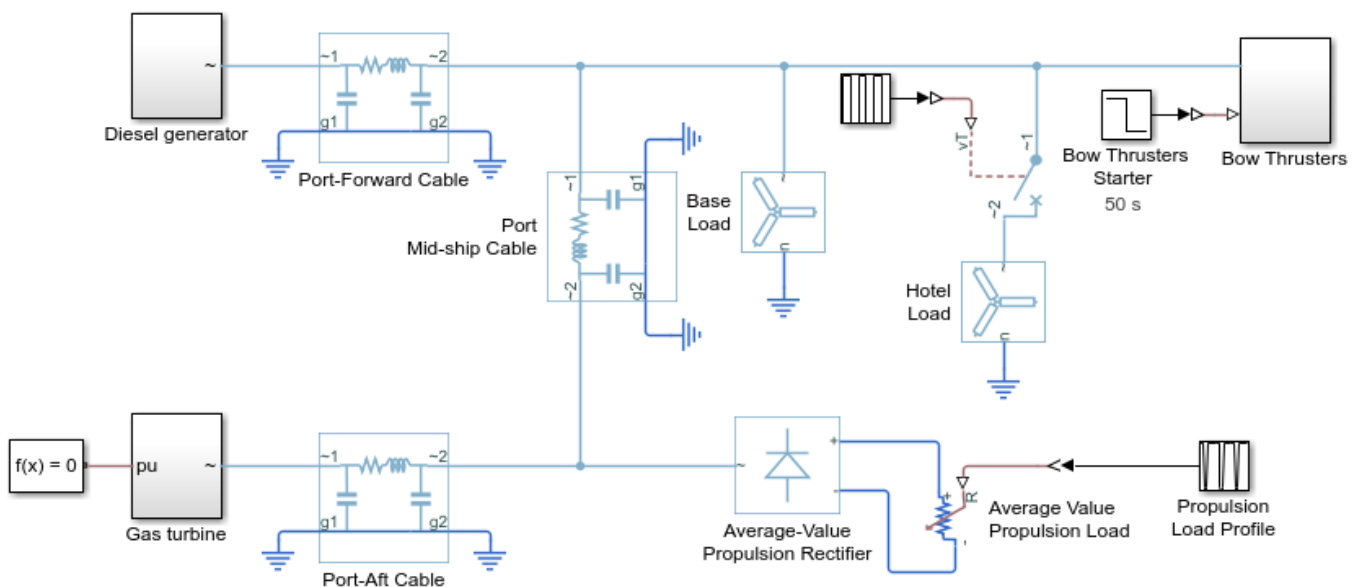
Marine Full Electric Propulsion Power System

1. Plot diesel generator and gas turbine power output (see code)
2. Explore simulation results using sscexplore
3. Learn more about this example

Simulation Results from Simscape Logging

The plot below shows the diesel generator and gas turbine power outputs.
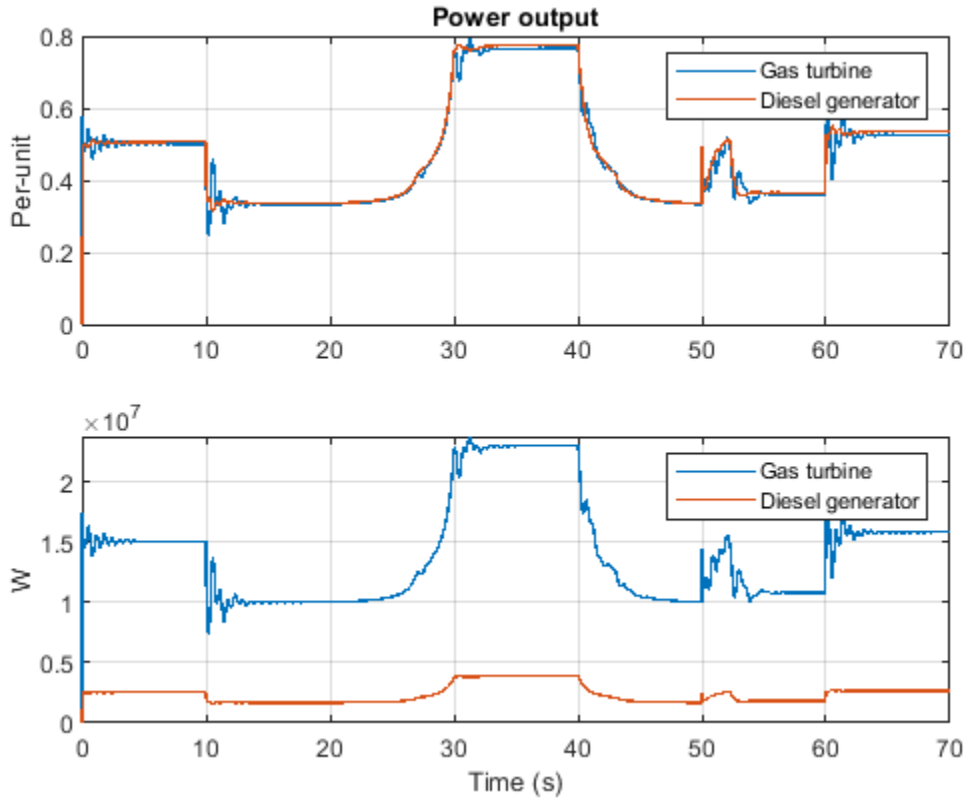
Three-Phase Modular Multilevel Converter

This example shows how to control in open-loop a three-phase modular multilevel converter (MMC). Each MMC arm consist of four half-bridge submodules. A wye-connected series RLC structure provides the load to the system.

Model
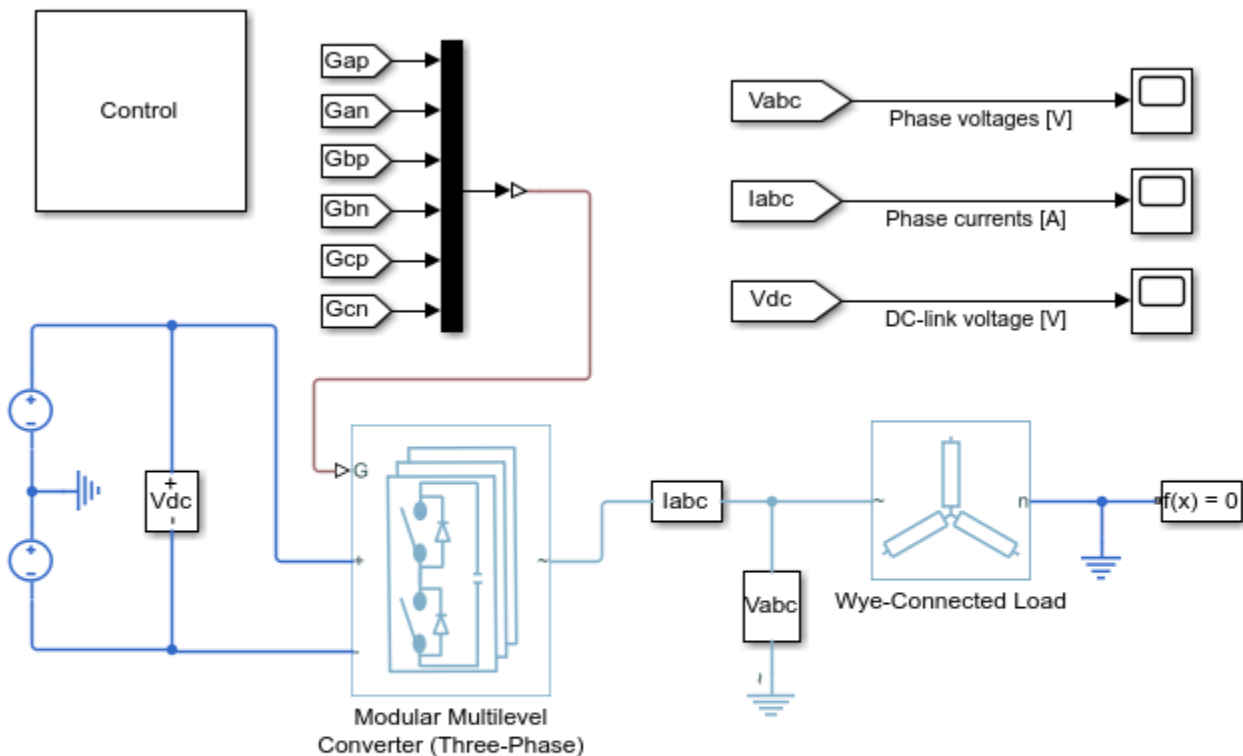
Three-Phase Modular Multilevel Converter

1. Plot voltage and current (see code)
2. Explore simulation results using sscexplore
3. Modify model parameters
4. Learn more about this example

Simulation Results from Simscape Logging

The plot below shows the phase voltages and currents.
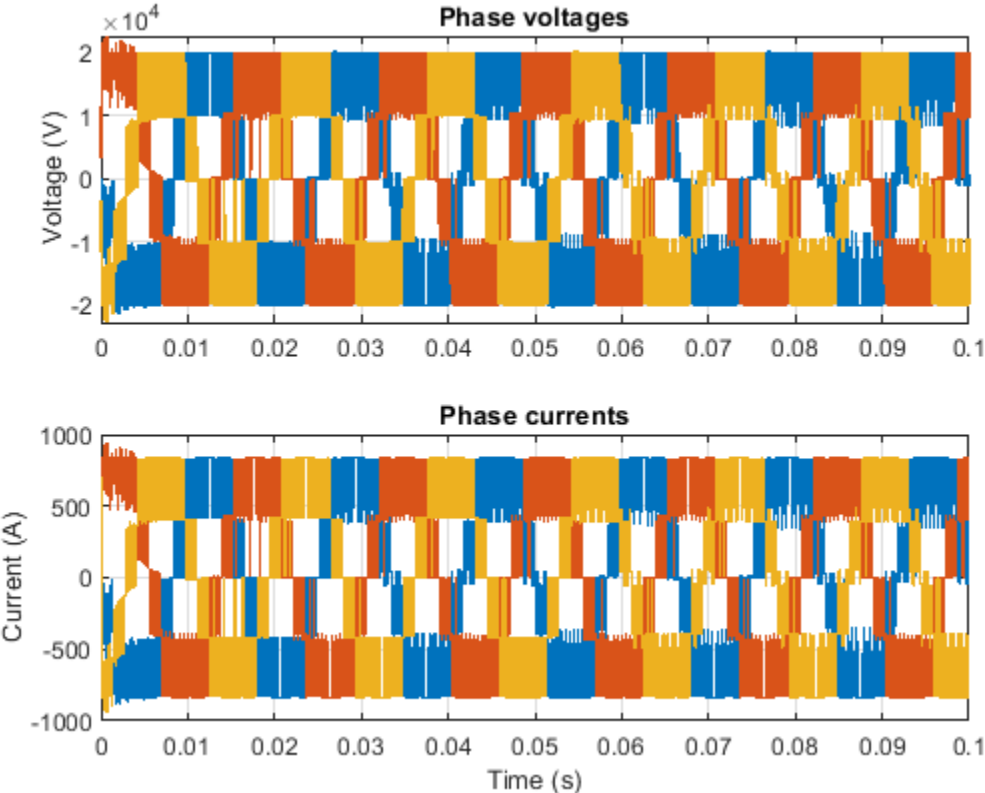
One-Quadrant Chopper Control

This example shows how to control a one-quadrant chopper. The Control subsystem implements a simple PI-based control algorithm for controlling the output current.

Model
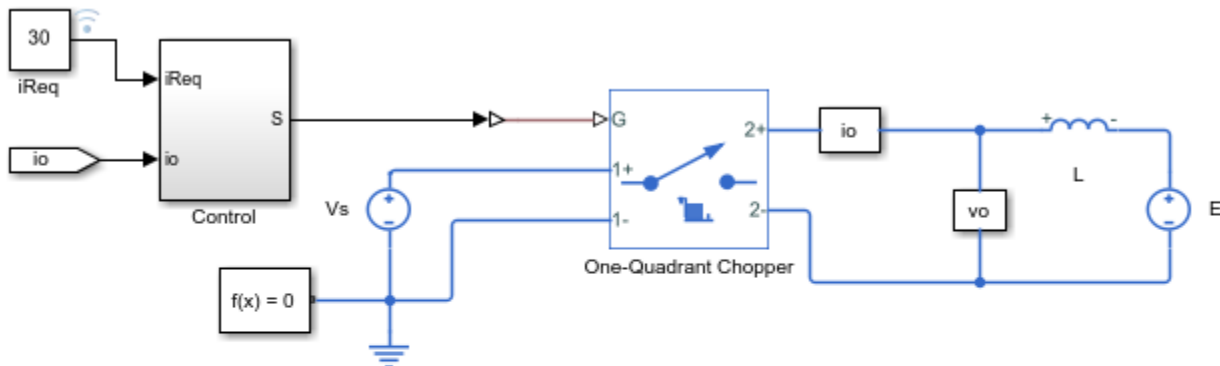
One-Quadrant Chopper Control

1. Plot current (see code)
2. Explore simulation results using sscexplore
3. Modify model parameters
4. Learn more about this example
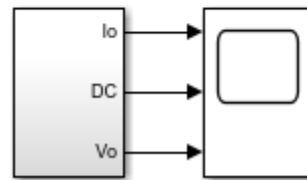
Simulation Results from Simscape Logging

The plot below shows the requested and measured current for the test and the output voltage in the circuit.
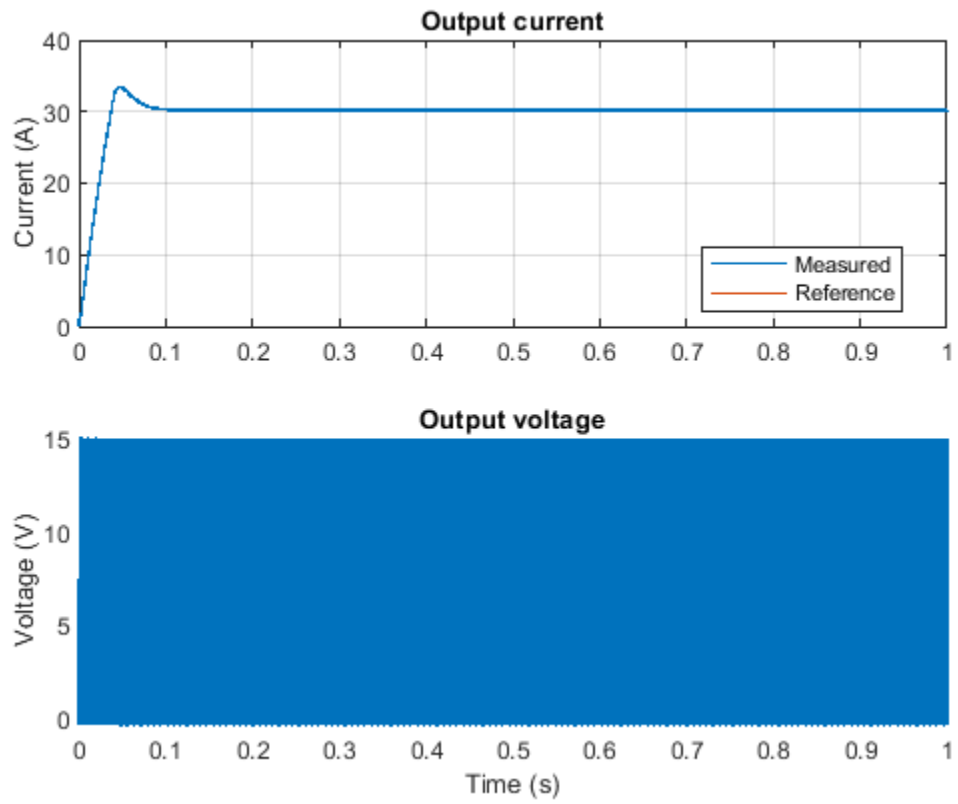
Three-Phase PMSM Drive

This example shows a Permanent Magnet Synchronous Machine (PMSM) in wye-wound and delta-wound configuration and an inverter sized for use in a typical hybrid vehicle. The inverter is connected directly to the vehicle battery, but you can also implement a DC-DC converter stage in between. You can use this model to design the PMSM controller, by selecting the architecture and gains to achieve the desired performance. To check the timing of IGBT turn-on and turn-off, you can replace the IGBT devices with the more detailed N-Channel IGBT block. For complete vehicle modeling, you can use the Motor & Drive (System Level) block to abstract the PMSM, inverter, and controller with an energy-based model. The Gmin resistor provides a very small conductance to ground that improves the numerical properties of the model when using a variable-step solver.

Model
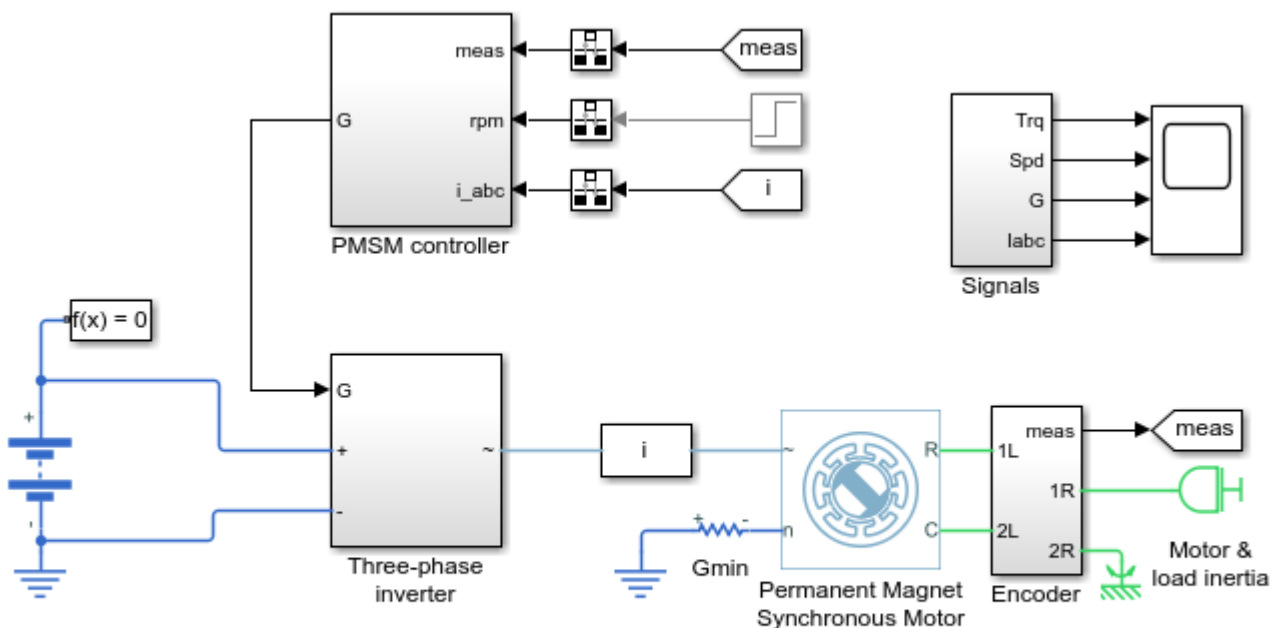
Three-Phase PMSM Drive

1. Explore simulation results using sscexplore2. Motor winding: Wye-wound, Delta-wound (see code)
3. Learn more about this example

PMSM Field-Weakening Control

This example shows how to control the rotor angular velocity above the nominal velocity in a permanent magnet synchronous machine (PMSM) based electrical-traction drive. A high-voltage battery feeds the PMSM through a controlled three-phase converter. The Control subsystem includes a multi-rate PI-based cascade control structure which has an outer angular-velocity-control loop and two inner current-control loops. The velocity controller generates a torque reference. A zero d-axis controller converts this torque reference to current references. A field weakening controller adjusts the current references to satisfy the voltage constraints above the nominal velocity. A Stateflow® state machine implements the task scheduling in the Control subsystem. During the 0.7 s simulation, the angular velocity demand ramps up from 0 to 4000 rpm. Above 1630 rpm, the PMSM enters in field weakening mode. The Scopes subsystem contains scopes that allow you to see the simulation results.

Model
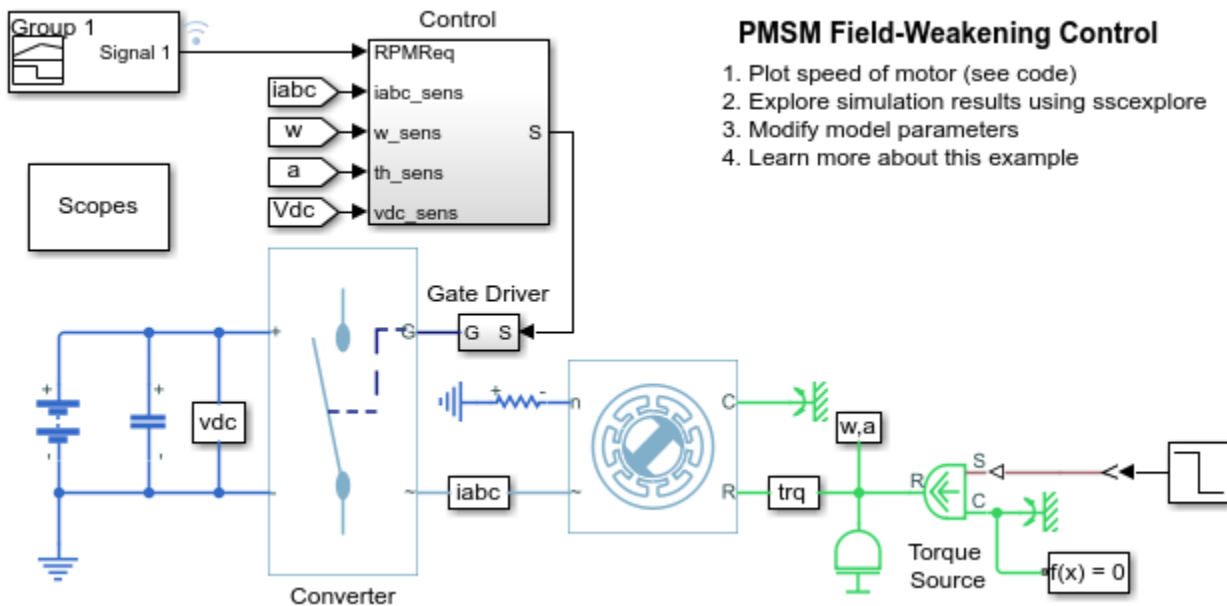
Simulation Results from Simscape Logging

The plot below shows the requested and measured speed for the test, as well as the phase currents in the electric drive.
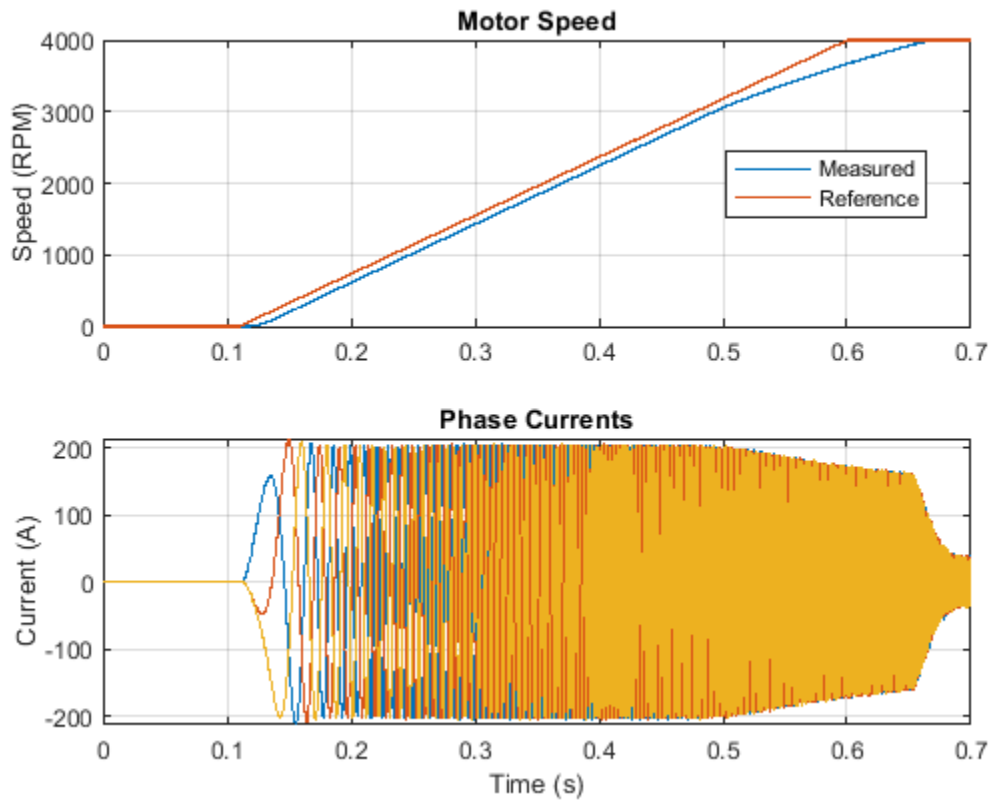
PMSM Parameterization from Datasheet

This example shows two test harnesses that add confidence that a PMSM is correctly parameterized from a datasheet. It also calculates motor efficiency at rated speed and torque.

Model

The rated conditions test harness below uses datasheet values for damping and friction plus it adds an additional linear damper that is sized to reach rated torque when rotating at rated speed. The three current sources deliver rated current. This test harness verifies that the motor is capable of delivering the rated torque at rated speed and also determines the motor efficiency.

The stall conditions test harness replicates a stall event by effectively shorting the mechanical R and C connections via the torque sensor. The torque measured by the torque sensor is checked against the datasheet value for the stall torque. An incorrect stall torque could indicate an incorrect value for the torque constant, perhaps because the measurement convention has not been defined on the datasheet.
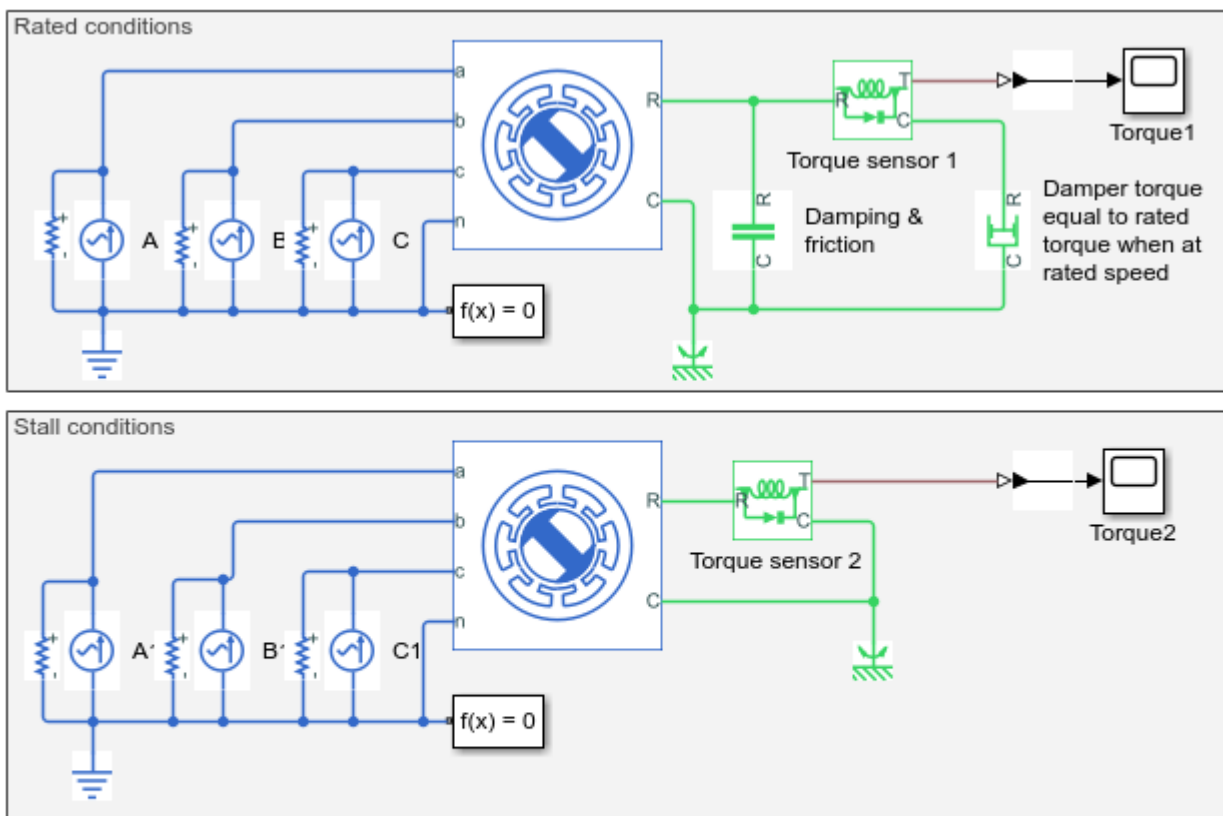
PMSM Parameterization from Datasheet

1. Plot torque and calculate efficiency at rated load (see code)
2. Confirm stall torque (see code)
3. Set parameters for model
4. Explore simulation results using sscexplore
5. Learn more about this example

Rated Conditions

The plot below is generated from the rated conditions test harness and verifies that the motor is capable of delivering the rated torque at rated speed and also determines the motor efficiency.
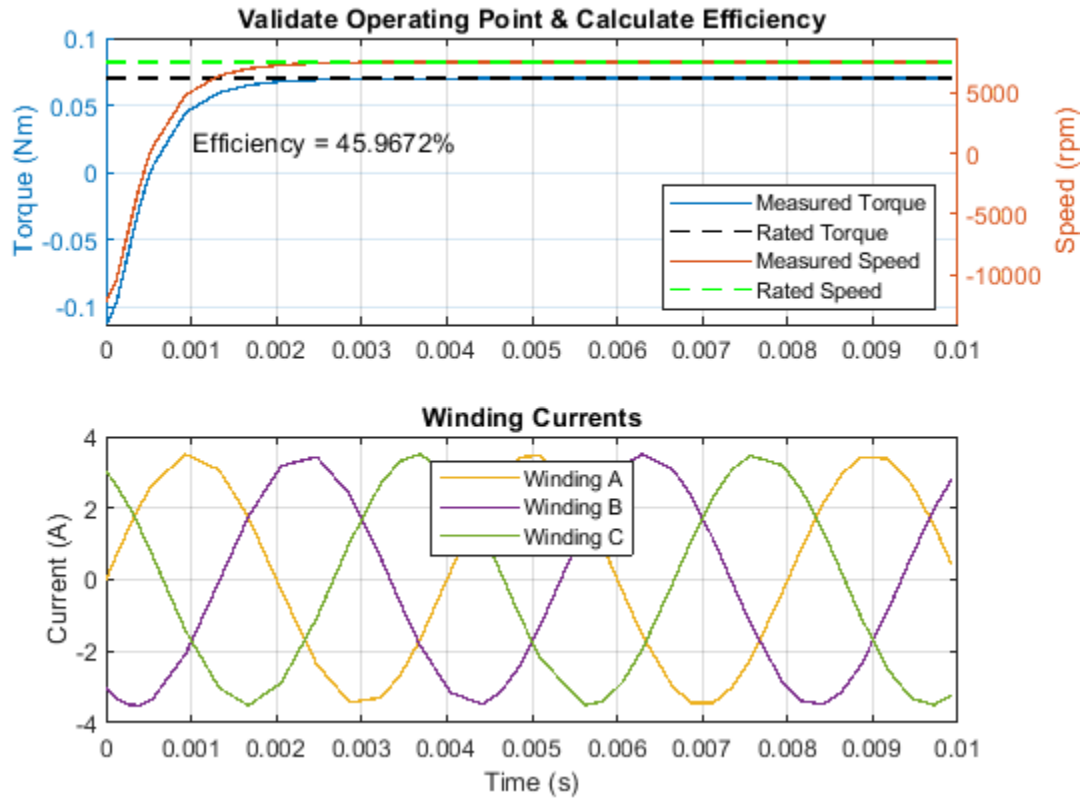
Stall Conditions

The plot below confirms that the simulated stall torque is equal to the datasheet stall torque.
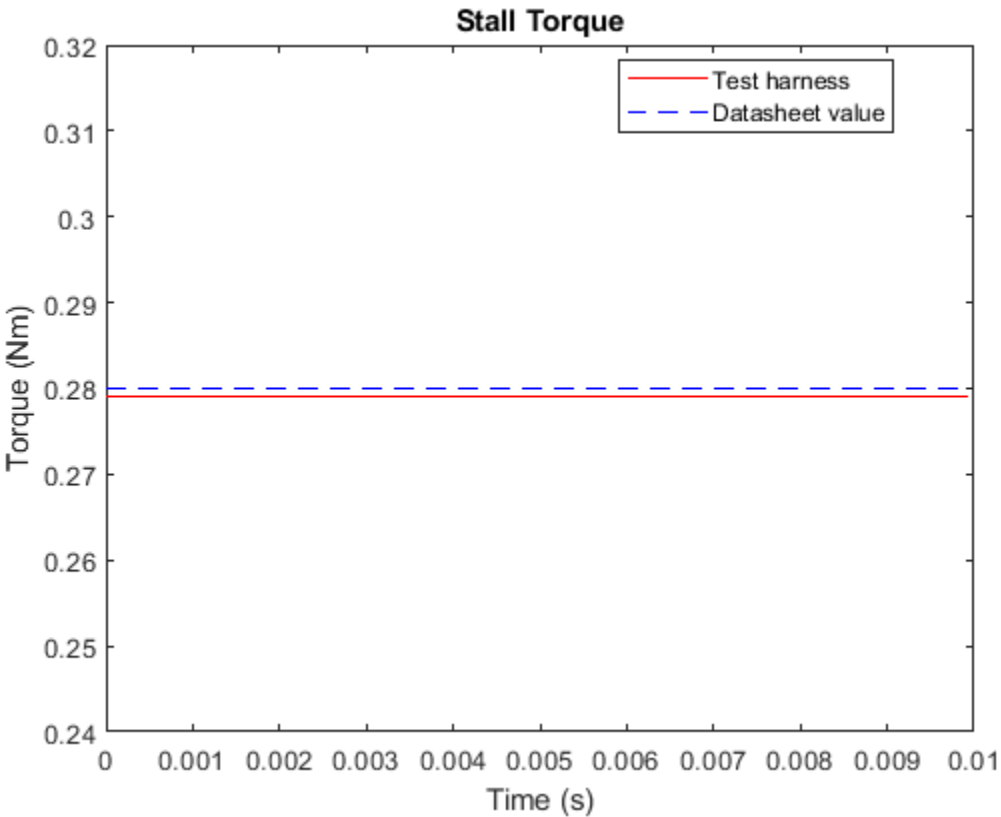
PMSM Parameterization from Measurements

This example shows how to identify PMSM motor parameters from experimental measurements.

Model

The parameter identification requires three separate tests that are represented here by three models as shown below. In this example we assume known values for the motor parameters and then show that we can reproduce them from the simulated identification tests.
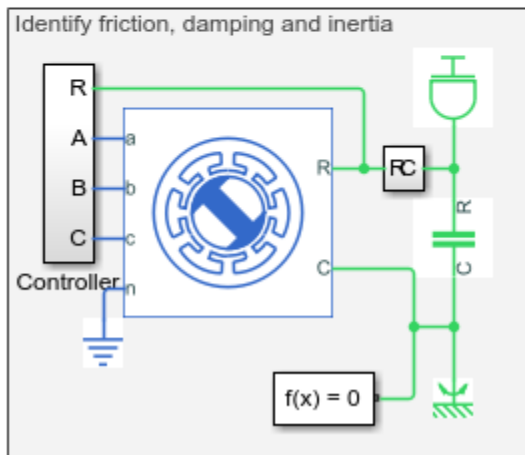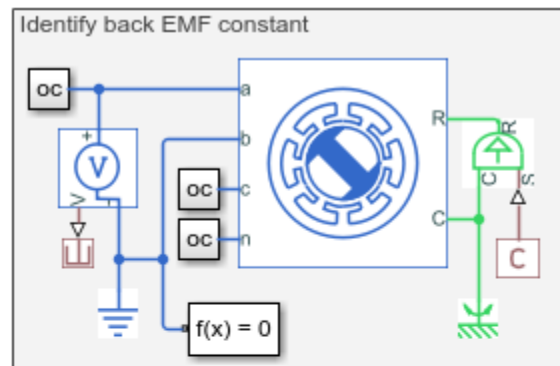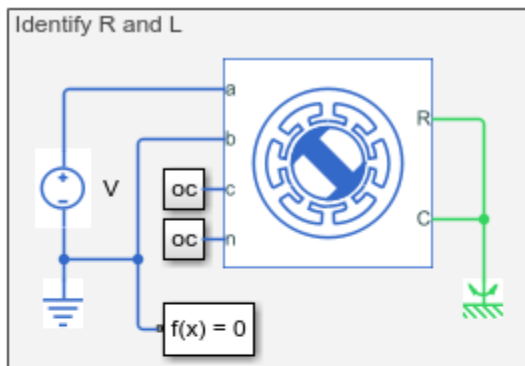
PMSM Parameterization from Measurements

1. Identify R and L and plot time constant (see code)
2. Identify back EMF constant (see code)
3. Identify friction, damping and inertia (see code)
4. Set parameters for model
5. Explore simulation results using sscexplore
6. Learn more about this example

Locked Rotor Test: Identify R and L

The first step is to lock the rotor and apply a voltage step to one of the stator windings. The resulting first-order time constant is defined by the stator resistance and inductance values, and the steady-state current by the stator resistance.
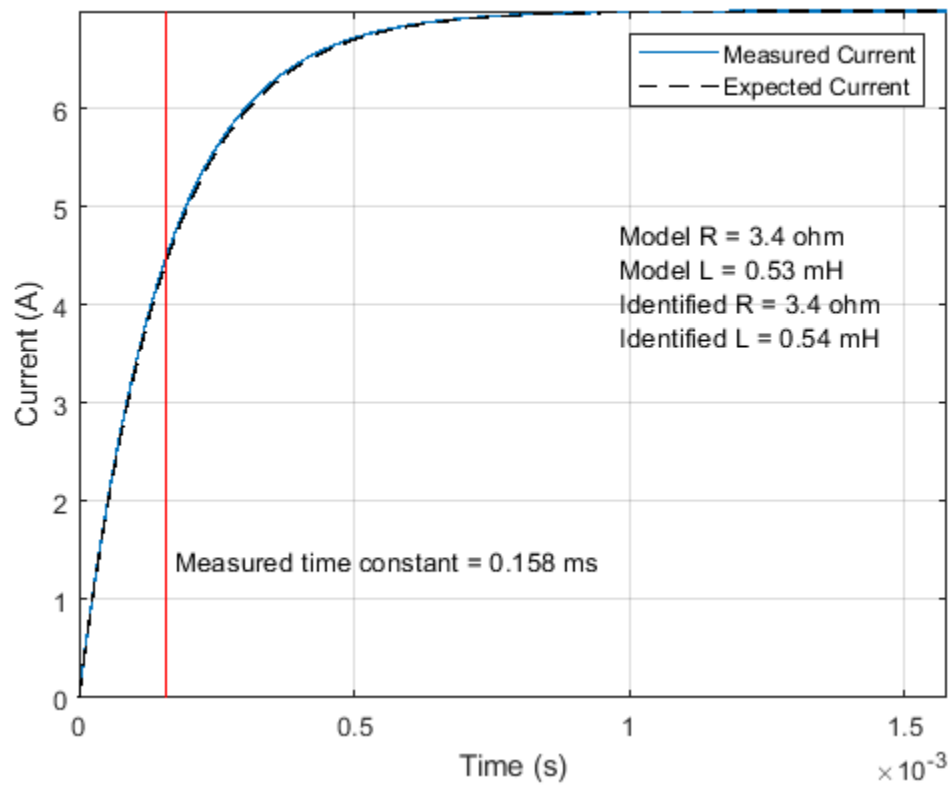
Fixed Speed No Load Test: Identify Back EMF Constant

The second step is to spin the motor in a dynamometer with no electrical load. This permits estimation of the back EMF constant. Note that, when expressed in SI units, the back EMF constant is equal to the torque constant.
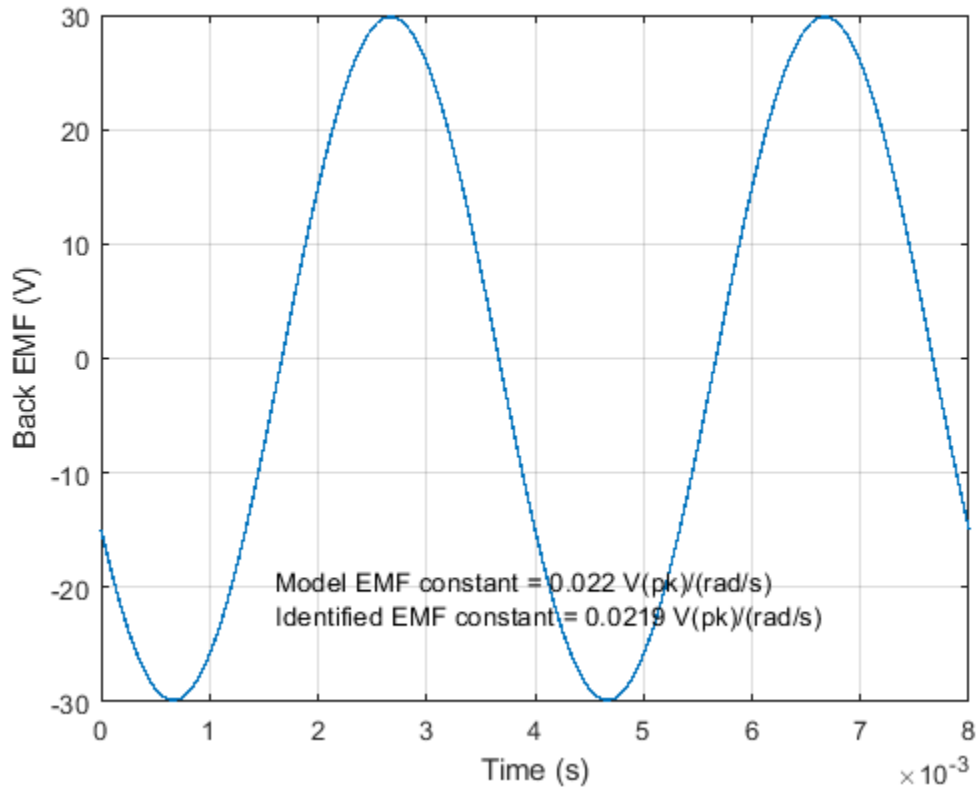
Controlled Motor Test: Identify Friction, Damping and Inertia

In this final step, the motor is spun using a motor controller and with no mechanical load. Mechanical torques due to friction and damping are measured by converting stator currents to torques using the already-identified torque constant.

The first plot below shows the mechanical torque required to maintain constant speed at four different speeds. The torque required at the lower speeds is primarily overcoming friction resistance, whereas at higher speeds viscous damping dominates. A straight line is fitted through the points, and the zero-speed intercept gives friction torque and the slope gives the viscous damping coefficient.

The second plot shows a speed run-down test. For this the demanded torque is set to zero, or the motor is unpowered. The gradient gives the deceleration from which the motor inertia can be determined given values for friction and damping torques.

10-539
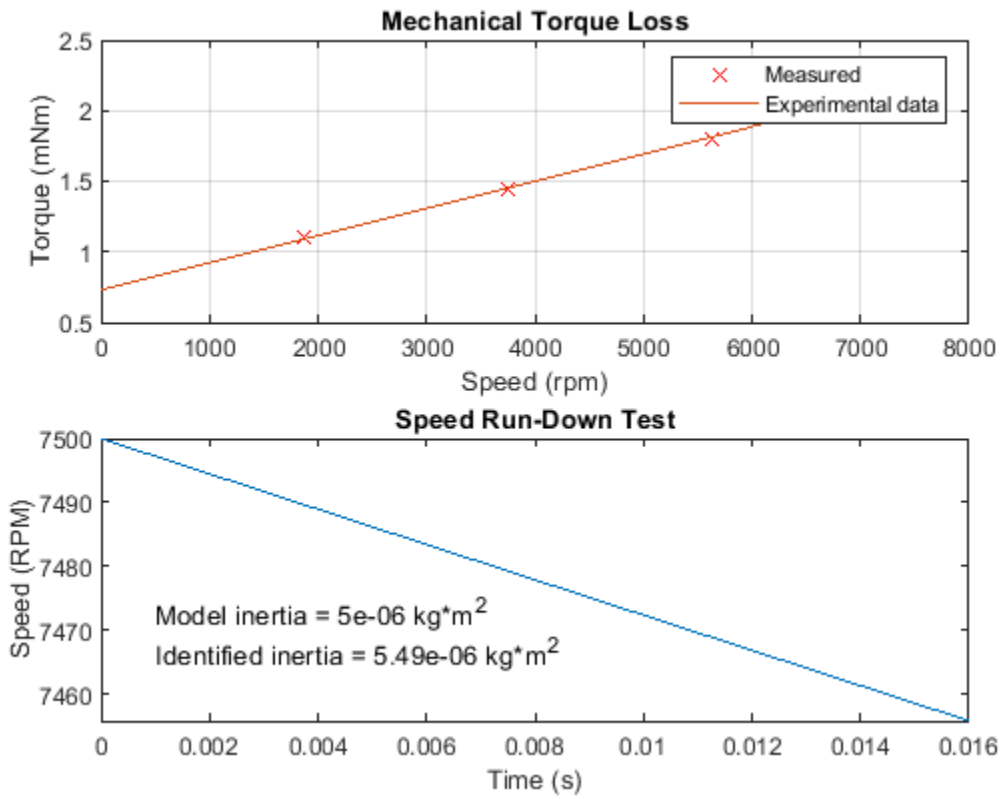
Parameterize a Permanent Magnet Synchronous Motor

This example shows how to estimate the back EMF and torque constants of a blackbox permanent magnet synchronous motor (PMSM) with an unknown flux linkage. You can use either the back EMF or torque constant to describe the flux linkage and parameterize a Simscape™ Electrical™ PMSM block. This parameterization allows you to accurately replicate the blackbox motor's behaviour in simulation.

In this example, the number of pole pairs, stator resistance and stator inductances of the simulation and blackbox PMSMs are already specified to match.

Open Model

Open the model.

```
model='ee_pmsm_parameterization';
open_system(model)
set_param(find_system('ee_pmsm_parameterization','FindAll', 'on','type','annotation','Tag','Mode
```
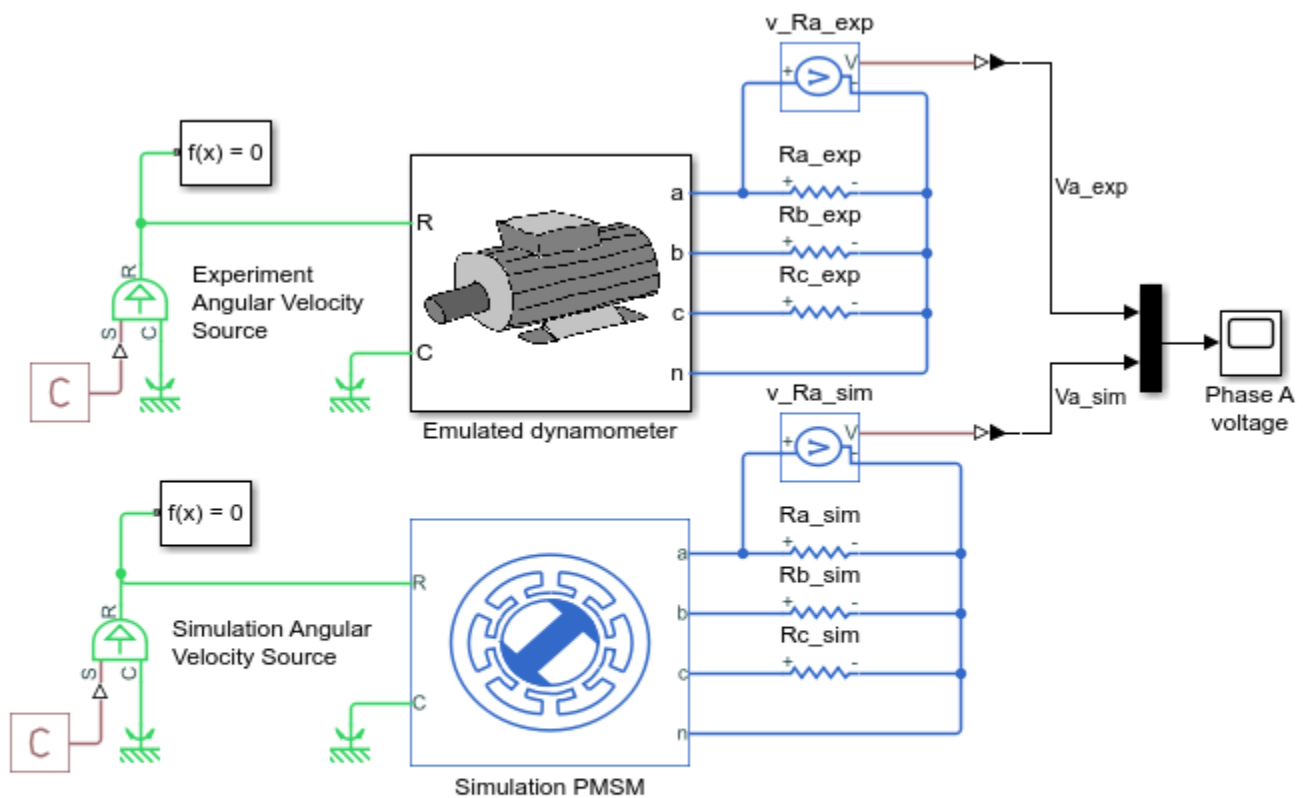

Parameterize a Permanent Magnet Synchronous Motor

1. Set back EMF constant to 0.1
2. Set back EMF constant to estimated value
3. Modify example script
4. Explore simulation results using sscexplore
5. Learn more about this example

Set Back EMF Constant

Select the back EMF parameterization option for the simulation PMSM and initialize its value to an arbitrary default value of 0.1 V*s/rad.

```
Simulation_PMSM = [model,'/Simulation PMSM'];
Ke = 0.1;
set_param(Simulation_PMSM,'pmflux_param','3');
```

Alternatively, you can set the back EMF parameterization option and specify its value from the PMSM block mask by selecting **Specify back EMF** under **Permanent magnet flux linkage parameterization** and specifying **Back EMF constant**.

Compare Results

Simulate the model and plot the voltage over time across the a-winding for both the experimental and simulation PMSMs. Observe that there is a significant difference in voltage amplitude between the experimental and simulation PMSMs.

```
sim(model);
v_a_exp = simlog_ee_pmsm_parameterization.v_Ra_exp.V;
v_a_sim = simlog_ee_pmsm_parameterization.v_Ra_sim.V;
simscape.logging.plot({v_a_exp,v_a_sim},...
    'names',{'Experimental PMSM','Simulation PMSM'});
title('Voltage across a-winding')
ylabel('Voltage, V');
```
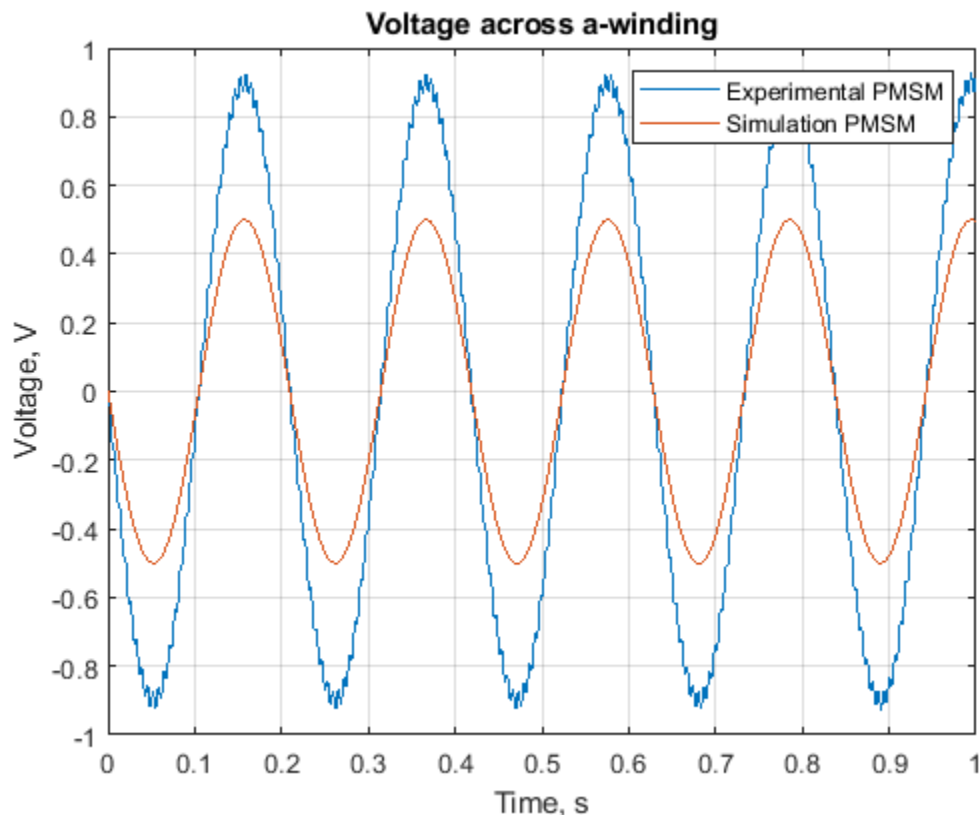
Calculate Voltage Constant

Calculate the voltage constant K_e of the blackbox experimental PMSM by using the defining equation

$$K_e = \frac{V_{a,pk}}{\omega},$$

where ω is the angular velocity of the PMSM shaft, and $V_{a,pk}$ is the voltage drop across the a-winding of the motor. This equation is valid only when the current through the windings is very small. The small current restriction is enforced in the model by attaching each of the winding free ends to ground via a very high resistance, effectively making each winding an open-circuit.

$V_{a,pk}$ is calculated by taking the average of the maximum and minimum values of the voltage drop across the a-winding, and dividing the result by two. You can improve the accuracy of this calculation by first filtering out the high frequency noise of the measured voltage signal:

```
window_size = 100;
v_a_exp_filt = filter(1/window_size*ones(1,window_size),1,v_a_exp.series.values);
v_a_exp_peak = (max(v_a_exp_filt)-min(v_a_exp_filt))/2;
```

The angular velocity can be measured directly from the source block:

```
angular_velocity_exp = mean(simlog_ee_pmsm_parameterization.Experiment_Angular_Velocity_Source.w
```

The back EMF constant can now be calculated from the above results:

```
Ke = v_a_exp_peak / angular_velocity_exp;
disp(Ke);

    0.1803
```

Change Back EMF Constant

Change the back EMF constant for the simulation motor to the estimated value K_e, calculated in the previous section.

```
Simulation_PMSM = [model,'/Simulation PMSM'];
set_param(Simulation_PMSM,'pmflux_param','3');
```

Compare New Results

Resimulate the model and plot the voltage across the a-winding. The new parameterization of the voltage constant improves the simulation PMSM's ability to replicate the behaviour of the experimental PMSM.

```
sim(model);
v_a_exp = simlog_ee_pmsm_parameterization.v_Ra_exp.V;
v_a_sim = simlog_ee_pmsm_parameterization.v_Ra_sim.V;
simscape.logging.plot({v_a_exp,v_a_sim},...
    'names',{'Experimental PMSM','Simulation PMSM'});
title('Voltage across a-winding')
ylabel('Voltage, V');
```

Voltage across a-winding
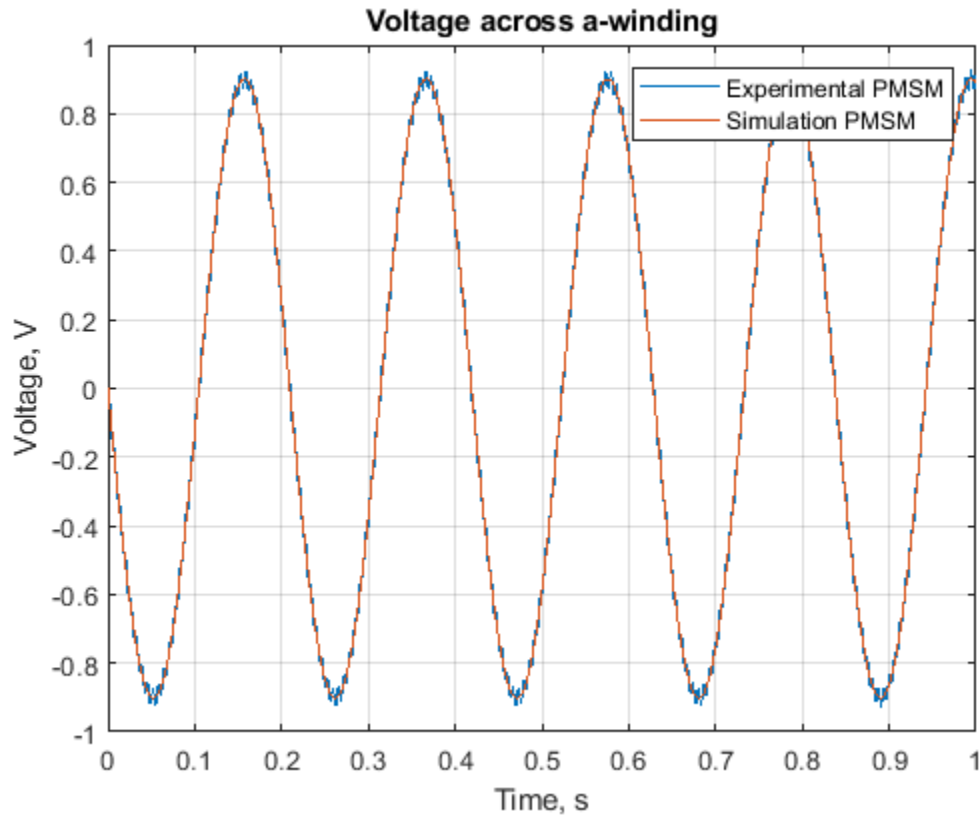
Calculate Torque Constant

Calculate the torque constant K_t of the blackbox PMSM using the defining equation:

$$K_t = \frac{2}{3}\left(\frac{T}{I_{a,pk}}\right),$$

where $I_{a,pk}$ is the peak current through the a-winding and T is the overall mechanical torque driving the PMSM.

```
i_a_exp = simlog_ee_pmsm_parameterization.Ra_exp.i;
i_a_sim = simlog_ee_pmsm_parameterization.Ra_sim.i;

T = abs(mean(simlog_ee_pmsm_parameterization.Experiment_Angular_Velocity_Source.t.series.values)

window_size = 100;
i_a_exp_filt = filter(1/window_size*ones(1,window_size),1,i_a_exp.series.values);
i_a_exp_peak = (max(i_a_exp_filt)-min(i_a_exp_filt))/2;

Kt = 2/3*T/i_a_exp_peak;
disp(Kt);

simscape.logging.plot({i_a_exp,i_a_sim},...
    'names',{'Experimental PMSM','Simulation PMSM'});
title('Current across a-winding')
ylabel('Current, A');
```

```
0.1797
```

Current across a-winding
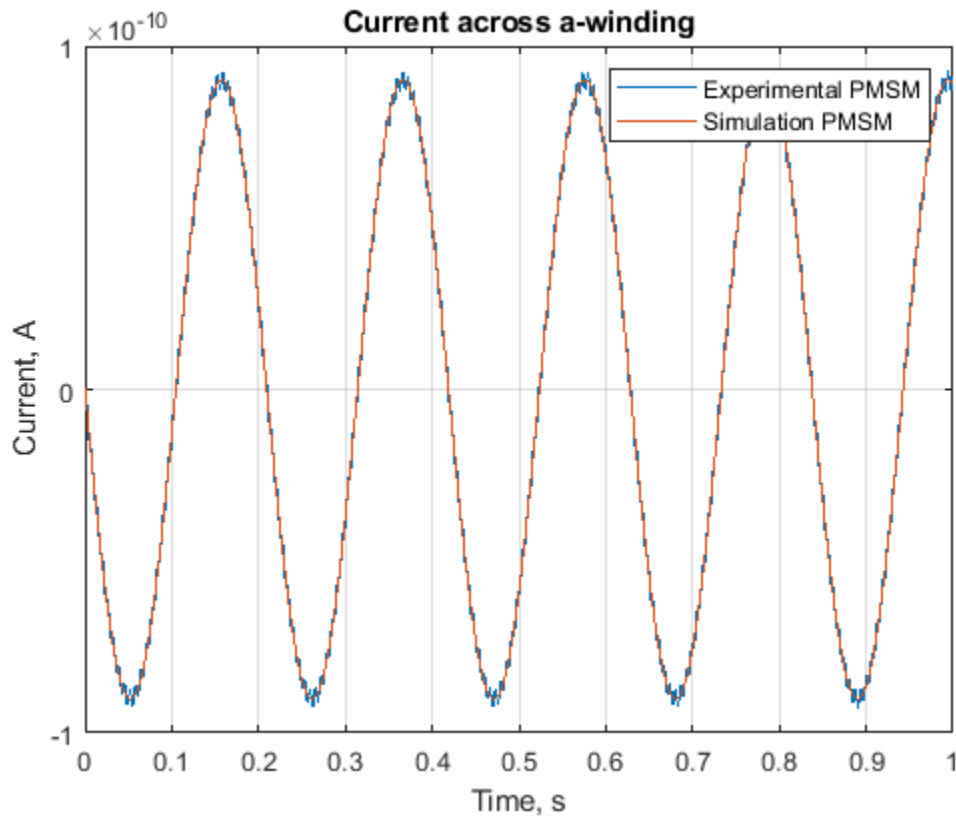
Compare Flux Linkage Parameterizations

As expected, the measured torque and back EMF constants have approximately the same value (0.18) and are related to the permanent magnet flux linkage ψ_m via the number of pole pairs N of the motor:

$$K_e = K_t = N\psi_m.$$

You can parameterize the permanent magnet flux linkage of the motor by specifying any of K_e, K_t or ψ_m in the Simulation PMSM block mask. Back EMF and torque constants are more commonly given than permanent magnet flux linkage on motor datasheets.

Learn More

See the PMSM block page for more information.

Push-Pull Buck Converter in Continuous Conduction Mode

This example shows how to control the output voltage of a push-pull buck converter. The current flowing through the inductor is never zero, therefore the DC-DC converter operates in Continuous Conduction Mode (CCM). To convert and maintain the nominal output voltage, the PI Controller subsystem uses a simple integral control. During startup, the reference voltage is ramped up to the desired output voltage.

The converter operates in CCM only if

- $K > K_{critical}$,

where:

- $K = 2 * L/(R * Tsw)$.

- $K_{critcal} = 1 - D$.

- L is the filter inductance.

- R is the load resistance.

- T_{sw} is the switching period for each MOSFET. That is, $T_{sw} = 0.5/f_{sw}$, where f_{sw} is the switching frequency.

- D is the duty cycle of PWM input to the gate of each MOSFET. That is, $D = T_{on}/T_{sw}$, where T_{on} is the ON time of the MOSFET.

Open Model

```
open_system('ee_push_pull_converter_ccm.slx');
```


Push-Pull Buck Converter in Continuous Conduction Mode

1. Modify model parameters
2. Explore simulation results using sscexplore
3. Learn more about this example.
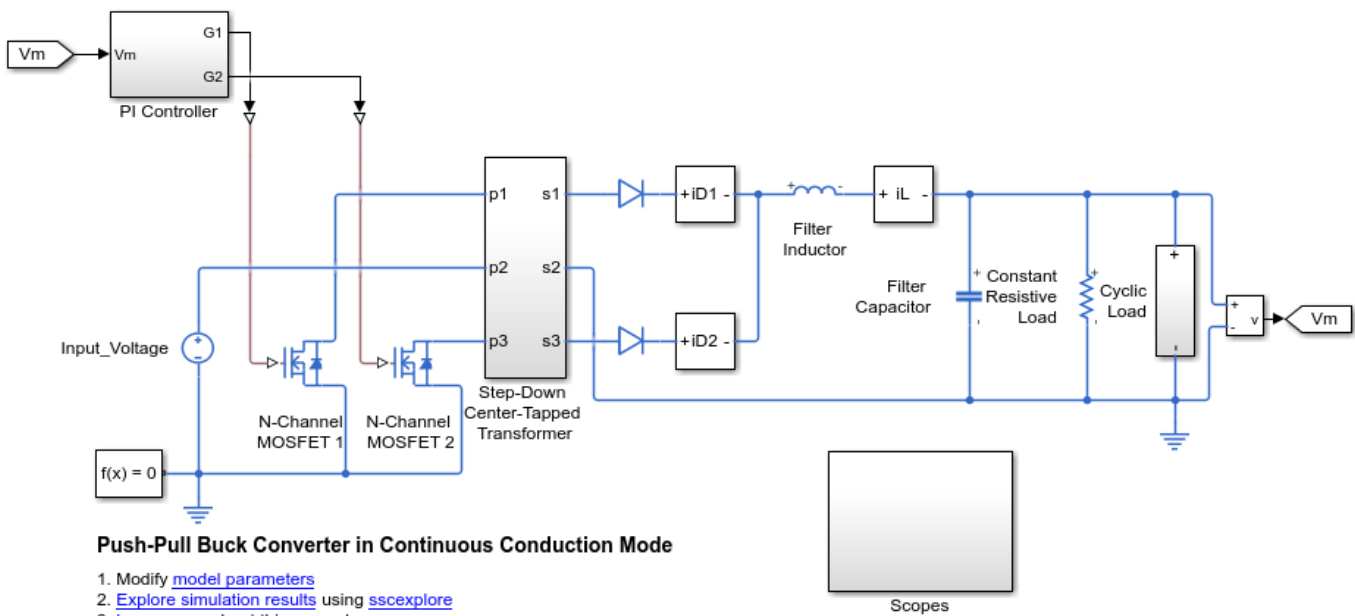
Specify the Design Parameters

The system is required to generate and maintain an output voltage of 80 V with a full load power capability of 1000 W. The input voltage is 400 V and the transformer turns ratio is 2. The full load includes a constant load and a cyclic load. The 'ee_push_pull_converter_ccm_data.m' script defines the design parameters as variables in the MATLAB® workspace.

```
Input_Voltage          = 400;                               %  Input voltage to the push-pull
Output_Voltage         = 80;                                %  Desired output voltage from the
Output_Power           = 1000;                              %  Full load power output [W]
fsw_Hz                 = 40000;                             %  MOSFET switching frequency [Hz]
primary_winding        = 200;                               %  Number of turns in the primary
secondary_winding      = 100;                               %  Number of turns in the secondary
TR                     = primary_winding/secondary_winding; %  Turns ratio
Kp                     = 0.01;                              %  Proportional gain for PI control
Ki                     = 20;                                %  Integral gain for PI controller
del_I                  = 40;                                %  Peak-peak inductor ripple curren
del_V                  = 1;                                 %  Peak-peak output voltage ripple
share_constload        = 70;                                %  Percentage of load current drawn
share_cyclicload       = 100-share_constload;               %  Percentage of load current drawn
cyclic_load_period     = 1/20;                              %  Cyclic load period
cyclic_load_pul_width   = 50;                                %  Pulse width of the current pulse
Ts                     = 1e-7;                              %  Sampling time for the solver
```

Calculate the Open-Loop Duty Cycle

The duty cycle depends on the input voltage, the turns ratio and the desired output voltage.

```
Duty = Output_Voltage/(Input_Voltage/TR);
```

Determine the Constant Load Resistance

```
I_fl_average = Output_Power/Output_Voltage;                 %  Full load average current tha
R_const = Output_Voltage/I_fl_average;
```

Calculate the Filter Inductance

Choose the inductance value based on the input and output specifications of the converter. The inductance value depends on the input and output specifications of the converter. For this example, the converter is required to work in CCM for 20-100% of full load power. When, at the lower boundary condition, the power is 20% of full load power, the average load current is 20% of full load average current, I_fl_average. At the end of each cycle at the lower boundary condition, the inductor current goes to zero. The inductor ripple current, del_I, at this point is twice the average output load current, that is 40% of the full load average output current.

```
L_min = (Input_Voltage/TR)*Duty*(1-Duty)/(2*fsw_Hz*del_I*I_fl_average*...
    0.01);
```

Plot Inductance Versus Inductor Current Ripple

Generate this plot to see how the filter inductance relates to the inductor ripple current (expressed as a percentage of full load current). For this example, the marker at 40% corresponds to an inductance of 1.2e-04 H.

```
del_I_range = 20:0.1:50;     %  Percentage of full load current (20-50%)
L_range = (Input_Voltage/TR)*Duty*(1-Duty)./(2*fsw_Hz*del_I_range*...
    I_fl_average*0.01);
figure;
```

10-547

```
hold on;
plot(del_I_range,L_range);
hold on;
L_del_I = (Input_Voltage/TR)*Duty*(1-Duty)/(2*fsw_Hz*del_I*...
    I_fl_average*0.01);
plot(del_I,L_del_I, '*');
xlabel('Inductor current ripple (% of full load current)');
ylabel('Inductance (H)');
title('Inductance Vs Inductor Current Ripple');
```
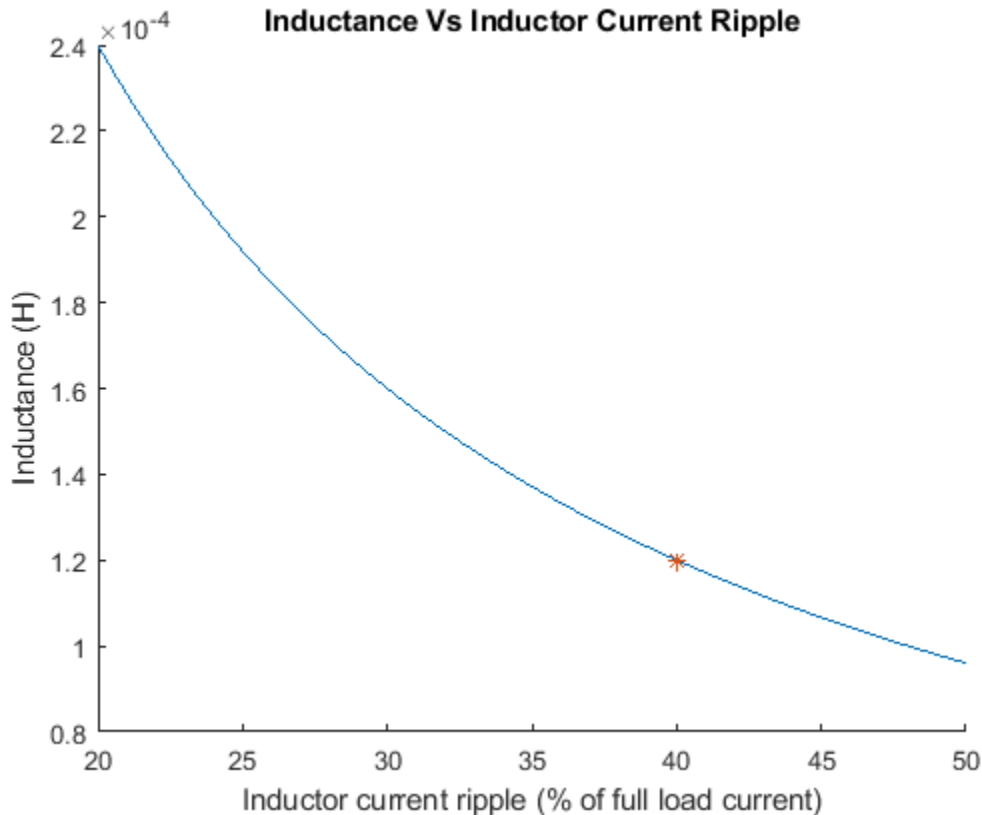

Choose a Filter Capacitance

```
C_min = (Input_Voltage/TR)*Duty*(1-Duty)/(8*(2*fsw_Hz)^2*L_min*...
    Output_Voltage*del_V*0.01);
```

Plot Capacitance Versus Voltage Ripple

Generate this plot to see how capacitance for limiting the output voltage ripple varies depending on the design parameters. For this example, the marker at 1% Output Voltage Ripple corresponds to a capacitance of 9.766e-06 F.

```
del_V_range = 0.5:0.1:5;
C_range = (Input_Voltage/TR-Output_Voltage)*Duty./(8*(2*fsw_Hz)^2*L_min*...
    Output_Voltage*del_V_range*0.01);
figure;
hold on;
plot(del_V_range,C_range);
hold on;
```

```
C = (Input_Voltage/TR-Output_Voltage)*Duty/(8*(2*fsw_Hz)^2*L_min*...
    Output_Voltage*del_V*0.01);
plot(del_V,C,'*');
xlabel('Voltage Ripple (%)');
ylabel('Capacitance (F)');
title('Capacitance Vs Voltage Ripple');
```
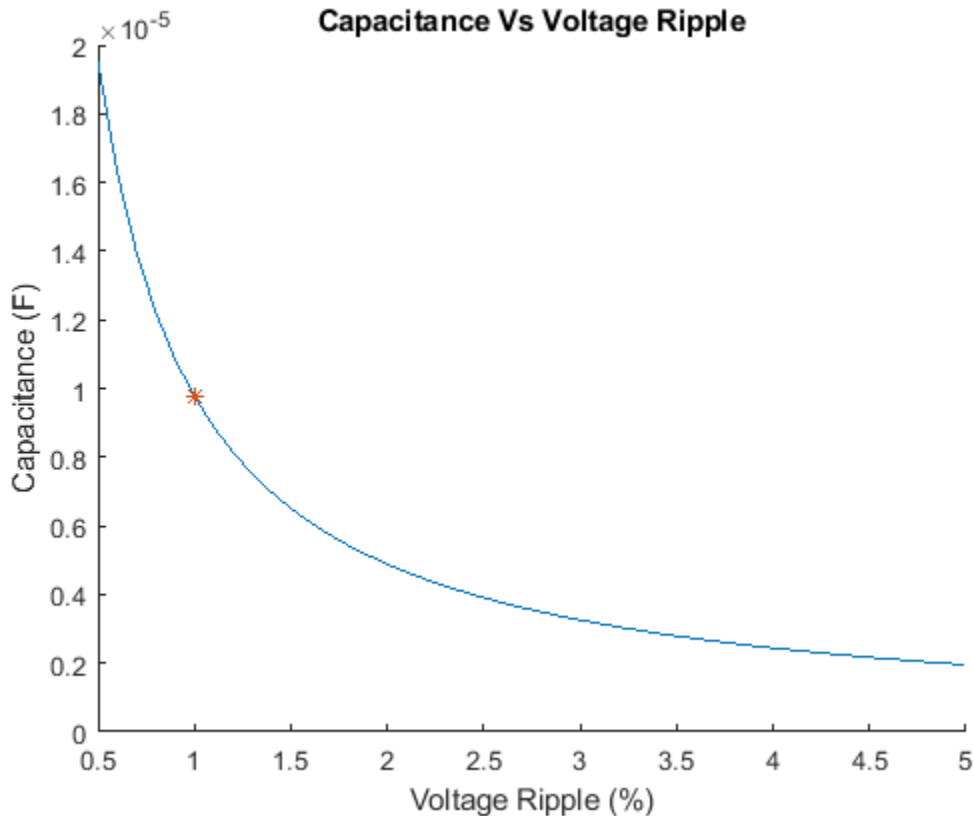

Run the Simulation

```
sim('ee_push_pull_converter_ccm.slx');
```

View Simulation Results

To view summary results during or after the simulation, open the Circuit Scope block from the model window or by entering, at the MATLAB command prompt:

```
open_system('ee_push_pull_converter_ccm/Scopes/Circuit Scope');
```
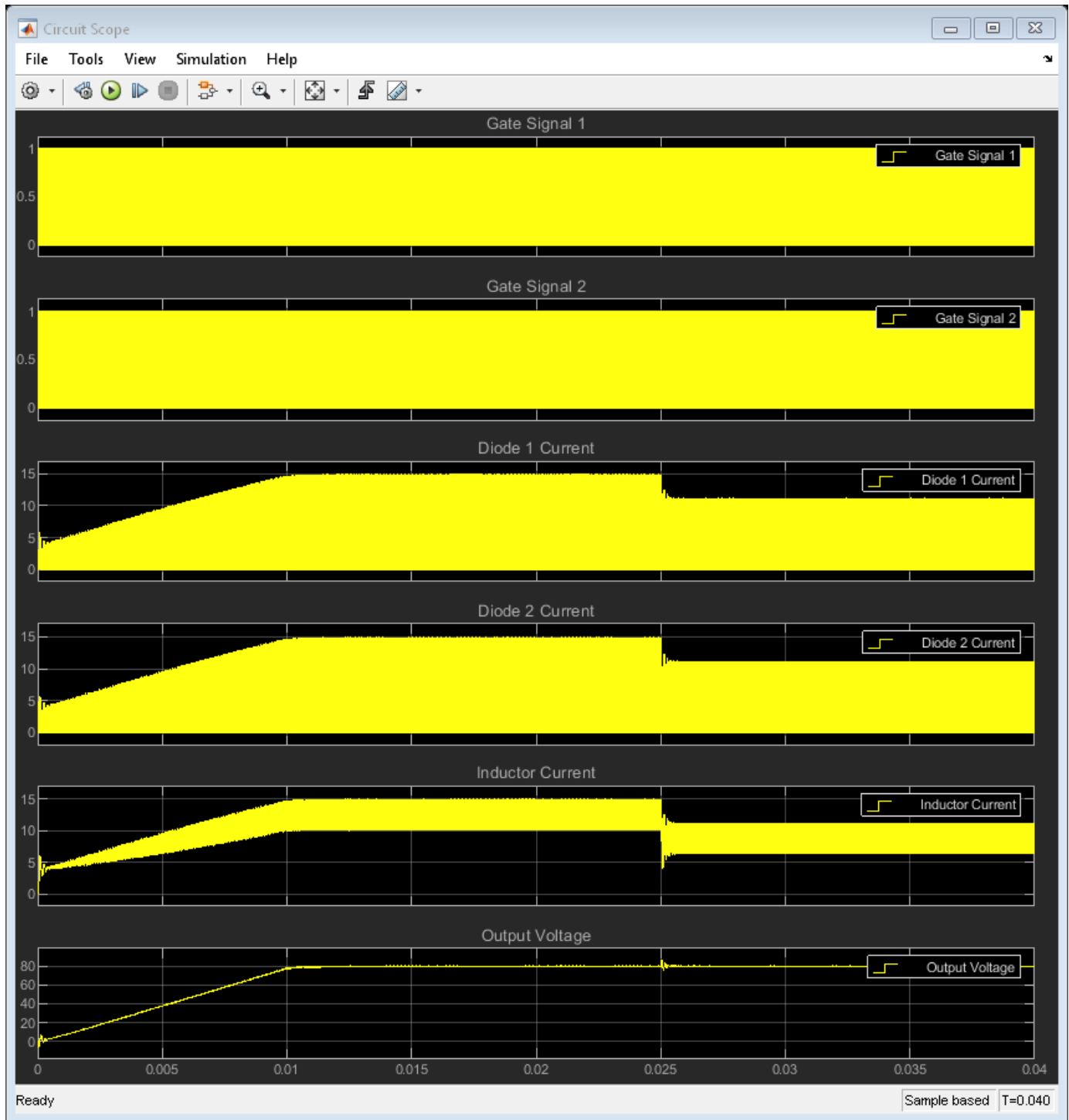
To view the control and error data during or after the simulation, open the PI Controller Scope block from the model window or, enter:

```
open_system('ee_push_pull_converter_ccm/Scopes/PI Controller Scope');
```
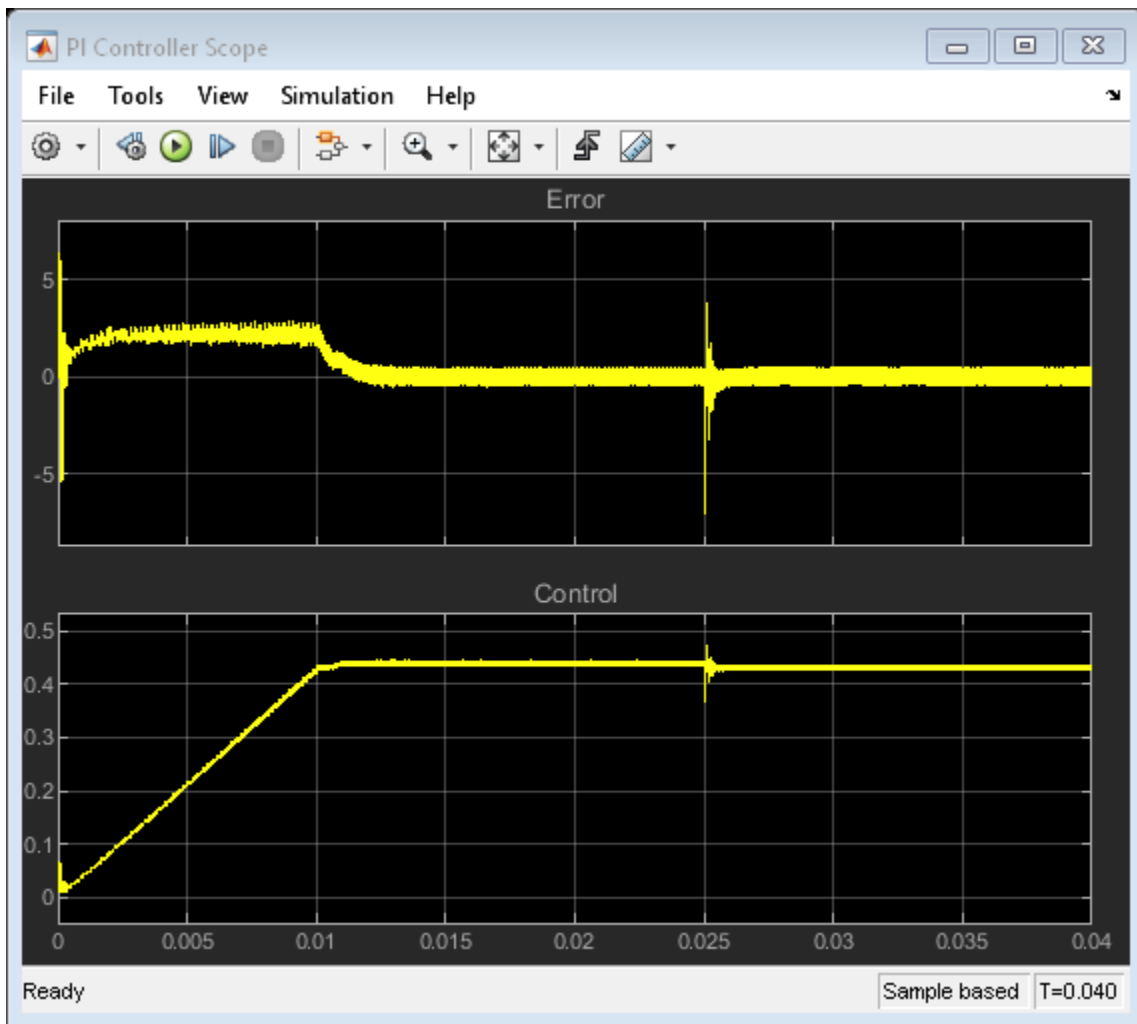
After the simulation, to view logged Simscape™ data using the Simscape Results Explorer, enter:

```
sscexplore(simlog_ee_push_pull_converter_ccm);
%
```
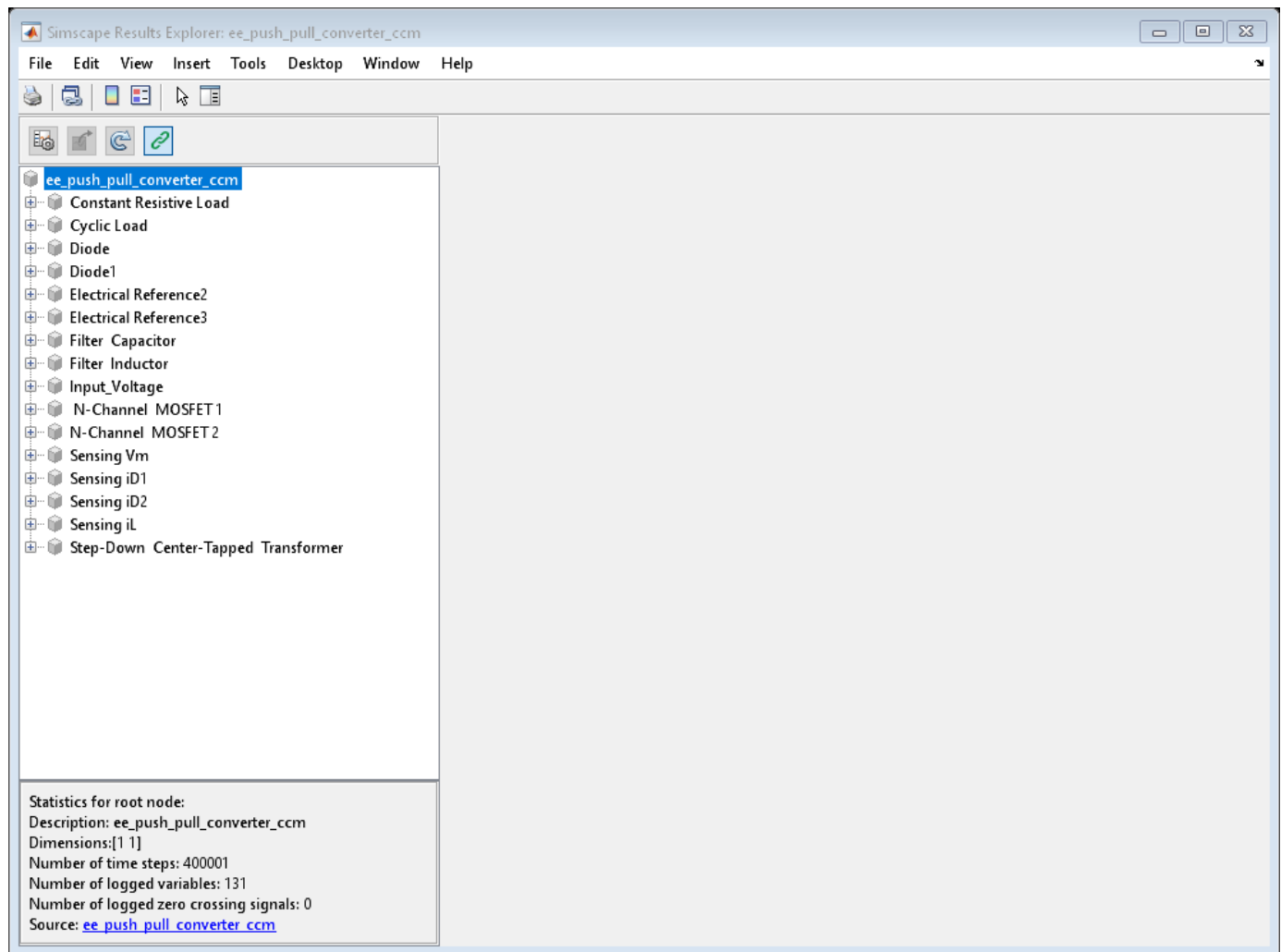
Push-Pull Buck Converter in Discontinuous Conduction Mode

This example shows how to control the output voltage of a push-pull buck converter. The current flowing through the inductor reaches zero during the switch off cycle of the MOSFETs and therefore the DC-DC converter operates in Discontinuous Conduction Mode (DCM). This mode of conduction is mostly used for low-power applications. To convert and maintain the input DC voltage as a nominal output voltage, the PI Controller subsystem uses a simple integral control. During startup, the reference voltage is ramped up to the desired output voltage.

The converter operates in DCM only if

* $K < K_{critical}$,

where:

* $K = 2 * L/(R * Tsw)$.

* $K_{critcal} = 1 - D$.

* L is the filter inductance.

* R is the load resistance.

* T_{sw} is the switching period for each MOSFET. That is, $T_{sw} = 0.5/f_{sw}$, where f_{sw} is the switching frequency.

* D is the duty cycle of PWM input to the gate of each MOSFET. That is, $D = T_{on}/T_{sw}$, where T_{on} is the ON time of the MOSFET.

Open Model

```
open_system('ee_push_pull_converter_dcm.slx');
```


Push-Pull Buck Converter in Discontinuous Conduction Mode

1. Modify model parameters
2. Explore simulation results using sscexplore
3. Learn more about this example
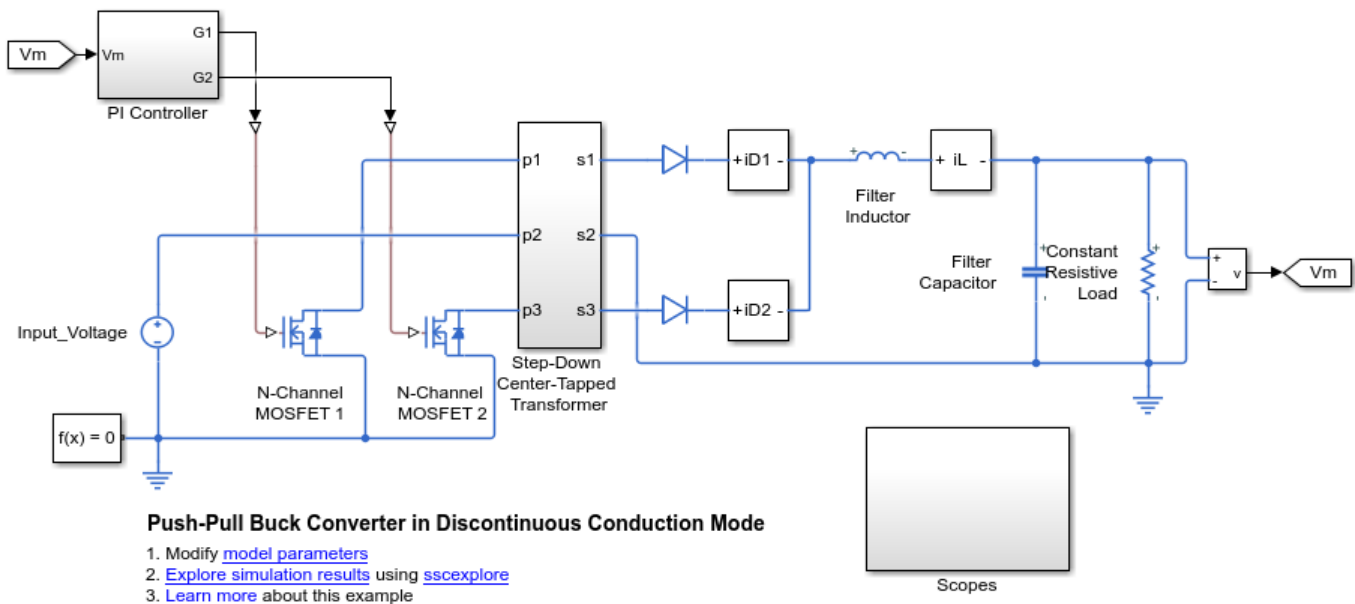
Specify the Design Parameters

The system is required to generate and maintain an output voltage of 80 V with a power requirement of 100 W. The input voltage is 400 V and the transformer turns ratio is 2. The load includes a constant resistive load. The 'ee_push_pull_converter_dcm_data.m' script defines the design parameters as variables in the MATLAB® workspace.

```
Input_Voltage          = 400;                              %  Input Voltage to the push-pull
Output_Voltage         = 80;                               %  Desired Output Voltage from the
Output_Power           = 1000;                             %  Full Load Power Output [W]
fsw_Hz                 = 40000;                            %  MOSFET Switching Frequency [Hz]
primary_winding        = 200;                              %  Number of turns in the primary
secondary_winding      = 100;                              %  Number of turns in the secondary
TR                     = primary_winding/secondary_winding;  %  Turns Ratio
Kp                     = 0.01;                             %  Proportional Gain for PI Contro
Ki                     = 20;                               %  Integral Gain for PI Controller
del_V                  = 1;                                %  Peak-Peak Output Voltage Ripple
K                      = 0.3;                              %  Denotes trajectory that gives a
Ts                     = 1e-7;                             %  Sampling time for the solver
```

Operating Boundary Between Discontinuous Mode of Conduction and Continuous Mode of Conduction

Based on the constraint established by K and the Duty Cycle, the operating mode switches between Discontinuous Conduction Mode (DCM) and Continuous Conduction Mode (CCM). The Output Voltage Ratios of CCM and DCM are:

- Continuous Conduction Mode

- $V_{output}/V_{input} = D$

- Discontinuous Conduction Mode

- $V_{output}/V_{Input} = 2/(1 + \sqrt{1 + 4 * K/D^2})$

Calculate the Open-Loop Duty Cycle for DCM

The operating modes can be visualised for different values of K by generating a plot between the Output Voltage Ratio and the Duty Cycle. From this plot, find the corresponding Duty Cycle needed to achieve the Output Voltage Ratio as specified in the Design Parameters for the particular value of K chosen.

```
figure;
D_range = 0:0.001:1;
Voltage_ratio = zeros(length(D_range));
for i=1:length(D_range)
    K_crit = 1-D_range(i);
    if K < K_crit
        Voltage_ratio(i) = 2/(1+sqrt(1+4*K/D_range(i)^2));
    else
        Voltage_ratio(i) = D_range(i);
    end
end
VR = Output_Voltage/(Input_Voltage/TR);
Duty = sqrt(4*K/((2/VR-1)^2-1));
hold on;
```

```
plot(Voltage_ratio,D_range);
hold on;
plot(VR,Duty,'*');
ylabel('Duty Cycle');
xlabel('Output to Input Voltage Ratio');
title('Discontinuous Mode Operating Region');

% The Open-Loop Duty Cycle required would be approximately 28.28% from the
% graph.
```
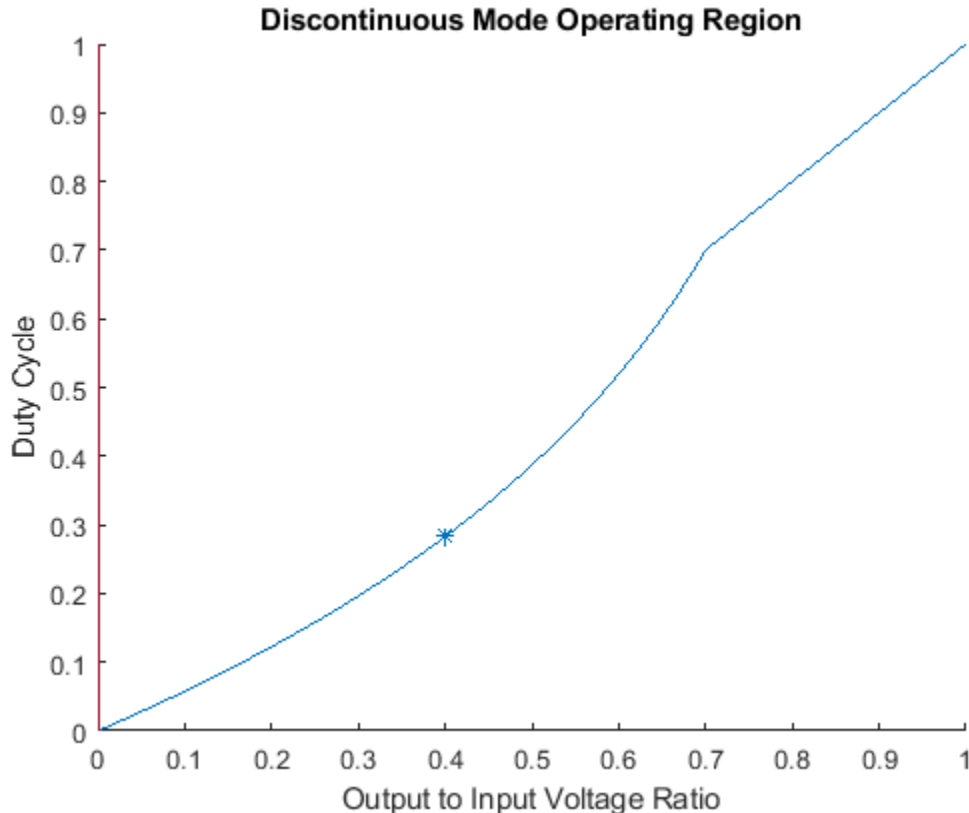

Determine the Constant Load Resistance

```
I_average = Output_Power/Output_Voltage;                    %  Average current that flows throug
R_const = Output_Voltage/I_average;
```

Determine the Filter Inductance

To estimate the inductance required for DCM, use this relationship between K, Resistance, and the Switching Time Period.

```
L_min = (K*R_const)/(2*2*fsw_Hz);
```

Verify the estimated inductance values result in DCM operation. If the value results in CCM operation, choose a different value of K and recalculate. Iterate until you find an inductance value that results in DCM operation.

Choose a Filter Capacitance

The relation between the capacitance and the Output Voltage Ripple is

```
C_min = (2-(Duty/(Output_Voltage/(Input_Voltage/TR)))))^2/(4*R_const*2*fsw_Hz*del_V*0.01);
```

Plot Capacitance Versus Voltage Ripple

Generate a plot to see the capacitance that is required for limiting the output voltage ripple varies depending on the design parameters. For this example, the marker at 1% Output Voltage Ripple corresponds to a capacitance of 8.157e-06 F.

```
del_V_range = 0.5:0.1:5;
C_range = (2-(Duty/(Output_Voltage/(Input_Voltage/TR)))))^2./(4*R_const*2*fsw_Hz*del_V_range*0.01);
figure;
hold on;
plot(del_V_range,C_range);
hold on;
plot(del_V,C_min,'*');
xlabel('Voltage Ripple (%)');
ylabel('Capacitance (F)');
title('Capacitance Vs Voltage Ripple');
```
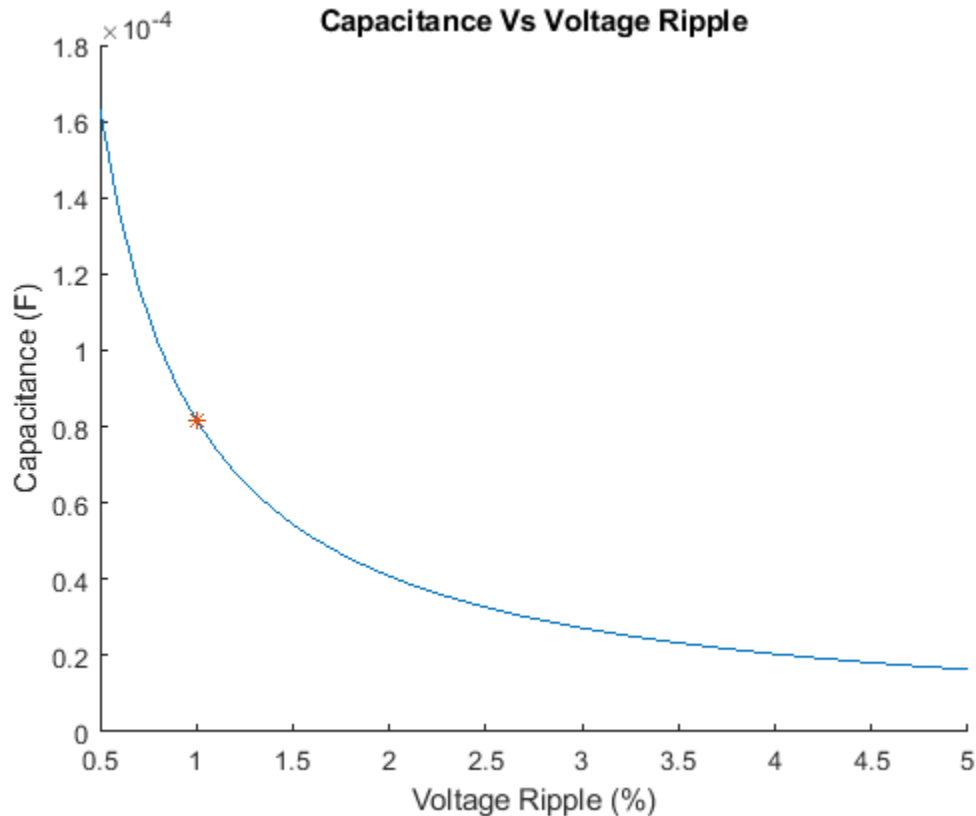

Run the Simulation

```
sim('ee_push_pull_converter_dcm.slx');
```

View Simulation Results

To view summary results during or after the simulation, open the Circuit Scope block from the Scopes subsystem or by entering, at the MATLAB command prompt:

```
open_system('ee_push_pull_converter_dcm/Scopes/Circuit Scope');
```
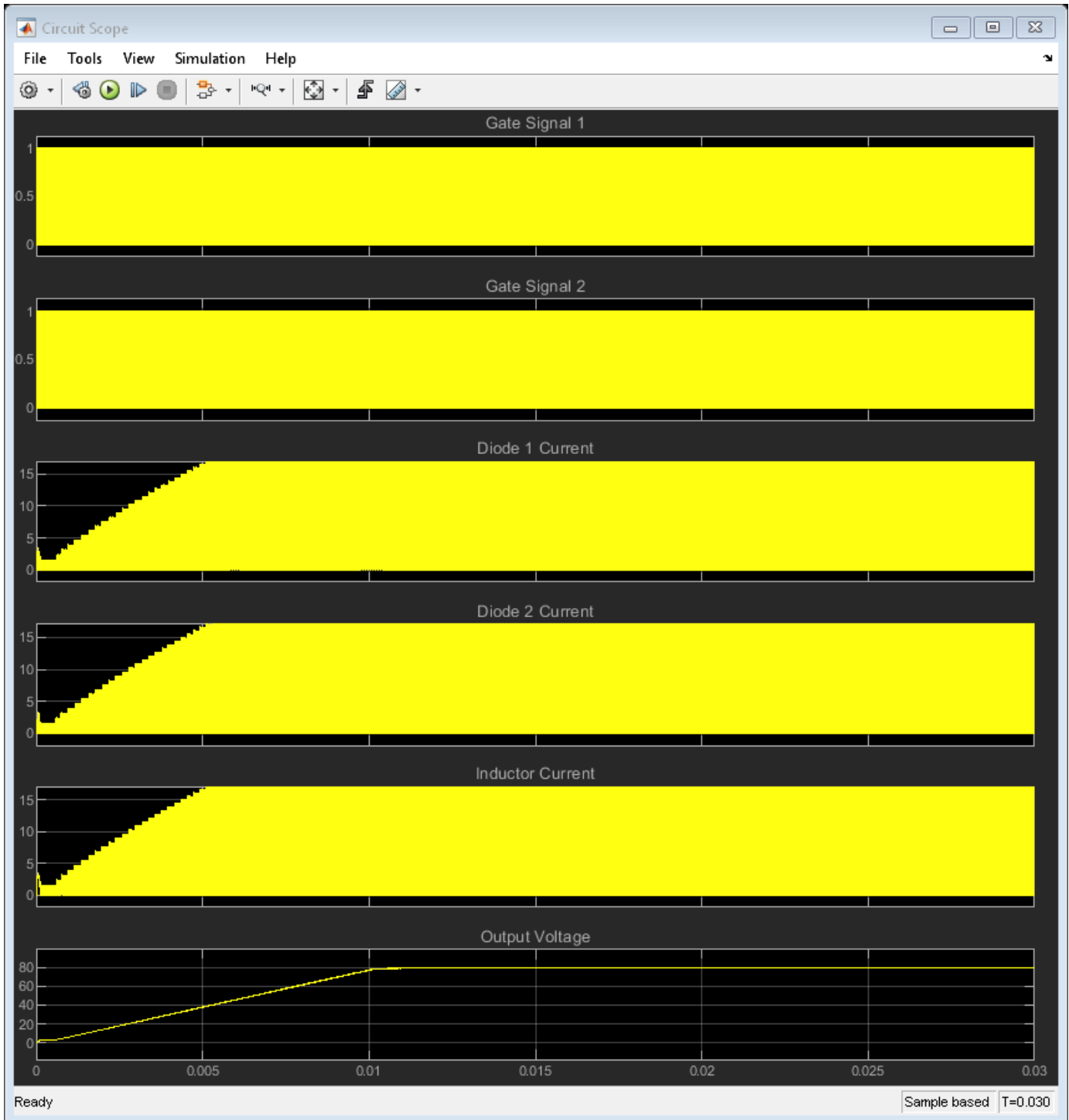
To view the control and error data during or after the simulation, open the PI Controller Scope block from the Scopes subsystem or, enter:

```
open_system('ee_push_pull_converter_dcm/Scopes/PI Controller Scope');
```
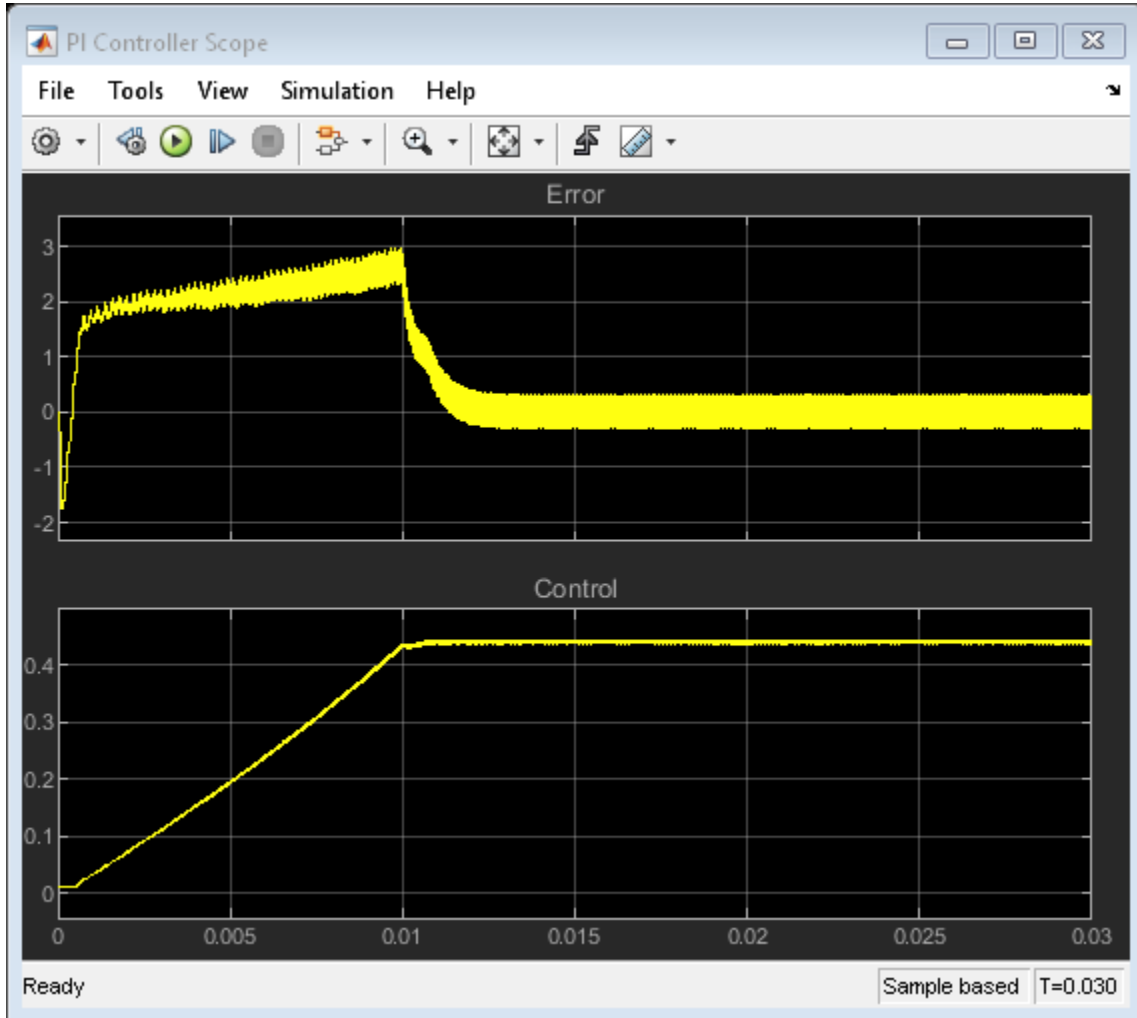

After the simulation, to view logged Simscape™ data using the Simscape Results Explorer, enter:

```
sscexplore(simlog_ee_push_pull_converter_dcm);
%
```
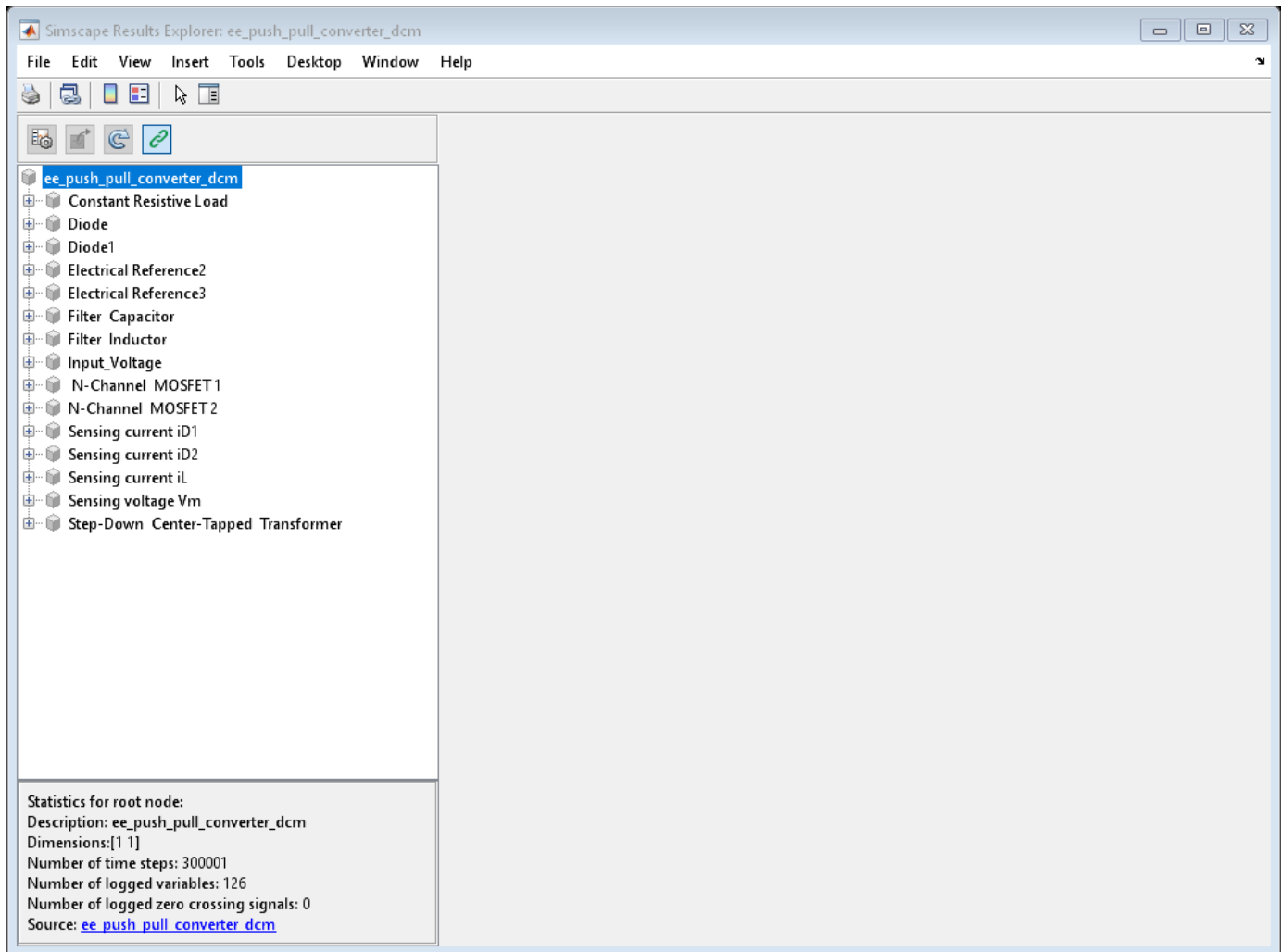
Three-Phase Three-Level PWM Generator

This example shows how to use the PWM Generator (Three-phase, Three-level) to control a Three-Level Converter. The upper and lower supply voltages are input to a Neutral point controller, which balances the DC-link capacitor voltages. Reference AC waveforms are used as inputs to the PWM Generator. There is one time scope for the controller waveforms.

Model
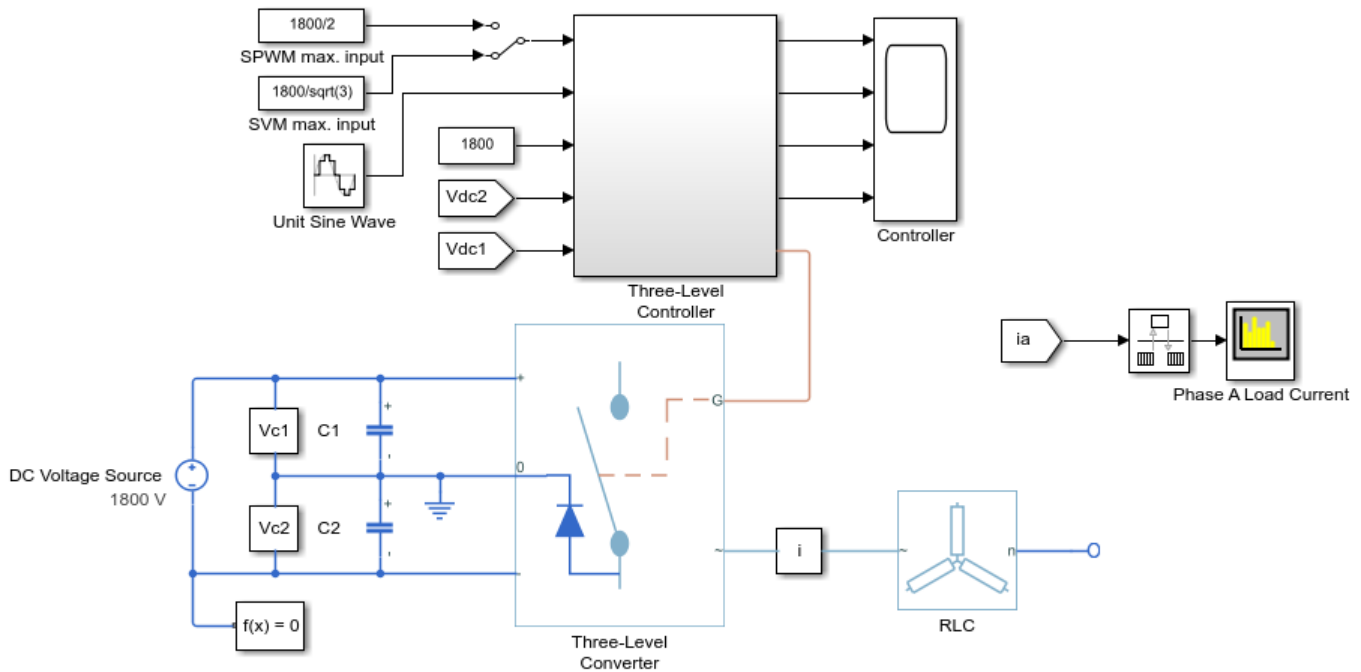
Three-Phase Three-Level PWM Generator

1. Plot currents and voltages in RLC load (see code)
2. Explore simulation results using sscexplore
3. Learn more about this example

Simulation Results from Simscape Logging

The plot below shows the load voltages and currents over time.
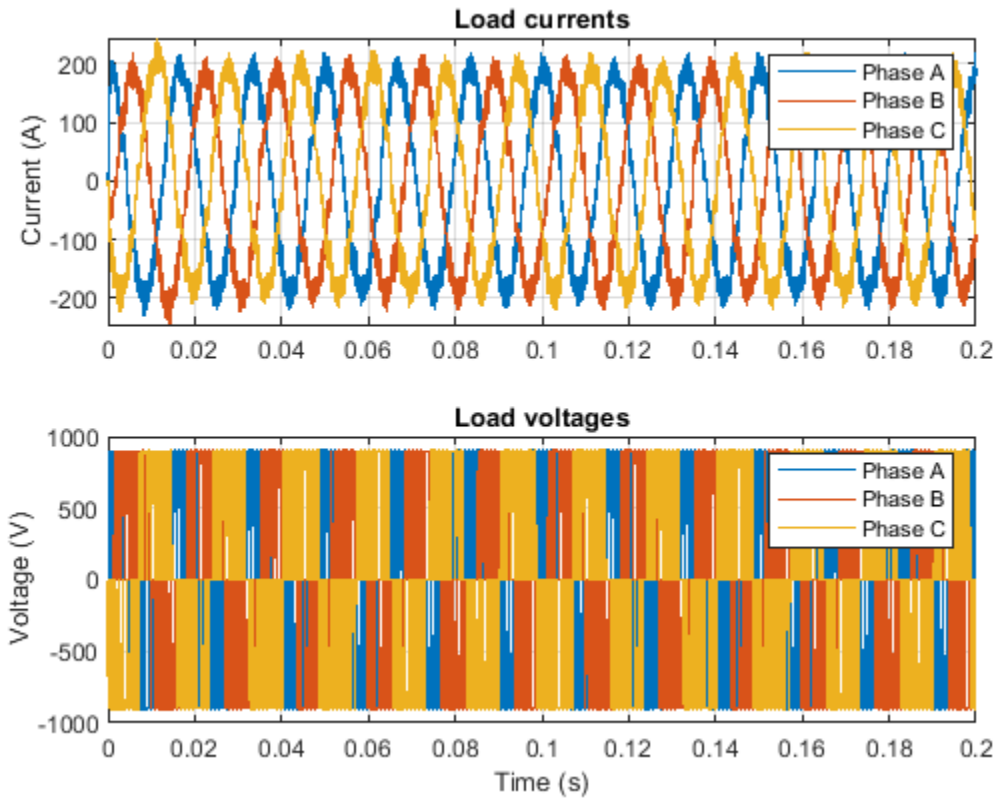
Simulation Results from Scopes

The spectrum plotted in the Spectrum Analyzer displays frequency data for the Phase A Load Current.
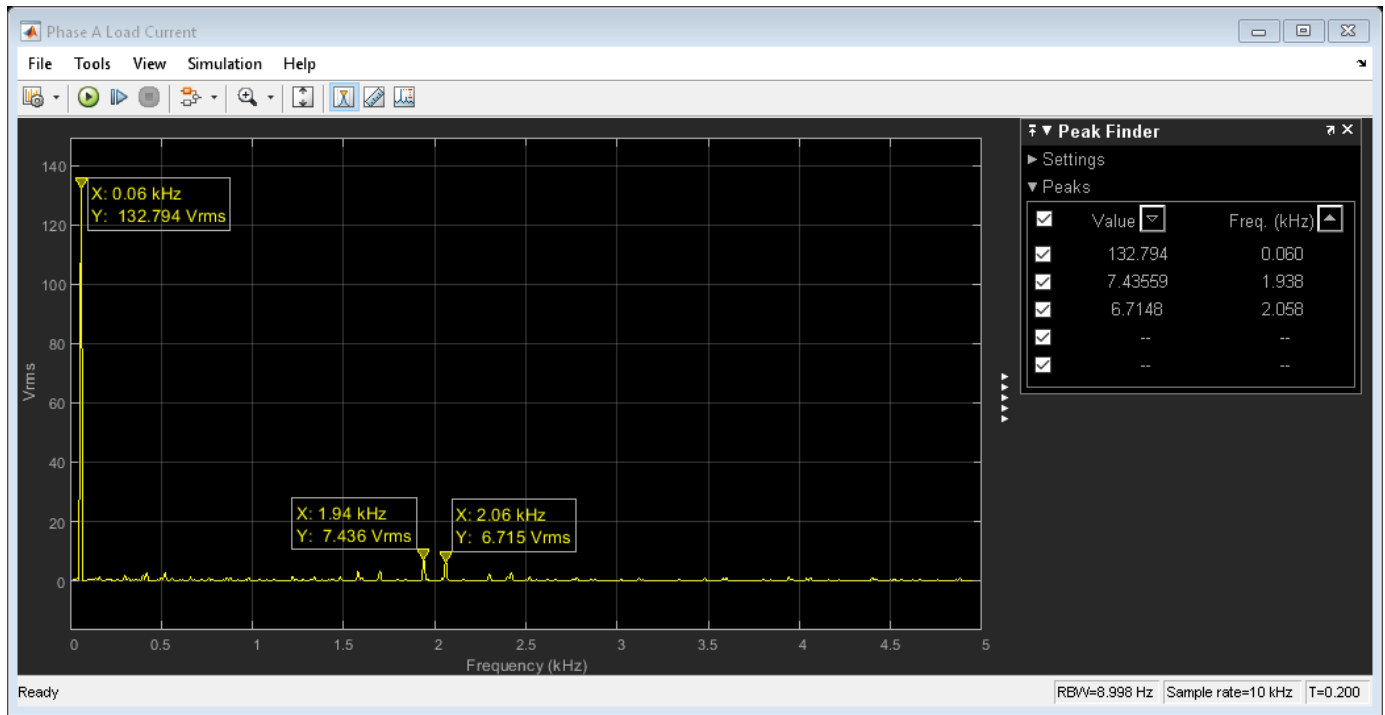
Three-Phase Two-Level PWM Generator

This example shows how to use the PWM Generator (Three-phase, Two-level) to control a Converter. The inputs to the PWM Generator are reference AC waveforms and a DC-link voltage of 400 V. There is one time scope for the controller waveforms.
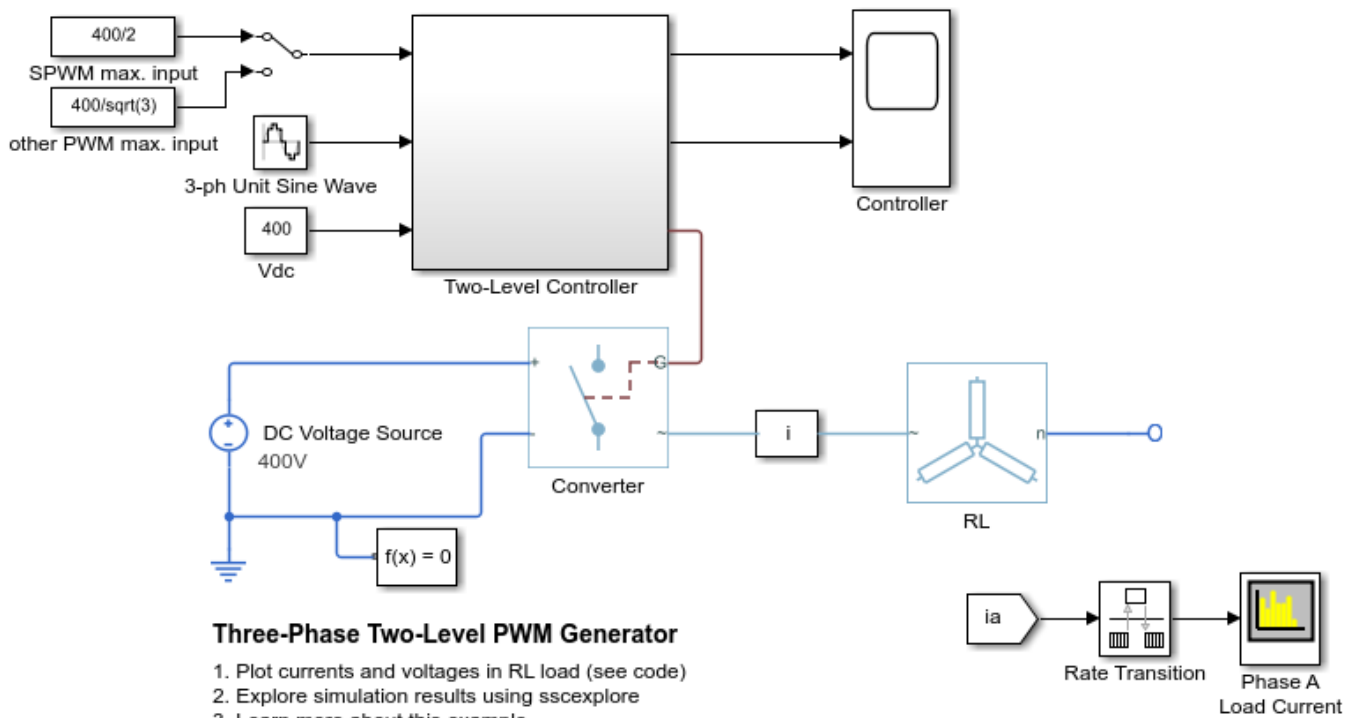
Model

Three-Phase Two-Level PWM Generator

1. Plot currents and voltages in RL load (see code)
2. Explore simulation results using sscexplore
3. Learn more about this example

Simulation Results from Simscape Logging

The plot below shows the load voltages and currents over time.
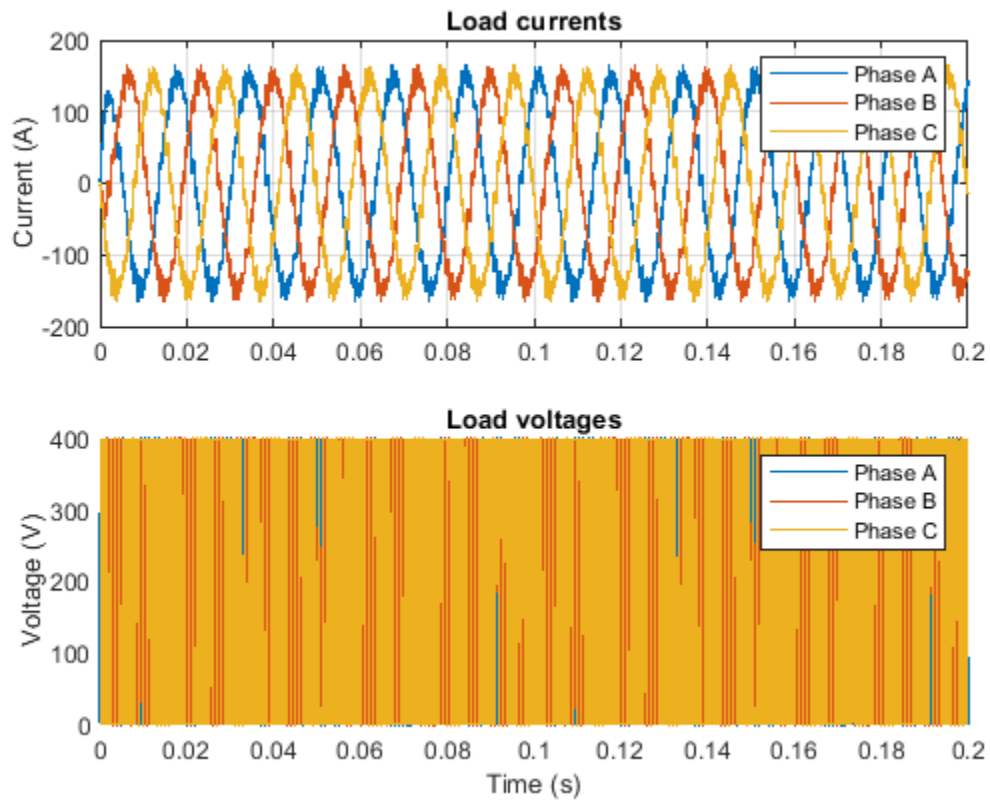
Simulation Results from Scopes

The spectrum plotted in the Spectrum Analyzer displays frequency data for the Phase A Load Current.
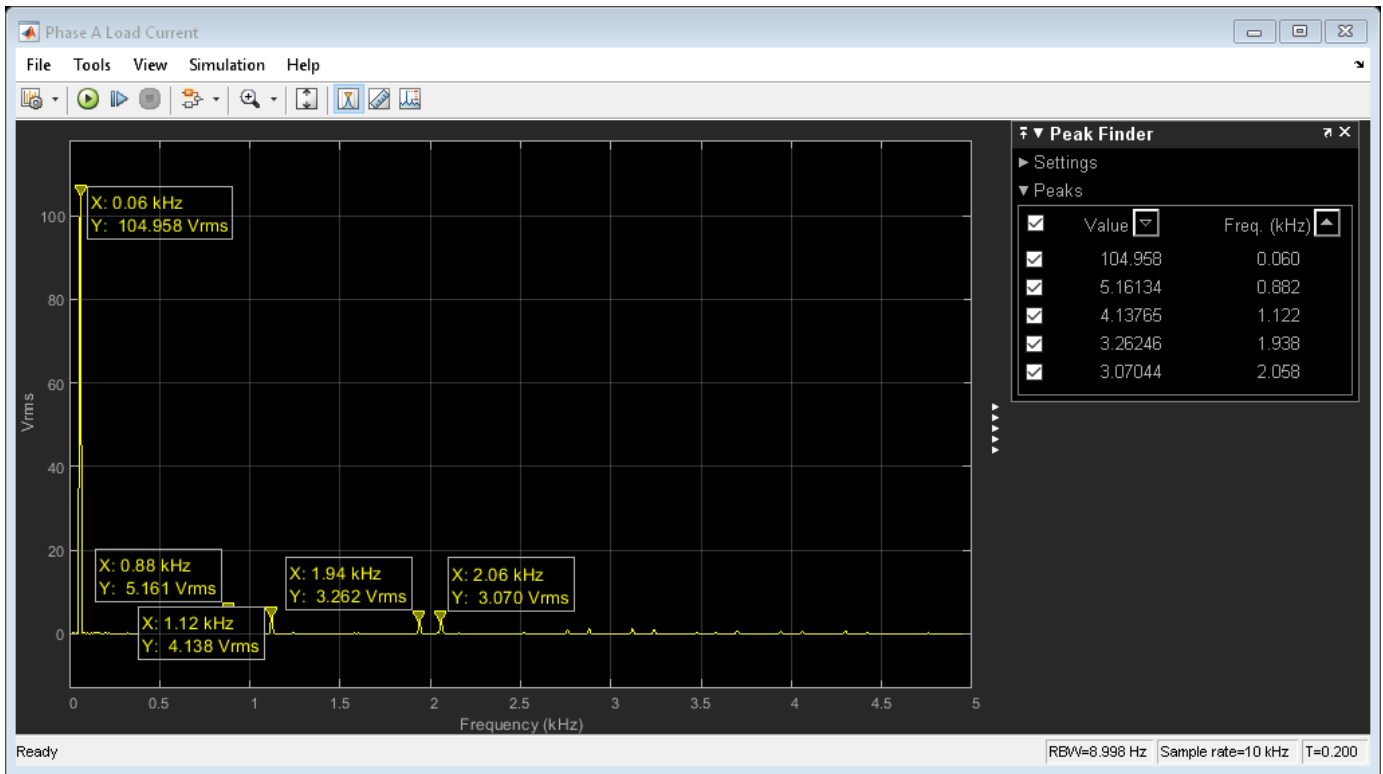
Power-Loss Analysis of a Three-Phase Rectifier

This example shows how to perform a power-loss analysis using the ee_getPowerLossSummary function and the power_dissipated variable. The Rectifier contains six Diode blocks. Diodes D1, D2, D4, and D5 each dissipate, on average, 52.19 W over the course of the 0.5 second simulation. Diodes D3 and D6 each dissipate an average of 52.22 W for the same time period because they experience a 340 W transient power-dissipation spike due to DC-load capacitor charging before $t = 1e\text{-}3$ s. For each diode, the average power dissipated when the rectifier is operating in steady-state, $t = 0.4$ to 0.5 s, is 52.19 W.

Model

Power-Loss Analysis of a Three-Phase Rectifier

1. Calculate power dissipated by all of the diodes in the model for the entire simulation time (see command window for output).
2. Calculate power dissipated by diode D6 for the entire simulation time (see command window for output).
3. Explore simulation results using sscexplore.
4. Calculate the maximum power dissipated by diode D6. (see command window for output).
5. Calculate power dissipated by diode D6 between simulation time, t = 1e-3 to 0.5 s (see command window for output).
6. Plot power dissipated by the diodes (see code).
7. Calculate power dissipated by all of the diodes between simulation time, t = 0.4 to 0.5 s (see command window for output).
8. Learn more about the ee_getPowerLossSummary function.

Single-Phase Asynchronous Machine Direct Torque Control

This example shows how to control the rotor speed in a single-phase asynchronous machine (ASM) based electrical drive using direct torque control. An ideal torque source provides the load. The Control subsystem uses a cascade control structure. An outer PI-based speed control loop provides the torque and flux references to the direct torque control algorithm from the inner loop. The single-phase ASM is fed by an H bridge. The Scopes subsystem contains scopes that allow you to see the simulation results.

Model

Single-Phase Asynchronous Machine Direct Torque Control

1. Plot speed of rotor (see code)
2. Plot torque and flux (see code)
3. Explore simulation results using sscexplore
4. Modify model parameters
5. Learn more about this example
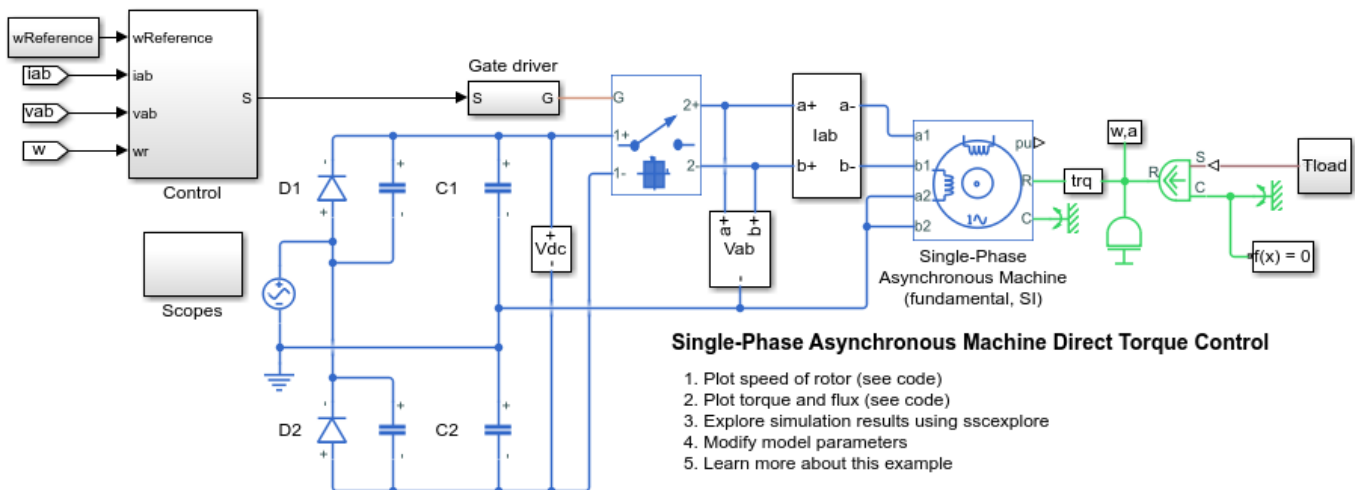
Simulation Results from Simscape Logging

The plot below shows the requested and measured speed for the test, as well as the stator currents in the electric drive.
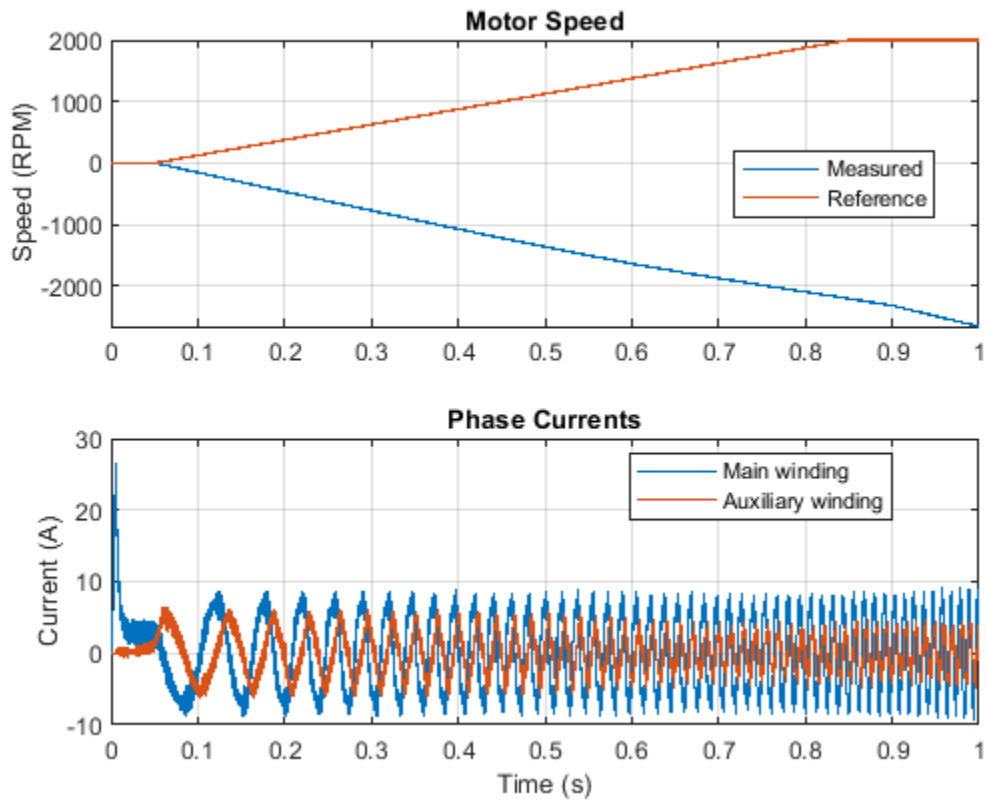
The plot below shows the requested and measured torque for the test, as well as the requested and estimated flux in the electric drive.
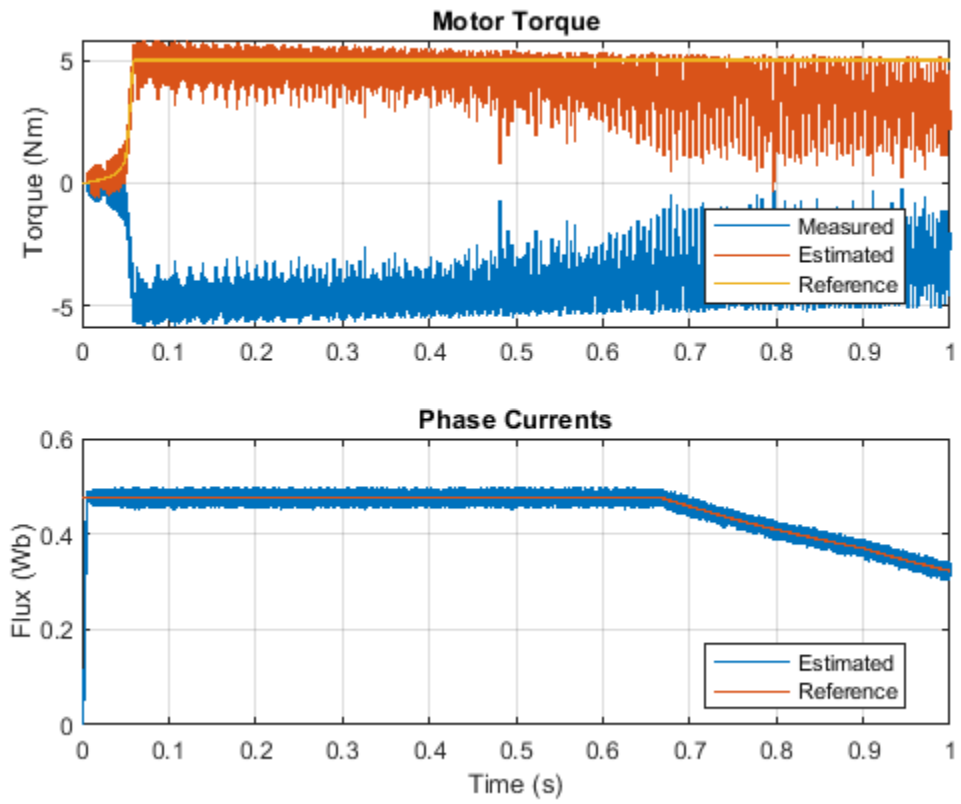
Single-Phase Asynchronous Machine Field-Oriented Control

This example shows how to control the rotor speed in a single-phase asynchronous machine (ASM) based electrical drive using field-oriented control. An ideal torque source provides the load. The Control subsystem uses a PI-based cascade control structure with an outer speed control loop and two inner current control loops. The single-phase ASM is fed by an H bridge. The Scopes subsystem contains scopes that allow you to see the simulation results.

Model

Single-Phase Asynchronous Machine Field-Oriented Control

1. Plot speed of rotor (see code)
2. Explore simulation results using sscexplore
3. Modify model parameters
4. Learn more about this example
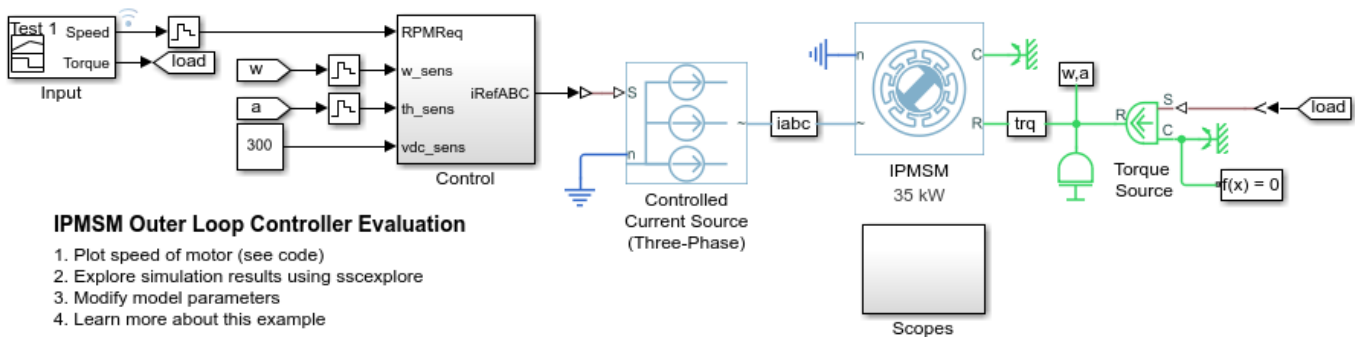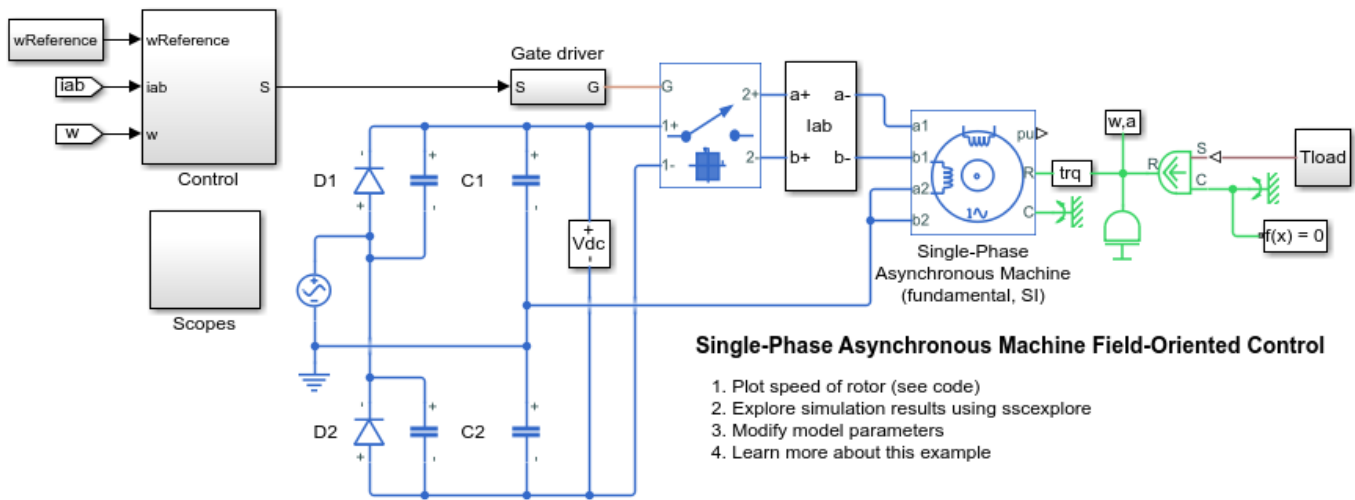
Simulation Results from Simscape Logging

The plot below shows the requested and measured speed for the test, as well as the phase currents in the electric drive.
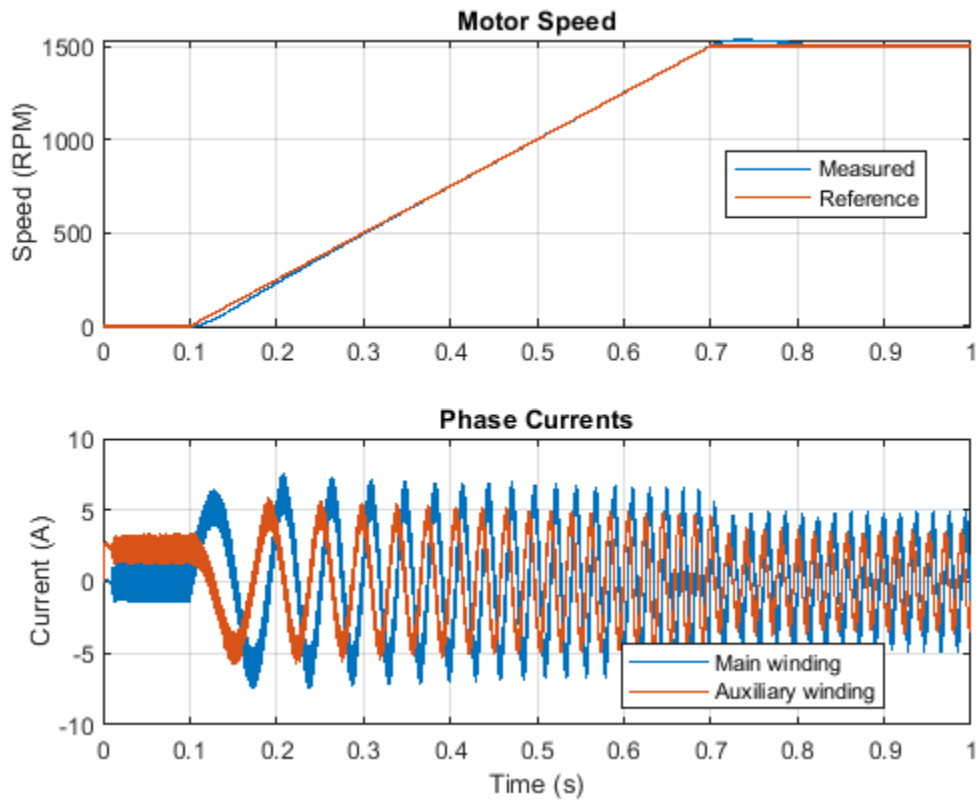
Three-Phase Synchronous Machine Control

This example shows how to control and initialize a Synchronous Machine (SM). The test circuit shows the SM operating as a generator. The terminal voltage is controlled using an AVR and the speed is controlled using a governor.

To view the SM machine base values and initial conditions, right-click the Synchronous Machine Round Rotor (standard) block and select 'Electrical' and then 'Display Base Values', 'Display Associated Base Values', or 'Display Associated Initial Conditions'. The overall model is initialized to start in periodic steady state to supply a load of 250 MW/15 Mvar.

Model

Three-Phase Synchronous Machine Control
1. Plot terminal voltage and rotor speed (see code)
2. Modify model parameters
3. Explore simulation results using sscexplore
4. Learn more about this example
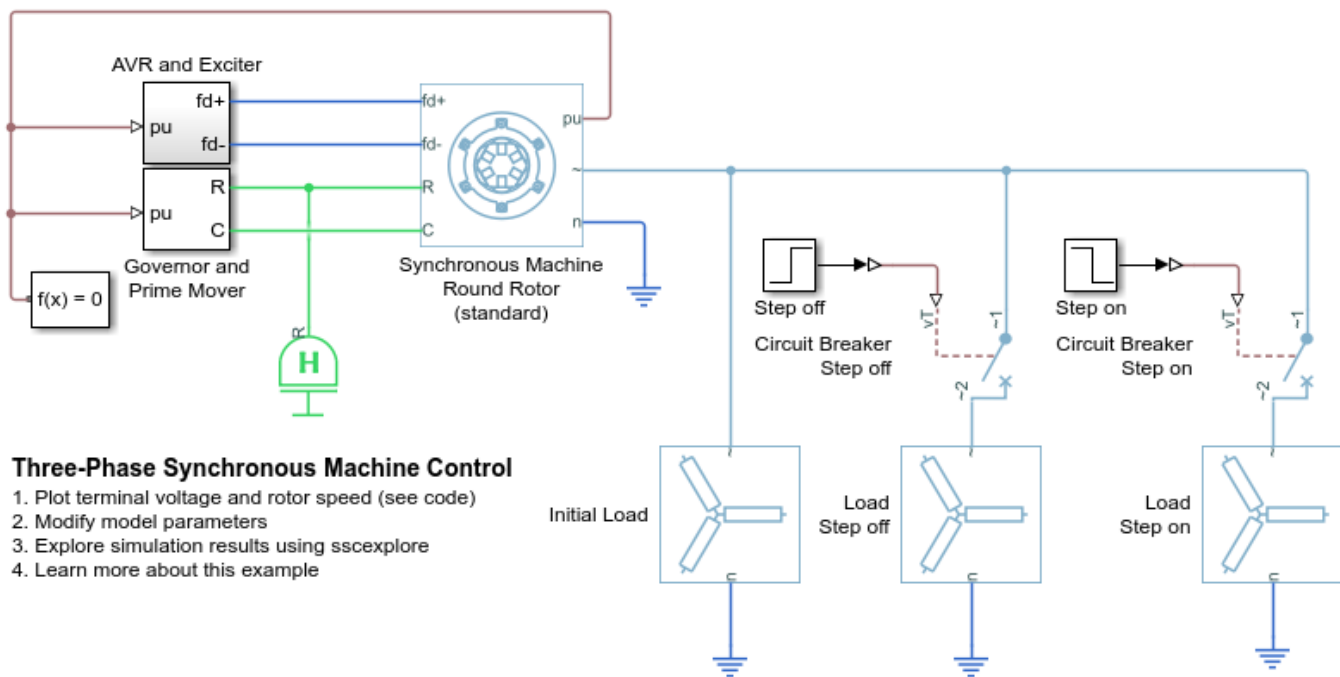
Simulation Results from Simscape Logging

The plot below shows the terminal voltage and rotor velocity for the Synchronous Machine.
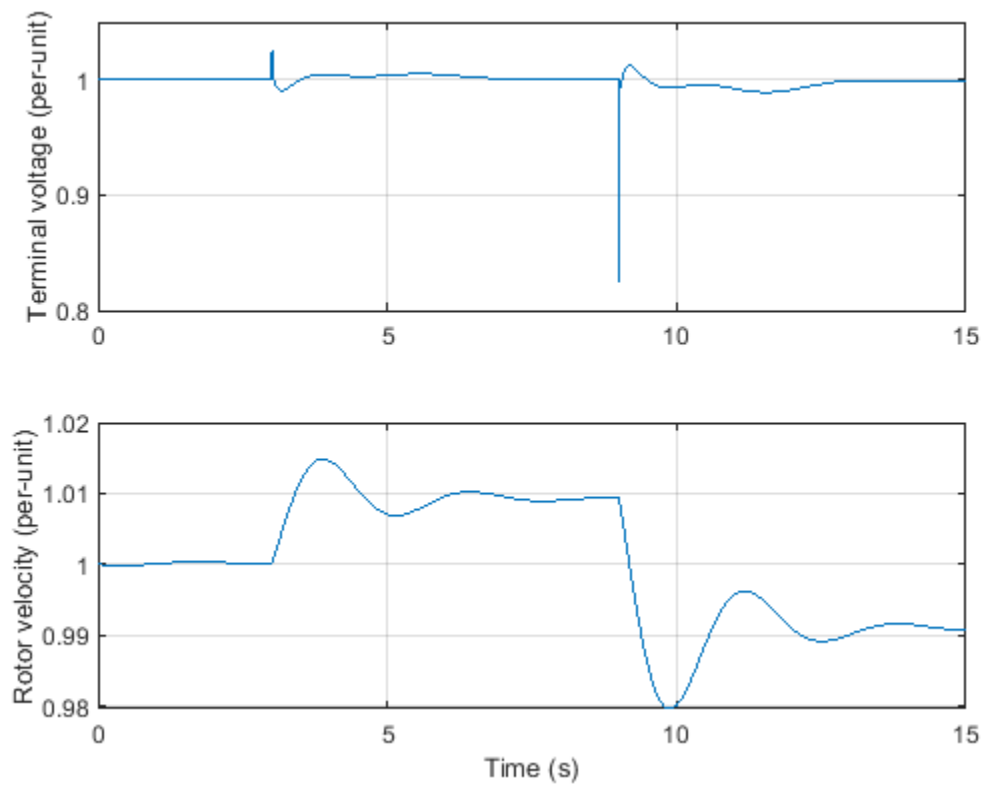
Three-Phase Synchronous Machine Drive

This example shows how to control the rotor speed in a Synchronous Machine (SM) based electrical drive. A high-voltage battery feeds the SM through a controlled three-phase converter for the stator windings and through a controlled two-quadrant chopper for the rotor winding. Use the model to design the SM controller, selecting architecture and gains to achieve desired performance. The Scopes subsystem contains scopes that allow you to see the simulation results.

Model
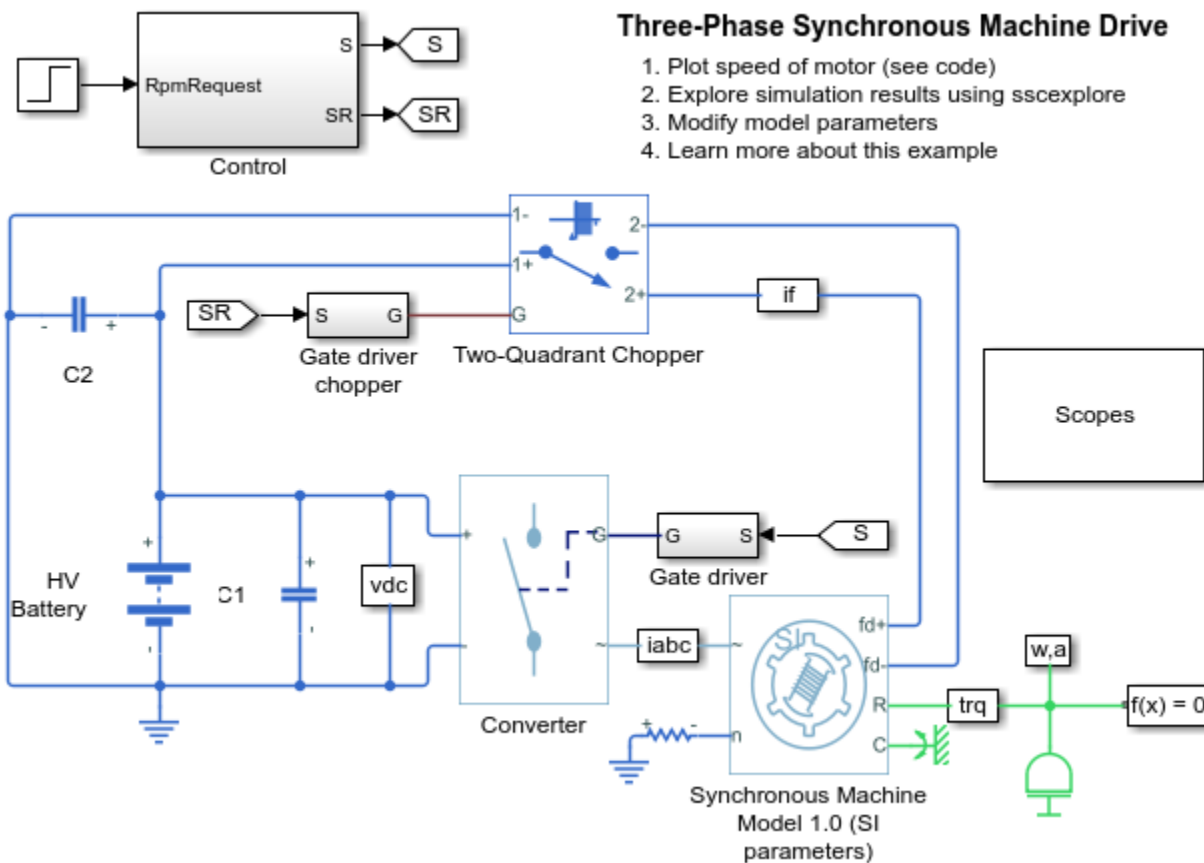
Simulation Results from Simscape Logging

The plot below shows the requested and measured speed for the test, as well as the phase currents in the electric drive.
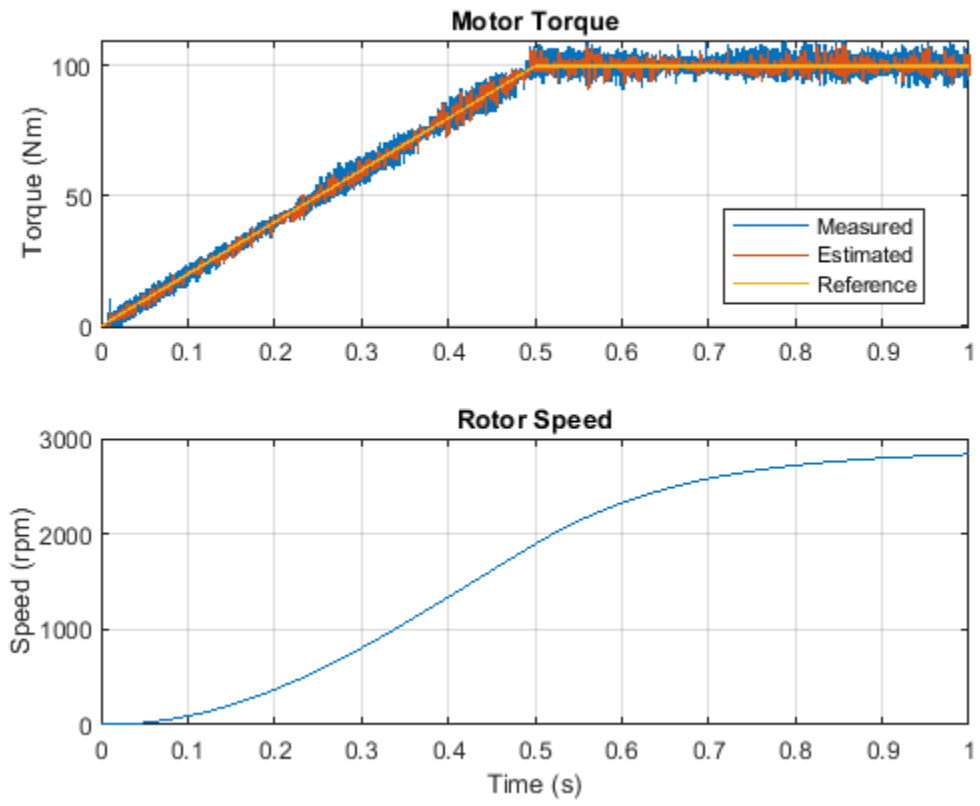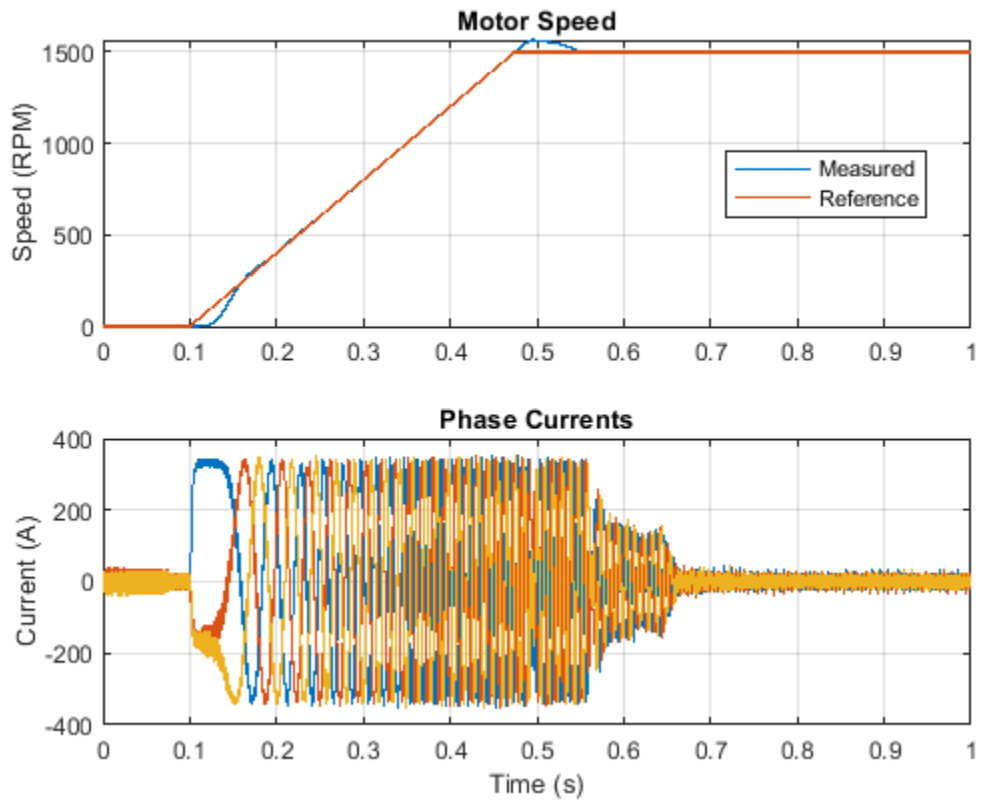
Three-Phase Synchronous Machine Governor Control Design

This example script shows how you can linearize a Simscape™ Electrical™ model to support control system stability analysis and design. It uses example model ee_sm_governor_control_design.

The Synchronous Machine in this example operates as a generator. The machine is initialized to start in periodic steady state to supply a load of 250 MW/15 Mvar.

An alternative and recommended way to linearize Simulink® and Simscape models is to use Simulink Control Design™. Simulink Control Design linearizes your model at operating points you specify. It also returns a state-space model object with state names. If you have Simulink Control Design, open the model ee_sm_governor_control_design. On the Apps tab, under Control Systems, click Model Linearizer. In the Model Linearizer, on the Linear Analysis tab, in the Setup section, select Operating Point > Linearize At. Set the simulation snapshot time to 4 seconds, then click OK. In the Linearize section, click Bode.

Open Model

Open the model.

```
open_system('ee_sm_governor_control_design')
set_param(find_system('ee_sm_governor_control_design','FindAll', 'on','type','annotation','Tag',
```
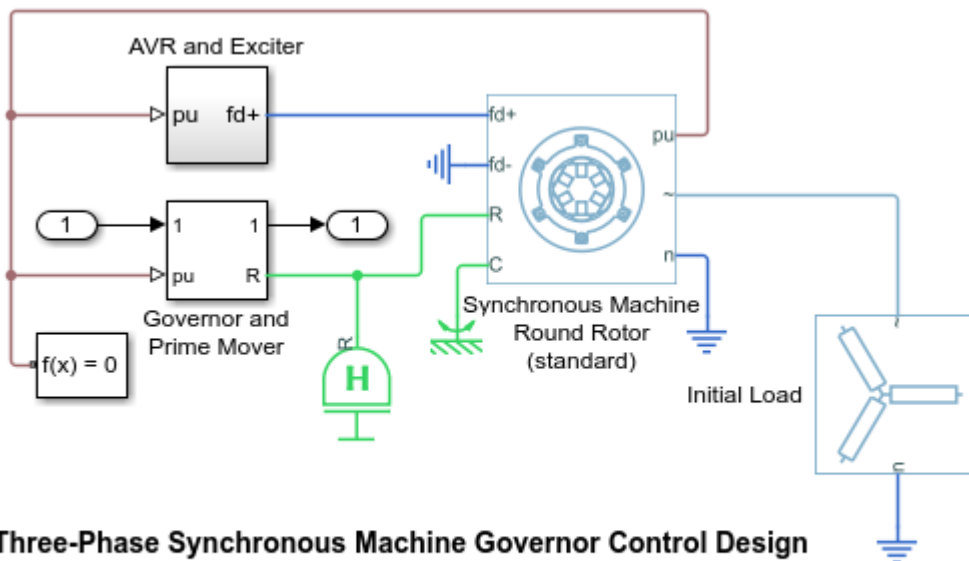

Three-Phase Synchronous Machine Governor Control Design
1. Modify script that generates the linear model
2. Generate a linearized version of the model
3. Explore simulation results using sscexplore
4. Learn more about this example

Trim Model

Trim the model by running closed-loop and selecting the state at 4 seconds when generator is in steady-state.

```
assignin('base','ClosedLoop',1); % Close the speed-control feedback loop
[t,x,y] = sim('ee_sm_governor_control_design');
```

```
idx = find(t>4,1);
X = x(idx,:); U = y(idx);
```

Linearize Speed-Control Feedback Loop

Open the speed-control feedback loop, linearize the model, and close the speed-control feedback loop.

```
assignin('base','ClosedLoop',0); % Open the speed-control feedback loop
[a,b,c,d]=linmod('ee_sm_governor_control_design',X,U);
assignin('base','ClosedLoop',1); % Close the speed-control feedback loop
```

Bode Diagram

Plot the Bode diagram.

```
c = -c; d = -d; % Negative feedback convention
npts = 100; w = logspace(-3,5,npts); G = zeros(1,npts);
for i=1:npts
    G(i) = c*(1i*w(i)*eye(size(a))-a)^-1*b +d;
end
figure
ax1 = subplot(2,1,1);
semilogx(w,20*log10(abs(G)))
grid on
ylabel('Magnitude (dB)')
title('Bode diagram: speed-control feedback loop')
ax2 = subplot(2,1,2);
semilogx(w,180/pi*unwrap(angle(G)))
ylabel('Phase (degrees)')
xlabel('Frequency (rad/s)')
linkaxes([ax1,ax2],'x')
xlim(w([1 end]))
grid on
```

Bode diagram: speed-control feedback loop

Synchronous Machine State-Space Control

This example shows how to control currents in a synchronous machine (SM) based traction drive using state-space control. A high-voltage battery feeds the SM through a controlled three-phase converter for the stator windings and through a controlled two-quadrant chopper for the rotor winding. An ideal angular velocity source provides the load. The SM operates below the base speed. At each sample instant, the torque request is converted to relevant current references using the zero d-axis control approach. A state-feedback controller controls the currents in the rotor reference frame. A Luenberger observer obtains the velocity-dependent feedforward pre-control terms. The simulation uses several torque steps in both motor and generator modes. The task scheduling is implemented as a Stateflow® state machine. The Scopes subsystem contains scopes that allow you to see the simulation results.

Model

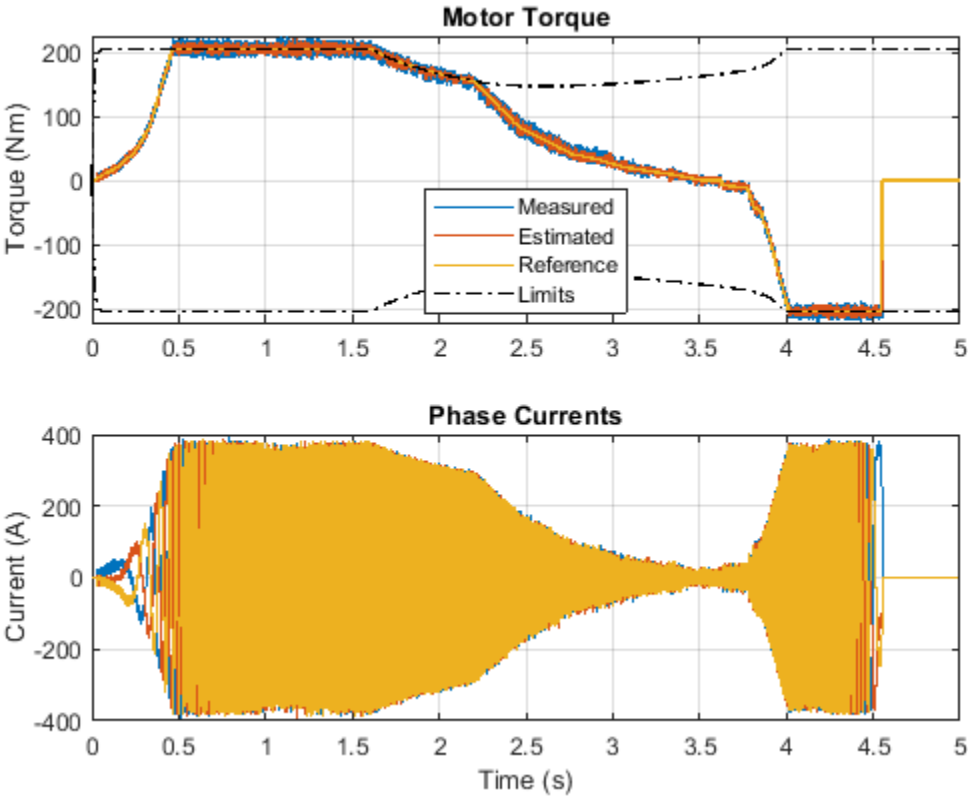### Synchronous Machine State-Space Control
1. Plot torque of motor (see code)
2. Explore simulation results using sscexplore
3. Modify model parameters
4. Learn more about this example

Simulation Results from Simscape Logging
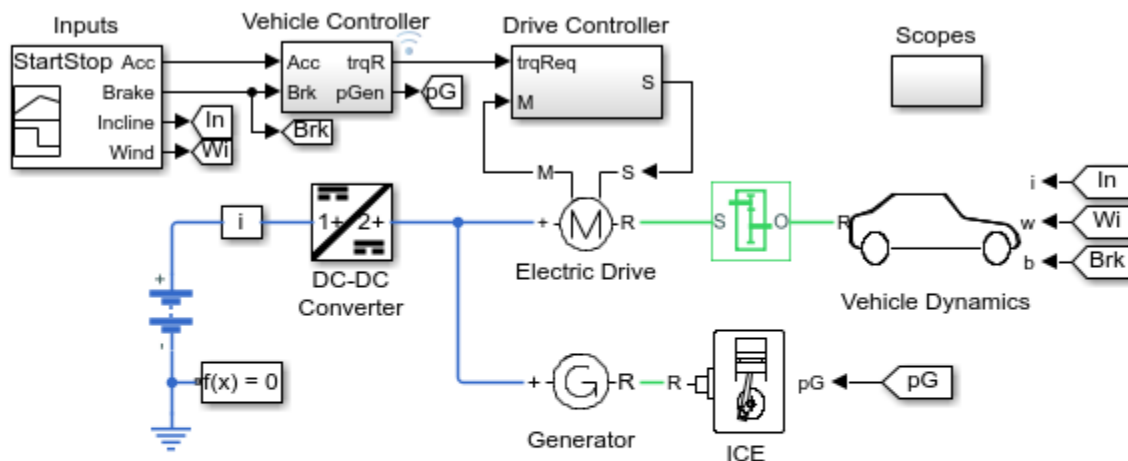
The plot below shows the requested and measured torque for the test, as well as the phase currents in the electric drive.

SM Torque Control

This example shows how to control the torque in a synchronous machine (SM) based electrical-traction drive. A high-voltage battery feeds the SM through a controlled three-phase converter for the stator windings and a controlled four quadrant chopper for the rotor winding. An ideal angular velocity source provides the load. The Control subsystem uses an open-loop approach to control the torque and a closed-loop approach to control the current. At each sample instant, the torque request is converted to relevant current references. The current control is PI-based. The simulation uses several torque steps in both motor and generator modes. The task scheduling is implemented as a Stateflow® state machine. The Visualization subsystem contains scopes that allow you to see the simulation results.

Model

SM Torque Control

1. Plot torque in Synchronous Machine (see code)
2. Modify model parameters
3. Explore simulation results using sscexplore
4. Learn more about this example
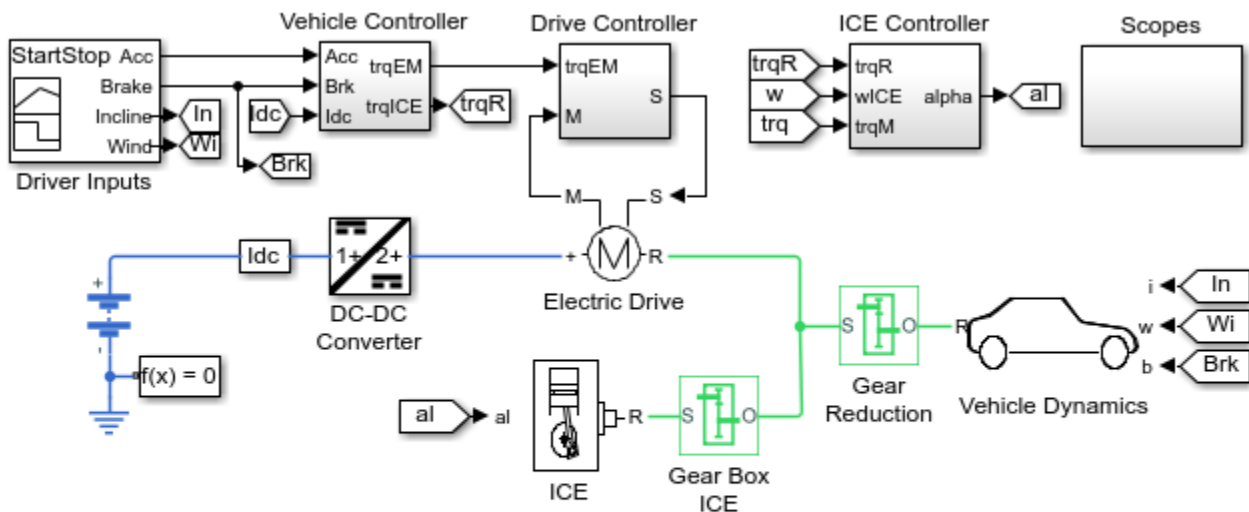
Simulation Results from Simscape Logging

The plot below shows the requested and measured torque for the test, as well as the phase currents in the electric drive.

SM Velocity Control

This example shows how to control the rotor angular velocity in a synchronous machine (SM) based electrical-traction drive. A high-voltage battery feeds the SM through a controlled three-phase converter for the stator windings and a controlled four quadrant chopper for the rotor winding. An ideal torque source provides the load. The Control subsystem includes a multi-rate PI-based cascade control structure which has an outer angular-velocity-control loop and three inner current-control loops. The task scheduling in the Control subsystem is implemented as a Stateflow® state machine. The Visualization subsystem contains scopes that allow you to see the simulation results.

Model

SM Velocity Control

1. Plot torque in Synchronous Machine (see code)
2. Explore simulation results using sscexplore
3. Modify model parameters
4. Learn more about this example

Simulation Results from Simscape Logging

The plot below shows the requested and measured torque for the test, as well as the phase currents in the electric drive.
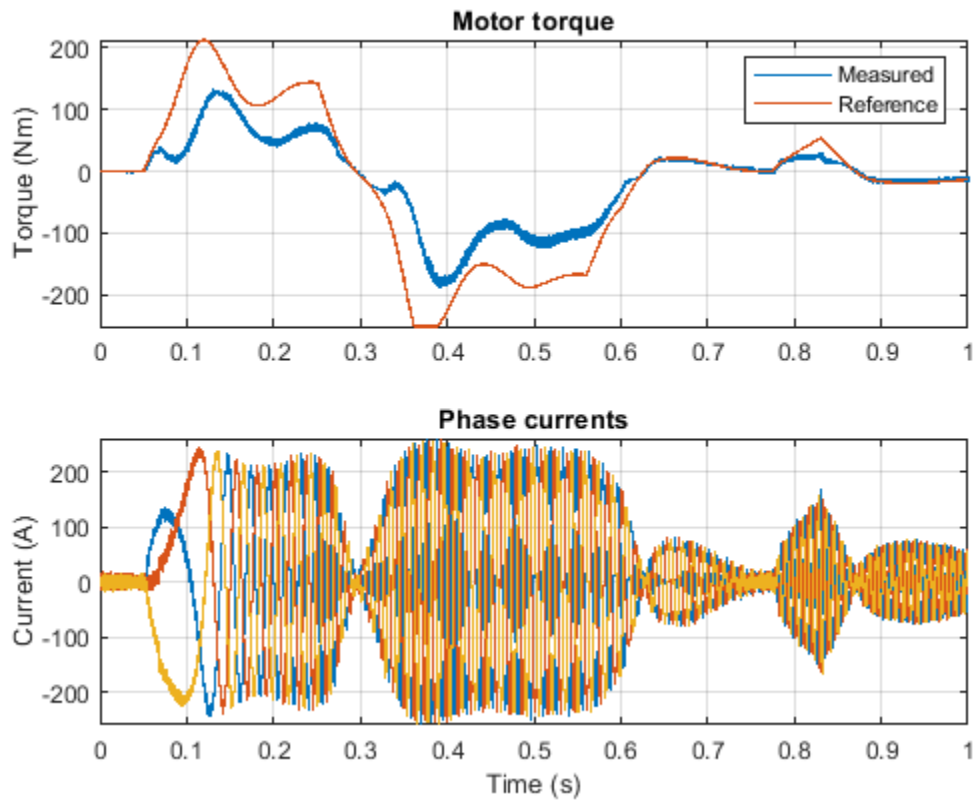
Switched Reluctance Machine Current Control

This example shows how to control the current amplitude in a switched reluctance machine (SRM) based electrical drive. A DC voltage source feeds the SRM through a controlled three-arm bridge. An ideal angular velocity source provides the load. The converter turn-on and turn-off angles are maintained constant. A PI-based current controller regulates the current amplitude.
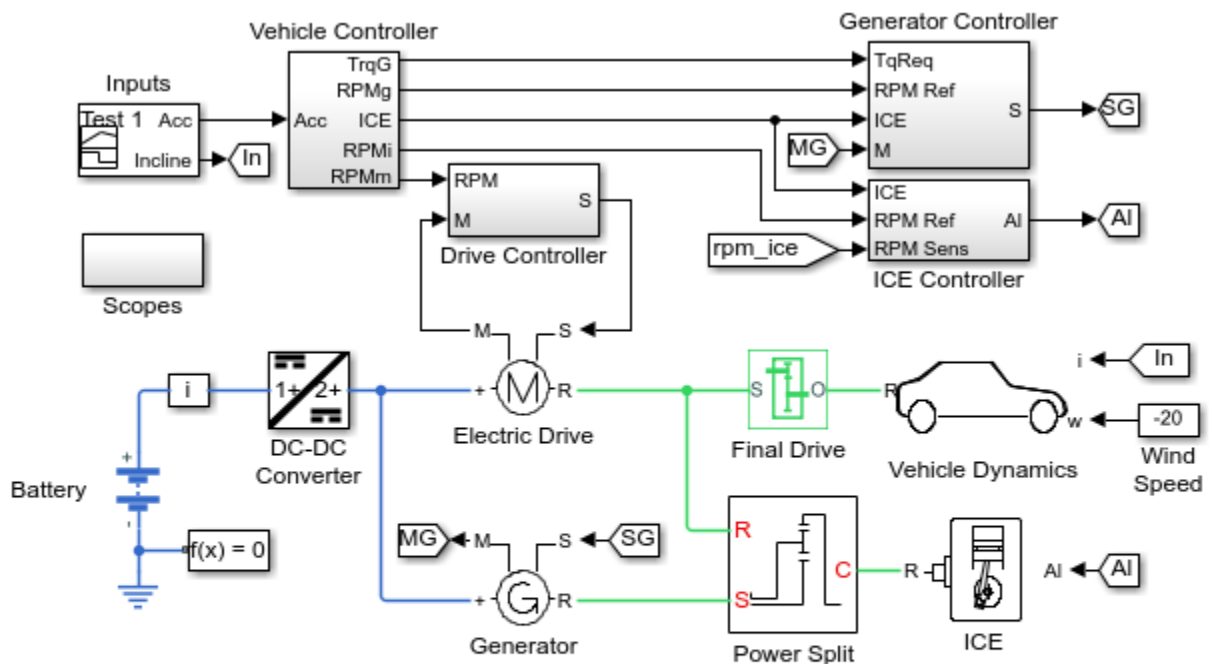
Model
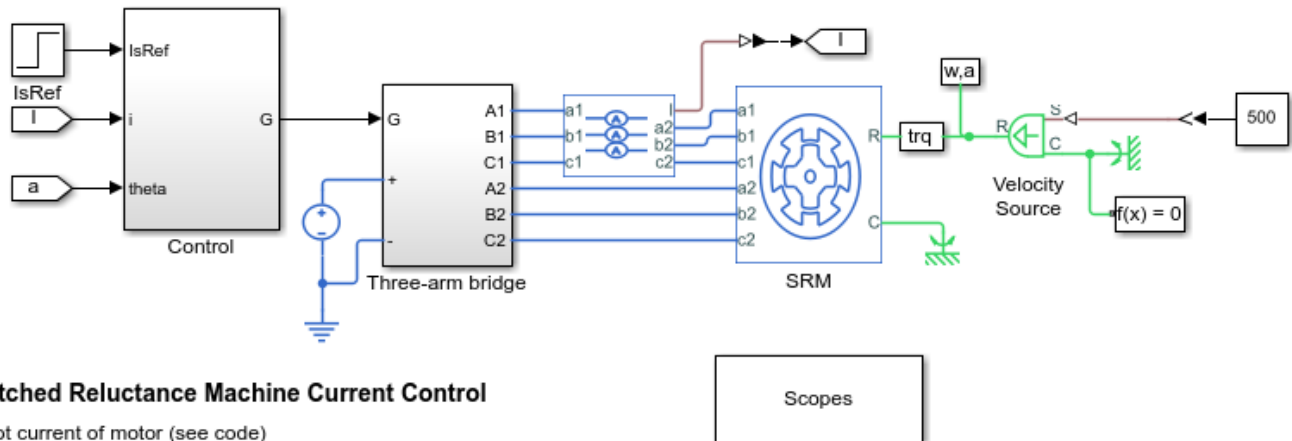
Switched Reluctance Machine Current Control

1. Plot current of motor (see code)
2. Explore simulation results using sscexplore
3. Modify model parameters
4. Learn more about this example

Simulation Results from Simscape Logging

The plot below shows the requested and measured current for the test and the electric torque in the electric drive.
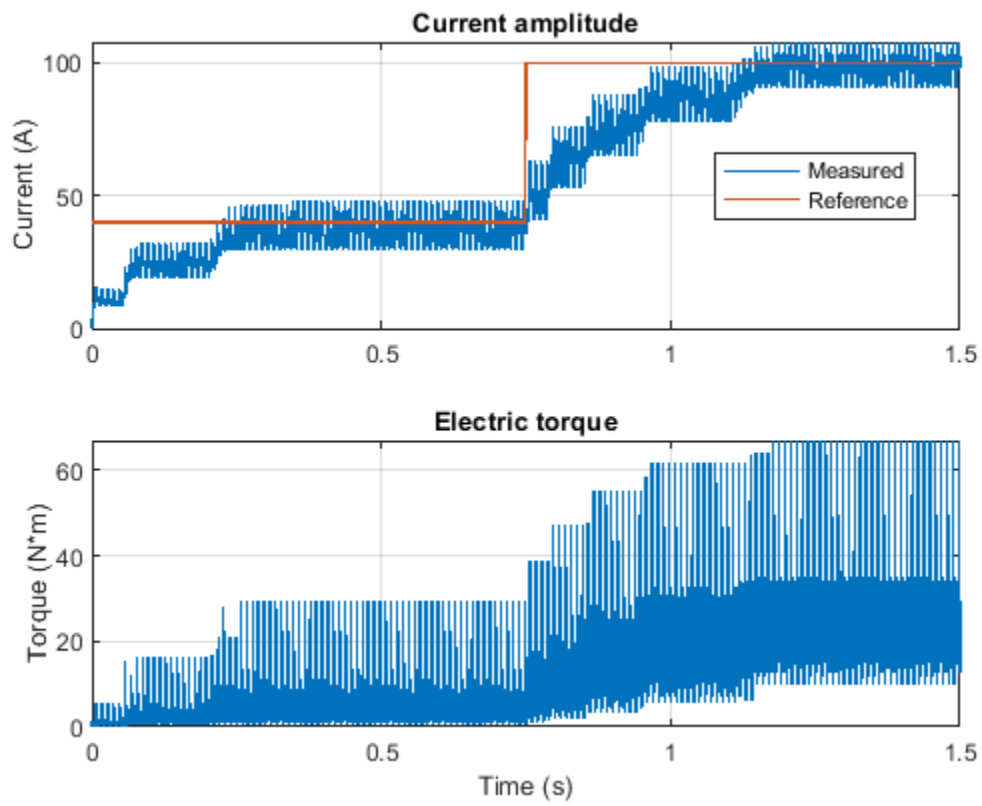
Switched Reluctance Machine Speed Control

This example shows how to control the rotor speed in a switched reluctance machine (SRM) based electrical drive. A DC voltage source feeds the SRM through a controlled three-arm bridge. The converter turn-on and turn-off angles are maintained constant.

Model
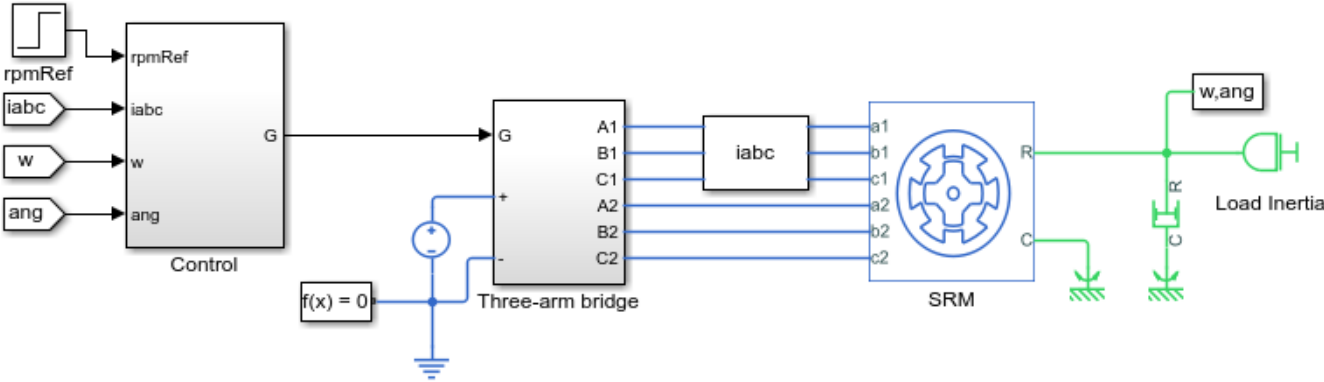
Switched Reluctance Machine Speed Control

1. Modify model parameters
2. Explore simulation results using sscexplore
3. Learn more about this example
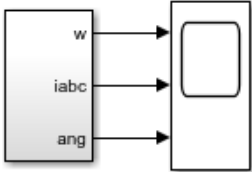
Supercapacitor Charging and Discharging Behavior

This example shows the voltage output by a Supercapacitor block as it is charged and then discharged. To charge the Supercapacitor, a current of 100 mA is input to the Supercapacitor for 100 seconds. The Supercapacitor is then rested for one minute. For the next hour, to discharge the Supercapacitor, a load of 50 mA is stepped on for one second in every 50 seconds. The Supercapacitor is then rested until the end of the simulation. The scope displays the Supercapacitor charging/discharging current and voltage.

Model
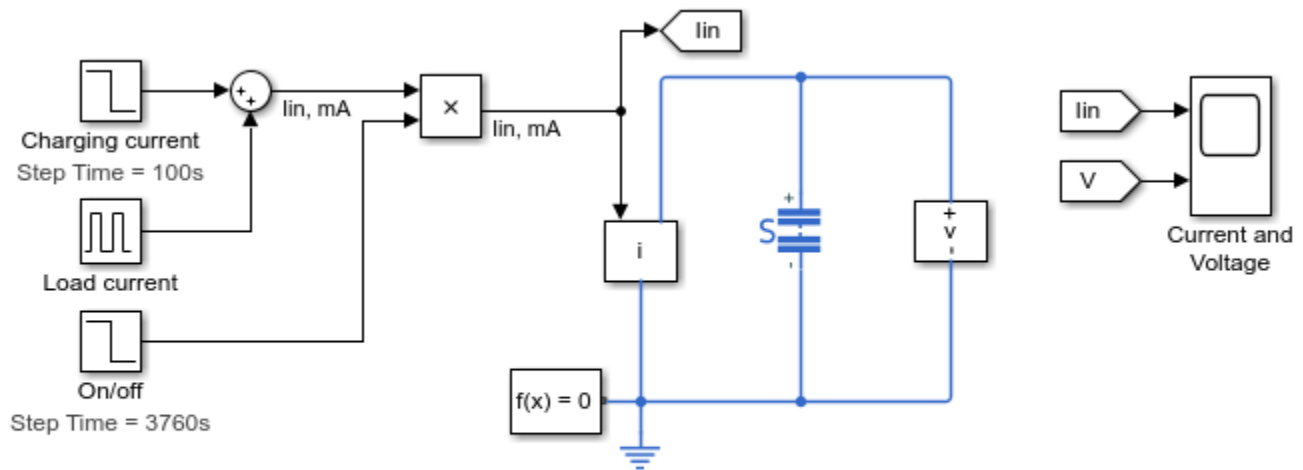
Supercapacitor Charging and Discharging Behavior
1. Explore simulation results using sscexplore
2. Learn more about this example

Supercapacitor Parameter Identification

This example shows how to identify the parameters of a supercapacitor. Instead of collecting voltage and current waveforms from a real supercapacitor, the example generates voltage and current waveforms by running a simulation of a supercapacitor using parameter values that are already known. Then the example applies a parameter identification methodology [1] to the waveforms.

To assess the accuracy of the methodology, the example compares the identified parameters to the known parameter values. The example also shows how to further refine the parameter values using the Parameter Estimation tool provided by Simulink® Design Optimization™.

To identify the parameters of an actual supercapacitor empirically, you can:

1   Collect voltage and current waveforms from the supercapacitor.
2   Identify parameter values using the waveform data and the methodology described in [1].

To identify the parameters of a modeled supercapacitor, this example:

1   Generates voltage and current waveforms by simulating a model that it configures using known values for supercapacitor parameters.
2   Identifies supercapacitor parameter values using the generated waveform data and the methodology described in [1].
3   Configures and simulates the supercapacitor using the identified supercapacitor parameter values.

To see how the approach works for a real supercapacitor, evaluate the accuracy of the identification methodology by comparing:

•   the data generated using known parameter values and the data generated using identified parameter values.
•   the known parameter values and identified parameter values.

If the accuracy is not sufficient, you can use the Parameter Estimation tool from Simulink Design Optimization to improve it. Use the identified parameter values as the starting values for the optimization.

Generate Data

Generate voltage and current waveforms by configuring and simulating a model using known values for the fixed resistances, fixed capacitances, and voltage-dependent capacitor gain parameters of the supercapacitor. Model the input current, configure the physical characteristics and self-discharge resistance of the supercapacitor using realistic values.

Observe the supercapacitor behavior during three distinct phases:

1   Charge with constant current
2   Charge redistribution from immediate to delayed branch
3   Charge redistribution from immediate and delayed branches to long-term branch

```
% Open model
modelName = 'ee_supercapacitor_identification';
open_system(modelName);
set_param(find_system('ee_supercapacitor_identification','FindAll', 'on','type','annotation','Tag
```

```
% Configure the supercapacitor using known parameter values.
%
% Supercapacitor block Cell Characteristics parameters
Kv = 190;                      % Voltage-dependent capacitor gain
R = [2.5e-3 0.9 5.2];          % Fixed resistances, [R1 R2 R3]
C = [270 100 220];             % Fixed capacitances, [C1 C2 C3]

% Store a copy of the known parameter values for an eventual comparison to
% parameter values identified using the methodology described in [1].
Kv_known = Kv;
R_known = R;
C_known = C;

% Specify input current by configuring the Step block.
%
% Step block parameters
stepTime = [40 1900 1917];  % Step time
initialValue = [28 0 0];    % Initial value
finalValue = [0 -25 25];    % Final value

% Specify the physical characteristics of the supercapacitor.
%
% Supercapacitor block Cell Characteristics and Configuration parameters
R_discharge = 9e3;          % Self-discharge resistance
N_series = 1;               % Number of series cells
N_parallel = 1;             % Number of parallel cells

% Specify the simulation duration
stopTime = 2100;

% Simulate and store voltage and current waveform data
sim(modelName);
t = simlog_ee_supercapacitor_identification.Sensing_current.It.i.series.time;
i = simlog_ee_supercapacitor_identification.Sensing_current.It.i.series.values;
v = simlog_ee_supercapacitor_identification.Sensing_voltage.V.V.series.values;
```
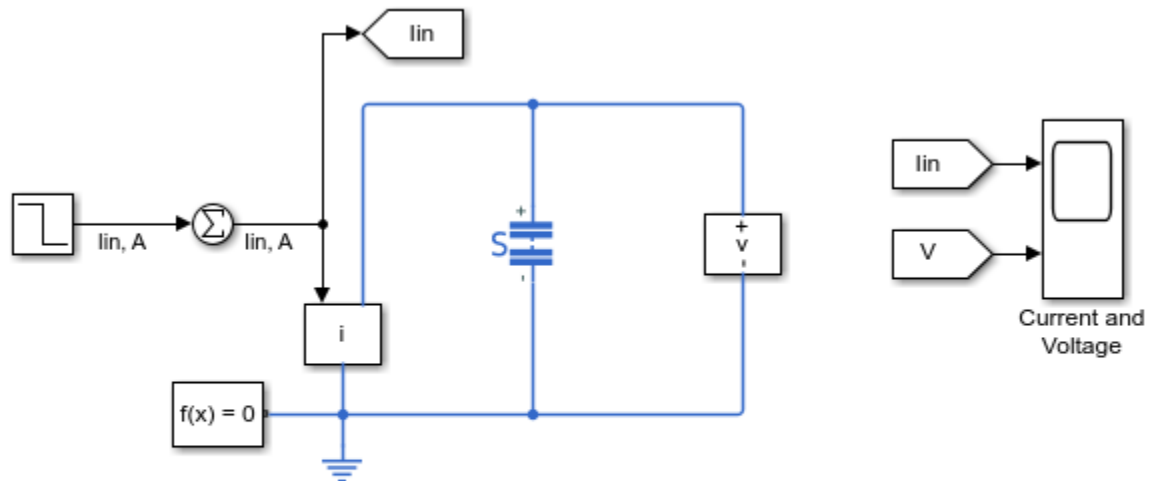
Supercapacitor Parameter Identification

Identify and evaluate the supercapacitor parameters (see code) Explore supercapacitor data from the simulation that uses identified parameter values using sscexplore Explore all simulation results from the simulation that uses identified parameter values using sscexplore Learn more about the Supercapacitor block

Perform Parameter Identification

Using the waveform data from the simulation, apply the methodology described in [1].

Immediate Branch Parameter Identification

During the first stage of the identification, a fully discharged supercapacitor is charged with constant current. The method assumes that the immediate branch stores all the initial charge because the time constant for the branch is relatively small.

Immediate branch parameters are calculated by taking measurements of the charge characteristic. Once charging current has reached steady-state at $t_1 = 20e^{-3}$s, measure v_1 and use

$$R_i = \frac{v_1}{i_1}$$

where:

- t_1 is time at parameter identification event $n = 1$, s
- v_1 is terminal voltage at t_1, V
- i_1 is charging current at t_1, A
- R_i is fixed resistance of immediate branch, Ω

Once voltage has increased from v_1 by approximately $50e^{-3}$V, measure t_2 and v_2 and use

$$C_{i0} = i_1 \frac{t_2 - t_1}{v_2 - v_1}$$

where:

10-591

- t_2 is time at parameter identification event $n = 2$, s
- v_2 is terminal voltage at t_2, V
- C_{i0} is fixed capacitance of immediate branch, $\mathbf{F}$

Once voltage has reached rated voltage, measure v_3 and t_3, and turn off charging current. After $20e^{-3}$s, once charging current has reached steady-state value of $0\mathrm{A}$ measure t_4 and v_4 and use

$$t_4 = t_3 + 20e^{-3}$$

$$C_{i1} = \tfrac{2}{v_4}\left(\tfrac{i_1(t_4 - t_1)}{v_4} - C_{i0}\right)$$

where:

- t_3 is time at parameter identification event $n = 3$, s
- v_3 is terminal voltage at t_3, V
- t_4 is time at parameter identification event $n = 4$, s
- v_4 is terminal voltage at t_4, V
- C_{i1} is voltage-dependent capacitance coefficient, $\mathrm{F/V}$

```matlab
% Event n=1
t1 = 20e-3;
i1 = interp1(t,i,t1);
v1 = interp1(t,v,t1);

Ri = v1/i1;

% Extract charging data that interp1 can use to find time values rather
% than voltage values
[v3, v_max_idx] = max(v);
[v_charge, v_charge_idx] = unique(v(1:v_max_idx));
t_charge = t(v_charge_idx);

% Event n=2
delta_v = 50e-3;
v2 = v1+delta_v;
t2 = interp1(v_charge,t_charge,v2);
i2 = interp1(t,i,t2);

Ci0 = i2*(t2-t1)/delta_v;

% Event n=3
t3 = t(v_max_idx);

% Event n=4
delta_t = 20e-3;
t4 = t3+delta_t;
v4 = interp1(t,v,t4);
Qtotal = i1*(t4-t1);
Cq = Qtotal/v4;

Ci1 = (2/v4)*(Cq-Ci0);
```

Delayed Branch Parameter Identification

During the second stage of the identification, charge redistributes from the immediate branch to the delayed branch.

Delayed branch parameters are calculated by taking measurements of the hold charge characteristic.

Once voltage has decreased from v_4 by approximately $50e^{-3}\text{V}$, measure t_5 and v_5 and use

$$\Delta v = v_4 - v_5$$

$$V_{ci} = v_4 - \frac{\Delta V}{2}$$

$$C_{diff} = C_{i0} + C_{i1} V_{ci}$$

$$R_d = \frac{(v_4 - \frac{\Delta V}{2})(t_5 - t_4)}{C_{diff} \Delta V}$$

where:

- t_5 is time at parameter identification event $n = 5$, s
- v_5 is terminal voltage at t_5, V
- V_{ci} is voltage at which the total immediate capacitance is to be calculated, V
- C_{diff} is total immediate capacitance at V_{ci}, F
- R_d is delayed branch resistance, Ω

Wait for 300 seconds, measure t_6 and v_6 and use

$$Q_{total} = i_1(t_4 - t_1)$$

$$C_d = \frac{Q_{total}}{v_6} - (C_{i0} + \frac{C_{i1}}{2} v_6)$$

where:

- t_6 is time at parameter identification event $n = 6$, s
- v_6 is terminal voltage at t_6, V
- Q_{total} is total charge supplied to the supercapacitor, C
- C_d is delayed branch capacitance, F

```
% Event n=5
v5 = v4-delta_v;
discharge_idx = find(i<-20,1,'first');
t_discharge = t(v_max_idx:discharge_idx);
v_discharge = v(v_max_idx:discharge_idx);
t5 = interp1(v_discharge,t_discharge,v5);
delta_t = t5-t4;
Vci = v4-(delta_v/2);
Cdiff = Ci0+Ci1*Vci;
Rd = (v4-(delta_v/2))*delta_t/(Cdiff*delta_v);
```

```
% Event n=6
TypicallyRdTimesCd = 100;
t6 = t5 + 3*TypicallyRdTimesCd;
v6 = interp1(t,v,t6);
Cd = (Qtotal/v6)-(Ci0+((Ci1/2)*v6));
```

Long-term Branch Parameter Identification

During the third, and final, stage of the identification, charge redistributes from the immediate and delayed branches to the long-term branch.

Long-term branch parameters are calculated by taking measurements of the hold charge characteristic.

Once voltage has decreased from v_6 by approximately $50e^{-3}$V, measure t_7 and v_7 and use

$$\Delta v = v_6 - v_7$$

$$R_l = \frac{(v_6 - \frac{\Delta V}{2})(t_7 - t_6)}{C_{diff}\Delta V}$$

where:

- t_7 is time at parameter identification event $n = 7$, s
- v_7 is terminal voltage at t_7, V
- R_l is long-term branch resistance, Ω

After 30 minutes from the start of the charging/discharging process, measure t_8 and v_8 and use

$$C_l = \frac{Q_{total}}{v_8} - (C_{i0} + \frac{C_{i1}}{2}v_8) - C_d$$

where:

- t_8 is time at parameter identification event $n = 8$, s
- v_8 is terminal voltage at t_8, V
- C_l is long-term branch capacitance, **F**

```
% Event n=7
v7 = v6-delta_v;
t7 = interp1(v_discharge,t_discharge,v7);
delta_t = t7-t6;
Vci = v6-(delta_v/2);
Cdiff = Ci0+Ci1*Vci;
Rl = (v6-(delta_v/2))*delta_t/(Cdiff*delta_v); % Rl value large
```

```
% Event n=8
t8 = 30*60;
v8 = interp1(t,v,t8);
Cl = (Qtotal/v8)-(Ci0+(v8*Ci1/2))-Cd; % Cl value too large
```

Collate Time and Voltage Data

Collate the time and voltage data for each parameter identification event in a MATLAB® table:

```
DataTable = table((1:8)',...
    [t1 t2 t3 t4 t5 t6 t7 t8]',...
    [v1 v2 v3 v4 v5 v6 v7 v8]',...
    'VariableNames',{'Event','Time','Voltage'}) %#ok<NOPTS>


DataTable =

  8x3 table

    Event      Time       Voltage
    _____    _____     _____

      1         0.02     0.071799
      2      0.51803       0.1218
      3           40       2.2717
      4        40.02       2.2019
      5       56.675       2.1519
      6       356.67       1.8473
      7       499.28       1.7973
      8         1800       1.5865
```

Evaluate Accuracy of Identified Parameters

Configure and simulate the model using the identified supercapacitor parameters. Then, to evaluate the accuracy of the identified parameter values, compare the waveform output to the data generated by a simulation that uses known parameters.

```
% Create parameters for supercapacitor block using identified values and
% create copies for easy reference.
Kv = Ci1;
R = [Ri Rd Rl];
C = [Ci0 Cd Cl];
Kv_identified = Kv;
R_identified = R;
C_identified = C;

% Simulate the model and store voltage and current waveforms.
sim(modelName);
t_ = simlog_ee_supercapacitor_identification.Sensing_current.It.i.series.time;
v_ = simlog_ee_supercapacitor_identification.Sensing_voltage.V.V.series.values;

% Plot the data generated from simulating with known parameters on the
% same axis as the data generated from simulating with identified
% parameters.
figure;
plot(t,v,DataTable.Time,DataTable.Voltage,'o',t_,v_);
grid('on');
legend('Known parameters simulation',...
    'Measurements for parameter identification',...
    'Identified parameters simulation');
```
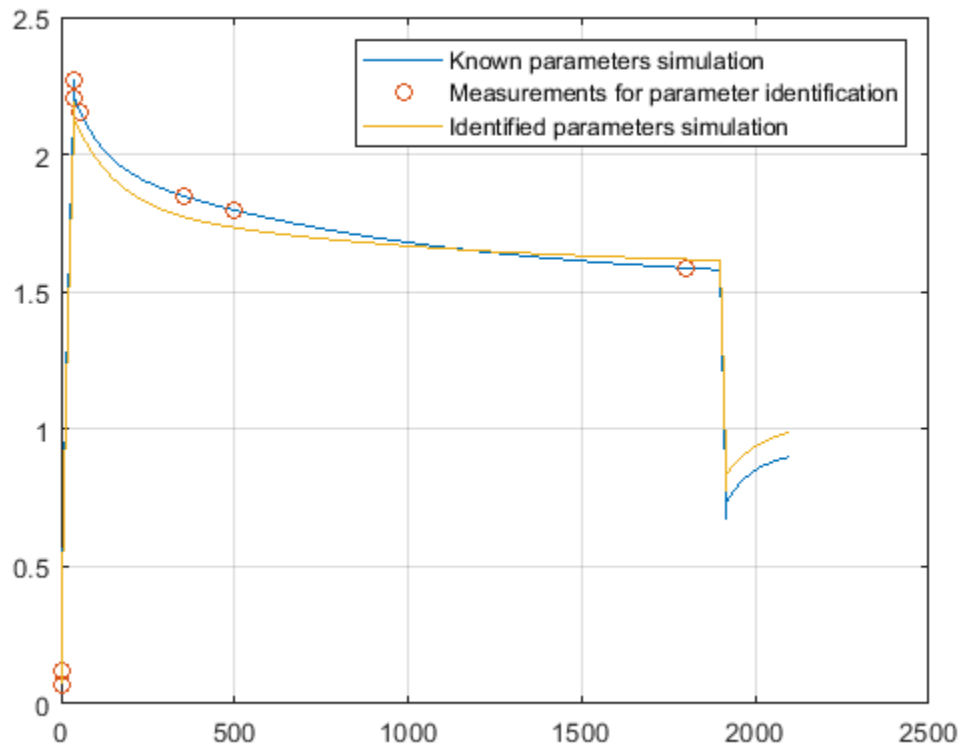
Optimization

Prerequisites:

1   Simulink Design Optimization license

If the traces are not similar enough to meet requirements, then the identified parameter values can serve as initial parameter values for the Parameter Estimation tool from Simulink Design Optimization.

- spetool('ee_supercapacitor_spesession');

Optimization Results

Display the parameter trajectory created using the **Sum Squared Error** cost function and **Nonlinear least squares** optimization method.

```
openfig('ee_supercapacitor_speresults');
```
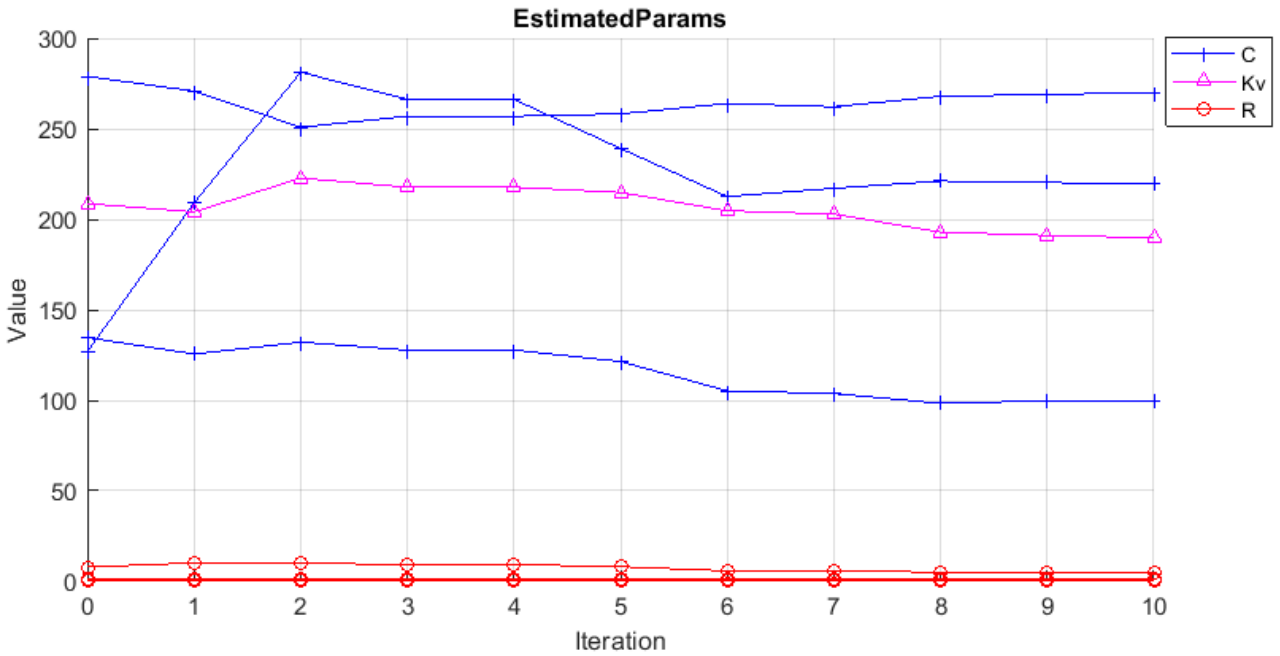
References

[1] Zubieta, L. and R. Bonert. "Characterization of Double-Layer Capacitors for Power Electronics Applications." IEEE Transactions on Industry Applications, Vol. 36, No. 1, 2000, pp. 199-205.

Synchronous Reluctance Machine Torque Control

This example shows how to control the torque in a synchronous reluctance machine (SynRM) based electrical drive. A high-voltage battery feeds the SynRM through a controlled three-phase converter. An ideal angular velocity source provides the load. The Control subsystem uses an open-loop approach to control the torque and a closed-loop approach to control the current. At each sample instant, the torque request is converted to relevant current references using the maximum torque per Ampere strategy. The current control is PI-based. The simulation uses torque steps in both the motor and generator modes. The Visualization subsystem contains scopes that allow you to see the simulation results.

Model

Synchronous Reluctance Machine Torque Control

1. Modify model parameters
2. Explore simulation results using sscexplore
3. Learn more about this example

Synchronous Reluctance Machine Velocity Control

This example shows how to control the rotor angular velocity in a synchronous reluctance machine (SynRM) based electrical drive. A high-voltage battery feeds the SynRM through a controlled three-phase converter. An ideal torque source provides the load. The Control subsystem includes a multi-rate PI-based cascade control structure. The control structure has an outer angular-velocity-control loop and two inner current-control loops. The Visualization subsystem contains scopes that allow you to see the simulation results.
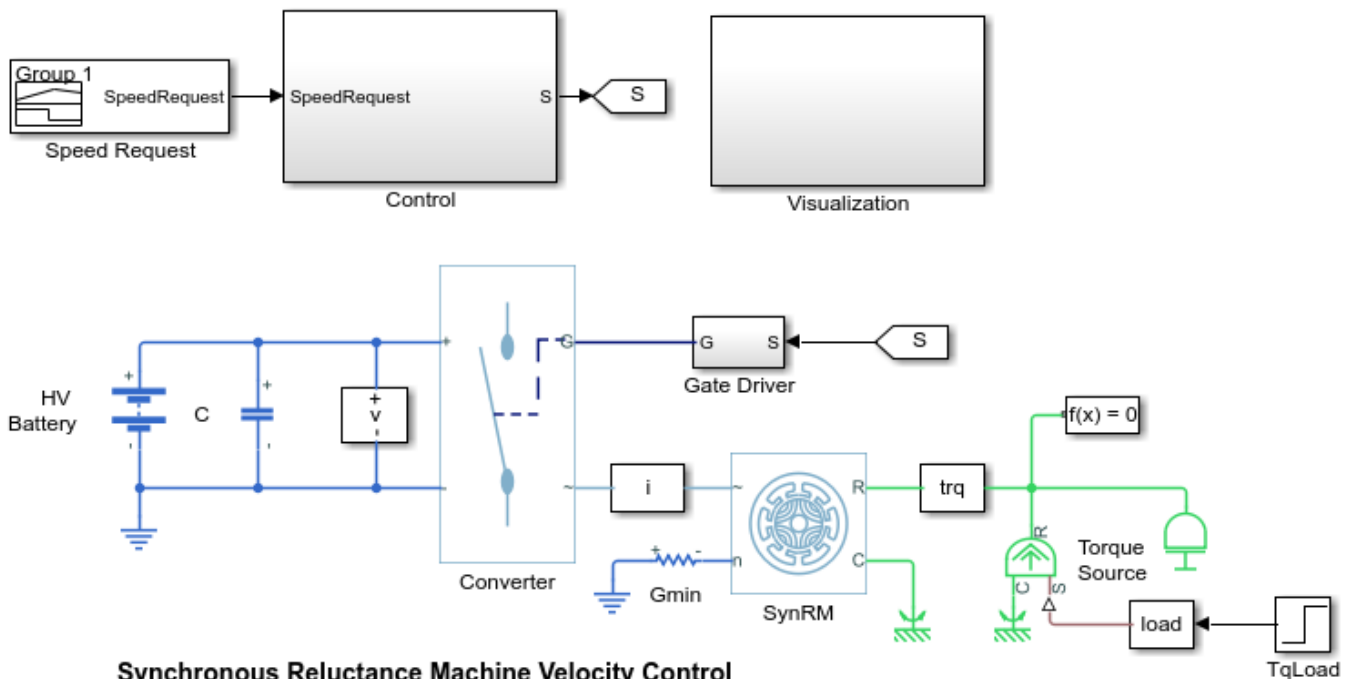
Model

Synchronous Reluctance Machine Velocity Control

1. Modify model parameters
2. Explore simulation results using sscexplore
3. Learn more about this example.

Tap-Changing Transformer for Automatic Voltage Regulation

This example shows how to use a single-phase tap-changing transformer to control the voltage across an RLC load. The system contains an AC voltage source that generates a 60 Hz sine wave (located on the left-hand side of the circuit). The root-mean-square value for the voltage generated by this source is 120 V (reference voltage).

The Feedback Controller uses the peak voltages across the AC voltage source and RLC load to generate the control signal provided to the tap-changing transformer.

The tap-changer location is on the transformer's secondary winding. The values defined for the maximum and minimum voltages across the RLC load are 1.01 and 0.99 of the reference voltage, respectively. The tap-changing transformer has 33 positions for the tap setting, between -16 and +16, including the 0 position that corresponds to the nominal tap position. The nominal turns ratio (primary/secondary) is 0.9.

The step voltage per tap change is 1.5% of the transformer's primary voltage. The sample time is 1e-5 seconds.

Model
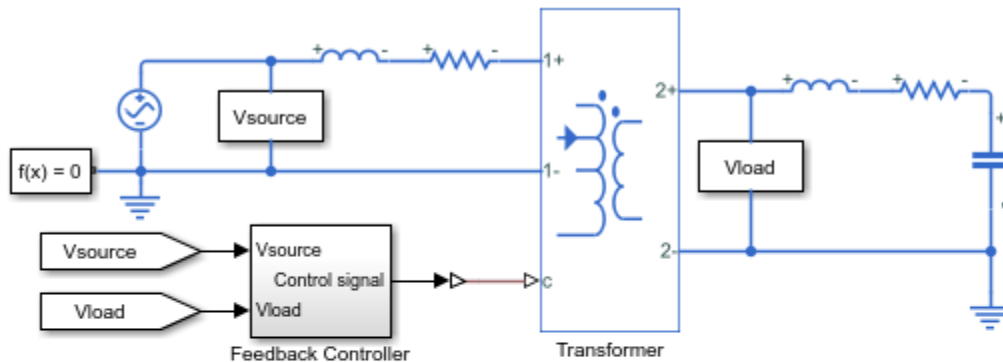
Tap-Changing Transformer for Automatic Voltage Regulation

1. Plot voltages and tap position (see code)
2. Set initial tap position: max_tap, min_tap, nom_tap
3. Explore simulation results using sscexplore
4. Learn more about this example

Simulation Results from Simscape Logging

The plot below shows the simulated voltages across the AC voltage source and RLC Load and the evolution of the tap position.
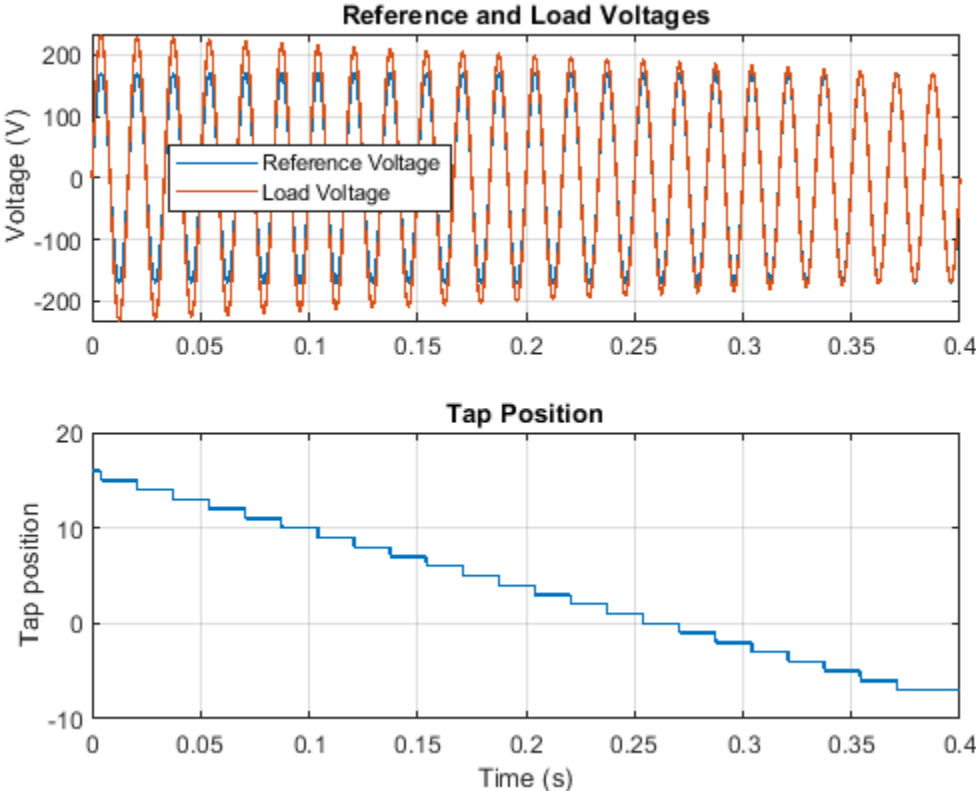
Three-Phase Bridge Cycloconverter

This example shows a three-phase bridge cycloconverter. The cycloconverter consists of 36 thyristors and has the capacity to lower the frequency of the input voltage. The Control subsystem implements the cycloconverter RMS voltage control. It also provides pulse generation for the firing of the thyristors. The Visualization subsystem contains scopes that allow you to see the simulation results. The simulation time, t, is 1 second. The load increases when Load1 switches on at t = 0.75 seconds.

Model
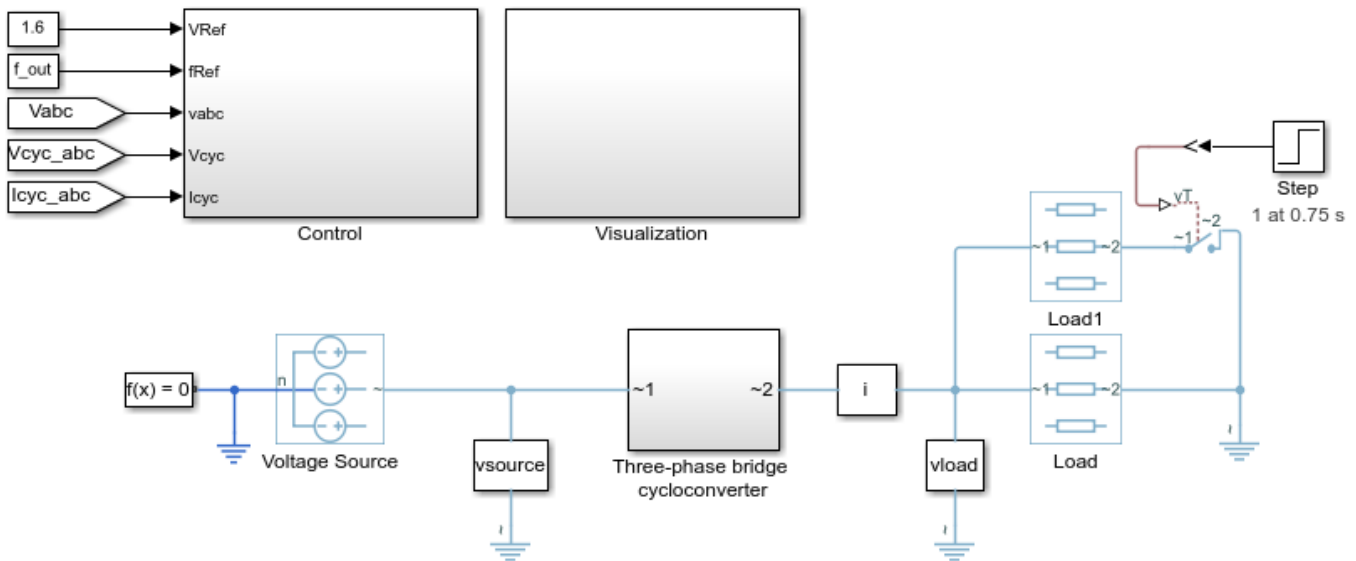
Three-Phase Bridge Cycloconverter

1. Modify model parameters
2. Explore simulation results using sscexplore
3. Learn more about this example

Comparison of Three-Phase Port Types

This example shows a comparison of Composite three-phase ports versus Expanded three-phase ports. The first circuit shows a Voltage Source configured with a Composite three-phase port. A Phase Splitter block provides the interface to the Simscape™ foundation library electrical elements. The second circuit shows a Voltage Source configured with an Expanded three-phase port which can connect directly to the Simscape foundation library electrical elements. The Voltage Source can be changed from Composite to Expanded three-phase ports by use of the Simscape block choices option on the right-click context menu.

Both circuits are equivalent. Both consider instantaneous three-phase voltages and currents. Both give the same results.

Model
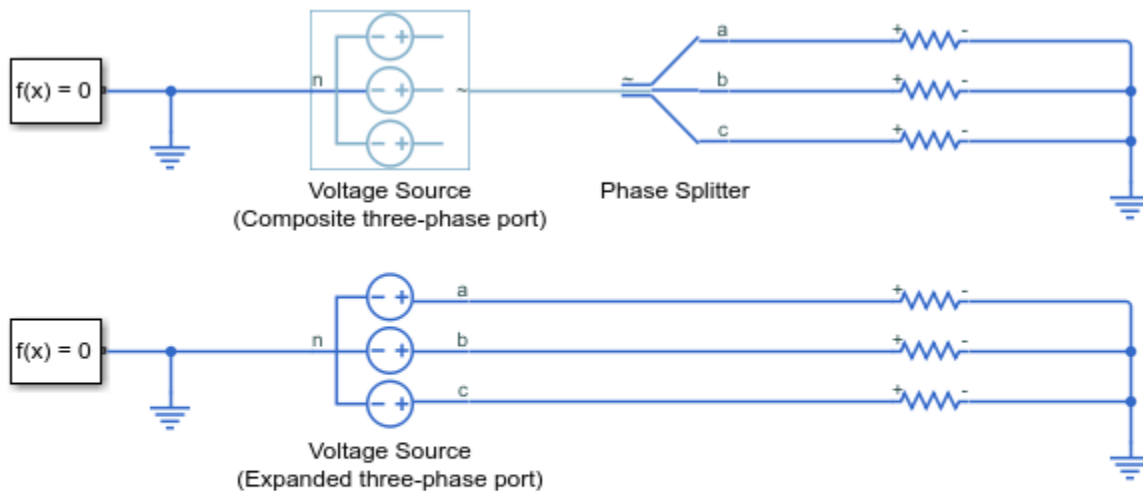
Comparison of Three-Phase Port Types

1. Explore simulation results using sscexplore
2. Learn more about this example

Thyristor-Based Rectifier

This example shows a thyristor-based rectifier.

Model
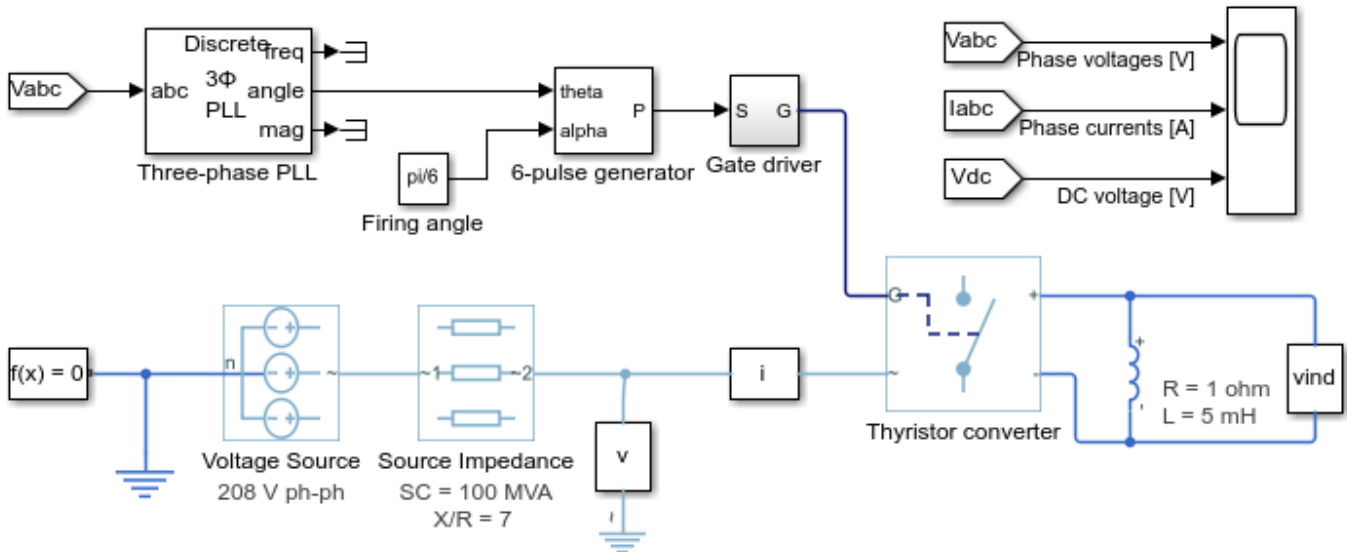
Thyristor-Based Rectifier

1. Explore simulation results using sscexplore
2. Learn more about this example

Twelve-Pulse Thyristor Rectifier

This example shows how to control a twelve-pulse thyristor rectifier. Two thyristor converters are connected to a Wye-Delta-Wye transformer on the input. A Thyristor 12-Pulse Generator block generates the gate signals for the two converters.

Model
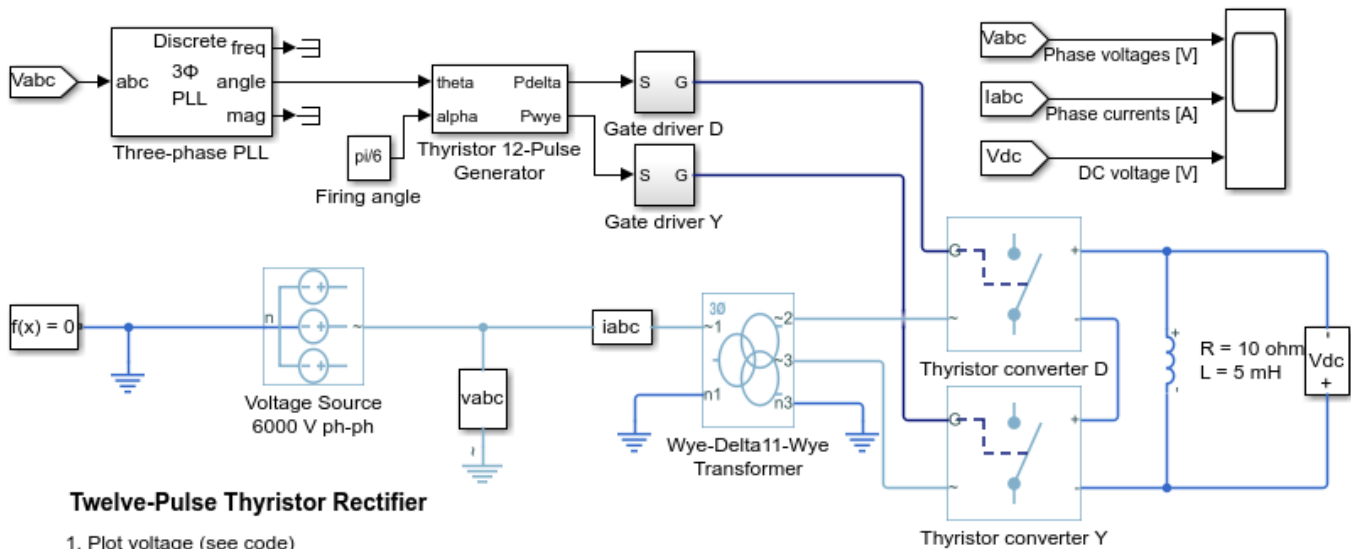
Twelve-Pulse Thyristor Rectifier

1. Plot voltage (see code)
2. Explore simulation results using sscexplore
3. Learn more about this example

Simulation Results from Simscape Logging

The plot below shows the output DC voltage and the input phase voltages.
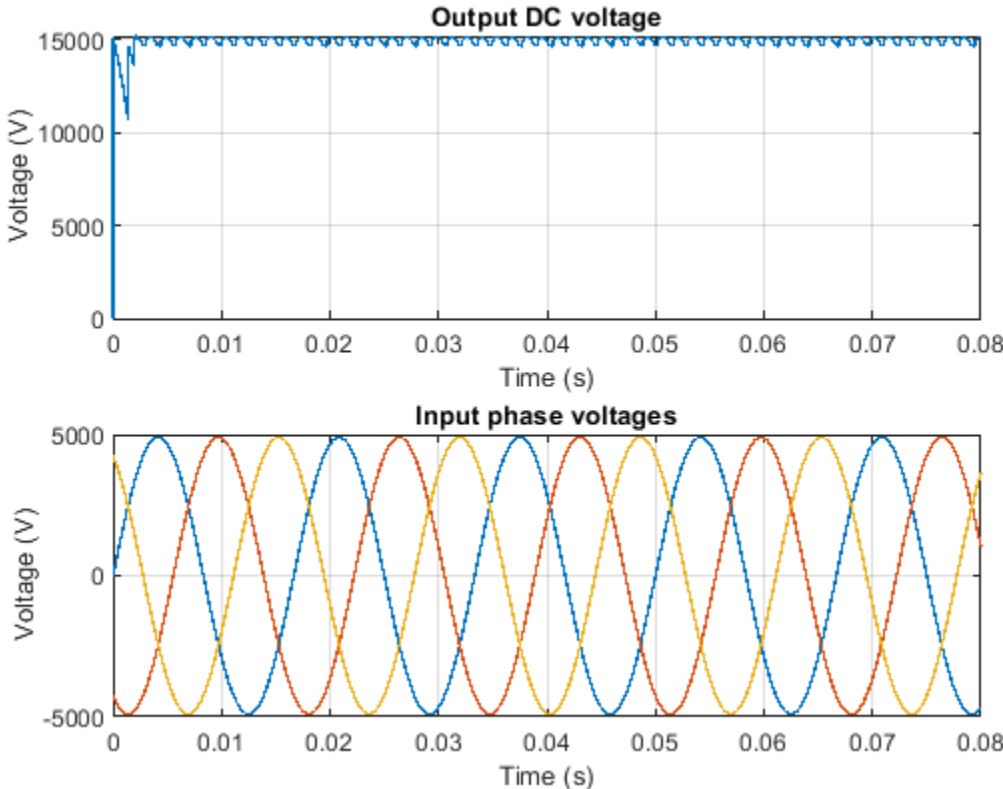
First and Second Quadrant Chopper Control

This example shows how to control a two-quadrant chopper. The two-quadrant chopper operates in the first and second quadrants, allowing positive and negative output current. The Control subsystem implements a simple PI-based control algorithm for controlling the output current. The load of the system is considered constant throughout the simulation.

Model
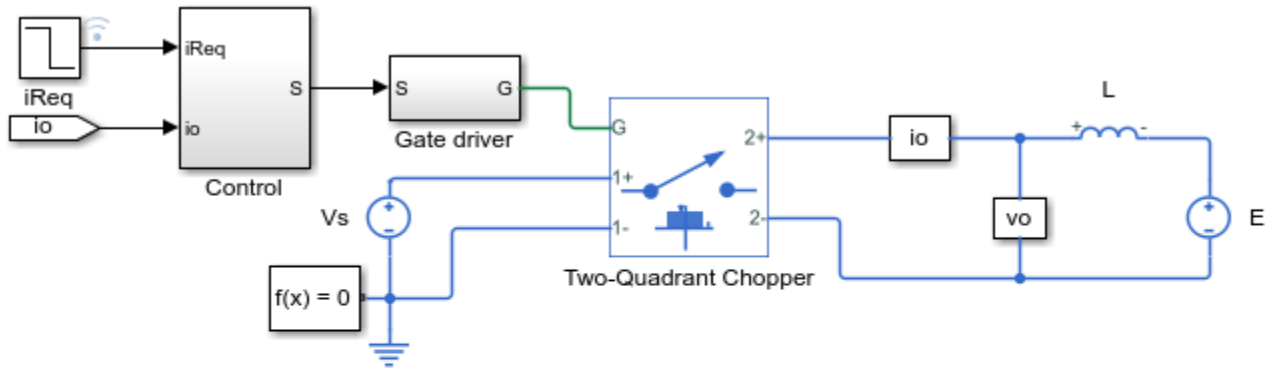
First and Second Quadrant Chopper Control

1. Plot current (see code)
2. Explore simulation results using sscexplore
3. Modify model parameters
4. Learn more about this example
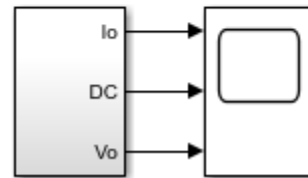
Simulation Results from Simscape Logging

The plot below shows the requested and measured current for the test and the output voltage in the circuit.
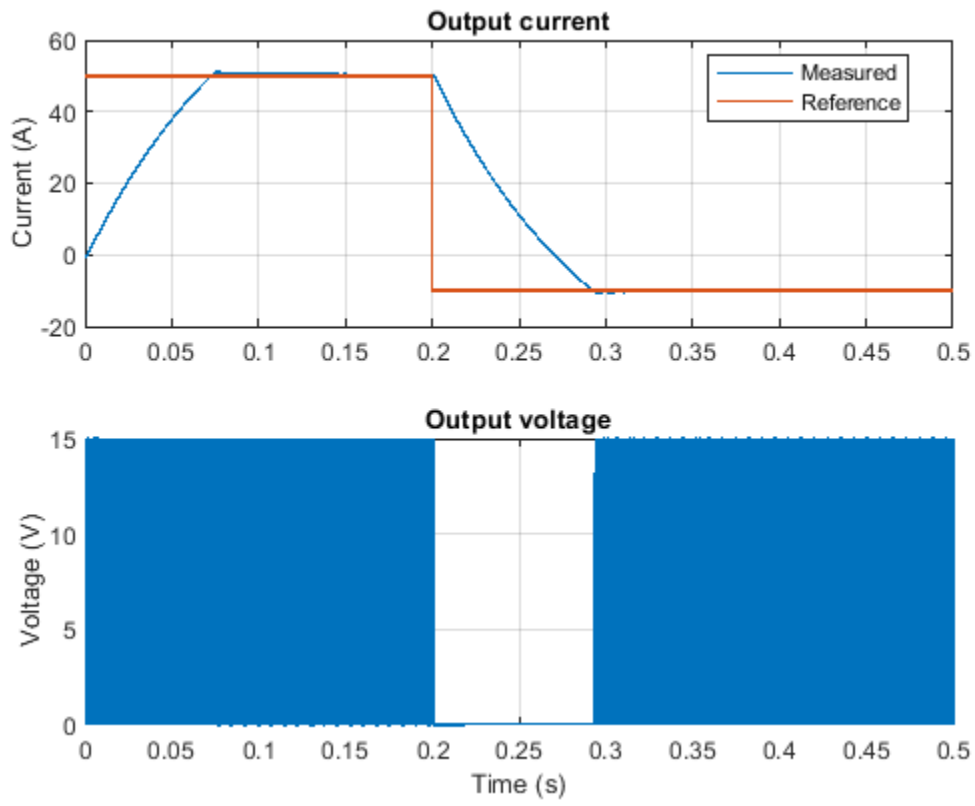
First and Fourth Quadrant Chopper Control

This example shows how to control a two-quadrant chopper. The two-quadrant chopper operates in the first and fourth quadrants, allowing positive and negative output voltage. The Control subsystem implements a simple PI-based control algorithm for controlling the output current. The total simulation time (t) is 0.5 seconds. At t = 0.25 seconds, the polarity of the load DC source E is changed.
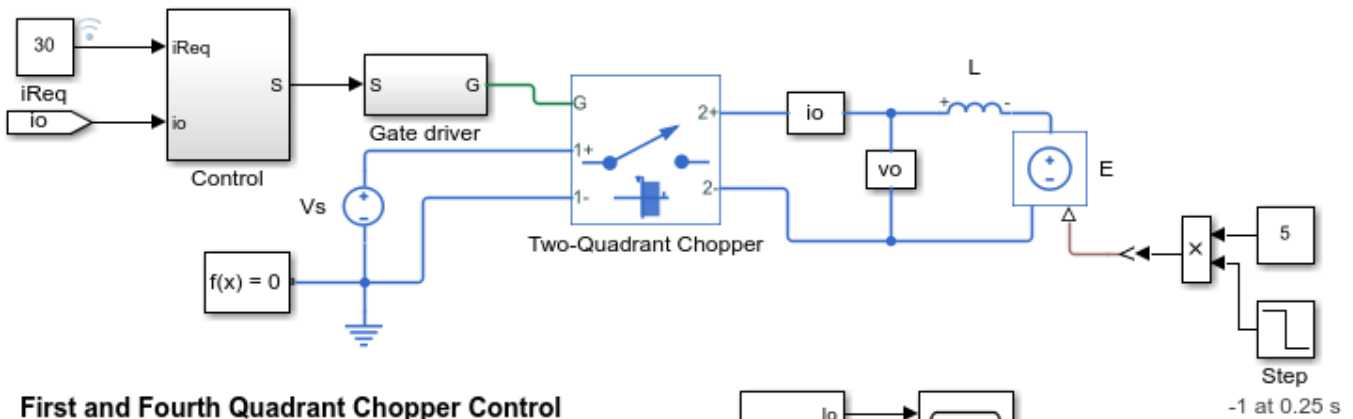
Model

First and Fourth Quadrant Chopper Control

1. Plot current (see code)
2. Explore simulation results using sscexplore
3. Modify model parameters
4. Learn more about this example
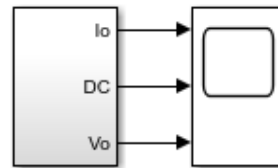
Simulation Results from Simscape Logging

The plot below shows the requested and measured current for the test and the output voltage in the circuit.
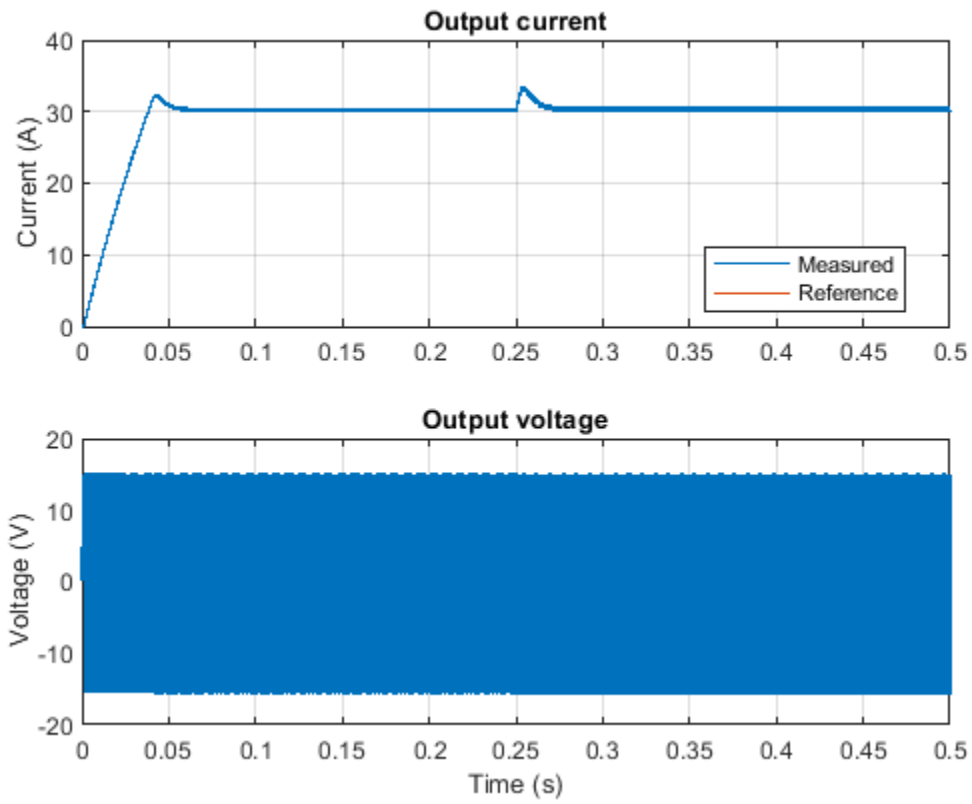
Vienna Rectifier Control

This example shows how to control a Vienna rectifier. The Vienna rectifier subsystem consists of three-phase legs. Each leg has one power switch and six power diodes. The Control subsystem implements a closed-loop control strategy for the Vienna rectifier using space-vector modulation. Total simulation time is 0.1 s. At time 0.1 s, the Vienna Rectifier is engaged. At times 0.4 s and 0.6 s, the load steps up on the DC side.

Model
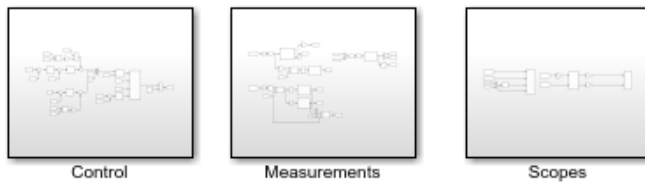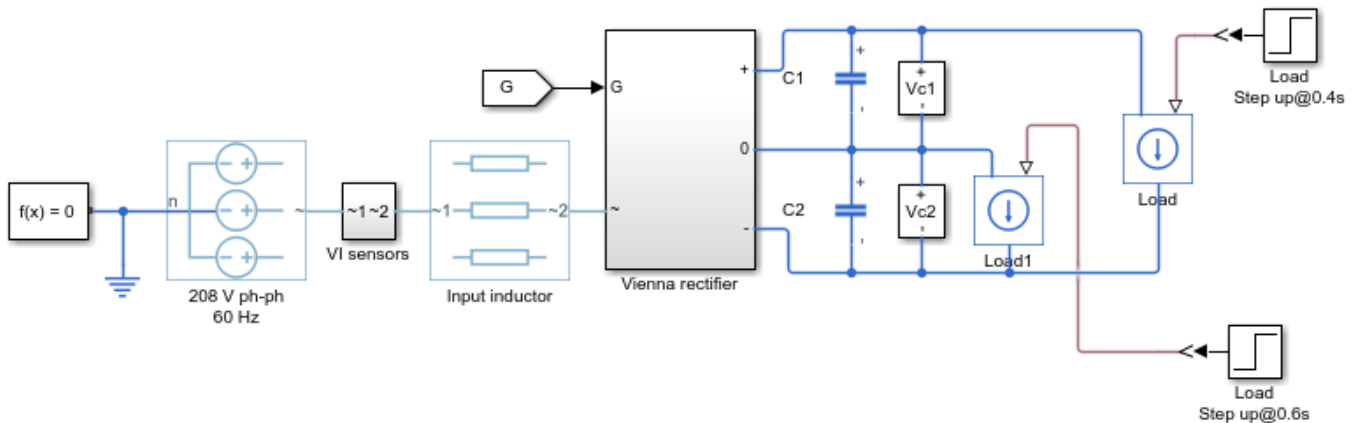
Simulation Results from Simscape Logging

The plot below shows the measured dc-link voltage for the test and the grid currents.
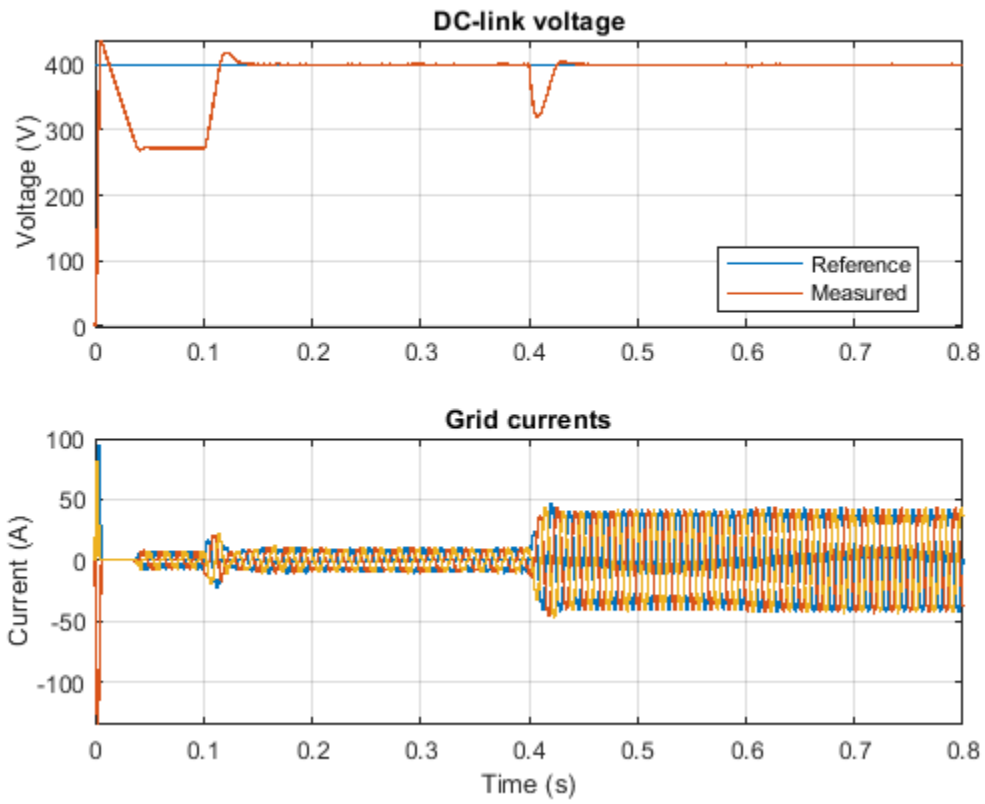
Three-Phase Voltage-Sourced Converter (FLB)

This example shows how to model a three-phase voltage-sourced converter that uses Fixed Low-side Bias (FLB) modulation. This modulation scheme minimises the switching in the converter as at any given time one phase is not being pulse modulated. The trade-off is the need for narrower pulses for a given level of acceptable harmonics. The model can be used to support selection of suitable values for L, C and the pulse modulation scheme parameters.
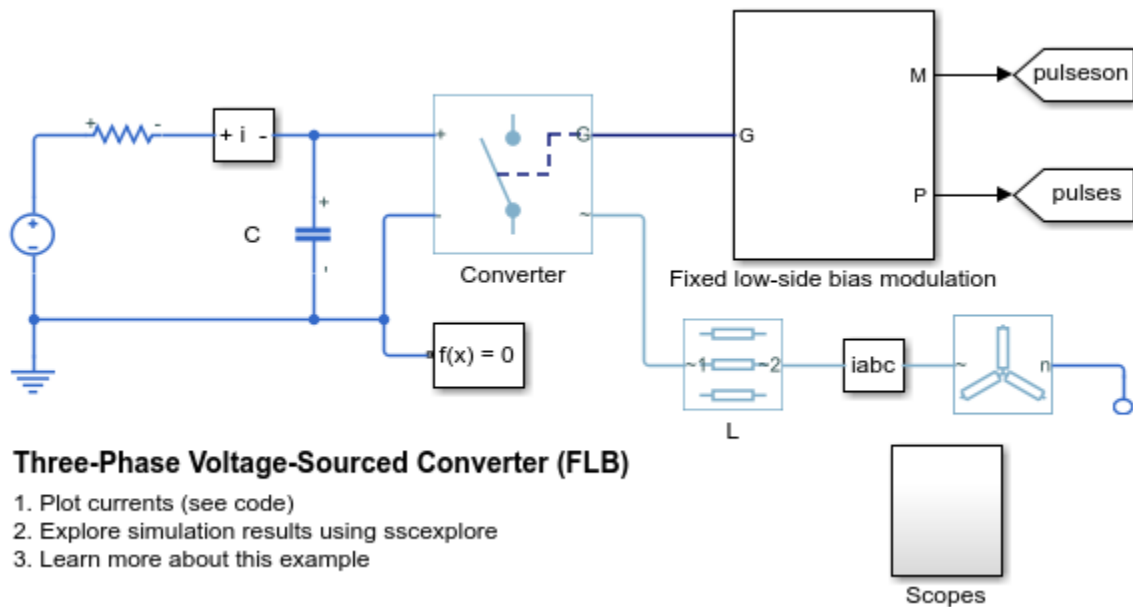
Model

Three-Phase Voltage-Sourced Converter (FLB)

1. Plot currents (see code)
2. Explore simulation results using sscexplore
3. Learn more about this example

Three-Phase Voltage-Sourced Converter (SPWM)

This example shows how to model a three-phase voltage-sourced converter that uses Sinusoidal Pulse-Width Modulation (SPWM). This modulation scheme compares a reference sine wave with a higher-frequency repeating triangle wave in order to generate the pulses. The model can be used to support selection of suitable values for L, C and the pulse modulation scheme parameters.

Model
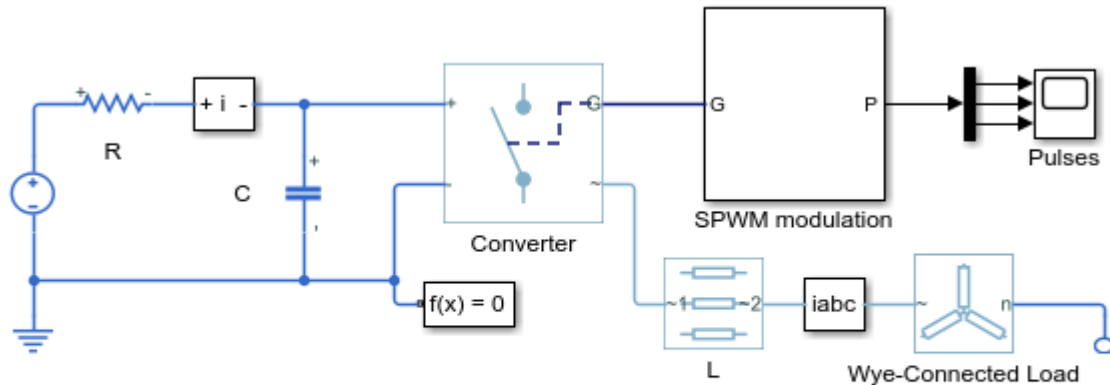
Three-Phase Voltage-Sourced Converter (SPWM)

1. Plot currents (see code)
2. Explore simulation results using sscexplore
3. Learn more about this example

Simulation Results from Simscape Logging

The plot below shows the DC supply current, as well as the AC phase currents in the Wye-Connected Load.
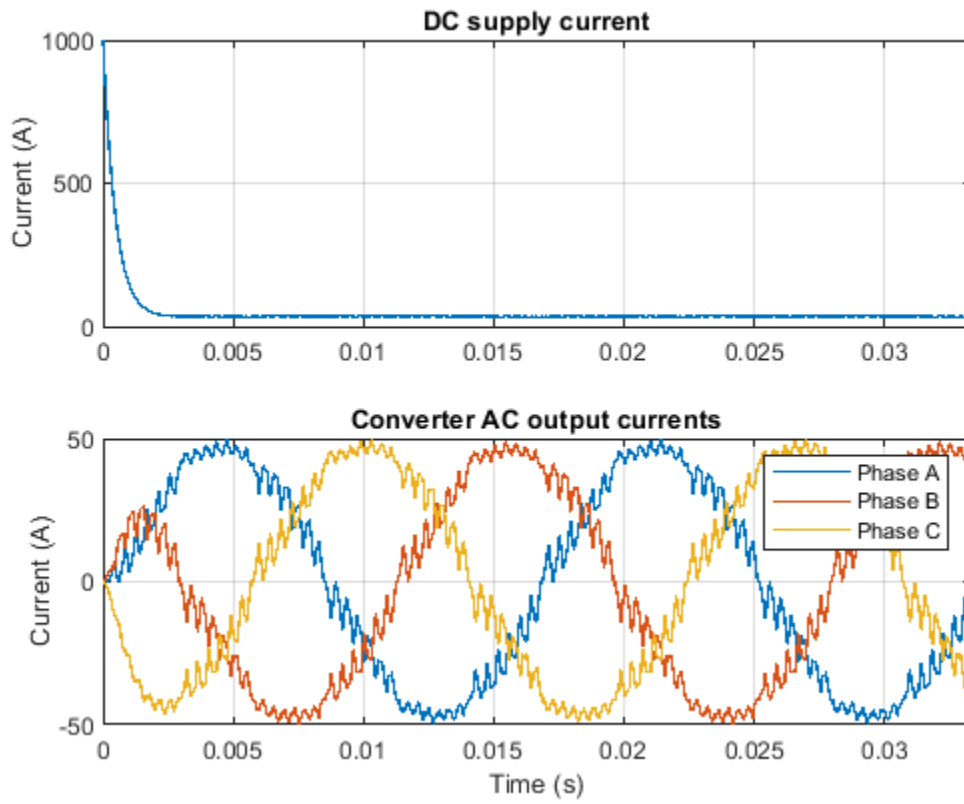
Field-Oriented Control of an Induction Motor Drive Used in a Ground Transportation System

This example demonstrates the operation of a variable-speed AC drive in a trolleybus traction system.
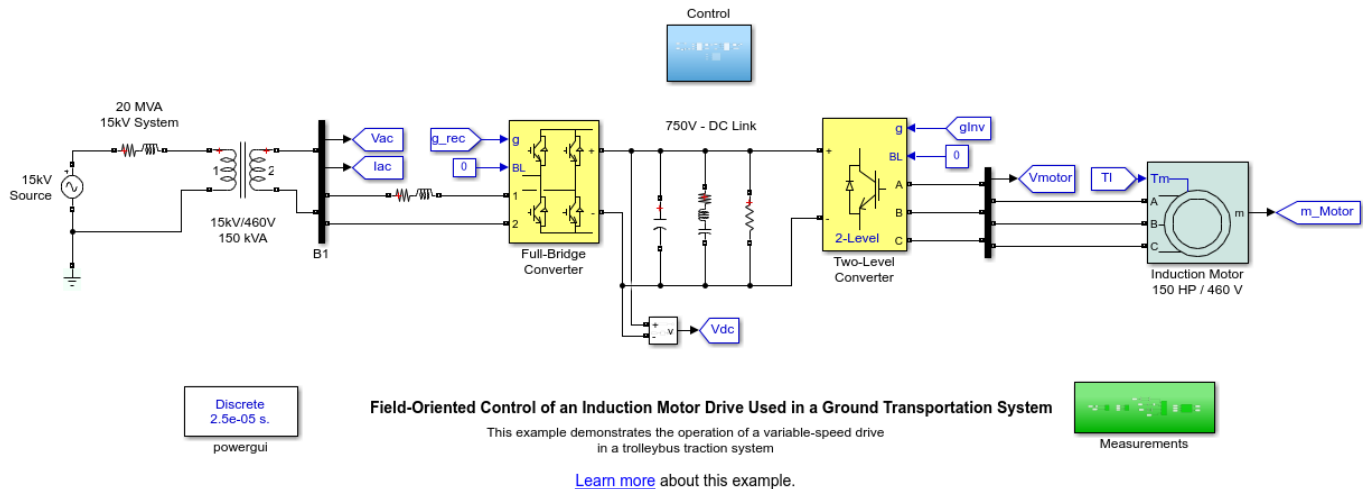
Description

1. **Traction System**

The electric energy is supplied by a catenary (15 kV, single-phase 60 Hz) through a 15-kV/460-V transformer. The transformer secondary is connected to a PWM AC/DC full-bridge converter producing a DC voltage of 750 V on the DC link. This voltage is filtered by a filter tuned to the second harmonic (120 Hz). The DC link voltage is connected to a PWM-controlled three-phase two-level converter. This converter will generate the variable voltage and frequency required for variable-speed operation of the induction motor. The AC/DC converter allows reversing the power flow during braking period. The kinetic energy of the bus is then converted into electric energy and injected back to the power system. In addition, unity power factor operation is possible with this kind of converter.

An inverter-fed induction motor drive can be controlled by using various schemes depending on application, desired performance, and controller design complexity. Popular used schemes are **Scalar control** (V/Hz control, open loop flux control) and **Vector control** (Field-Oriented Control, direct torque control). In these schemes, the motor speed is controlled by changing the supply frequency. In order to develop an optimum torque without magnetic saturation, it is necessary to maintain a constant air gap flux, near the nominal value, so that the voltage is kept proportional to the frequency. In our example, we use a Field-Oriented Control (FOC) scheme.

2. **Field-Oriented Control (FOC)**

FOC is a control scheme for induction motors in which a d-q coordinates reference frame locked to the motor flux space vector is used to achieve decoupling between the motor flux and torque. Thus they can be separately controlled by stator direct-axis and quadrature-axis currents respectively. The following figure show the stator Fs and rotor Fr magnetomotive forces (MMF) at a given instant.
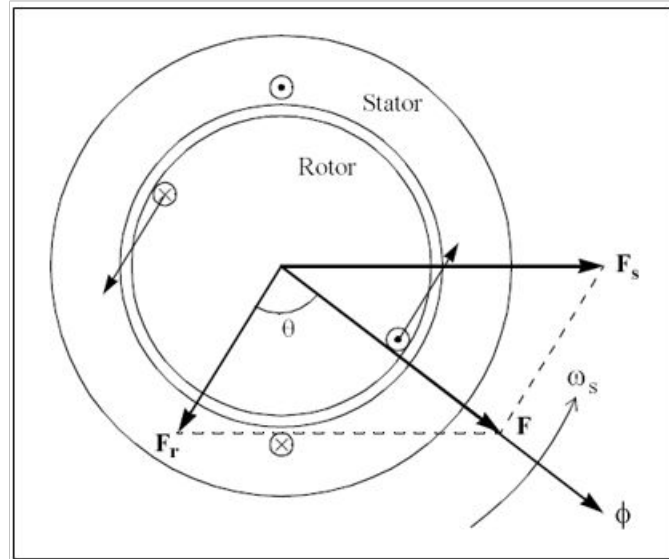
The stator and rotor three-phase windings are simply represented here by two single-turn equivalent windings. Fs and Fr rotate at synchronous speed determined by the supply frequency. The resulting F vector (sum of Fs and Fr) produces the motor flux which rotates at synchronous speed.

The torque developed by the motor is given by:

$$T = k * Fr * \phi * sin(\theta)$$

where ϕ is the motor flux, θ is the angle between Fr and ϕ, and k is a constant that depends on the motor construction. This relation indicates that the developed torque is maximum for θ equal to 90 degrees. The challenge of the FOC control scheme is then to keep this angle as close as possible to 90 degrees and, except during field-weakening operation, to maintain the motor flux at its nominal value.

In the next figure, the current space vectors Is and Ir represent the Fs and Fr values.
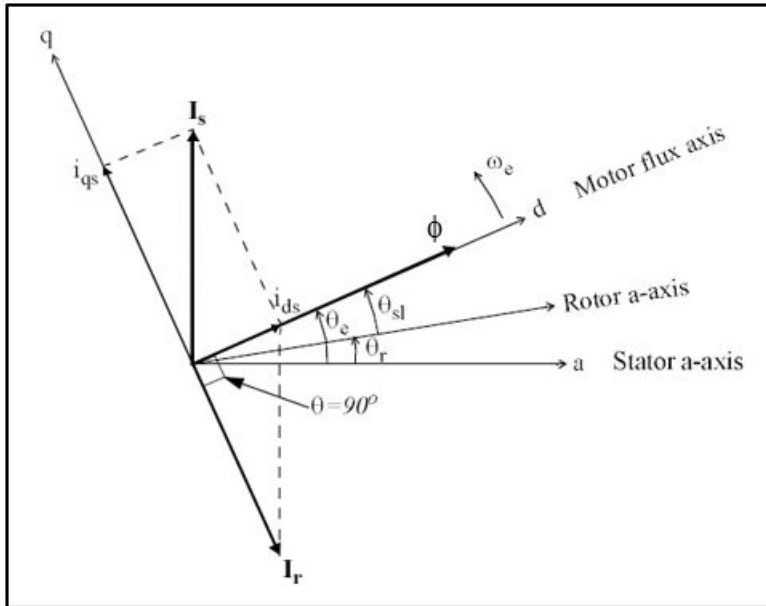
The rotor current space vector Ir is perpendicular to the magnetizing current ids that produces the motor flux and that the motor flux position θ_e required for coordinates transformation is generated from the rotor speed ω_m and slip frequency ω_s:

$$\theta_e = \int (p * \omega_m + \omega_s)dt$$

where P is the number of pole pairs.

Computation of slip frequency ω_s and reference currents ids_{ref}, iqs_{ref} requires that a model of the motor is implemented inside the controller. ω_s is evaluated from the stator reference current iqs_{ref} and the motor parameters, Lm, Lr and Rr as follows:

$$\omega_s = Lm/phi * Rr/((Lr + Lm)) * iqs$$

where ϕ is the motor flux linkage, Lm the mutual inductance, and Lr and Rr the rotor inductance and resistance.

The motor flux linkage ϕ is calculated as follows:

$$phi = (Lm * ids)/(1 + \tau_r)$$

where the rotor time constant $\tau_r = (Lr + Lm)/Rr$

The stator quadrature-axis reference iqs_ref is calculated from torque reference Te_{ref} as:

$$iqs_{ref} = 2/3 * 1/p * ((Lr + Lm))/Lm * (Te_{ref})/\phi$$

The stator direct-axis current reference ids_ref is obtained from the motor flux linkage reference ϕ_{ref}

$$ids_{ref} = \phi_{ref}/Lm$$

In the FOC subsystem, the motor speed ω_m and the speed reference ω_{ref} are fed to the Speed Regulator to produce a torque reference Te_{ref}. The role of the speed regulator is to keep the motor speed equal to the speed reference in steady-state while providing a fast dynamic response during transients. The calculated ids_{ref} and iqs_{ref} current references are fed to the Current Regulators. The regulators process the measured and reference currents to produce the reference voltages $Vref$.

The three-phase reference signals $Vref$ are connected to the PWM modulator that generates pulses to the motor inverter. The modulator uses the Space Vector PWM method with pulse averaging and a switching frequency of 2 kHz. ?

3. Single-phase rectifier control system

The functions of this system are to regulate the DC-link voltage and to maintain a unity input power factor. The system consists of the following main blocks:

- PLL & Measurements system - The PLL is synchronized to the secondary voltage of the transformer. Secondary voltage & current are measured and transformed to d-q coordinates.

- Current Regulator (inner control loop) consists of two PI controllers for Id and Iq currents. The controllers outputs are the Vd and Vq signals that are transformed to a reference signal $Vref$ for the PWM generator. The Iq reference is kept equal to zero because a unity power factor is desired. The Id reference is provided by the outer voltage control loop.

- Voltage Regulator (outer control loop) that regulates the DC-link voltage.

The reference signal $Vref$ is connected to the PWM generator that produces pulses to the full-bridge AC/DC rectifier. The generator uses carrier-based PWM method with pulse averaging and a switching frequency of 2340 (39*60) Hz.

Simulation

Run the simulation and observe waveforms on Scope1. Note that at the change of load torque (from 500 N.m to 100 N.m at 1 s), the speed regulator will work to maintain the motor speed at 1400 RPM. This change in load torque could be interpreted as an abrupt change of the road slope. Since the trolleybus goes down the hill, the kinetic energy is converted into electric energy by the induction motor then operating as a generator. This energy is returned to the power grid through the DC link and the single-phase converter, which then operates as an inverter. Note the active power reversing (around minus 6 kW at 1.25s) shown on the third graph (blue trace) of the figure.

Real-Time Simulation

You can easily run this example in real-time using a Speedgoat target. Simply uncomment the Subsystem **Simulink Real-Time (Run on Target)**, set the simulation time to `inf` and click **Run on Target** button. The model is then built, deployed to the target and the simulation is automatically started. On your screen target, you should see something similar the following image:
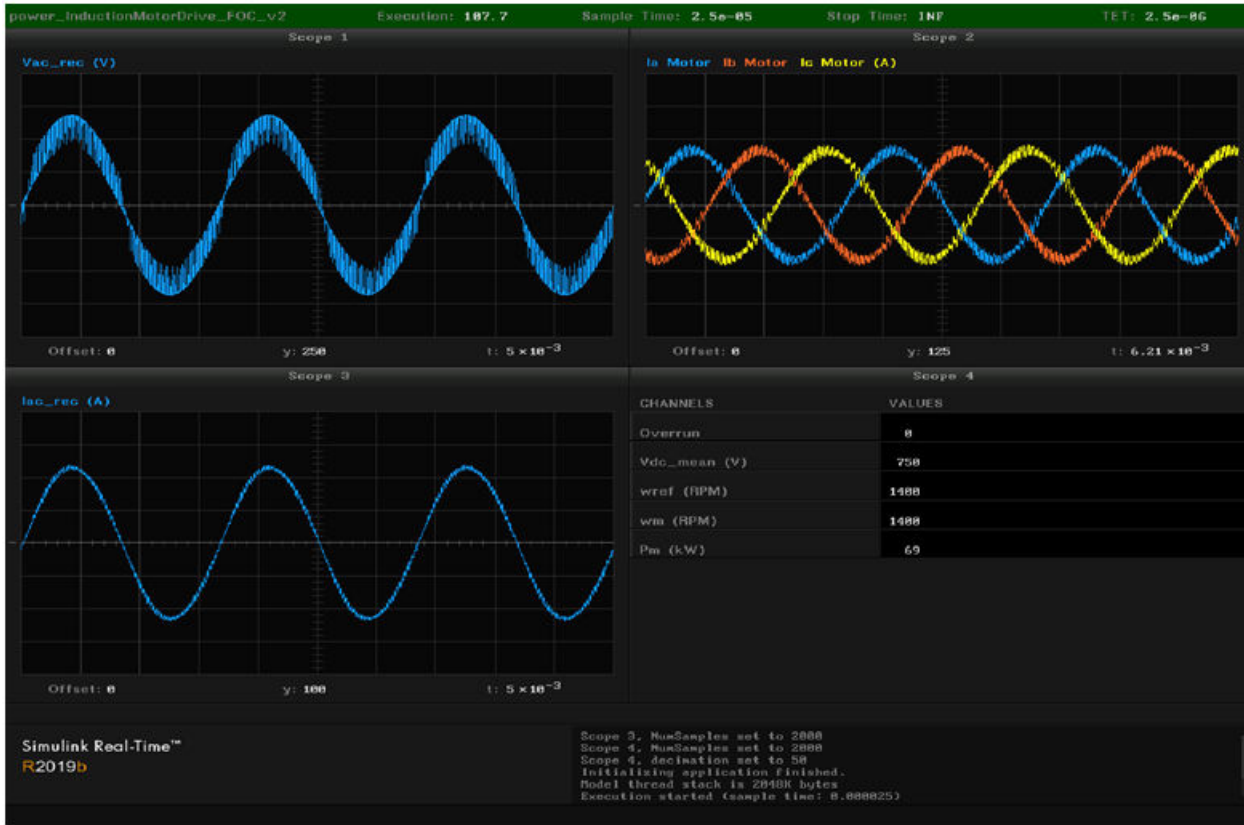
MMC-STATCOM Connected to a 735-kV Transmission System

This example demonstrates the operation of a 50 MVA, MMC STATCOM (22 power modules per arm) connected to a 735-kV grid.
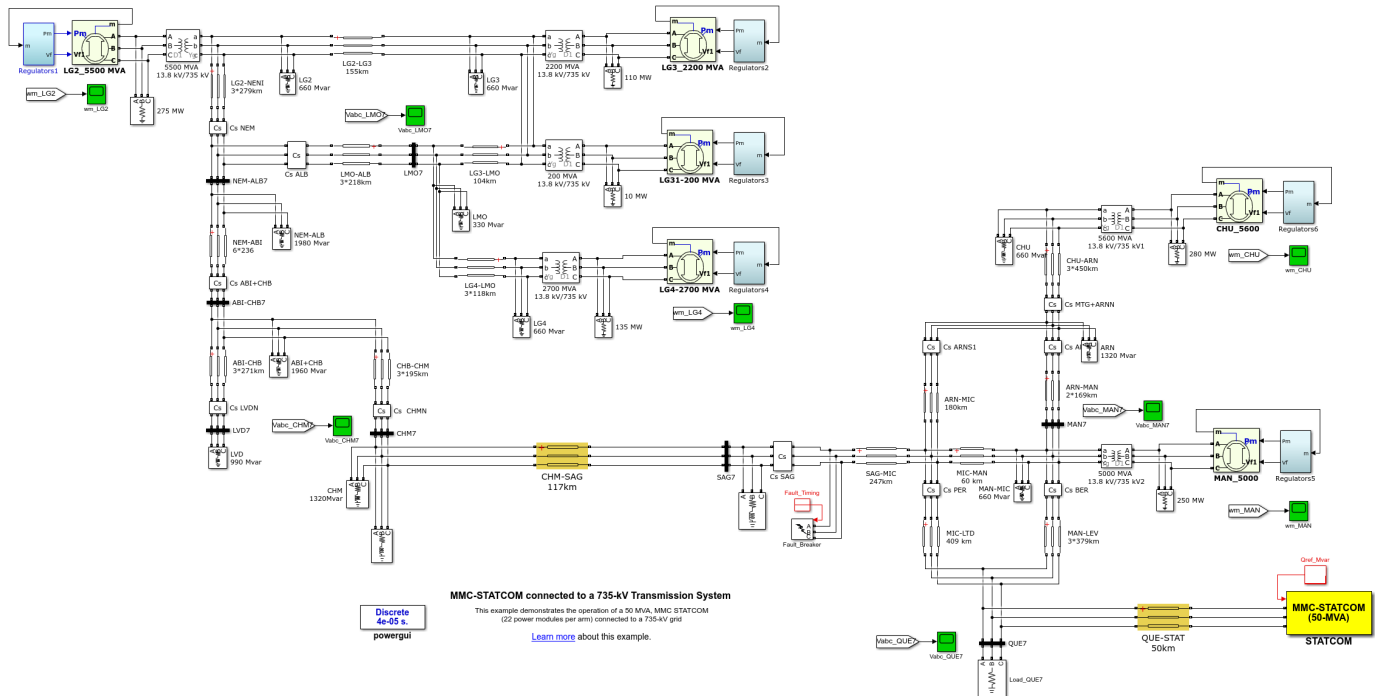
Description

In electrical grids, shunt compensation is often used for reactive power and voltage control. This example models a shunt compensation device that is increasingly used in modern grids: the Modular-Multi-level (MMC) STATCOM. The MMC-STATCOM is built using the Full-Bridge MMC block to represent a power electronics converter of 22 power modules (PM) per arm. The three MMC arms are delta-connected to an 18kV/735kV power transformer. Principle of Operation The STATCOM can absorb or generate reactive power. The reactive power transfer is done through the phase reactance. The converter generates a voltage in phase with the grid voltage. When the amplitude of the converter voltage is lower than that of the grid voltage, the STATCOM acts like an inductance absorbing reactive power. When the amplitude of the converter voltage is higher than that of the bus voltage, the STATCOM acts like a capacitor generating reactive power. 735-kV Transmission System The 60-Hz power system is a simplified model of a series-compensated 735-kV transmission system. The grid is both series- and shunt-compensated using fixed capacitors and inductors. It comprises the following main components: - Thirty-two three-phase buses - Six power plants. Each power plant is represented by a synchronous machine, an hydraulic turbine and governor, an excitation system, and a power system stabilizer (PSS) - Six three-phase power transformers - Twenty three-phase shunts and loads - Seventeen 735-kV distributed parameters lines. Eleven lines are series-compensated by capacitors in order to increase the transmission capability (compensation factor = 15% to 42% of the line reactance). - 50-MVA STATCOM with twenty-two cascaded full-bridge converters per arm

Control System

The STATCOM control system consists of the following main subsystems: - Measurements computes the d-axis and q-axis components of the primary voltages and currents by performing an abc-dq

transformation in the synchronous reference provided by a three-phase PLL synchronized on the primary voltages. - Reactive Power Regulator controls the reactive power absorbed or generated by the STATCOM by determining the required reactive current reference value (Iq_ref). - DC Voltage Regulator maintains the average value for all capacitors PM to the specified reference value (Vnom_PM=1600V). To perform this task, the regulator controls the active power absorbed or generated by the STATCOM by determining the required active current reference value (Id_ref). - Current Regulator generates the converter output voltage signals (Vd,Vq) in order to have measured current values (Id/Iq) equal to the reference values (Id_ref/Iq_ref). - Insertion Indices Computation determines the insertion indices (n) using the nearest-level modulation technique. The indices are calculated based on the current regulator outputs (Vd,Vq), the number and nominal voltage of the capacitors power modules. - Power Modules Selection selects which power modules will be inserted or removed based on the requested insertion indices and on a voltage balancing algorithm. The voltage balancing algorithm is based on sorting the capacitor voltages and on measuring the polarity of the current flowing into the arm. Double-click on the subsystem to see how the algorithm is implemented.

Simulation

Run the simulation and observe waveforms on Scope1. You can see that the simulation starts in steady state and that the STATCOM operates at 0 Mvar. At 0.1s, the set-point Qref value is changed to -30 Mvar (inductive var). At 0.3 second, the set-point changes from -30 Mvar to 40 Mvar (capacitive var). You can see that the STATCOM control system reacts very rapidly to modify the inverter output voltage in order to follow the new Qref set-point. You can also program the Fault_Timing block in order to produce a 6-cycle three-phase fault at the SAG7 substation and observe the STATCOM operation.

References

[1] M. Pereira, Member, IEEE, D. Retzmann, Member, IEEE, J. Lottes, M. Wiesinger, G. Wong, SVC PLUS: An MMC STATCOM for Network and Grid Access Applications 2011 IEEE Trondheim PowerTech

[2] Mattia Ricco, Laszlo Mathe, Manel Hammami, Francesco Lo Franco, Claudio Rossi and Remus Teodorescu, A Capacitor Voltage Balancing Approach Based on Mapping Strategy for MMC Applications Electronics Open Access Journal, April 2019

PV Home On-Grid Solar System

This example shows the operation of a photovoltaic (PV) residential system connected to the electrical grid.
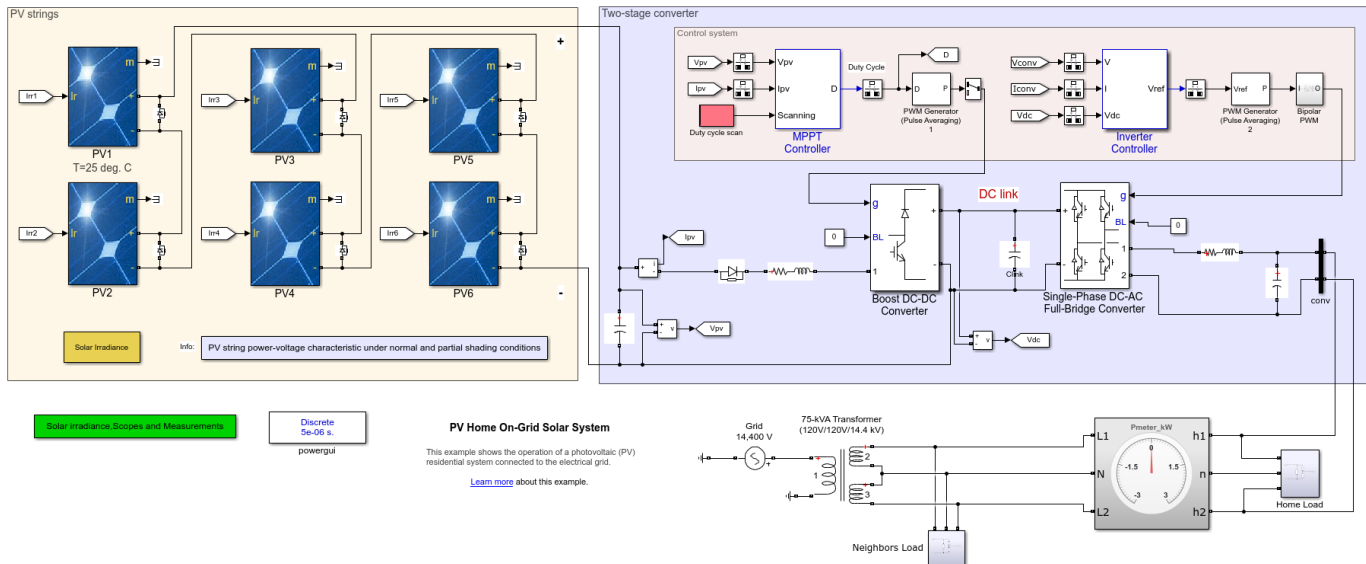
PV Strings

The PV strings section implements a home installation of six PV array blocks in series that can produce 2400 W of power at a solar irradiance of 1000 W/m2. In the Advanced tab of the PV blocks, the robust discrete model method is selected, and a fixed operating temperature is set to 25 deg. C.

Two-Stage Converter

The power produced by the PV strings is fed to the house and utility grid using a two-stage converter: a boost DC-DC converter and a single-phase DC-AC full-bridge converter. Both converters are PWM-controlled with a switching frequency of 20 kHz. They are using the Switching Function method with PWM pulse averaging, allowing a simulation sample time of 5 microseconds and good accuracy on harmonics generated.

Control Systems

- The MPPT Controller: The Maximum Power Point Tracking (MPPT) controller is based on the Perturb and Observe technique with scanning capability. The MPPT system automatically varies the duty cycle of the boost converter to generate the required voltage across the PV string in order to extract maximum power. Under partial shading condition, a duty cycle is initiated to find the global maximum power point (GMPP) among various local maximum power points (LMPP).

- The Inverter Controller: The inverter control maintain the DC link voltage at 400 V while keeping a unity power factor. The controller uses a voltage regulator outer loop and a fast inner loop current regulator to generate the appropriate reference voltage (Vref) for the PWM generator controlling the full-bridge converter.

Load & Utility Grid

The grid is modeled using a typical pole-mounted transformer and an ideal AC source of 14.4 kVrms. The transformer 240 volt secondary winding is center-tapped and the central neutral wire is grounded. The inverter, the 2500 W residential load as well as the neighbors' load are connected to the 240V secondary winding.

Simulation

Run the simulation and observe the resulting signals on the various scopes.

(1) At 0.25s, with a solar irradiance of 1000 W/m2 on all PV modules, steady state is reached. The solar system generates 2400 Watts and the DC link is maintained at 400 volts with a small 120-Hz ripple due to the single-phase power extracted from the PV string. The Utility meter indicates that the system takes almost no power from the grid to supply the home total load. (2) At 0.3s, a partial shading condition is created by reducing the irradiance on some PV modules. When steady-state is reached at 0.35s, the MPPT controller has set the boost duty cycle at 0.44, generating a PV string voltage of 225 V. With this voltage, 920 W is extracted from the PV string. As you can see on the PV curve characteristic, the system is operating at a local maximum power point but not at the global maximum power point. (3) At 0.4s, a duty cycle scan of 0.25 seconds is performed by the MPPT controller to find the GMPP point. (4) At 0.7s, the MPPT controller has set the boost duty cycle at 0.58 generating a PV string voltage of 168 V. With this voltage, 1364 W is extracted from the PV string which is the GMPP value. The Utility meter indicates that it takes now around 1100 W (2500 W residential load - 1364 W supplied by PV) from the grid to supply the home total load.

References

1. https://en.wikipedia.org/wiki/Photovoltaic_system

2. https://www.lg.com/us/business/download/resources/BT00002151/LG400N2W-A5.pdf

2-MW PV Farm Connected to a 25-kV Distribution System

This example shows a model of a 2-MW PV farm connected to a 25-kV distribution system.

2 MW PV Farm Connected to a 25-kV Distribution System

This example shows a 2-MW PV Farm connected to a 25-kV distribution system. The blocks in yellow are tuned to perform a simulation using the Switching devices modeling technique for the power electronics blocks.
Click here to open the example modified to use the Switching function modeling technique

Learn more about this example.
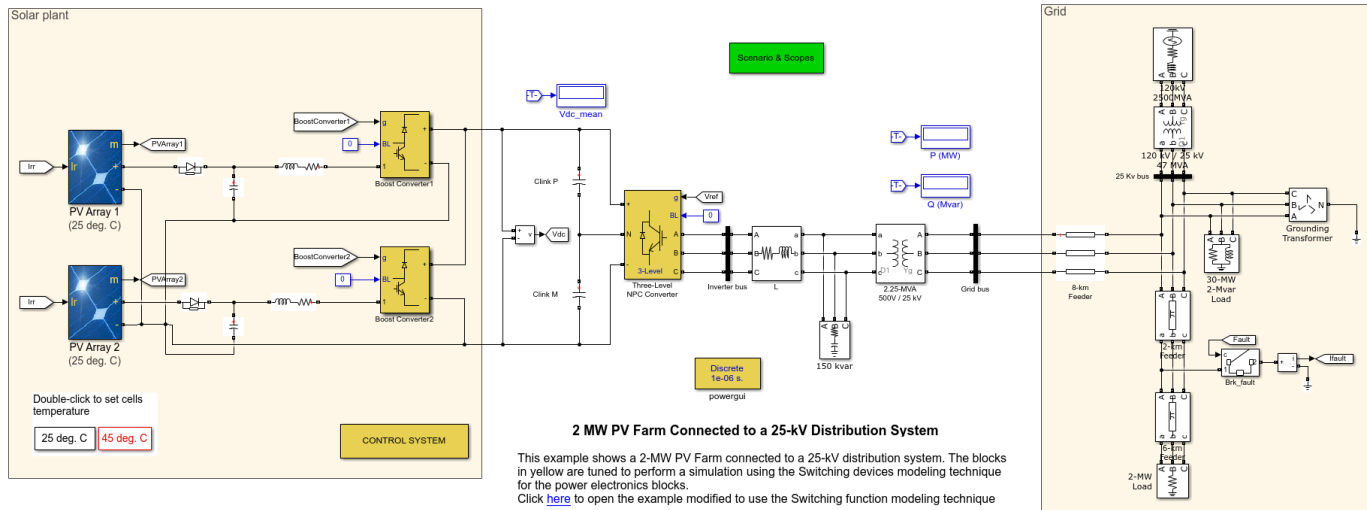
Description

The PV farm consists of two PV arrays: PV Array 1 and PV Array 2 can produce respectively 1.5 MW and 500 kW at 1000 W/m2 sun irradiance and at cell temperature of 25 deg C. Each PV array is connected to a boost converter. Each boost is individually controlled by a Maximum Power Point Trackers (MPPT) system. The MPPTs use the Perturb and Observe technique to vary the voltage across the terminals of the PV array in order to extract the maximum possible power. The outputs of the boost converters are connected to a common DC bus of 1000 V. A three-level NPC converter converts the 1000 V DC to around 500 V AC. The NPC converter is controlled by a DC voltage regulator whose job is to maintain the DC link voltage to 1000V whatever the amount of active power delivered by the PV arrays. In addition, the controller has a reactive power regulator allowing the converter to generate or absorb up to 1 Mvar. A 2.25-MVA 500V/25kV three-phase coupling transformer is used to connect the converter to the grid. The grid model consists of typical 25-kV distribution feeders and a 120-kV equivalent transmission system.

Simulation

Start the simulation and see the resulting signals on the various scopes.

In the Scenario & Scopes subsystem you can program four various disturbances: 1) Irradiance variation 2) DC link reference voltage step 3) Reactive power set-point variation 4) System fault.

You can simulate the model with the PV cells temperature set to 45 deg.C or to 25 degC by double-clicking on the corresponding blocks below the PV Arrays blocks.

The blocks in yellow in the model are tuned to perform a simulation using the Switching devices modeling technique for the power electronics blocks. You can refer to the Please refer to the documentation pages of the Boost Converter and Three-Level NPC Converter blocks for more information on the available modeling techniques of the power electronics blocks.

The same example is simulated using an average model for the boost converters and a switching function model for the three-level NPC converter. This allows to run the model with a larger sample time resulting in a much faster simulation while obtaining almost identical results.

References

1. Horowitz, Kelsey, Zac Peterson, Michael Coddington, Fei Ding, Ben Sigrin, Danish Saleem, Sara E. Baldwin, et al. 2019. An Overview of Distributed Energy Resource (DER) Interconnection: Current Practices and Emerging Solutions. Golden, CO: National Renewable Energy Laboratory. NREL/TP-6A20-72102

2. Haidar Islam, Saad Mekhilef, Noraisyah Binti,Mohamed Shah, Tey Kok Soon,Mehdi Seyedmahmousian, Ben Horan and Alex Stojcevski. Performance Evaluation of Maximum Power Point Tracking Approaches and Photovoltaic Systems. Energies 2018, 11, 365; doi:10.3390/en11020365.

On Load Tap Changer (OLTC) Regulating Transformer Using Variable-Ratio Transformer Blocks

This example illustrates the use of an On Load Tap Changer Transformer (OLTC) to regulate positive-sequence voltage at a 25-kV bus in a transmission network.
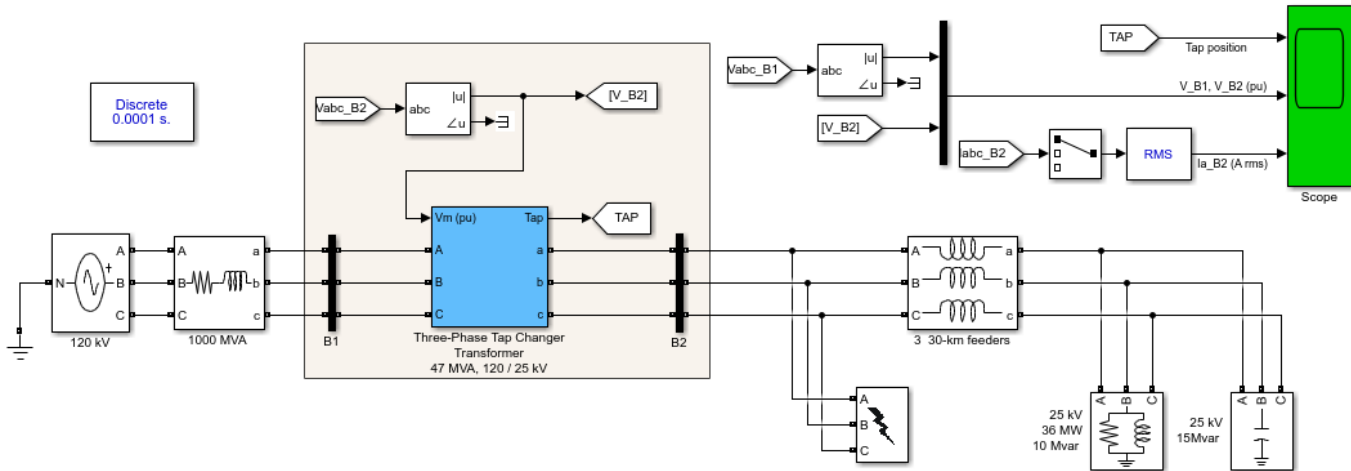
On Load Tap Changer (OLTC) Regulating Transformer Using Variable-Ratio Transformer Blocks

The On-Line Tap Changer Transformer (OLTC) regulates positive-sequence voltage at B2 25-kV bus.
Reference voltage is set at 1.0 pu. Observe OLTC reaction for the following 3 events:

· 1.1 pu over-voltage in 120 kV network at t= 2 sec
· 0.95 pu under-voltage in 120 kV network at t= 10 sec
· 0.2 sec single-phase fault at B2 bus at t= 20 sec. OLTC does not react because under-voltage duration is lower than specified Delay (0.4 sec)

Note: In order to reduce simulation time, the OLTC tap selection time parameter (usually between 3 sec and 10 sec) has been shortened to 0.5 sec.

Description

This example illustrates the use of an On Load Tap Changer Transformer (OLTC) to regulate positive-sequence voltage at a 25-kV bus in a transmission network. During the simulation we can observe OLTC reaction for a 1.1 pu over-voltage in 120 kV network, a 0.95 pu under-voltage in 120 kV network, and a 0.2 sec single-phase fault at the 25 KV bus.

Simulation

Reference voltage is set at 1.0 pu. Observe OLTC reaction for the following 3 events: * 1.1 pu over-voltage in 120 kV network at t = 2 sec. * 0.95 pu under-voltage in 120 kV network at t = 10 sec. * 0.2 sec single-phase fault at B2 bus at t = 20 sec. OLTC does not react because under-voltage duration is lower than the specified Delay (0.4 sec).

Three-Phase Three-Limb (Core-Type) Two-Winding Transformer

This example illustrates saturation in a Three-Limb Core-Type transformer and compares the Simscape™ Electrical™ Specialized Power Systems model with a Simscape-based physical model.
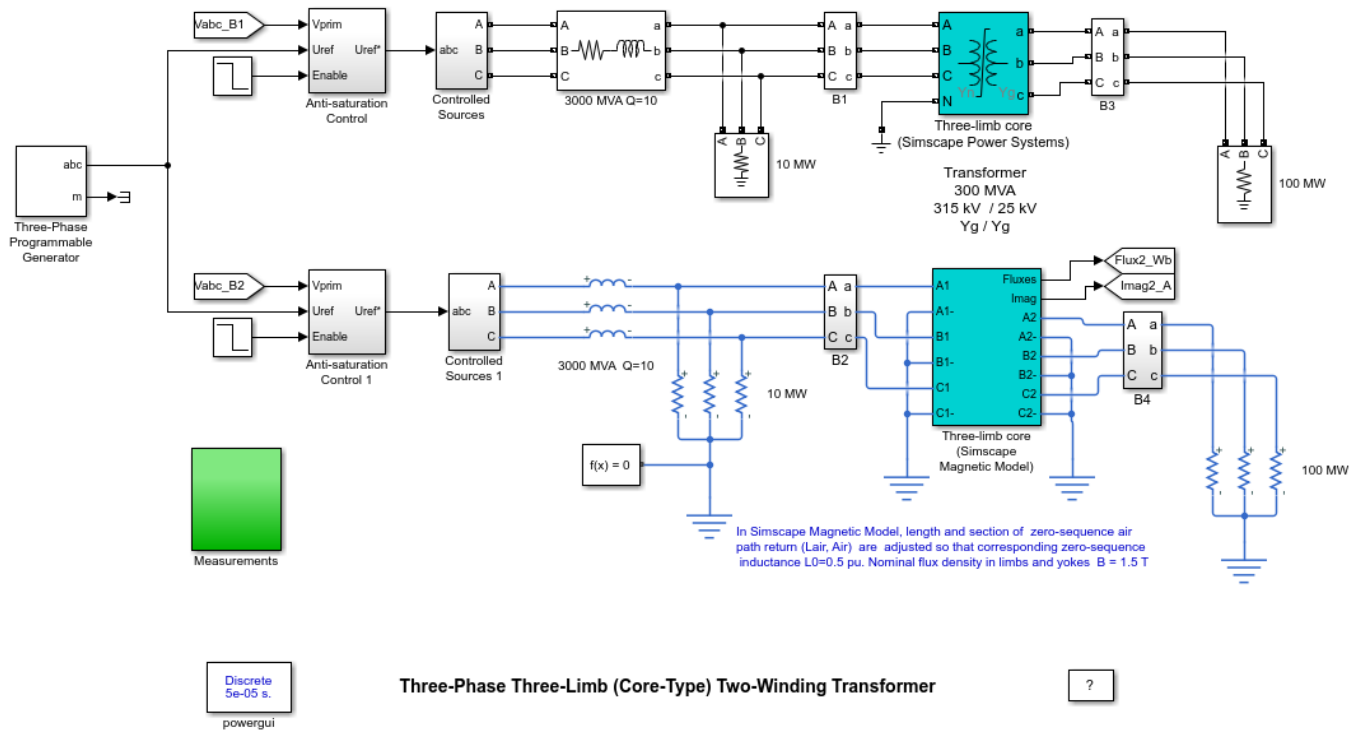
Description

The top circuit uses Simscape Electrical Specialized Power Systems blocks to implement a 300 MVA, 315 kV/25 kV, Yg/Yg transformer connected to a 315 kV grid. During asymmetrical voltage conditions, the zero-sequence flux returns outside the core, through an air gap, structural steel and tank. Thus, the natural zero-sequence inductance L0 (without Delta winding) of such a core-type transformer is usually very low (typically 0.5 pu < L0 < 2 pu) compared to a three-phase transformer using three single-phase units (L0 > 100 pu) or with a five-limb shell-type transformer. This low L0 value will affect voltage, current and flux imbalances both during linear and saturated operation.

The bottom circuit implements the same circuit using the Simscape electrical and magnetic foundation libraries. Look under the mask of the Simscape transformer to see how the magnetic circuit is implemented. Blue blocks represent steel reluctances of the 3 limbs and 4 yokes with specified nonlinear B-H characteristic. Yellow blocks represent reluctances of air paths modeling the winding leakage inductances. The orange block models reluctance of the zero-sequence flux path through the air. Its average length and section are adjusted in order to get a 0.5 pu zero-sequence inductance (as specified in Specialized Power Systems model).

The Three-Phase Programmable Generator block is used to simulate a 1.7 pu overvoltage on phase A (from 0.2 sec to 0.3 sec), producing saturation of phase A. Transformers in both models start with zero initial fluxes. In order to decrease the time required to reach steady-state, Anti-saturation Control blocks are used to quickly force the average fluxes in the three phases to zero.

Observe that both models produce the same voltage, current, and flux waveforms.

Three-Phase Five-Limb (Shell-Type) Three-Winding Transformer

This example illustrates saturation in a Five-Limb Shell-Type transformer and compares the Specialized Power Systems model with a Simscape-based physical model.
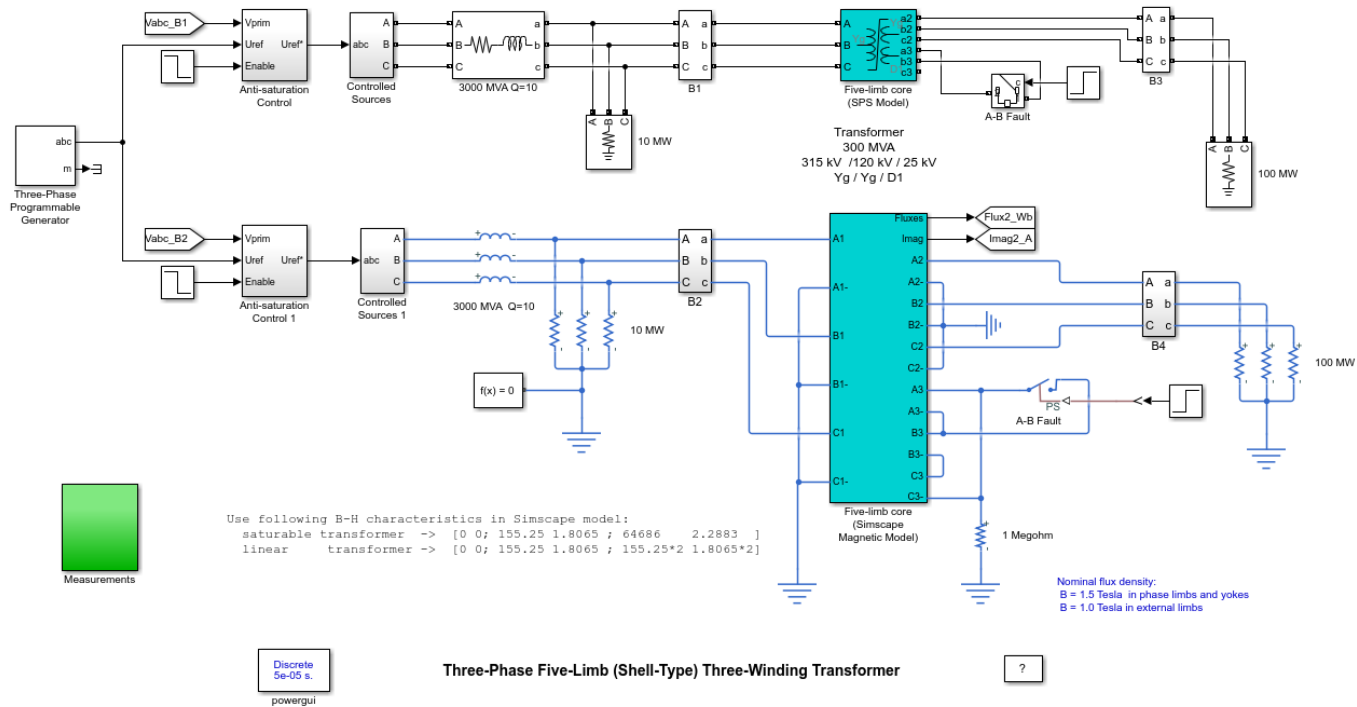
Description

The top circuit uses SimPowerSytems blocks to implement a 300 MVA, 315 kV/120 kV/25 kV, Yg/Yg/D transformer connected to a 315 kV grid. During unbalanced voltage conditions, contrary to the three-limb transformer, the zero-sequence flux of the five-limb transformer stays inside the steel core and returns through the two external limbs. The natural zero-sequence inductance (without Delta) is very high (L0 > 100 pu). Therefore, except for small current imbalances due to core asymmetry, the behavior of the five-limb shell-type transformer is very similar to that of a three-phase transformer built with three single-phase units.

The bottom circuit implements the same circuit using the Simscape electrical and magnetic foundation libraries. Look under the mask of the Simscape transformer block to see how the magnetic circuit is implemented. Blue blocks represent steel reluctances of the 5 limbs and 8 yokes with specified nonlinear B-H characteristic. Yellow blocks represent reluctances of air paths modeling the winding leakage inductances.

The Three-Phase Programmable Generator block is used to simulate a 1.7 pu overvoltage on phase A (from 0.2 sec to 0.3 sec), producing saturation of phase A. Then, a phase-phase (A-B) fault is applied across the Delta winding at t=0.35 sec. Transformers in both models start with zero initial fluxes. In order to decrease the time required to reach steady-state, Anti-saturation Control blocks are used to quickly force the average fluxes in the three phases to zero.

Observe that voltages, currents, and fluxes produced by the two models are very similar.

Switching an Inductive Circuit Using a Breaker With No Snubber

This example shows the Ideal Switching device solution method of the Powergui block to model a circuit breaker.

G. Sybille (Hydro-Quebec)

Description

The use the Ideal Switching device solution method allow to simulate a circuit breaker without snubber and connected in series with an inductive circuit.

Zoom on breaker current and note the "exact" zero current when the breaker is open.

This solution technique introduced in R2008b is available with continuous solvers. To enable this option, check the "Enable use of ideal switching devices" parameter of the powergui block.

Steady-State Analysis of a Linear Circuit

This example shows the use of the Powergui and Impedance Measurement blocks to analyze the steady-state operation of a linear electrical circuit

G. Sybille (Hydro-Quebec)
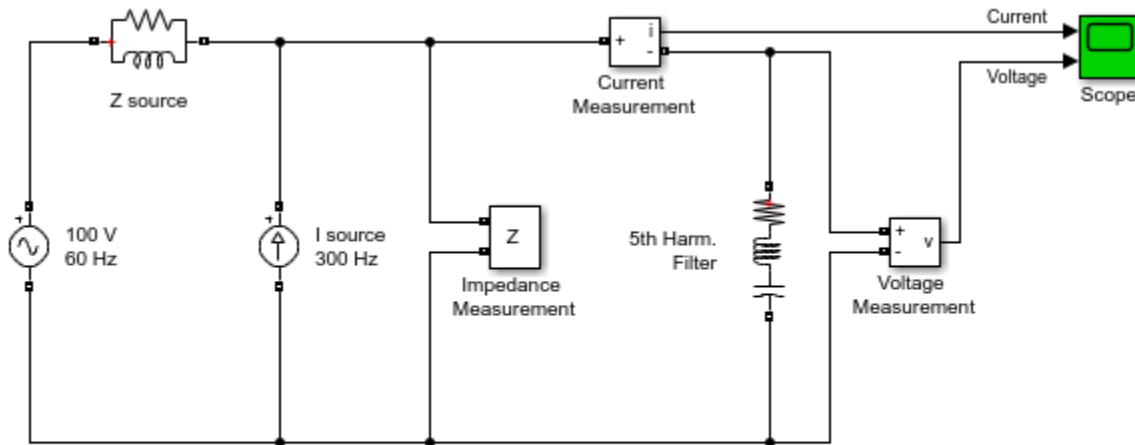
Steady-State Analysis of a Linear Circuit

Description

A 5th harmonic filter is connected at a bus bar fed by a 60 Hz, 100 V inductive source. A 5th harmonic (300 Hz, 1 A) current is injected at the bus bar.

This linear system consists of 3 states (2 inductor currents and 1 capacitor voltage), 2 inputs (Vs, Is) and 2 outputs (Current and Voltage Measurement).

An Impedance Measurement block is used to compute the impedance versus frequency of the circuit.

Simulation

1. Use the Powergui block to find the steady-state 60Hz and 300 Hz components of voltage and current phasors. The values of the 3 states (phasors and initial values) can be also obtained from the powergui block.

2. Open the scope and start the simulation from the Simulation/Start menu. Notice that the simulation starts in steady-state. Using the Powergui block, select Impedance vs Frequency Measurement. A new window opens.

3. The measurement will be performed for the specified frequency range vector [0: 2:1000] (0 to 1000 Hz by steps of 2 Hz). Click on the Display button. The impedance is displayed in a graphic window. Notice the series resonance at 300 Hz corresponding to the tuned frequency of the filter.

Transient Analysis of a Linear Circuit

This example shows steady-state and transient simulation of a linear circuit.

H. Le-Huy (Universite Laval, Quebec) and G. Sybille (Hydro-Quebec)

Description

This circuit is a simplified model of a 230 kV three-phase power system. Only one phase of the transmission system is represented. The equivalent source is modeled by a voltage source (230 kV rms/sqrt(3) or 187.8 kV peak, 60 Hz) in series with its internal impedance (Rs Ls) corresponding to a 3-phase 2000 MVA short circuit level and X/R = 10. (X = 230e3^2/2000e6 = 26.45 ohms or L = 0.0702 H, R = X/10 = 2.645 ohms). The source feeds a RL load through a 150 km transmission line. The line distributed parameters (R = 0.035ohm/km, L = 0.92 mH/km, C = 12.9 nF/km) are modeled by a single pi section (RL1 branch 5.2 ohm; 138 mH and two shunt capacitances C1 and C2 of 0.967 uF). The load (75 MW - 20 Mvar per phase) is modeled by a parallel RLC load block.

A circuit breaker is used to switch the load at the receiving end of the transmission line. The breaker which is initially closed is opened at t = 2 cycles, then it is reclosed at t = 7 cycles. Current and Voltage Measurement blocks provide signals for visualization purpose.

Simulation

1. Simulation using a continuous solver (ode23tb)

Start the simulation and observe line voltage and load current transients during load switching and note that the simulation starts in steady-state. Use the zoom buttons of the oscilloscope to observe the transient voltage at breaker reclosing.

2. Using the Powergui to obtain steady-state phasors and set initial states

Open the Powergui block and select "Steady State Voltage and Currents" to measure the steady-state voltage and current phasors. Using the Powergui select now "Initial States Setting" to obtain the

initial state values (voltage across capacitors and current in inductances). Now, reset all the initial states to zero by clicking the "to zero" button and then "Apply" to confirm changes. Restart the simulation and observe transients at simulation starting. Using the same Powergui window, you can also set selected states to specific values.

3. Discretizing your circuit and simulating at fixed steps

The Powergui block can also be used to discretize your circuit and simulate it at fixed steps. Open the Powergui. Select "Discretize electrical model" and specify a sample time of 50e-6 s. The state-space model will now be discretized using trapezoidal fixed step integration. The precision of results is now imposed by the sample time. Restart the simulation and compare simulation results with the continuous integration method. Vary the sample time of the discrete system and note the impact on precision of fast transients.

4. Using the phasor simulation method

You will now use a third simulation technique. The "phasor simulation" method consists to replace the circuit state-space model by a set of algebraic equations evaluated at a fixed frequency and to replace sinusoidal voltage and current sources by phasors (complex numbers). This method allows a fast computation of voltage and current phasors at a selected frequency, disregarding fast transients. It is particularly efficient to study electromechanical transients of generators and motors involving low frequency oscillation modes. Open the Powergui block and select "Phasor simulation". Restart the simulation. Observe that the magnitude of 60 Hz voltage and current is now displayed on the scope. If you double click on the voltage or current measurement block you can choose to output phasor signals in four different formats: Complex, Real/Imag, Magnitude/Angle (in degrees), or just Magnitude (default value). Notice that you cannot send a complex signal to an oscilloscope.

Three-Winding Distribution Transformer

This example shows the use of the linear transformer to simulate a three-winding distribution transformer rated 75 kVA - 14400/120/120 V.
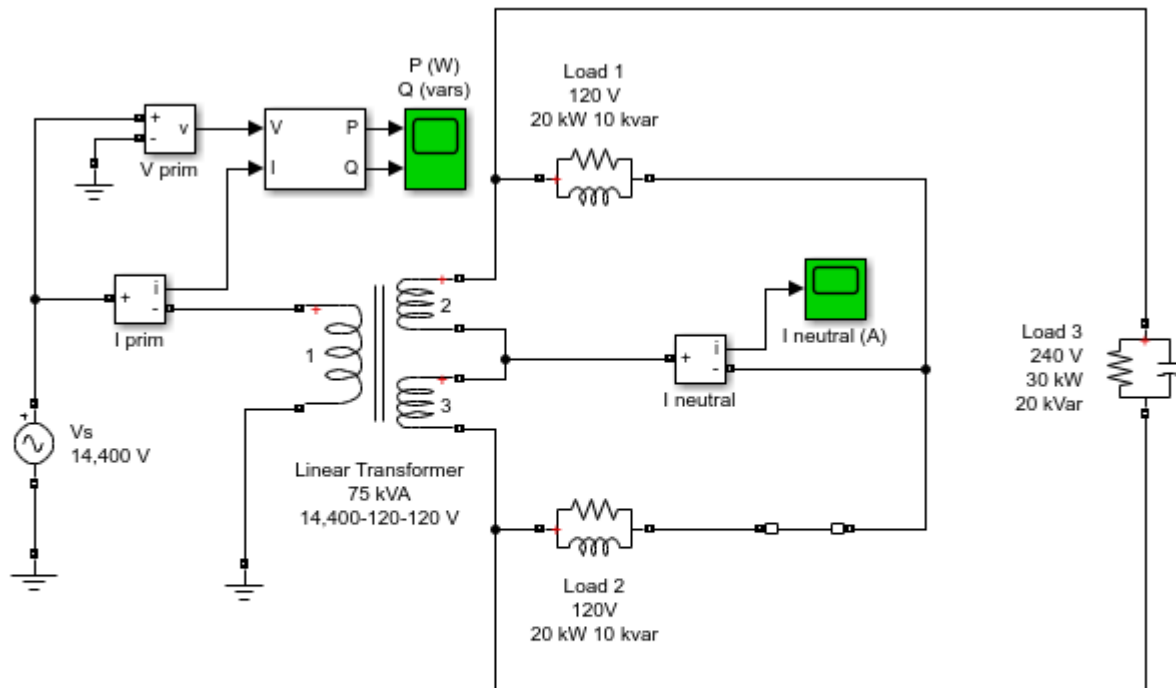
Three-Winding Distribution Transformer

Description

The transformer primary is connected to a high voltage source (14,400 V rms). Two identical inductive loads (20 kW -10 kvar) are connected to the two secondaries. A third capacitive load (30 kW -20 kvar) is fed at 240 V.

Initially, the circuit breaker in series with Load 2 is closed, so that the system is balanced.

Simulation

Open the powergui block to obtain the initial voltage and current phasors in steady state. As loads are balanced the neutral current is practically zero.

Furthermore, as the inductive reactive power of Load 1 and Load 2 (2*10 kvar) is compensated by the capacitive reactive power of Load 3 (20 kvar), the primary current is almost in phase with voltage The small phase shift (-2.8deg.) is due to the reactive power associated with transformer reactive losses.

Open the two scopes and start the simulation. When the circuit breaker opens, a current starts to flow in the neutral as a result of the load unbalance. When the breaker opens, the active power decreases from 70 kW to 50 kW.

Three-Phase Saturable Transformer

This example shows the transformer saturation phenomenon.

G. Sybille (Hydro-Quebec)
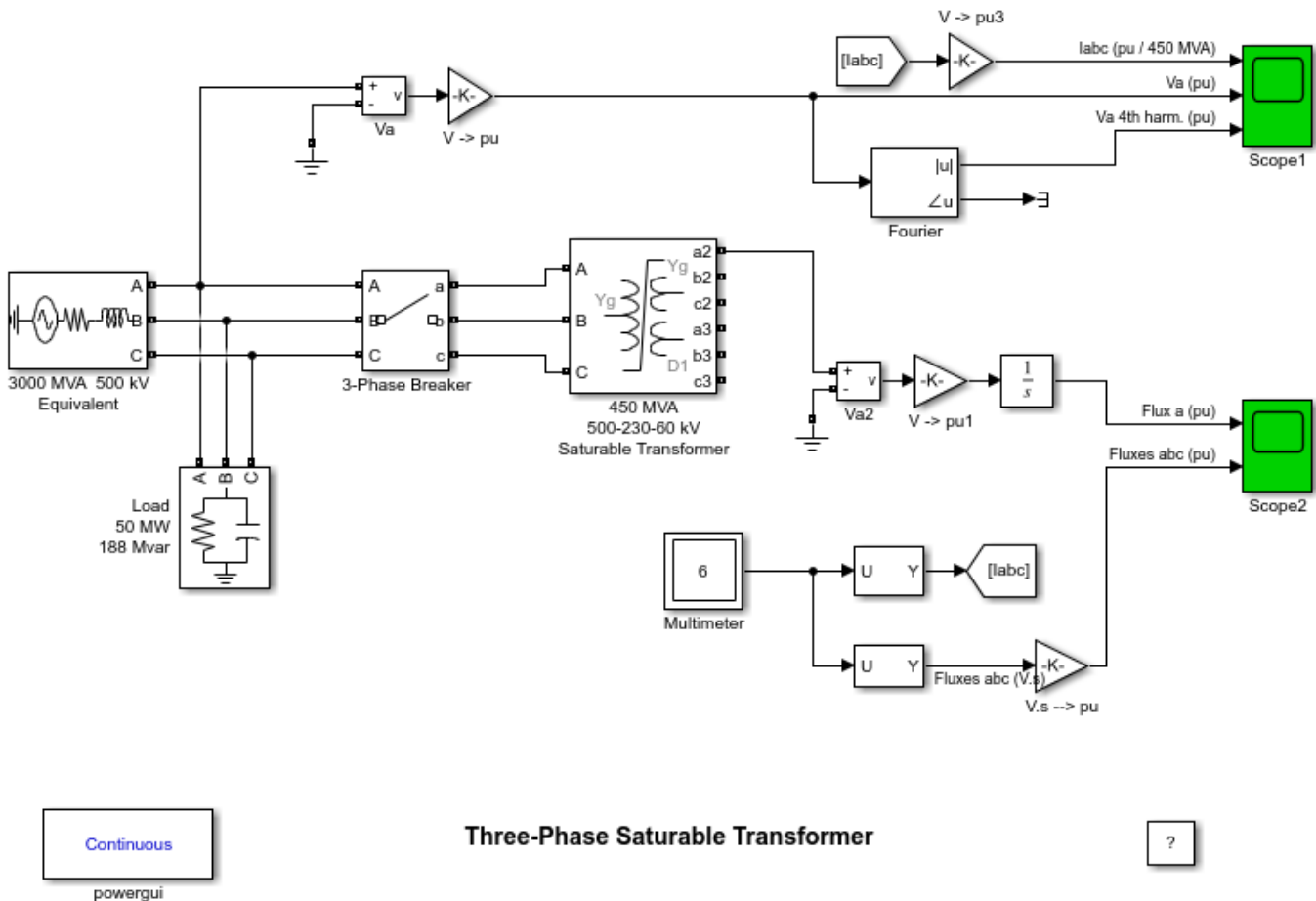
Three-Phase Saturable Transformer

Description

A three-phase transformer is energized on a 500 kV network. The transformer rated 450 MVA, 500 kV/230 kV/60 kV consists of three windings connected in Y/Y/Delta.

The power system is simulated by an equivalent circuit consisting of an inductive source (short-circuit power of 3000 MVA) and a parallel RC load. The capacitor reactive power has been selected in order to produce a resonance at 240 Hz (4th harmonic).

The transformer saturation characteristics is approximated by a single slope Xsat of 0.32 pu, corresponding to an air core reactance Xac = 0.40 pu (Xac = Xsat+Xh = 0.32+0.08 = 0.40 pu.) seen from the primary. Three residual fluxes (-0.8 - 0.4 0.4 pu) are specified for phases A B and C.

The Multimeter and Scope2 blocks are used to monitor extra signals without using measurement blocks. The six signals obtained at the output of the Multimeter are the three currents in the circuit breaker and the three fluxes inside the core of the saturable transformer. The flux on phase A is also

obtained by integrating the phase A voltage at the unloaded output of winding two. The voltage and flux are converted to p.u. with gain blocks using proper scaling. The Fourier block measures the 4th harmonic content of the phase A primary voltage.

In order to allow further signal processing, signals displayed on Scope1 are sampled at 1/60/333 s (333 samples/ cycle) and stored in a variable named 'psbtransfosat_str' (structure with time).

Simulation

Start the simulation and observe voltage, current and flux waveforms on Scope1 and Scope2.

Observe the transformer inrush currents and overvoltage after breaker closing. Because of the 0.8 pu residual flux on phase A and breaker closing at an instant causing maximum flux offset, the flux exceeds 2 pu on phase A. Compare the flux in phase A measured by the multimeter (yellow trace on 2nd input of Scope2) and the flux obtained by integration of the secondary voltage (first trace of Scope2).

Voltage Va contains a high level of 4th harmonic (displayed on 3rd trace of Scope1) due to the 4th harmonic content of current injected into the network resonating at that particular frequency (max. of 0.23 pu at t = 0.32 s). When simulation is completed, open the Powergui and select 'FFT Analysis' to display the 0 - 1000 Hz frequency spectrum of signals saved in the 'psbtransfosat_str' structure. The FFT will be performed on a 2-cycle window starting at 0.32 s (instant of highest 4th harmonic). Select input labeled 'Va (pu)' and click on Display to obtain the Va voltage spectrum. Note that distortion is caused mainly by harmonic orders 2 to 6 (highest content = 23% of 4th harmonic as obtained from the Fourier block).

Three-Phase Core-Type Transformer

This example shows the use of the Three-Phase Transformer Inductance Matrix Type block to model a three-phase core-type saturable transformer. It also shows that using three single-phase transformers to simulate a Yg/Yg core-type transformer is not acceptable.

Gilbert Sybille (Hydro-Quebec, IREQ)

Modeling a 225 kVA, 2400V/600V Three-Phase Core-Type Transformer
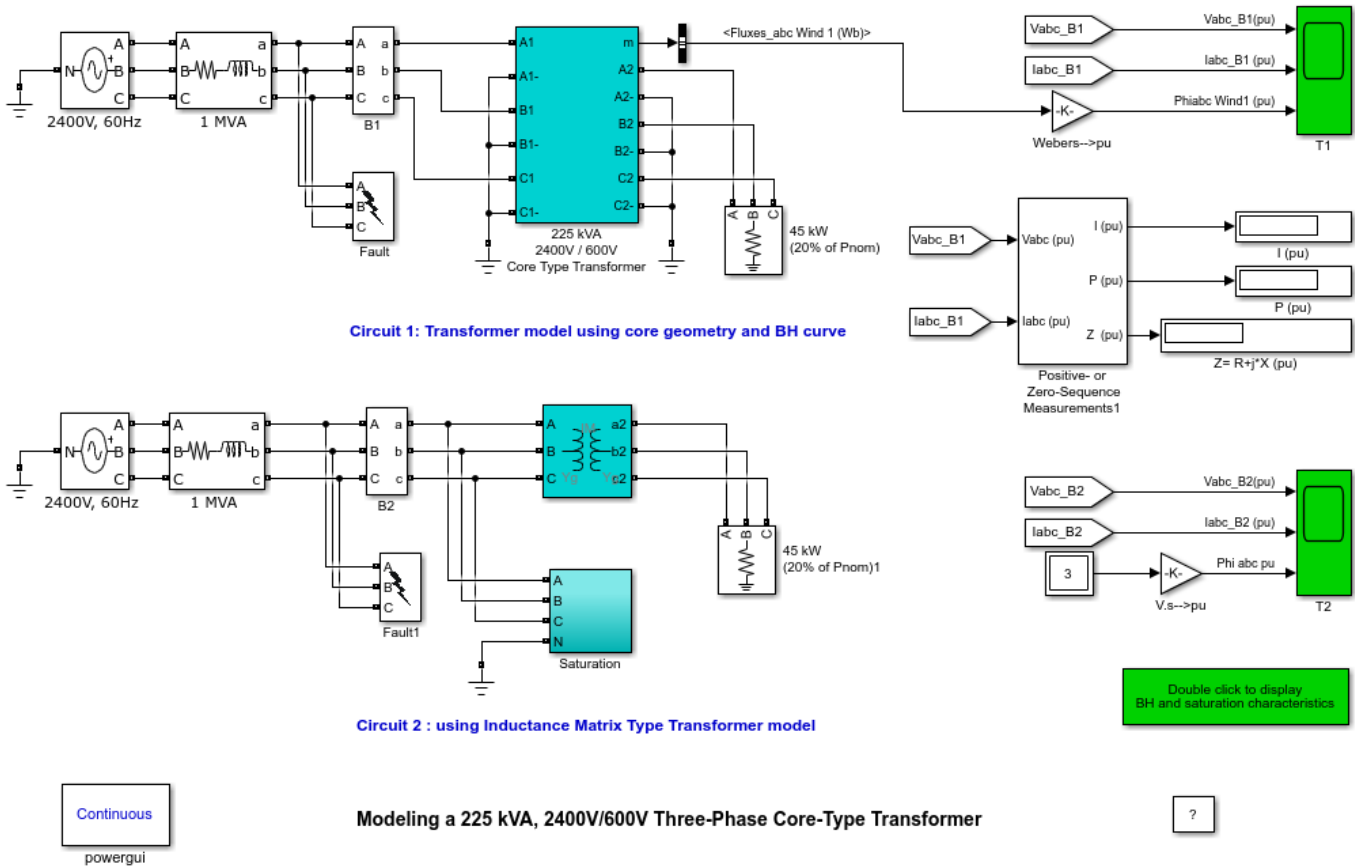
Description

The model shows two identical circuits with a three-phase transformer rated 225 kVA, 2400 V/600V, 60Hz, connected to a 1 MVA, 2400 V power network. A 45 kW resistive load (20 % of transformer nominal power) is connected on the 600 V side. Each phase of the transformer consists of two windings both connected in wye with a grounded neutral.

The transformers in circuit 1 and circuit 2 use two different models:

1) Circuit 1 uses a **physical model** where the core geometry and the B-H characteristic of the iron used to build the core are the basic parameters used for modelling the magnetic properties of the transformer.

2) Circuit 2 uses the **Three-Phase Transformer Inductance Matrix Type (Two Windings)** block for modeling the linear part of the model. Saturation is modeled in the "Saturation" subsystem by

three single-phase saturable transformers connected on the primary side of the linear transformer model.

In order to minimize the quantity of iron, the transformer core uses the core-type construction. Contrary to a three-phase transformer built with three independent units, the three phases of a core-type transformer are coupled. Because of these couplings, the transformer reactances in positive- and zero-sequence are quite different. When the three voltages applied on primary side are balanced, (positive-sequence voltage) the fluxes set up in each limb are also balanced and they stay trapped inside the magnetic core. However, when the voltage source or the load is unbalanced, a zero-sequence voltage is added to the positive- and negative sequences voltages. This zero-sequence voltage produces three flux components in phase in each limb, resulting in a zero-sequence flux component which has to circulate outside the iron core, through the air and transformer tank or casing. Due to the high reluctance (low permeability) of the flux return path through the air, the zero-sequence no-load excitation current is much higher than in positive sequence. For this particular model, zero-sequence excitation current exceeds 3 times the nominal current (344 %), as compared to only 2.2 % in positive sequence. Excitation current, no load active losses and short-circuit impedance R+j*X (where R=winding resistance, X=leakage reactance) have been measured for the physical model of circuit 1. Results are shown in the table below.

	positive-sequence	zero-sequence
No-load excitation current (% of nominal current)	2.28 %	353 %
No load active power losses (winding losses + iron losses) (% of nominal power)	2.0 %	14.0 %
Short-circuit impedance R+jX (pu)	0.02 + j*0.10 pu	0.0168 + j*0.0914 pu

Note : You can verify these values by using the "Sequence Measurements" block provided in the model. Specify either "Positive " or "Zero" sequence in the block menu. To perform the no-load measurements you must disconnect the load and use an infinite voltage source (source impedance bypassed), either in positive-sequence or a zero-sequence. In order to apply a zero-sequence voltage connect all three terminals of B1 measurement block to the same A terminal of the source. Also, in order to initialize fluxes in zero-sequence, specify the following vector of **Voltages for flux initialization** in the block menu:

[VmagA VmagB VmagC (pu) VangleA VangleB VangleC (deg)] =[1 1 1 0 0 0]

A schematic of the core geometry specified in the physical model is shown below.

```
        L2             L2
     ==================
     ||        ||          ||
     ||        ||          ||
     ||        ||          ||
     A         B           C    L1
     ||        ||          ||
     ||        ||          ||
     ==================
```
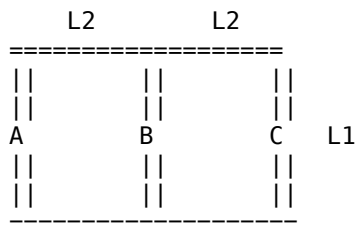
```
   L1 = average height of the three limbs bearing the windings (2 windings per limb) = 53 inches
   L2 = average length of the core yokes interconnecting the limbs = 21 inches
A1=A2 = cross section of the limbs and yokes = 45.48 square inches
Number of turns of high-voltage windings (2400/sqrt(3)= 1386 V) = 128
Number of turns of low-voltage  windings (600/sqrt(3) = 346.4 V)= 32
```

Look under the mask of the transformer of circuit 1 to see how the electrical and magnetic circuit models are built. The electrical part is implemented by six controlled current sources (one source per winding). These current sources are driven by the magnetomotive force developed by each winding. The "Core" subsystem uses the electric/magnetic analogy to implement the magnetic circuit which consists of 7 steel elements and 7 air elements representing flux leakages for each of the six coils and flux zero-sequence return path.

The three figures below show respectively:

1) Iron B-H characteristic

```
ans =

  Figure (1) with properties:

      Number: 1
        Name: ''
       Color: [0.9400 0.9400 0.9400]
    Position: [1 1 784 516]
       Units: 'pixels'

  Use GET to show all properties
```


Figure 1 : B-H characteristic of iron
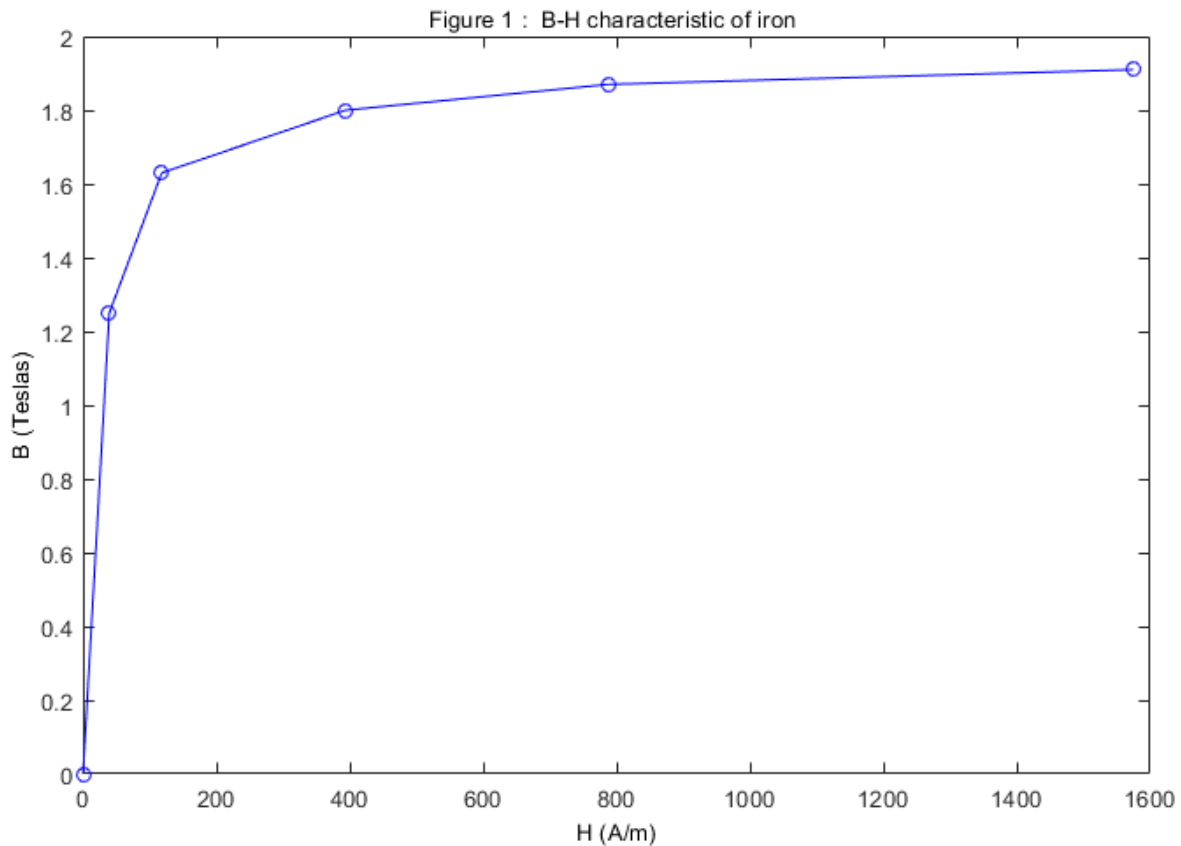
2) Saturation characteristics for the three phases (flux in pu as function of peak magnetizing current in pu) when the transformer is excited in positive sequence (3 balanced voltages). These saturation characteristics obtained in positive sequence are used in the three single-phase saturable transformers to model saturation of the core-type transformer.

```
ans =

  Figure (2) with properties:

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        Name: ''
       Color: [0.9400 0.9400 0.9400]
    Position: [105 490 560 420]
       Units: 'pixels'

  Use GET to show all properties
```


Figure 2 : Positive-sequence saturation characteristics for phases A, B and C
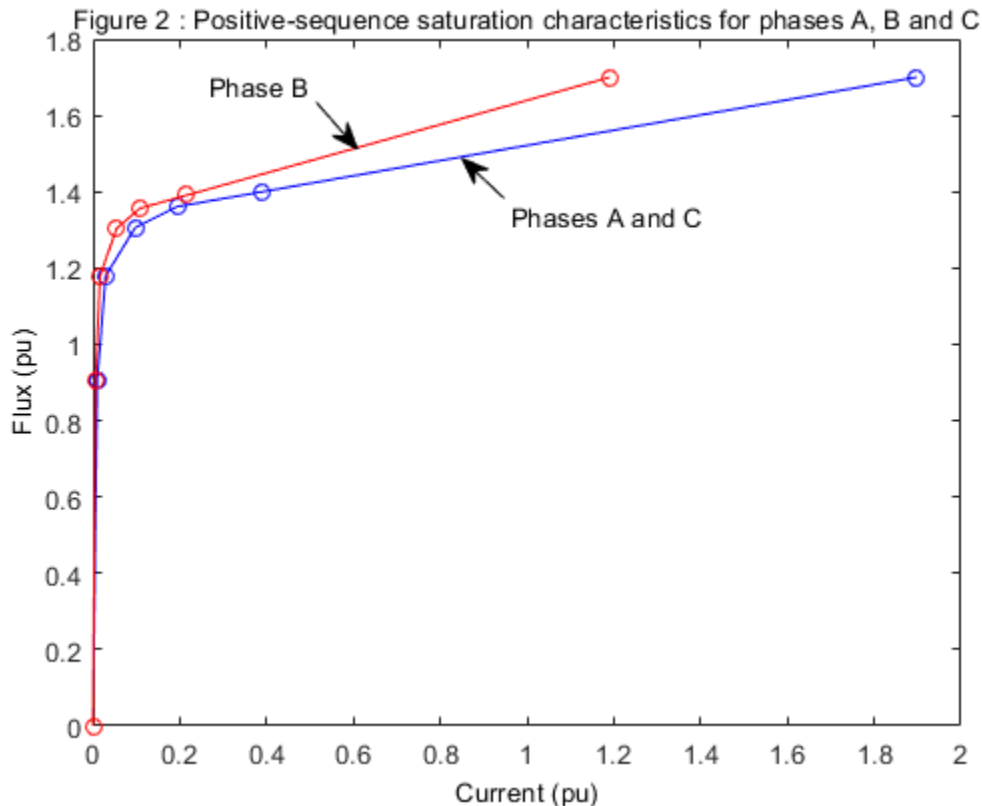
Using the positive-sequence saturation characteristics to model core saturation gives acceptable results even in presence of zero-sequence voltages. This is because the magnetic circuit used for conducting zero-sequence flux is mainly linear due to its large air gap. The large zero-sequence currents required to magnetize the high reluctance air path are taken into account in the linear model. Therefore, connecting a saturable transformer outside the three-limb linear model with a flux-current characteristic obtained in positive sequence will produce currents required for magnetization of the iron core.

3) Waveforms of excitation currents when a 1.5 pu voltage is applied at the 2400 V terminals.

```
ans =

  Figure (3) with properties:

      Number: 3
        Name: ''
       Color: [0.9400 0.9400 0.9400]
    Position: [674 496 560 414]
       Units: 'pixels'

  Use GET to show all properties
```


Figure 3: Excitation currents at 1.5 pu voltage
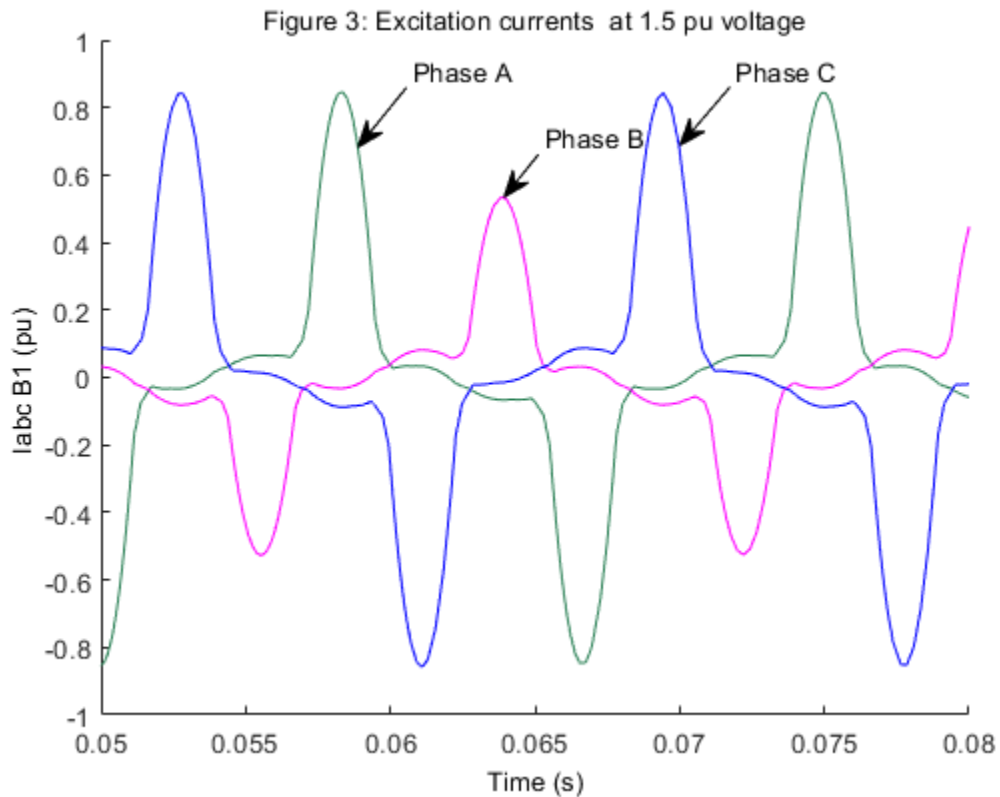
Notice on Figure 2 that, because of the core asymmetry, the magnetizing current of phase B is lower than the current obtained for phase A and phase C. See for example on Figure 3 the excitation currents obtained with 1.5 pu voltage.

Simulation

In order to emphasis the importance of a correct representation of transformer zero-sequence parameters, the transient performance of the Inductance Matrix Type transformer of circuit 2 is compared to the physical model of circuit 1 when a single-phase to ground fault is applied on phase A. A six-cycle fault is applied at 2400 V terminals at t=0.05 sec and cleared at t=0.15 sec.

Before starting simulation, open the Three-Phase Transformer Inductance Matrix Type block menu. Check that the "Core type" parameter is set to "Three-limb or five-limb core". Now, select the "Parameters" tab and check that the positive- and zero-sequence parameters are set according to the table given in the Circuit Description section.

1. Comparison of transient performance of transformer operating in linear region

Start the simulation. Observe on Scope1 and Scope2 respectively for circuit 1 and circuit 2 the following waveforms at 2400 V terminals: three-phase voltages, three-phase currents, three-phase fluxes.

When the fault is applied, the three currents flowing in the 2400 V windings increase from their steady state value (0.20 pu) to 1 pu and contain mainly a zero-sequence component (3 components in phase). During the fault, a DC flux is trapped in phase A, close to its value at fault application (~ -1 pu), whereas the sinusoidal fluxes in phases B and C do not exceed 1.3 pu, Therefore, transformer is operating mainly in the linear region (see Figure 2). Voltage and current waveforms of both models compare well, indicating that the Inductance Matrix Type transformer accurately represents the linear part of the core-type transformer.

2. Comparison of transient performance with saturated transformer

At fault clearing, a flux offset is produced on phase A, driving transformer into saturation. Flux in phase A reaches 1.5 pu, resulting in a strongly non linear current in phase A. Comparison of phase A currents is still acceptable although larger peak values are observed with the transformer of circuit 2. The reason is that the three saturable transformers modelling saturation in positive sequence are connected at winding terminals rather than being connected close to the core, behind the winding resistance and leakage reactances.

3. Simulating the core-type transformer with three single-phase transformers

You will now observe the impact of simulating the core-type transformer by using three single-phase transformers. Open the Three-Phase Transformer Inductance Matrix Type block menu and change the "Core type" parameter to "Three single-phase cores".

Restart simulation and compare waveforms of the two circuits. Notice that during fault the transformer currents of circuit 2 stay unchanged for phases B and C whereas current in phase A falls to zero. This test clearly shows that simulating a Yg/Yg core-type transformer with three single-phase units is unacceptable. The reason is that, in case of three single-phase units, positive-sequence parameters are assumed to be equal to zero-sequence parameters, and the low zero-sequence shunt reactance seen from the transformer input terminals does not exist anymore.

However, if the 600 V winding would be connected in Delta, simulation results would still be acceptable with three single-phase units because the delta connection would now allow circulation of zero-sequence current. To check the effect of using a Delta connection for the 600 V winding, change the Winding 2 connection of the Three-Phase Transformer Inductance Matrix Type to "Delta D1". In the transformer of circuit 1 you have to manually reconnect the secondary in Delta and, in its block menu, change the phase voltage of Winding 2 from 600/sqrt(3) to 600 V. Restart simulation and check that waveforms compare well for both circuits.

In summary, using three single-phase transformers with positive-sequence parameters to simulate a core-type transformer is acceptable only if one of the windings uses a Delta connection.

Current Transformer Saturation

This example shows the measurement distortion due to saturation of a current transformer (CT).

G. Sybille (Hydro-Quebec)
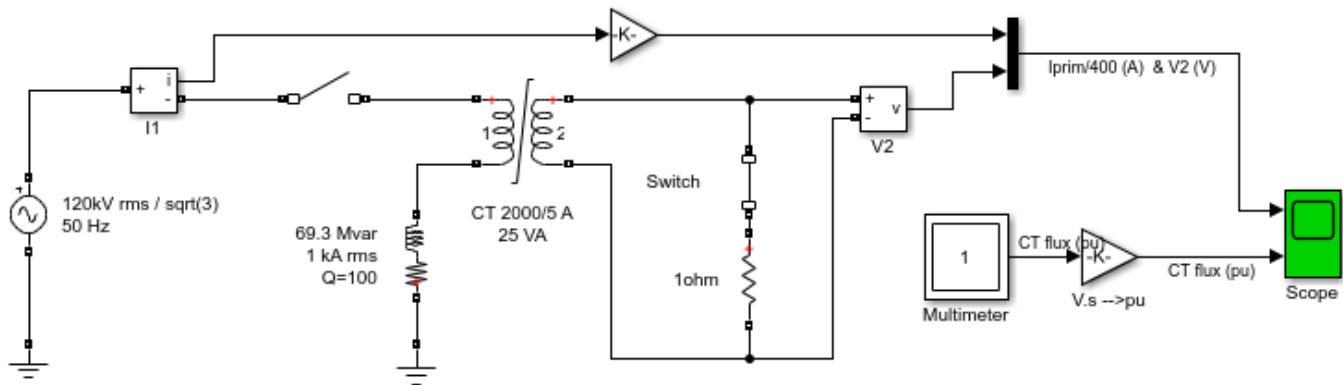
Description

A current transformer (CT) is used to measure current in a shunt inductor connected on a 120 kV network. The CT is rated 2000 A / 5 A, 5 VA. The primary winding which consists of a single turn passing through the CT toroidal core is connected in series with the shunt inductor rated 69.3 Mvar, 69.3 kV (120kV/sqrt(3)), 1 kA rms. The secondary winding consisting of 1*2000/5 = 400 turns is short circuited through a 1 ohm load resistance. A voltage sensor connected at the secondary reads a voltage which should be proportional to the primary current. In steady state, the current flowing in the secondary is 1000*5/2000 = 2.5 A (2.5 Vrms or 3.54 Vpeak read by the voltage measurement block V2).

Open the CT dialog box and observe how the CT parameters are specified. The CT is assumed to saturate at 10 pu and a simple 2 segment saturation characteristic is used.

The primary current reflected on the secondary and the voltage developed across the 1 ohm resistance are sent to trace 1 of the Scope block. The CT flux , measured by the Multimeter block is converted in pu and sent to trace 2. (1 pu flux = 0.0125 V *sqrt(2)/ (2*pi*50) = 5.63e-5 V.s)

The switch connected in series with the CT secondary is normally closed. This switch will be used later to illustrate overvoltages produced when CT secondary is left open.

Simulation

1. Normal operation

In this test , the breaker is closed at a peak of source voltage (t = 1.25 cycle). This switching produces no current asymmetry. Start the simulation and observe the CT primary current and

secondary voltage (first trace of Scope block). As expected the CT current and voltage are sinusoidal and the measurement error due to CT resistance and leakage reactances is not significant. The flux contains a DC component but it stays lower than the 10 pu saturation value.

2. CT saturation due to current asymmetry

Now, change the breaker closing time in order to close at a voltage zero crossing. Use t = 1/50 s. This switching instant will now produce full current asymmetry in the shunt reactor. Restart the simulation. Observe that for the first 3 cycles, the flux stays lower than the saturation knee point (10 pu). The CT voltage output V2 then follows the primary current. However, after 3 cycles, the flux asymmetry produced by the primary current causes CT saturation, thus producing large distortion of CT secondary voltage.

3. Overvoltage due to CT secondary opening

Reprogram the primary breaker closing time at t = 1.25/50 s (no flux asymmetry) and change the secondary switch opening time to t = 0.1 s. Restart the simulation and observe the large overvoltage produced when the CT secondary is opened. The flux has a square waveshape chopped at +10 and -10 pu. Large dphi/dt produced at flux inversion generates high voltage spikes (250 V).

Use of Surge Arresters in Transmission System

This example shows the use of surge arresters in a series and shunt compensated 735 kV AC transmission system

G. Sybille (Hydro-Quebec)
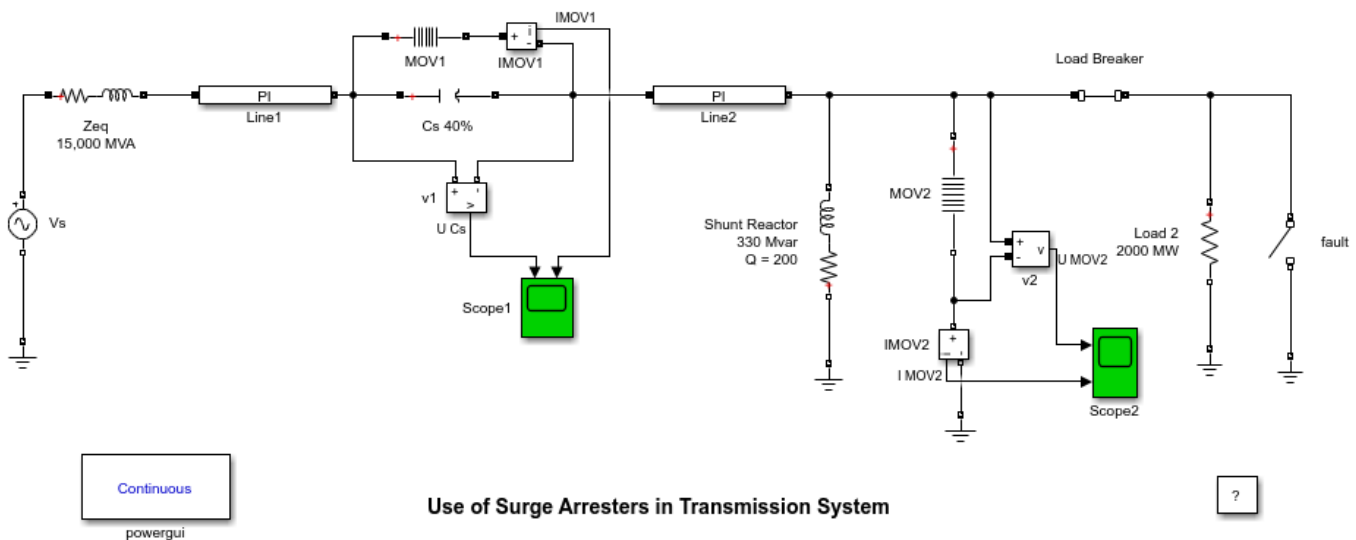
Description

A 735 kV equivalent transmission systems feeds a load through a 200 km transmission line. The line is series compensated at the middle point and shunt compensated at its receiving end. A fault is applied at the load terminals. The fault is cleared by load breaker opening. For simplification purpose, only one phase of the transmission system is modeled. All parameters correspond to positive-sequence.

The three-phase short circuit level of the transmission system is 15000 MVA. The line is 40% series compensated by a capacitor (26.2 ohms at 60 Hz) and shunt compensated by a 330 Mvar (110 Mvar/phase) inductor at the load end.

The series capacitor and shunt inductor are both protected by metal oxide varistors (MOV). The series capacitor varistor MOV1 consists of 30 columns protecting the capacitor at 2.5 times its rated voltage (rated voltage is obtained for a 2000 kA line rated current). The corresponding protection voltage (defined at 15 kA = 500 A per column) is 2.5*26.2 *2kA*sqrt(2) = 185 kV.

The shunt inductor is protected by a 2-column arrester (MOV2) at 1.8 p.u. of nominal phase-to-ground voltage (424.4 kVrms). The corresponding protection voltage (defined at 1 kA or 500A /column) is 1.8*424.4*sqrt(2) = 1080 kV.

Simulation

Start the simulation. Observe the MOV voltages and currents on Scope1 and Scope2.

At fault application, the capacitor voltage increases and MOV1 conducts. Peak current reaches 8.4 kA on the 2nd pulse 2 cycles after fault application, the MOV current is symmetric (4.5 kA current pulses at every half cycle).

At fault clearing, the overvoltage produced at bus B2 is limited by the shunt MOV2 . The fast transient produces a 850 A current pulse in the MOV.

Single Phase Line - Time and Frequency Domain Testing

This example shows time domain and frequency domain performance of distributed parameter line model and PI section line model.

G. Sybille (Hydro-Quebec)

Description

A 200 km line is connected on a 1 kV, 60 Hz infinite source. The line is deenergized and then reenergized after 2 cycles. The simulation is performed simultaneously with two different line models:

- Distributed parameter line

- PI section line consisting of two 100 km sections.

Currents at the sending end and voltages at the receiving end are compared for the two line models. Impedance Measurement blocks are connected at the open end of both lines in order to compare their frequency responses.

Simulation

1. Steady-state

Open the Powergui block and select **Steady-State Voltages and Currents** to display the voltage and current phasors. Observe that the values obtained with the two models are the same.

2. Time domain comparison

Open the two scopes and start the simulation. Observe the difference in current and voltage waveforms at breaker opening and reclosing. Note the sharp edges of the distributed parameter model. These voltage and current steps which are due to travelling wave reflections at line ends are filtered by the PI model.

3. Frequency domain comparison

Open the Powergui block and select **Impedance vs Frequency Measurement**. A new window appears, listing the two Impedance Measurement blocks Z_Dist and Z_PI connected to your circuit. Note also that parameters are set to compute impedance in the 0:2000 Hz frequency range by steps of 2 Hz. Using the Ctrl key, select both Z_Dist and Z_PI in the upper right window. Then, click on the Display button. The two impedances are computed and displayed on the same graph.

Note that the distributed parameter line shows a succession of poles and zeros equally spaced, every 486 Hz. The first pole occurs a 243 Hz, corresponding to frequency f = 1 / (4*T), where :

```
T = travelling time = length*sqrt(L*C) = 200*sqrt(2.137e-3*12.37e-9) = 1.028 ms
```

The PI section line only shows two poles because it consists of two PI sections. Impedance comparison shows that a two-PI line gives a good approximation of the distributed line for the 0-350 Hz frequency range.

Three-Phase Line (DPL and PI Section) - Single-Phase Energization

This example shows transients obtained with a distributed parameter line model and a PI section line model.

G. Sybille (Hydro-Quebec)

Description

A 100 km, three-phase line is energized on a 735 kV, 30,000 MVA equivalent network. The line is first energized on phase A at t = 1/4 cycle (maximum of source voltage) and then deenergized at t = 4 cycles. Phase B and phase C are left open. Switchings are performed simultaneously on two different line models:

- Distributed parameters line

- Three-phase PI section line consisting of two 50 km sections

Voltages obtained on phases A and B at the line receiving end with the two models are compared.

Simulation

Open the two scopes and start the simulation. Compare the voltages obtained on phase A and phase B (induced voltage) with the two models. Zoom on voltages right after energizing and notice the sharp edges obtained with the distributed parameter line (yellow trace). These voltage steps which are due to travelling wave reflections at both ends of the line are filtered by the PI model. Notice also that phases A and B stay charged at peak voltage when the breakers are opened.

Computation of R L and C Cable Parameters

This example shows the use of the power_cableparam function to calculate R,L, and C cable parameters.

Joseph Duron Schweizerische Bundesbahnen SBB Infrastruktur Energie Systemdesign

Use of power_cableparam function to compute RLC Cable Parameters

Description
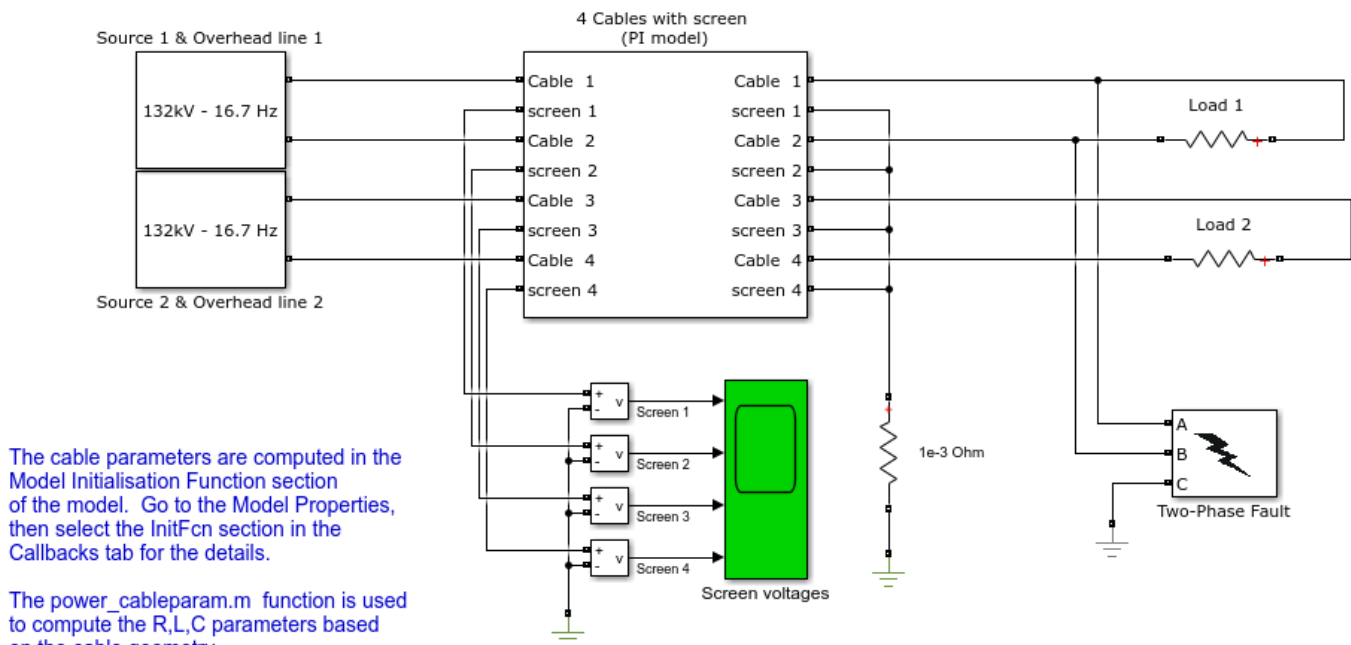
The cable consists of two 132kV 2-phase cables with screen cables. The RLC parameters are computed in the Model Initialisation Function section of the model. Go to the Model Properties, then select the InitFcn section in the Callbacks tab for the details.

Dynamic Load and Programmable Voltage Source

This example shows the use of the 3-Phase Dynamic Load and 3-Phase Programmable Voltage Source blocks

Description

A dynamic load is connected on a 500 kV, 60 Hz power network . The network is simulated by its Thevenin equivalent (voltage source behind a R-L impedance corresponding to a 3-phase short circuit level of 2000 MVA). The source internal voltage is modulated in order to simulate voltage variation during a power swing. As the dynamic load is a nonlinear model simulated by current sources, it cannot be connected to an inductive network (R-L in series). Therefore, a small resistive load (1 MW) has been added in parallel with the dynamic load.

The dynamic load power is a function of its terminal positive-sequence voltage V. Open the Dynamic Load menu and notice that both exponents np and nq are set to 1 and that the specified minimum voltage Vmin is 0.7 pu . It means that the load active power P and reactive power Q are defined by the following equations:

```
If V > Vmin
        P = Po*(V/Vo);    Q = Qo*(V/Vo)
If V < Vmin
        P = Po*(V/Vo)^2;   Q = Qo*(V/Vo)^2
```

In other words, as long as voltage is higher than 0.7 pu, the load current is constant. When voltage falls below 0.7 pu the load behaves as a constant impedance.

In order to simulate the variation of P and Q as function of voltage, the source internal voltage is controlled by the 3-Phase Programmable Voltage Source block. Open the source menu and notice that the specified type of amplitude variation is a sinusoidal modulation (Amplitude of the modulation = 0.5 pu, Frequency of the modulation = 1 Hz). Therefore, the source positive-sequence voltage varies

between 0.5 pu and 1.5 pu. The initial source voltage is 1 pu. Modulation starts at t = 0.2 s and stops after 1 cycle at t = 1.2 s.

Simulation

1. Initializing the dynamic load

In order to start the simulation in steady-state you have to specify the correct initial voltage Vo (magnitude and phase) corresponding to desired Po and Qo values.

The 'Machine Initialization' utility of Powergui block can be used to find this voltage and initialize the dynamic load. Open the Powergui and select 'Machine Initialization'. Specify the desired active power and reactive powers for the Dynamic Load (50 MW, 25 Mvar): Active Power = 50e6 Reactive Power = 25e6.

Then press the 'Compute and Apply' button. The phasors of AB and BC machine voltages as well as the currents flowing into of phases A and B are updated. The phase-neutral load voltage Uan is also displayed: (0.9844 pu - 1.41 degrees). If you now open the Dynamic Load dialog box you will notice that the values of Po, Qo and Vo have been updated.

2. Simulating the voltage swing

Start the simulation and observe load voltage, P&Q powers and current on Scope1. Observe that simulation starts in steady state. At t = 0.2 s, when voltage modulation is initiated, P and Q start to increase (trace 2), but, as np and nq are set to 1, the load current (trace 3) stays constant. When voltage falls below 0.7 pu the load behaves as a constant impedance. Therefore load current follows this voltage variation. Observe on Scope2 variations of instantaneous voltages and currents. Also, notice that computed Pand Q displayed on Scope3 are the same as P and Q internal signals returned by the Dynamic Load measurement output.

Single Phase Dynamic Load Block

This example shows a single-phase dynamic load block built with Simulink® blocks.

Graham Dudgeon, Senior Consultant, The MathWorks, Inc.
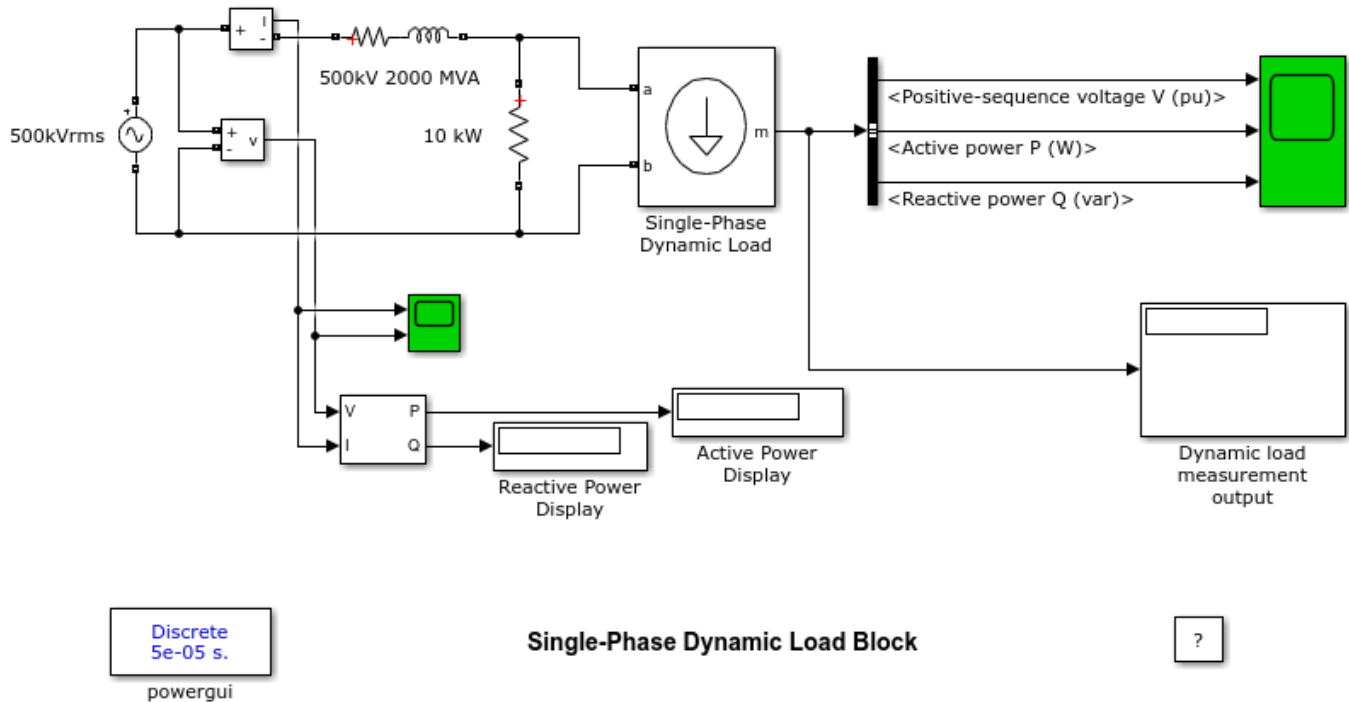
Description

Look under the mask of the custom-built Single-Phase Dynamic Load block and observe the three systems. The 'Measurement System' consist of the voltage measurement blocks which convert the Specialized Power Systems signal into Simulink signal. The 'Simulink System Model', contains the core functionality of the Single-Phase Dynamic Load block. Lastly, the 'Current Injection System' contains the controlled current source blocks which convert Simulink signals into Simscape™ Electrical™ Specialized Power Systems signals.

In general, measurement blocks (voltage/current/impedance) serve as an interface between Specialized Power Systems and Simulink, and controlled source blocks (voltage/current) between Simulink and Specialized Power Systems. This is the recommended template for interfacing custom-built blocks in Simulink, with Simscape Electrical Specialized Power Systems.

Limitations

1. The Single-Phase Dynamic Load block is modeled as a current source, it cannot be connected to an inductive network, or to another current source. Therefore, a small resistive load (10 Kw) has been added in parallel with the dynamic load.

2. The Machine Initialisation tool and the Load Flow tool of Powergui block cannot be used to initialize the Single-Phase Dynamic Load model.

3. The Single-Phase Dynamic Load block cannot be used with the Phasor simulation type mode of the Powergui.

Saturable Transformer with Hysteresis

This example shows the simulation of hysteresis in a saturable transformer.

S. Casoria (Hydro-Quebec) and P. Brunelle (TransEnergie)

Description

One phase of a three-phase transformer is connected on a 500 kV, 5000 MVA network. The transformer is rated 500 kV/230 kV, 450 MVA (150 MVA per phase). The flux-current saturation characteristic of the transformer is modeled with the hysteresis or with a simple piecewise nonlinear characteristic.

A Three-Phase Programmable Voltage Source is used to vary the internal voltage of the equivalent 500 kV network. During the first 3 cycles source voltage is programmed at 0.8 pu. Then, at t = 3 cycles (0.05 s) voltage is increased by 37.5% (up to 1.10 pu). In order to illustrate remnant flux and inrush current at transformer energization, the circuit breaker which is initially closed is first opened at t = 6 cycles (0.1 s), then it is reclosed at t = 9 cycles (0.15 s).

The Initial flux phi0 in the transformer is set at zero and source phase angle is adjusted at 90 degrees so that flux remains symmetrical around zero when simulation is started.

A Multimeter block and a Scope block are used to monitor waveforms of flux, magnetization current (not including the eddy currents which are modeled by the Rm resistance), excitation current (including eddy current modeled by Rm), voltages and current flowing into the primary winding. A X-Y Graph block is used to monitor the transformer operating point moving on the flux-current characteristic.

Simulation

1) Simulation of saturation with hysteresis

Open the transformer menu and select the 'Simulate hysteresis' check box. Using the 'Hysteresis design' tool of the Powergui, load the hysteresis characteristic (load file 'hysteresis.mat'). Notice that flux and excitation current are displayed in pu. Using the 'Parameter units' popup menu, you can convert the pu units to SI units. The saturation characteristics consists of two regions: 1) The main hysteresis loop: In this region there are 2 different flux values for a single current value. 2) The saturated region defined by a simple line segment starting from the maximum point (Is,Fs) of the main loop. The hysteresis loop is defined by the following 3 points marked by red crosses on the main loop: [I=0; Remnant flux(Fr = 0.85 pu)], [Coercive Current(Ic = 0.004 pu); F = 0], [Saturation current(Is = 0.015 pu); Saturation flux(Fs = 1.2 pu)] plus the slope dF/dI at coercive current(F = 0). Using the 'Zoom around hysteresis' checkbox and 'Display' button you can view the whole characteristic or zoom on the hysteresis.

Start the simulation and observe the following phenomena on the two scope blocks:

From 0 to 0.05 sec: Voltage and flux peak values are at 0.8 pu. Notice typical square wave of magnetization current. As no remnant flux was specified, magnetization current and flux are symmetrical. Flux travels on inner loops (inside the main loop).

From 0.05 to 0.1 sec: Voltage is 1.1 pu. Flux now reaches approximately +1.1pu. A slight flux asymmetry is produced at voltage change. and the flux which varies between +1.14 pu and -1.05 pu now travels on the main loop. Current pulses appear on the magnetization current (yellow trace, Imag), indicating beginning of saturation.

From 0.1 to 0.15 sec: At first zero crossing after the breaker opening order, the current is interrupted, and a flux of 0.84 pu stays trapped in the transformer core.

From 0.15 to 0.2 sec: The breaker is reclosed at t = 9 cycles, at a zero crossing of source voltage, producing an additional flux offset of approximately 1 pu. The peak flux now reaches 1.85 pu, driving the transformer into the saturated region. Peak excitation current now reaches 0.81 pu.

2) Simulation of saturation with a piecewise nonlinear characteristic

Open the transformer menu and deselect 'Simulate hysteresis'. The saturation will now be simulated by a piecewise nonlinear single-valued characteristic defined by 7 points. Current/Flux pairs (in pu) are: [0 0; 0.0 0.85; 0.015 1.2; 0.03 1.35; 0.06 1.5; 0.09 1.56; 0.12 1.572]. The saturated region is the

same, but the hysteresis loop is not simulated. Note that this single valued characteristics still allows specification of a remnant flux (keep phi0=0 flux as with hysteresis saturation model). Start simulation and compare waveforms.
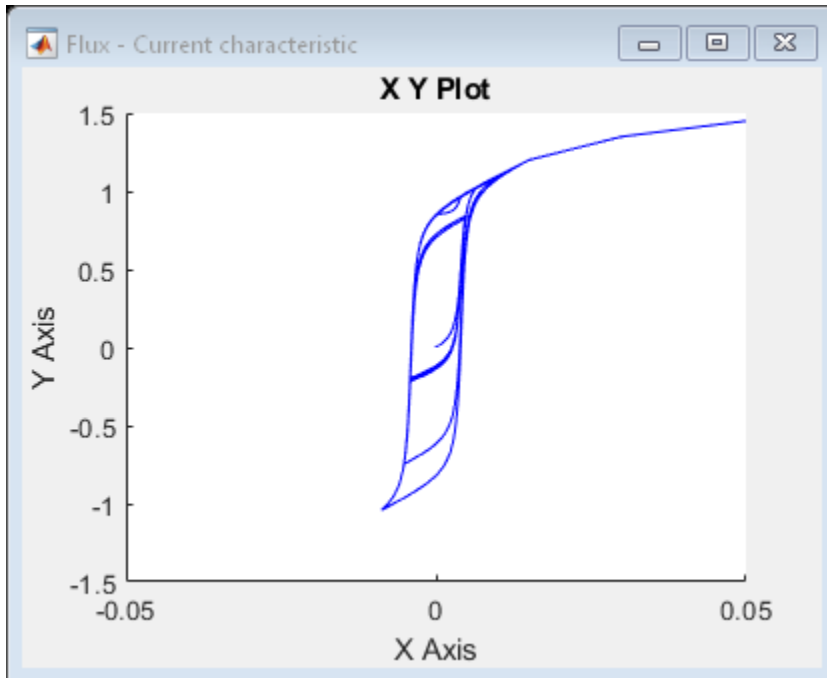
Cassie and Mayr Arc Models for a Circuit Breaker

This example shows the use of the Cassie and Mayr arc model for simulating high voltage circuit breaker interruption

P.H. Schavemaker, Power Systems Laboratory, Delft University of Technology, the Netherlands.
P.H.Schavemaker@its.tudelft.nl

Description

Two identical circuits are displayed: one with a Cassie and one with a Mayr arc model. The circuit is a simple representation of a circuit breaker interrupting a short-line fault. At the source side of the circuit breaker a circuit is present for reproducing a (2-parameter IEC) transient recovery voltage, while the RLC circuit at the line side represents a short transmission line that is short circuited.

A stiff solver is recommended for solving this problems because of the nonlinear behavior of the arc.

Simulation
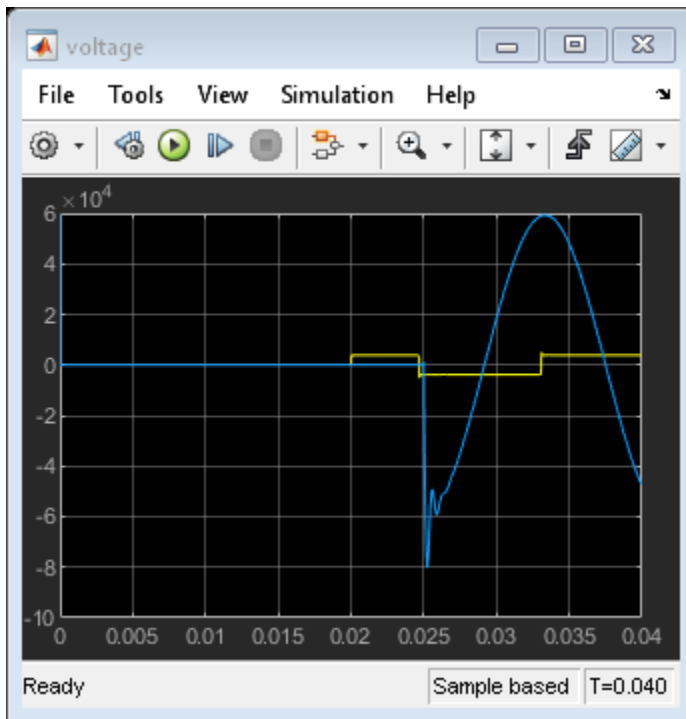
Figure 2: Voltage comparisons for the Cassie and Mayr arc models.
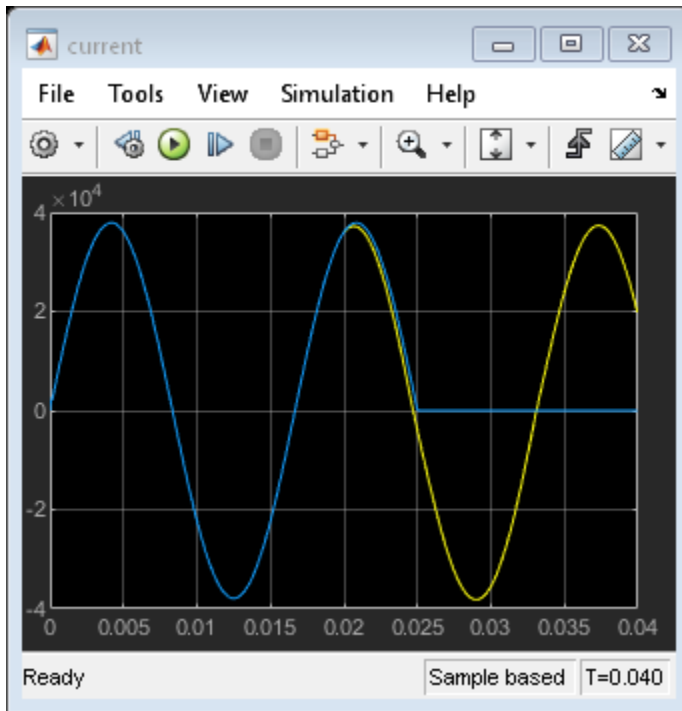
Figure 3: Current comparisons for the Cassie and Mayr arc models.

The following observations become clear from the simulation: The arc behaves as a non-linear resistance; therefore both arc current and voltage pass the zero value at the same time. As the power input into the arc channel is zero at that time, the current zero crossing is the place where the interruption takes place.

The Cassie arc model has a constant arc voltage in the high current interval, while the Mayr arc model shows an increasing arc voltage (the extinction peak) close to the current zero crossing.

The Cassie arc model fails to interrupt the short circuit current, while the Mayr arc model interrupts and shows a small post-arc current of 0.6A immediately after the current zero crossing. This current is caused by the transient recovery voltage building up over the non-infinite arc resistance.

In case of the Mayr arc model (the successful interruption), the transient recovery voltage (TRV) builds up over the breaker. This voltage consists of the 2-parameter TRV and the high frequency voltage oscillation of the line side, which you can observe by zooming in.

Additional Notes

This model cannot be run in accelerator mode due to modeling constraints.

Variable Inductance Modeling

This example shows a Simulink® model of a variable inductance that can be used in Specialized Power Systems.

Description

Equation of an Inductor: v = L di/dt

This model implements: i = 1/L * v/s

The current is defined to flow from the positive terminal to the negative terminal of the Inductance.

Interfacing Simulink Models with Simscape Electrical Specialized Power Systems

This example shows the interfaces between Simulink® and Simscape™ Electrical™ Specialized Power Systems to design a custom synchronous machine model. It also shows an example of a customized Synchronous Machine block

Introduction

In this example, we use the Synchronous machine blocks as an example to show the interfaces between Simulink and Simscape Electrical Specialized Power Systems. There are two similar circuits shown in parallel. The top circuit model shows the starting of a a synchronous machine using blocks from the Specialized Power Systems library. The circuit below it is identical in all respects, but for a custom-built model of the Synchronous machine.

Figure 1: Starting of the synchronous motor. The top model is modeled using Specialized Power Systems library blocks while the middle model is a custom-made model built using Simulink. The bottom model is a customized version of the Synchronous Machine block of powerlib, using the power_customize function of powergui.

Description

Click to look under the mask of the official Synchronous machine block, and find the core functionality of the block modeled in the 'Source' and the 'SM_mechanics' subsystems. It is important to note that these subsystems are built up from either Simulink elements or basic Specialized Power Systems blocks. Users may need to change this representation to add their own functionality to the block, and meet their custom needs.

Now look under the mask of the custom-built Synchronous machine block (in the middle circuit), and observe the three subsystems. The 'Measurement System' in green, contains the voltage measurement blocks which convert the Specialized Power Systems' signals into Simulink signals. The 'Simulink System Model', contains the core functionality of the Synchronous machine block, comprising the 'Source' and the 'SM_mechanics' subsystems, borrowed from the standard shipped block. It is here that users may customize and manipulate the extracted Simulink signals to any degree. Lastly, the 'Current Injection System', contains the controlled current source blocks which convert Simulink signals into Specialized Power Systems' signals.

In general, measurement blocks (voltage/current/impedance) serve as an interface between Simscape Electrical Specialized Power Systems and Simulink, and controlled source blocks (voltage/current) between Simulink and Simscape Electrical Specialized Power Systems. This is the recommended template for interfacing custom-built blocks in Simulink, with Simscape Electrical Specialized Power Systems.

Simulation

Run the example to see that there is no differences between the two circuits.
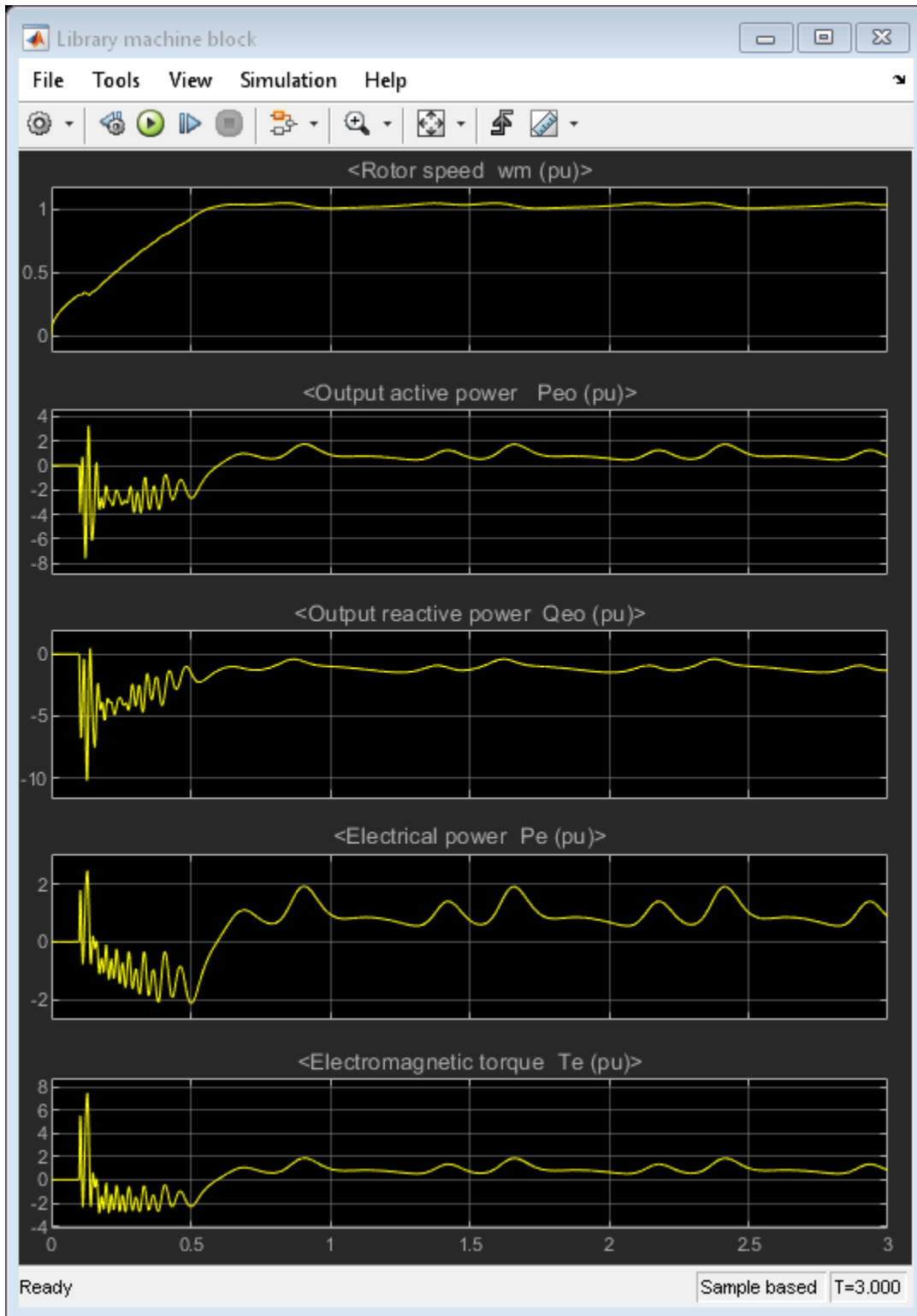
Figure 2: Results obtained from the Specialized Power Systems model.
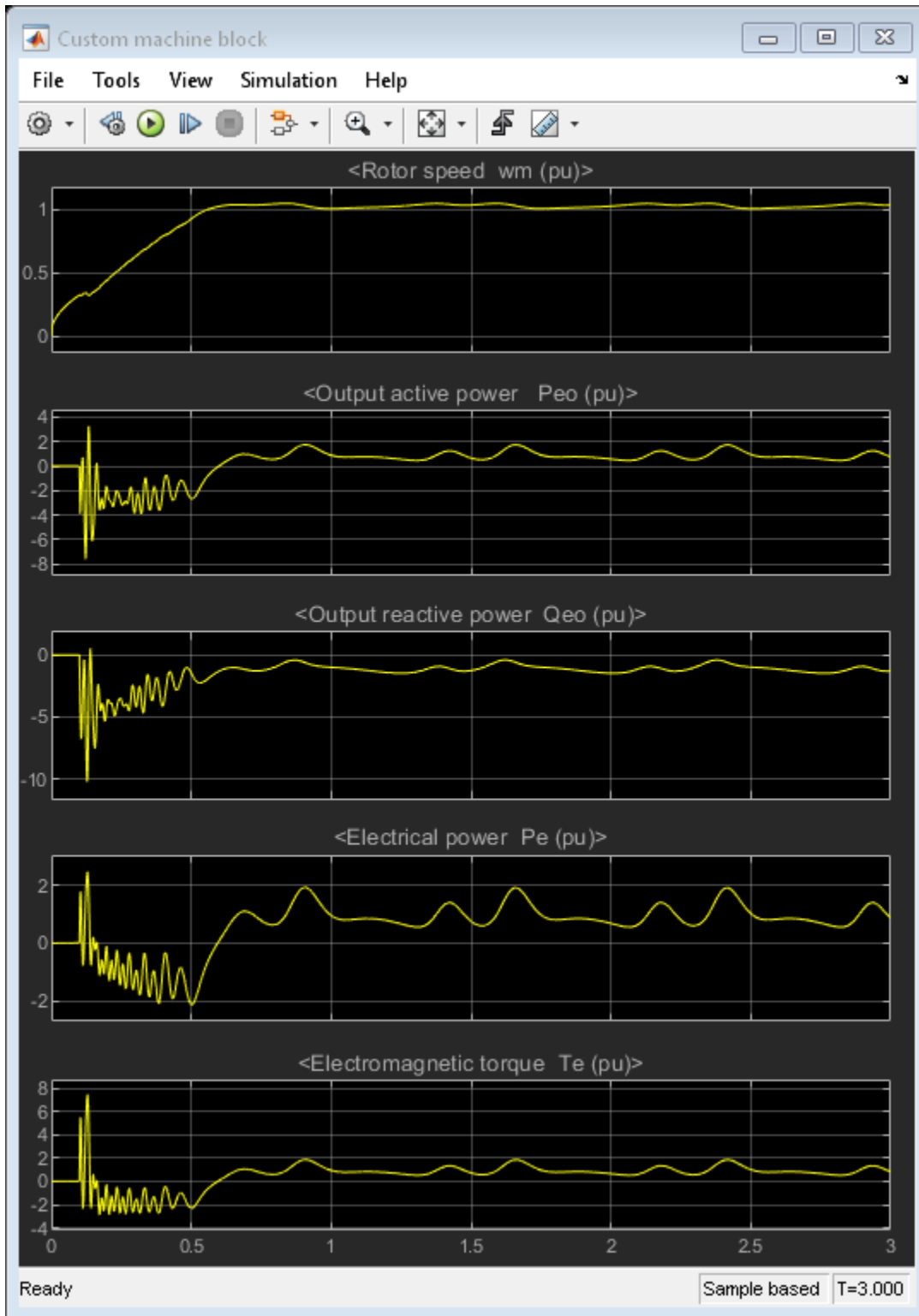
Figure 3: Results obtained from the custom-made Simulink model.

Conclusion

This model presents a model of the Synchronous machine block, but users may just as well employ and edit any standard block in Specialized Power Systems. Extending this further, using the recommended template, it is even possible to create any custom block not provided with Specialized Power Systems, in Simulink, and then interface it seamlessly with a Simscape Electrical Specialized Power Systems circuit.

Interfacing Simscape Models with Simscape Electrical Specialized Power Systems

This example shows the interface between Simscape™ and Simscape Electrical™ Specialized Power Systems to design a breaker model.

Introduction

There are two similar circuits shown in parallel. The top circuit model illustrates tripping of a circuit breaker protecting a 240 V residential distribution circuit. The Circuit Breaker model is built with Simscape Electrical Specialized Power Systems blocks. The circuit below is identical in all respects, but for a Simscape model of the Circuit Breaker.

Figure 1: In the top model the Circuit Breaker is modeled using two Specialized Power Systems blocks (Ideal Switch and Current Measurement) and associated logic implemented with Simulink® blocks while the lower model uses two Simscape blocks (Switch and Current Sensor).

Description

The top circuit illustrates tripping of a circuit breaker in a 240 V residential distribution circuit. Initially, the 240 V circuit feeds a 10 kW load. Then, at t=0.05 s, a second load (10 kW, 5 kvar) is switched on, causing current to exceed the trip setting (100 A). Current is interrupted at its first zero-crossing after the trip current has been exceeded. Finally, Load 1 is reconnected by resetting the circuit breaker at t= 0.2 s.

The bottom circuit is using the Simscape Interface Element block of powerlib to connect a Simscape circuit breaker model to the Specialized Power Systems circuit. The Simscape breaker block is a modified version of the breaker model found in ssc_circuitbreaker model.

Simulation

On running the model we can observe that the two different implementations of the circuit breaker device produce the same circuit breaker current and voltage.
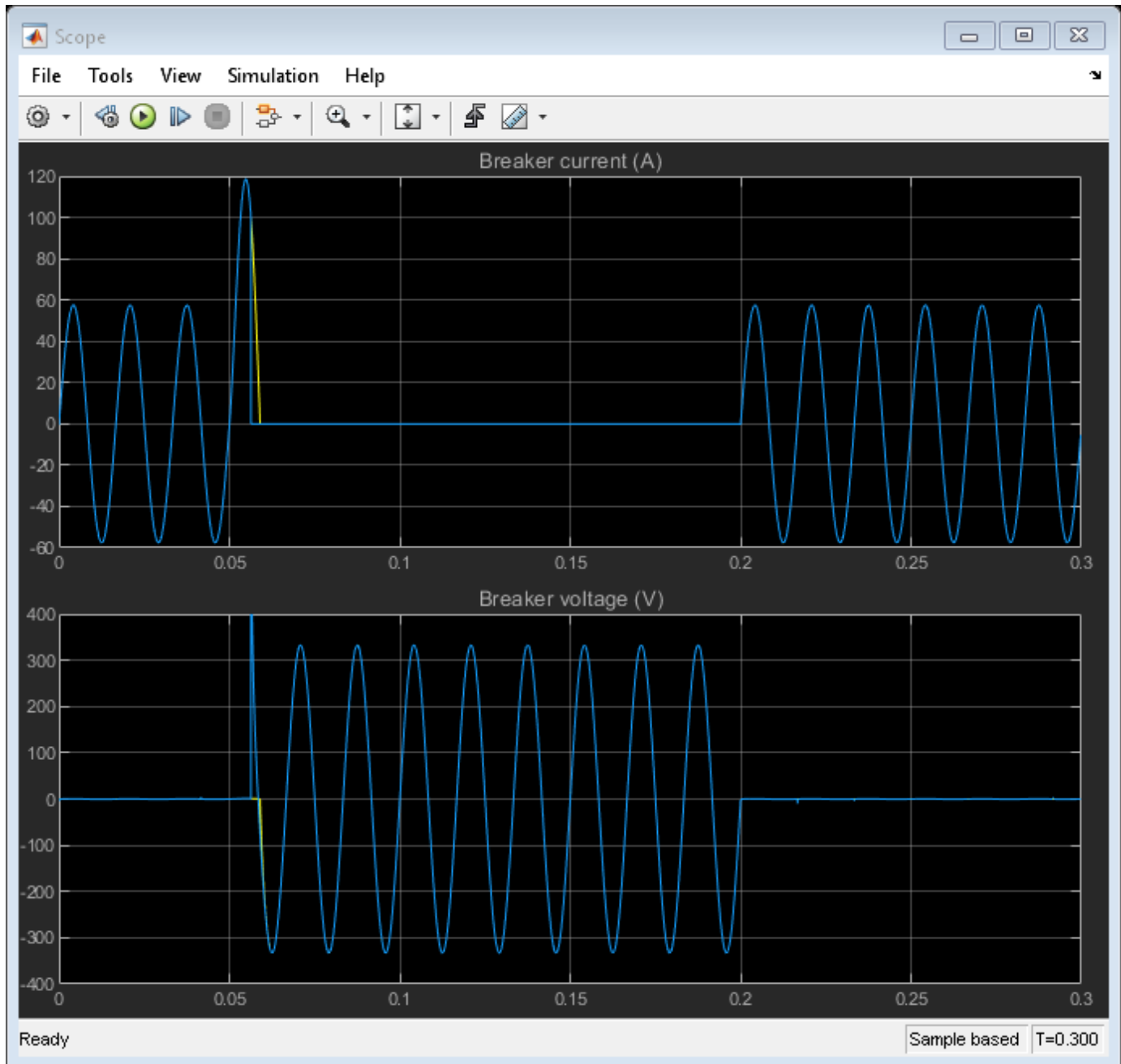
Figure 2: Results obtained from the Specialized Power Systems Breaker model are superimposed with results from Simscape Breaker model. Note that the breaker current is interrupted at zero-crossing after the 100 A trip current setting has been exceeded.

Conclusion

Using the Simscape Interface blocks of powerlib, it is possible to create any custom block not provided with Specialized Power Systems, in Simscape, and then interface it seamlessly with a Simscape Electrical Specialized Power Systems circuit.

Boost Converter

This example shows the operation of a boost converter.

Boost Converter

This example shows the operation of a boost converter.

Learn more about this example.
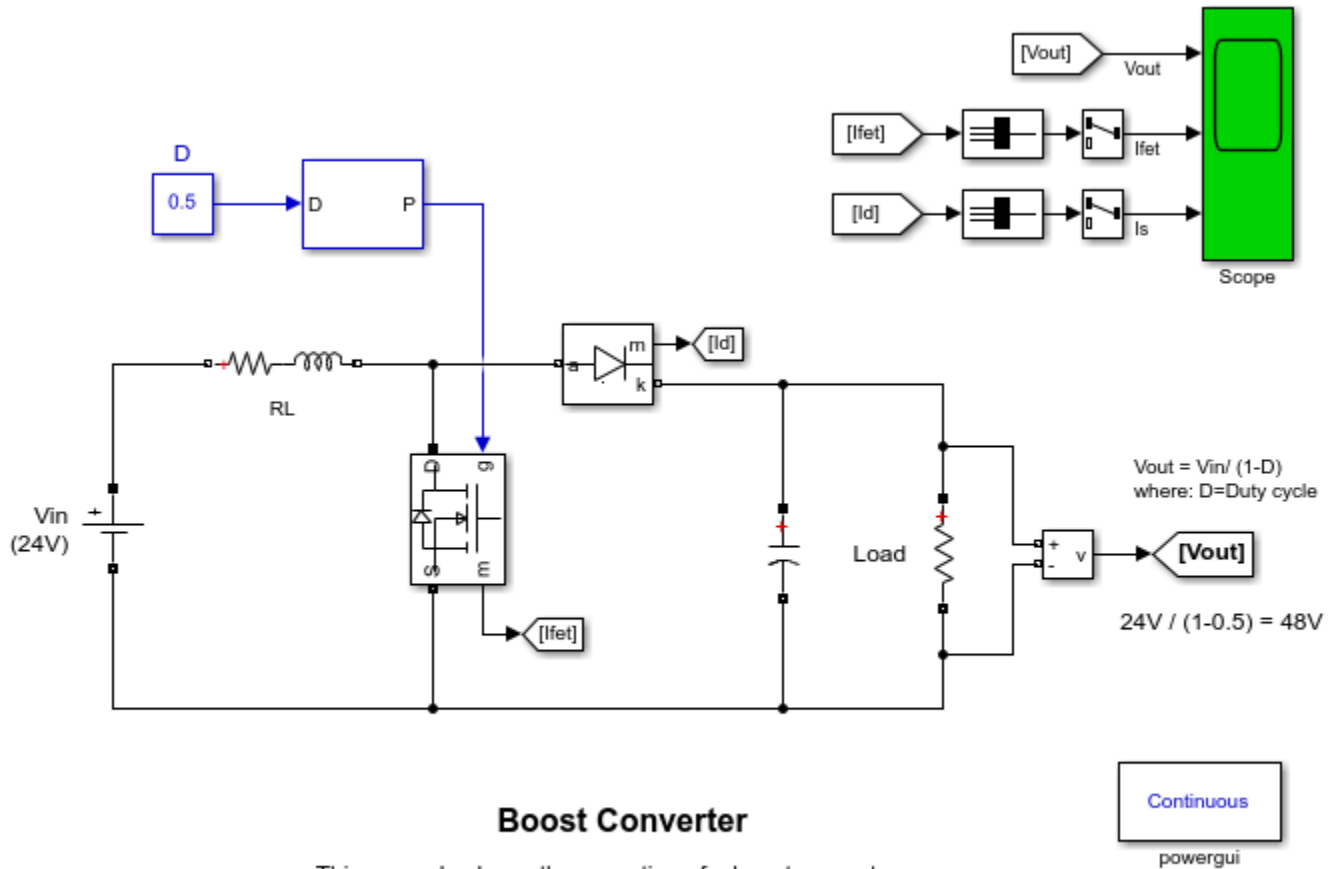
Description

A boost converter is a DC/DC power converter which steps up voltage from its input (source) to its output (load). In continuous conduction mode (current through the inductor never falls to zero), the theoretical transfer function of the boost converter is:

$$Vout/Vin = 1/(1 - D)$$

where D is the duty cycle.

In this example, the converter is feeding an RC load from a 24 V source and the PWM frequency is set to 20 kHz.

Simulation

Run the simulation and observe waveforms on Scope. Verify that the mean value of the load voltage (Vout) is very close to the theoretical value of: Vout = 24 / (1 - 0.5) = 48 V.

Buck Converter

This example shows the operation of a buck converter.
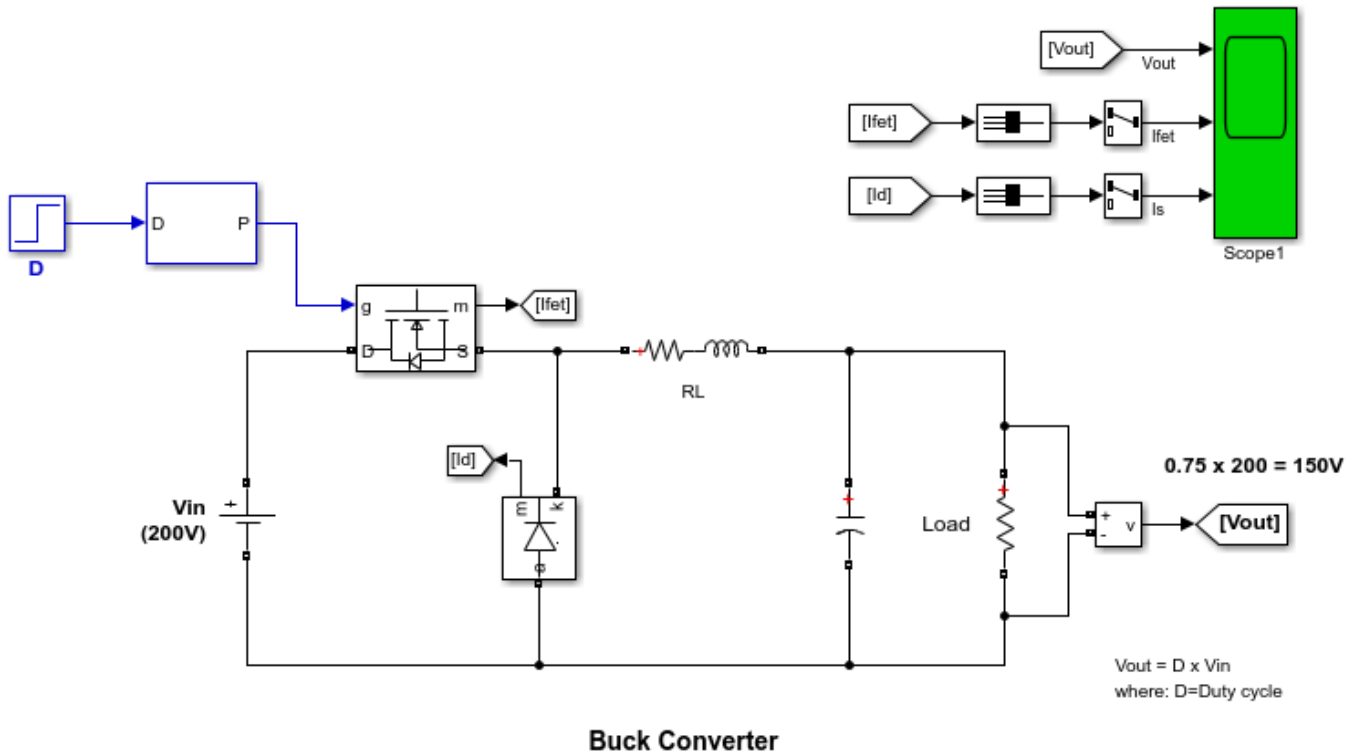
Buck Converter

This example shows the operation of a buck converter.

Learn more about this example.

Description

A buck converter is a DC/DC power converter which steps down voltage from its input (source) to its output (load). In continuous conduction mode (current through the inductor never falls to zero), the theoretical transfer function of the buck converter is:

$$Vout/Vin = D$$

where D is the duty cycle.

In this example, the converter is feeding an RC load from a 200 V source and the PWM frequency is set to 10 kHz.

Simulation

Run the simulation and observe waveforms on Scope1. At 3.5 ms, the duty cycle is increased from 0.5 to 0.75. Verify that the mean value of the load voltage (Vout) at the end of the simulation is very close to the theoretical value of: 0.75 * 200 = 150 V.

Voltage-Controlled Buck Converter

This example shows the operation of a voltage-controlled buck converter.

Voltage-Controlled Buck Converter

The DC motor is fed by the DC source through a Buck Converter block implementing an IGBT/diode pair. The motor drives a mechanical load characterized by inertia J, friction coefficient B, and load torque TL.

Learn more about this example.

Description

The DC motor is fed by the DC source through a Buck Converter block implementing an IGBT/diode pair. The motor drives a mechanical load characterized by inertia J, friction coefficient B, and load torque TL.

The hysteresis current controller compares the sensed current with the reference and generates the trigger signal for the IGBT thyristor to force the motor current to follow the reference. The speed control loop uses a proportional-integral controller which produces the reference for the current loop.

Buck Boost Converter

This example shows the operation of buck boost converters using the inverting and non-inverting topologies.

Description

The buck boost converter is a DC/DC converter with the output voltage magnitude that is either greater than or less than the input voltage magnitude. It is comparable to a flyback converter where an inductor is used in place of a transformer. The theoretical transfer function of the buck boost converter is:

$$abs(Vout) = D/(1-D) * Vin$$

where D is the duty cycle.

The inverting buck-boost topology produces an output voltage that is of the opposite polarity as the input voltage. The output voltage is determined by the duty cycle of the MOSFET transistor.

The non-inverting topology, also named the 4-switch topology, produces an output voltage that is of the same polarity as the input voltage. In the buck mode, the output voltage is determined by the operation of the MOSFET and diode D1. In the boost mode, the output voltage is determined by the operation of the IGBT and diode D2.

Cuk Converter

This example shows the operation of a non-isolated Cuk converter.
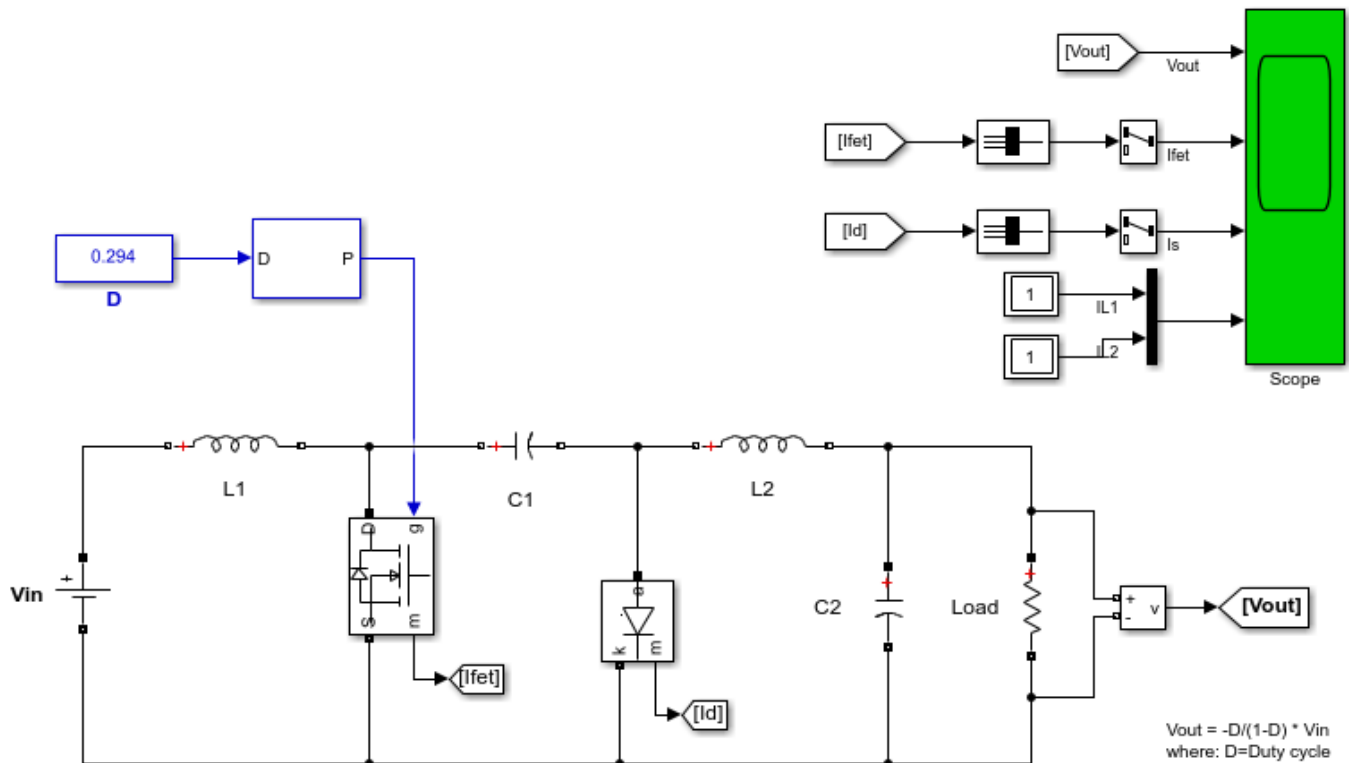
Cuk Converter

This example shows the operation of a non-isolated Cuk converter.

Learn more about this example.

Description

The non-isolated Cuk converter is a DC/DC power converter that, like a buck-boost converter, can produce an output voltage (Vout) magnitude that is either greater or less than the input voltage (Vin) magnitude. However, it is an inverter converter, so the output voltage is of opposite polarity with respect to the input voltage. In the Cuk converter topology, the capacitor C1 acts as the primary means of storing and transferring energy from the input to the output. The advantage of this converter is that both the input current (IL1) and the current feeding the output stage (IL2) are reasonably ripple-free (unlike the buck-boost converter where both these currents are highly discontinuous). Assuming a constant voltage across C1, the theoretical transfer function of the Cuk converter is:

$$Vout/Vin = -D/(1-D)$$

where D is the duty cycle.

In this example, the converter is feeding a 50-W load from a 12 V source and the PWM frequency is set to 50 kHz.

Simulation

Run the simulation and observe waveforms on Scope. Verify that the mean value of the load voltage (Vout) is very close to the theoretical value of: Vout = -0.294/ (1 - 0.294) * 12 = -5 V.

Reference

Ned Mohan, Tore M. Undeland, Power Electronics: Converters, Applications, and Design, John Wiley & Sons.

Forward Converter

This example shows the operation of a forward converter.

Forward Converter

This example shows the operation of a forward converter.

Learn more about this example.

Description

The forward converter is a DC/DC converter that uses a transformer in series with the switching device (MOSFET in this example) to isolate the source from the load and to increase or decrease the input voltage depending on the transformer turns ratio. The theoretical transfer function of the forward converter is:

$$Vout = n * D * Vin$$

where D is the duty cycle, and n is the turns ratio of the transformer (N_2/N_1)

Flyback Converter with Transformer Leakage

This example shows the operation of a flyback converter.
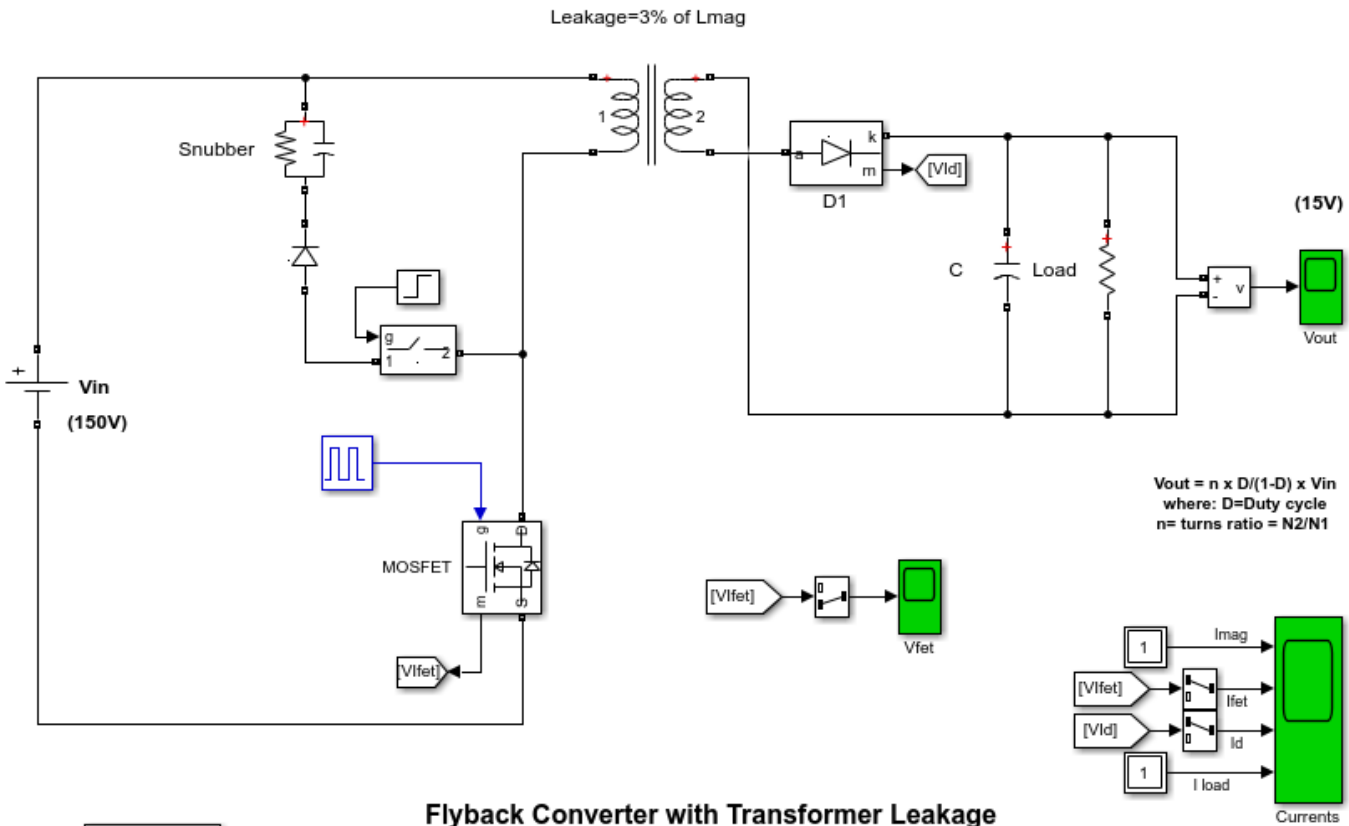
Flyback Converter with Transformer Leakage

The flyback converter is a buck-boost converter with isolation between its input and output

Learn more about this example.

Description

The flyback converter is a buck-boost converter with isolation between its input and output. The inductor used in the buck-boost configuration is replaced by a transformer for isolation and storing energy before sending to the output via the output capacitor.

The theoretical transfer function of the flyback converter is:

$$Vout = n * D/(1 - D) * Vin$$

where D is the duty cycle, and n, the turns ratio, is equal to $N2/N1$.
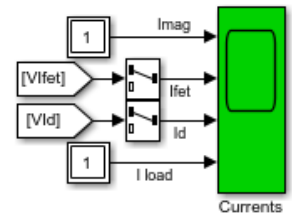
Resonant LLC Converter
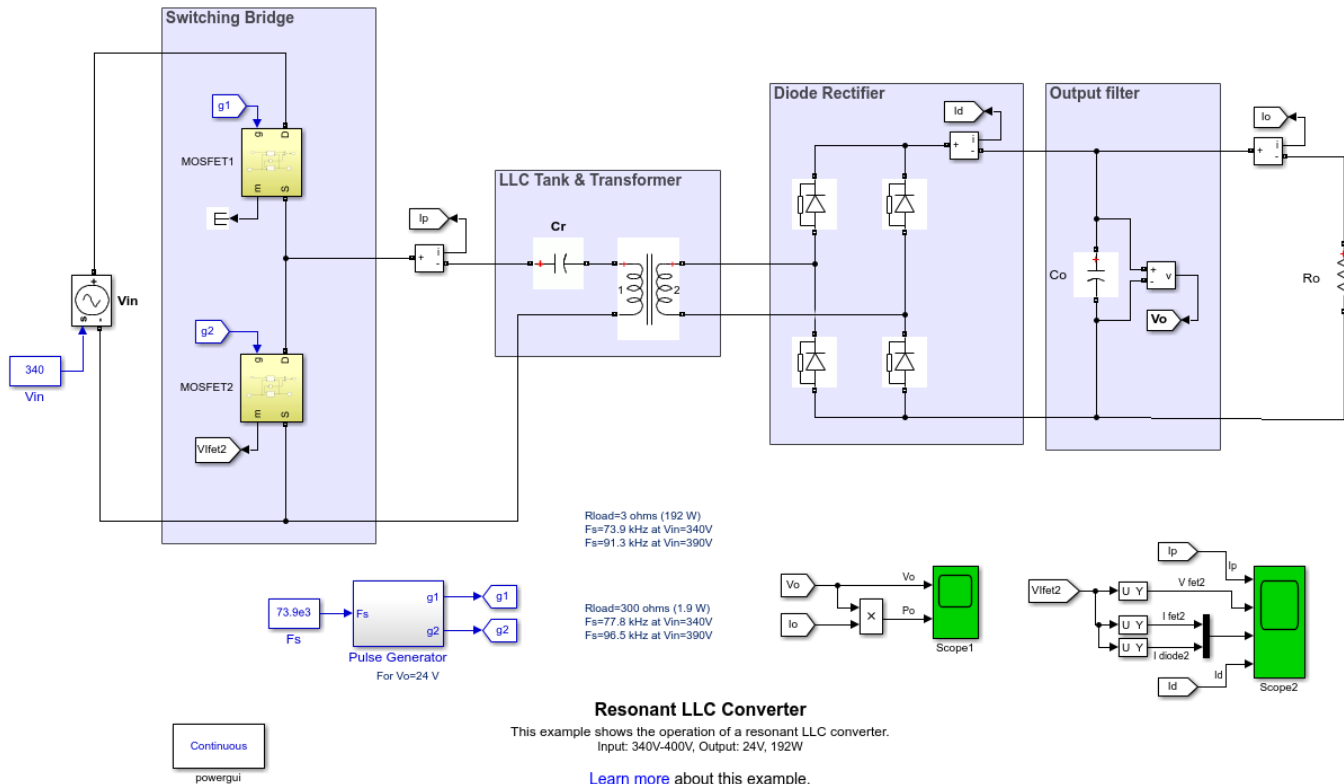
This example shows the operation of a resonant LLC converter.

Rload=3 ohms (192 W)
Fs=73.9 kHz at Vin=340V
Fs=91.3 kHz at Vin=390V

Rload=300 ohms (1.9 W)
Fs=77.8 kHz at Vin=340V
Fs=96.5 kHz at Vin=390V

Resonant LLC Converter
This example shows the operation of a resonant LLC converter.
Input: 340V-400V, Output: 24V, 192W

Learn more about this example.

Description

The LLC converter is a DC/DC converter based on a resonant circuit which allows soft-switching operation. The LLC resonant circuit reduces switching loss through zero-voltage switching (ZVS). Unlike the SLR converter, the LLC converter can keep the output voltage regulated even under light load condition.

The converter consists of a switching bridge (half-bridge MOSFET), an LLC resonant circuit, a full-bridge rectifier and an output filter (Co). The resonant circuit consists of the primary leakage inductance of a transformer (Llkp), its secondary leakage inductance (Llks), its magnetization inductance (Lm) and a capacitor Cr. The resonant frequency of this LLC circuit is given by:

$$w_0 = 1/sqrt(L_r * C_r) = 2 * pi * f_0$$

where $Lr = Llkp + Lm/(N^2 x Llks)$ and N is the transformer winding ratio.

Simulation

For a given load (3 or 30 ohms) and/or input voltage (340V or 390V), run several simulations using the appropriate switching frequency (Fs) values in order to maintain the output voltage at 24V (signal Vo on Scope1).

Also, observing waveforms on Scope2, you will see that the current lags the voltage applied to the resonant circuit which allows the MOSFETs to be turned on with zero voltage.

References

H. Choi; 'Half-Bridge LLC Resonant Converter Design Using FSFR-Series Fairchild Power Switch', Fairchild Semiconductors, AN-4151 S. Abdel-Rahman; "Resonant LLC Converter: Operation and Design", Infineon AN 2012-09

Series Load Resonant (SLR) Converter

This example shows the operation of a series load resonant converter.
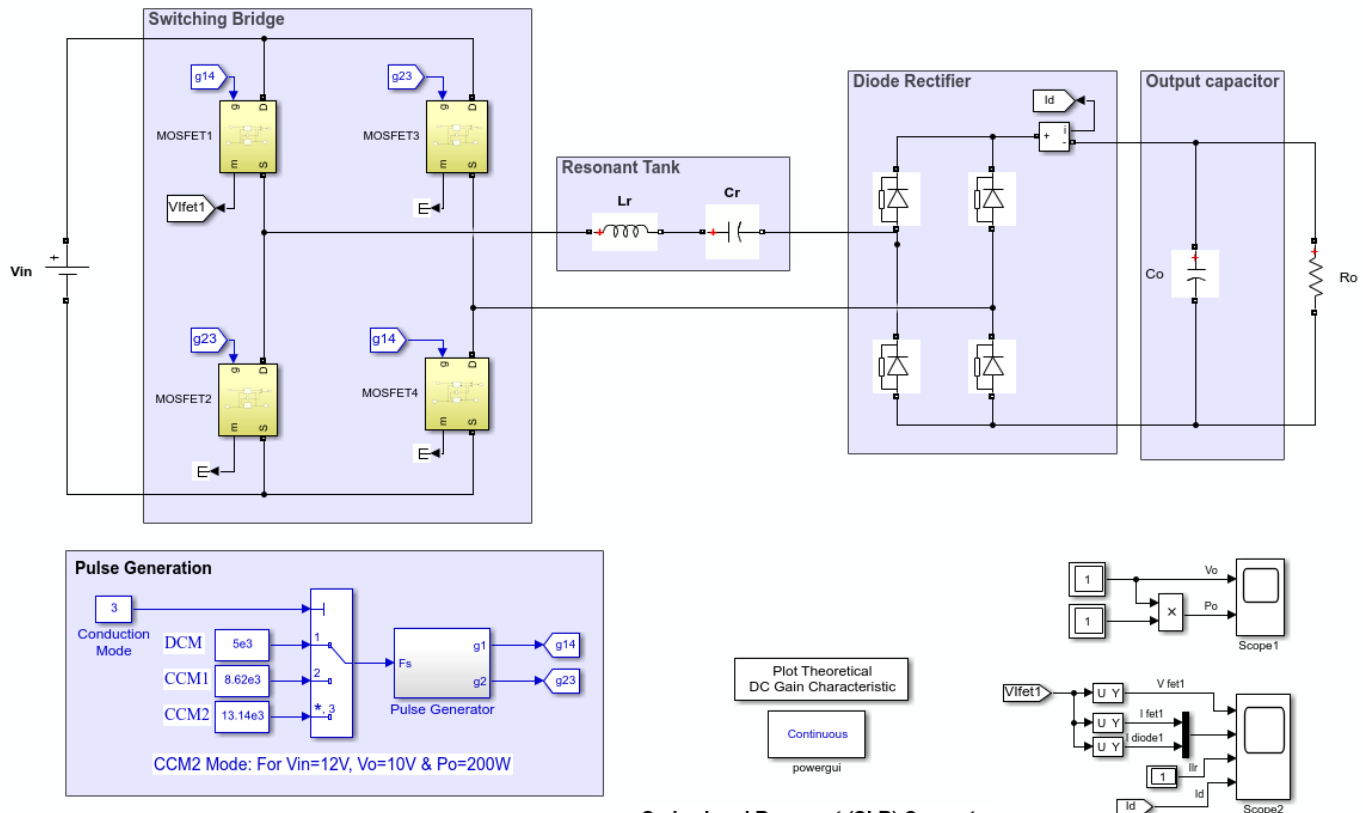
Series Load Resonant (SLR) Converter

This example shows the operation of a series load resonant converter.

Learn more about this example.

Description

The Series Load Resonant converter is a DC/DC converter based on a resonant circuit which allows soft-switching operation. In a soft-switching converter, switching occurs when voltage and/or current values are zero, thus significantly improving converter's efficiency. A switch is said to have zero-voltage switching (ZVS) / zero-current switching (ZCS) when the voltage/current is zero as the switch changes state.

The converter consists of a full-bridge MOSFET bridge, a resonant tank represented by the Lr inductor and Cr capacitor block, a full-bridge rectifier and an output filter (Co). The resonant frequency of the tank is given by:

$$w_r = 1/sqrt(L_r * C_r) = 2 * pi * f_r$$

The switching frequency f_s range determines the three possible operating modes of the SLR converter:

- Discontinuous Conduction Mode (DCM), where $f_s < f_r/2$, leading to a ZVS and ZCS at turn-off and a ZCS at turn-on.

- Continuous Conduction Mode (CCM1) where $f_r/2 < f_s < f_r$, leading to a ZVS and ZCS at turn-off.
- Continuous Conduction Mode (CCM2) where $f_s > f_r$, leading to a ZVS at turn-on.

Simulation

In this example the resonant frequency f_r = 11.25 kHz. Select a switching frequency f_s = 13.14 kHz (conduction mode 3 = CCM2) and run the simulation. Observe waveforms on Scope 1 and Scope 2. For a 0.5 Ohm load, you should get a 10V output for a 12V input voltage. Now try the other conduction modes and/or change the load from 0.5 Ohm to 5 Ohms. You will see that the output will no longer be 10V due to the DC gain characteristic of the resonant circuit for a given load.

Observe the DC gain characteristic plot (0.5 Ohm traces), you will note that the experimental gain values are slightly shifted relative to the theoretical gain values. This is due to the fact that the output filter Co is not taken into account in the theoretical gain calculation. In addition, as the DC gain characteristic plot suggests (5 Ohms trace), it will be difficult to regulate the output of the SLR converter at light load.

Reference

T.Taufik, M.McCarthy, S.Watkins, M.Anwari; "Performance Study of Series Loaded Resonant Converter Using Super Barrier Rectifiers", IEEE, TENCON 2009.

Two-Quadrant DC/DC Converter

This example shows the operation of a two-quadrant DC/DC converter.
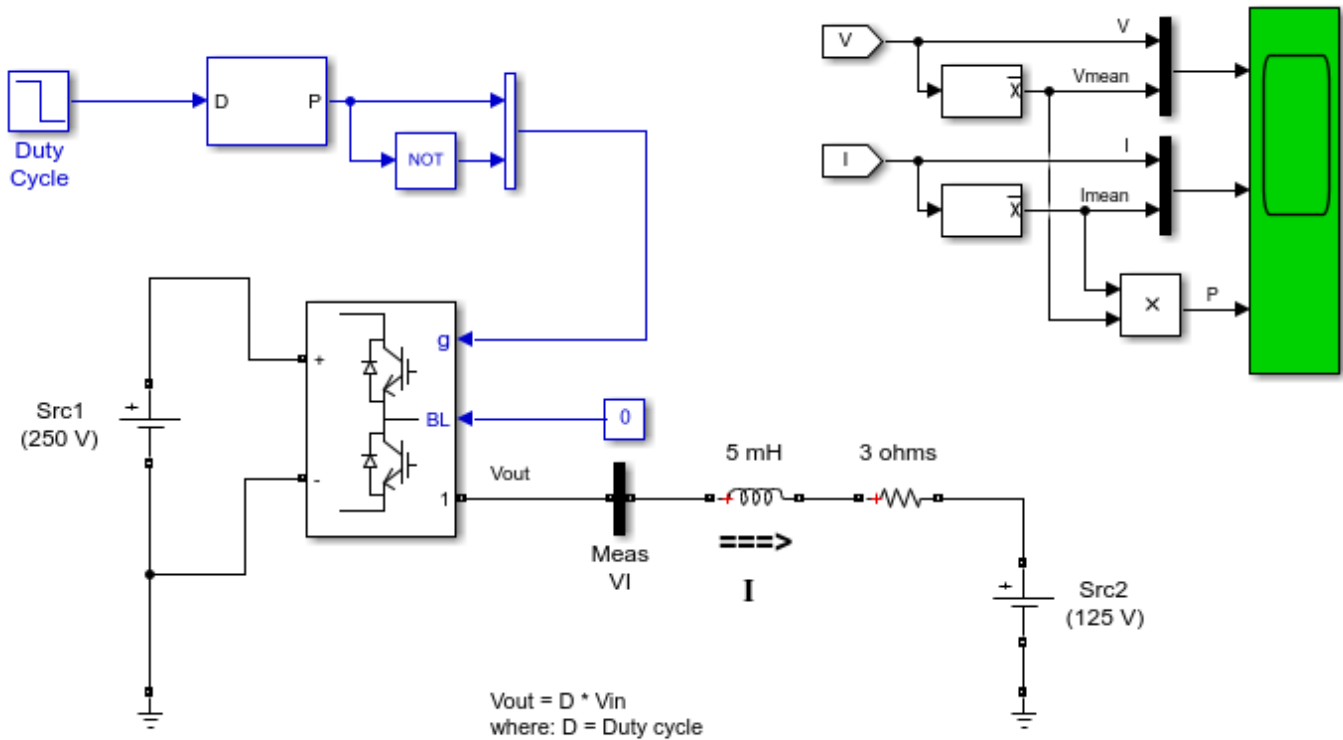
Two-Quadrant DC/DC Converter

This example shows th eoperation of a two-quadrant DC/DC converter.

Learn more about this example.

Description

The example illustrates the operation of a DC-DC converter that converts DC voltage from one voltage level to another one, of same polarity. The converter can operate in both the directions (current can be reversed).

AC-DC-AC Converter

This example shows the operation of an AC-DC-AC converter.

AC-DC-AC Converter

This example shows the operation of an AC-DC-AC converter
to feed a 50 Hz, 50 kW load from a 60Hz 600V source.

Learn more about this example.
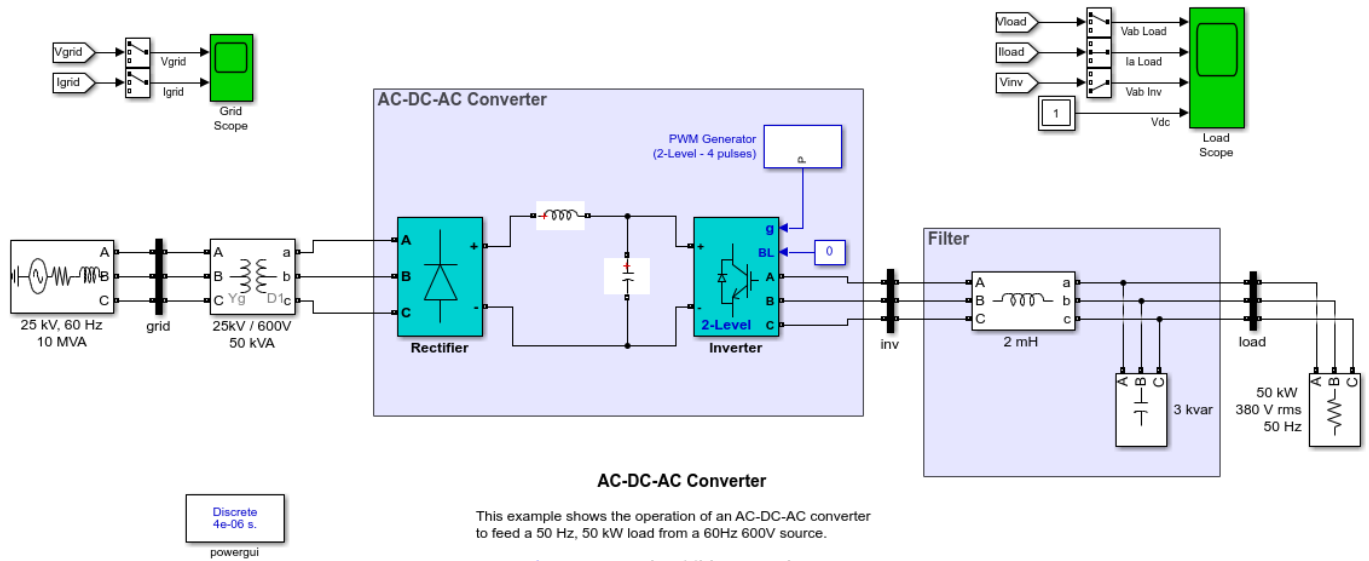
Description

A 60 Hz, voltage source feeds a 50 Hz, 50 kW load through an AC-DC-AC converter. The 600V, 60 Hz voltage obtained at the secondary of the Wye/Delta transformer is first rectified by a six pulse diode bridge. The filtered DC voltage is applied to an IGBT two-level inverter generating 50 Hz. The IGBT inverter uses Pulse Width Modulation (PWM) at a 2 kHz carrier frequency. The circuit is discretized at a sample time of 4 us.

Single-Phase PWM Inverter

This example shows the operation of a single-phase PWM inverter.
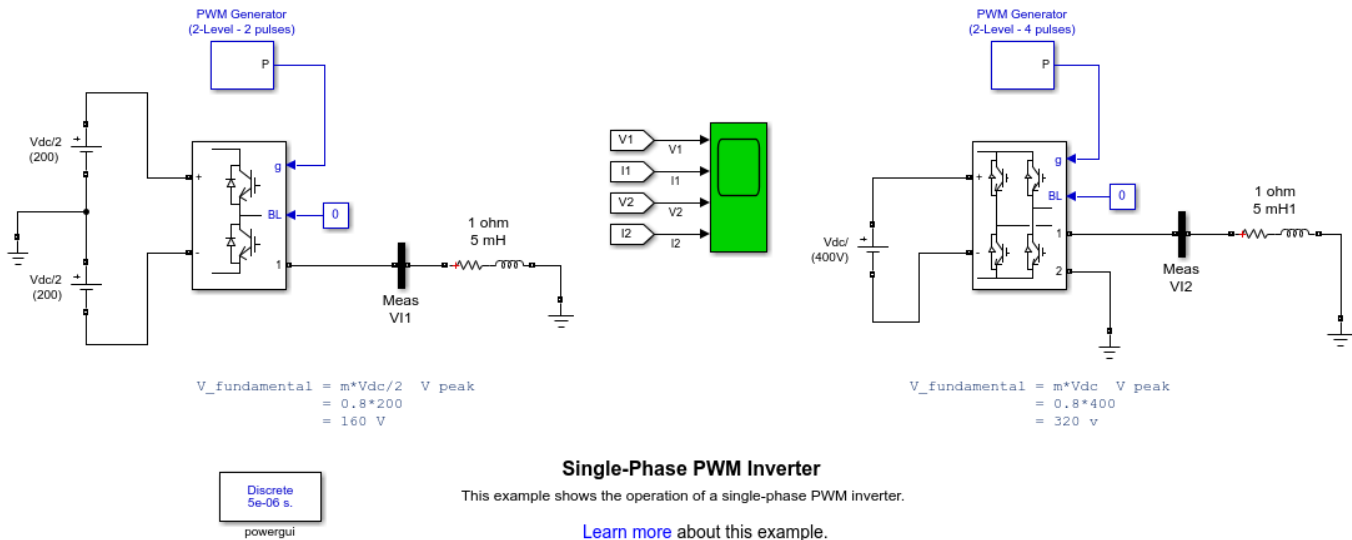
Description

The system consists of two independent circuits illustrating single-phase PWM voltage-sourced inverters.

The Half-Bridge Converter block and the Full-Bridge converter block are modeling simplified model of an IGBT/Diode pair where the forward voltages of the forced-commutated device and diode are ignored.

The converters are controlled in open loop with the PWM Generator blocks. The two circuits use the same DC voltage (Vdc = 400V), carrier frequency (1620 Hz) and modulation index (m = 0.8).

In order to allow further signal processing, signals displayed on the Scope block are stored in a variable named ScopeDataForFFT, in structure with time format.

Simulation

Run the simulation and observe the current into the loads and the voltage generated by the PWM inverters.

Once the simulation is completed, open the Powergui and select FFT Analysis to display the 0 - 5000 Hz frequency spectrum of signals saved in the ScopeDataForFFT structure. The FFT will be performed on a 2-cycle window starting at t = 0.07 - 2/60 (last 2 cycles of recording). Click on Display and observe the frequency spectrum of last 2 cycles.

The fundamental component of V inverter is displayed above the spectrum window. Compare the magnitude of the fundamental component of the inverter voltage with the theoretical values given in the circuit. Compare also the harmonic contents in the inverter voltage.

The half-bridge inverter generates a bipolar voltage (-200V or +200V) . Harmonics occur around the carrier frequency (1620 Hz +- k*60 Hz), with a maximum of 103% at 1620 Hz.

The full-bridge inverter generates a monopolar voltage varying between 0 and +400V for one half cycle and then between 0 and -400V for the next half cycle. For the same DC voltage and modulation index, the fundamental component magnitude is twice the value obtained with the half-bridge. Harmonics generated by the full-bridge are lower and they appear at twice the carrier frequency (maximum of 40% at 2*1620+-60 Hz). As a result, the current obtained with the full-bridge is smoother.

AC/DC Three-Level PWM Converter

This example shows the operation of an AC-DC three-level PWM Converter.

P. Giroux ; G. Sybille Hydro-Quebec (IREQ)
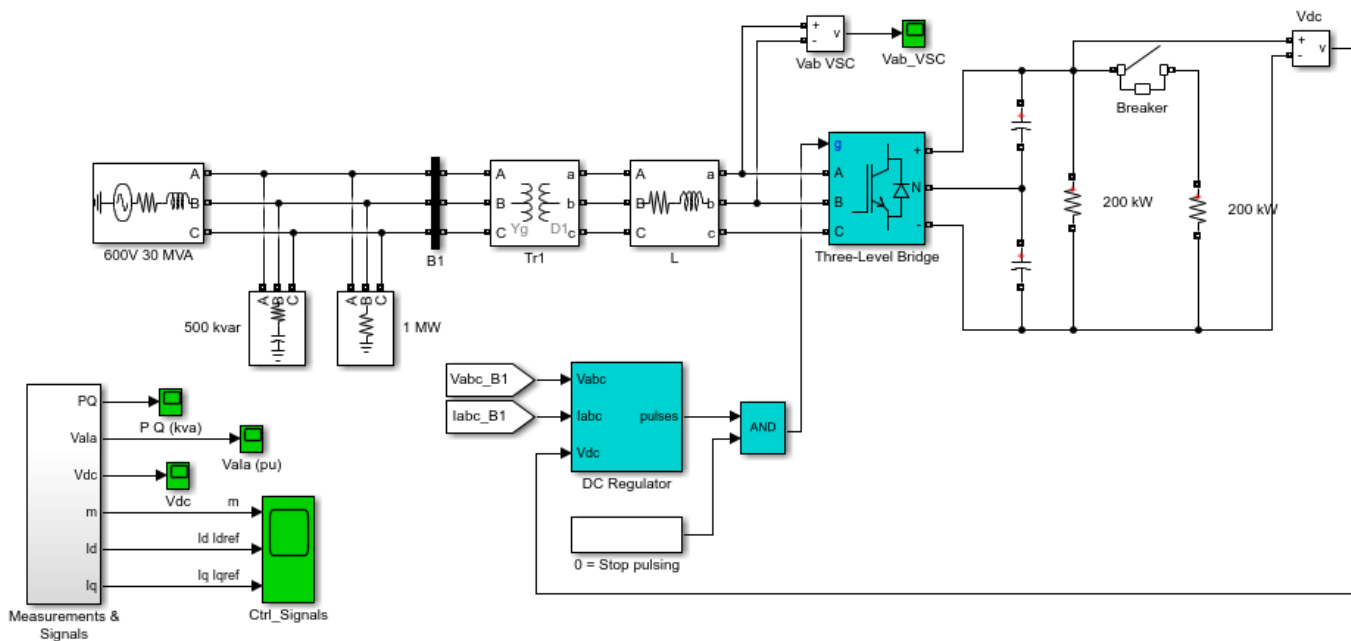
AC/DC Three-Level PWM Converter

This example shows the operation of an AC-DC three-level
PWM Converter.

Learn more about this example.

Description

1) Converter rating: 500 Volts DC, 500 kW

2) AC Supply: three-phase, 600 V, 30 MVA, 60 Hz system

3) Voltage-sourced Converter (VSC): - Three-level, three-phase IGBT bridge (modeled using the "Three-Level Bridge" block) controlled by a PWM modulator (carrier frequency of 1620 Hz) - DC Link: 2 capacitors of 75000 uF

4) Controller: The DC regulator uses two PI regulators to control the DC voltage while maintaining a unity input power factor for the AC supply.

Simulation Parameters

- Discrete (no continuous states)

- Two sample times: Ts_Power= 5 us (used by PSB for the simulation of the power system + converters) Ts_Control= 100 us (used for the simulation of the DC regulator)

- Initial conditions are set at the start of the simulation (by automatically loading the file power_3levelVSC.mat prior to the simulation). This file has been generated by running an initial simulation to steady-state for an integer number of cycles of 60 Hz. The final states (both Specialized Power Systems and Simulink® controllers states) were saved in a structure with time, called xInit. This variable was saved in "power_3levelVSC.mat" file.

Simulation

- In the simulation (lasting 200 ms), we can observe the following signals: 1) the DC voltage (Vdc Scope) 2) the primary voltage and current (phase A) of the AC supply (VaIa Scope), 3) the device currents of leg A of the IGBT bridge (double-click on the "Measurements & Signals" block to look inside the "Ia_Devices" Scope). 4) the phase-to-phase AC voltage generated by the VSC (Vab_VSC Scope)

- At t=50 ms, a 200-kW load is switched-in. We can see that the dynamic response of the DC regulator to this sudden load variation (200 kW to 400 kW) is satisfactory. The DC voltage is back to 500 V within 1.5 cycle and the unity power factor on the AC side is maintained.

- At t=100 ms, a "Stop Pulsing" signal is activated (pulses normally sent to the converter are blocked). We can see that the DC voltage drops to 315 V. A drastic change in the primary current waveform can also be observed. When the pulses are blocked, the Three-Level Bridge block operation becomes similar to a three-phase diode bridge.

Regenerate Initial Conditions

The initial states required to start in steady state have been saved in the "power_3levelVSC.mat" file. When you start simulation, the StartFcn callback (in the Model Properties/Callbacks) automatically loads into your workspace the contents of this .mat file ("xInit" variable).

If you modify this model, or change parameter values of power components, the initial conditions stored in the "xInit" variable will no longer be valid and Simulink will issue an error message. To regenerate the initial conditions for your modified model, follow the steps listed below:

1 In the Simulation/Configuration Parameters/Data Import/Export Parameters menu, uncheck the "Initial state" parameter and check the "Final states" parameter.

2 Double click on the Breaker block and temporarily disable the breaker closing by multiplying the "Switching time(s)" parameter by 100.

3 Double click on the Timer block labeled "0=Stop pulsing". Temporarily disable pulse blocking by multiplying the " Time(s)" parameter by 100.

4 Change the Simulation Stop Time to 0.5 second. Note that in order to generate initial conditions coherent with the 60 Hz voltage source phase angle, the Stop Time must an integer number of 60 Hz cycles.

5 Start simulation. When simulation is completed, verify that steady state has been reached by looking at waveforms displayed on the Ctrl_Signals scope. The final states which have been saved in the "xFinal" structure with time can be used as initial states for future simulations. Executing the next two commands copies these final conditions in "xInit" and saves this variable in a new file (myModel_init.mat). * >> xInit=xFinal; * >> save myModel_init xInit

6 In the File/Model Properties/Callbacks/StartFcn window, change the name of the initialization file from "power_3levelVSC" to "myModel_init". Next time you start a simulation with this model, the variable xInit saved in the myModel_init.mat file will be loaded in your workspace.

7 In the Simulation/Configuration Parameters menu, check "Initial state".

8 Start simulation and verify that your model starts in steady-state.

9 Double click on the Breaker block and reset the "Switching time(s)" parameter back to 0.05 s (remove the 100 multiplication factor).

10 Double click on the Timer block labeled "0=Stop pulsing". Re-enable the pulse blocking at t=0.1 s by removing the 100 multiplication factor.

11 Change the Simulation Stop time back to 0.2 s.

12 Save your Model.

Regulated Resonant LLC Converter

This example shows the operation of a regulated resonant LLC converter.
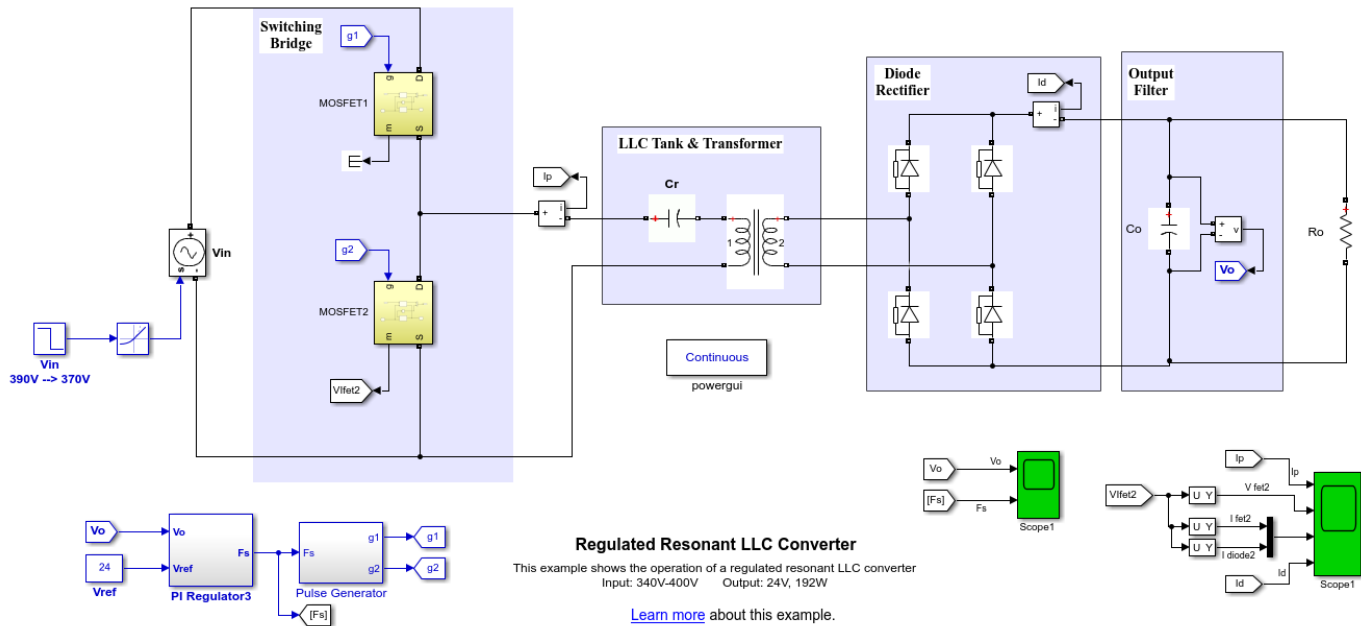
Description

The LLC converter is a DC/DC converter based on a resonant circuit which allows soft-switching operation.

The converter output voltage is regulated using a PI regulator. The output of the regulator is the switching frequency (Fs) which will keep the converter output voltage at the desired voltage set point (Vref). You can change the regulator gains by double-clicking on the PI Regulator block.

Simulation

Start the simulation and observe on Scope1, the behavior of the regulated converter under a input voltage step (340V to 390V). Also, observing waveforms on Scope2, you will see that the current lags the voltage applied to the resonant circuit which allows the MOSFETs to be turned on with zero voltage.

Watkins-Johnson Converter

This example shows the operation of a Watkins-Johnson Converter.
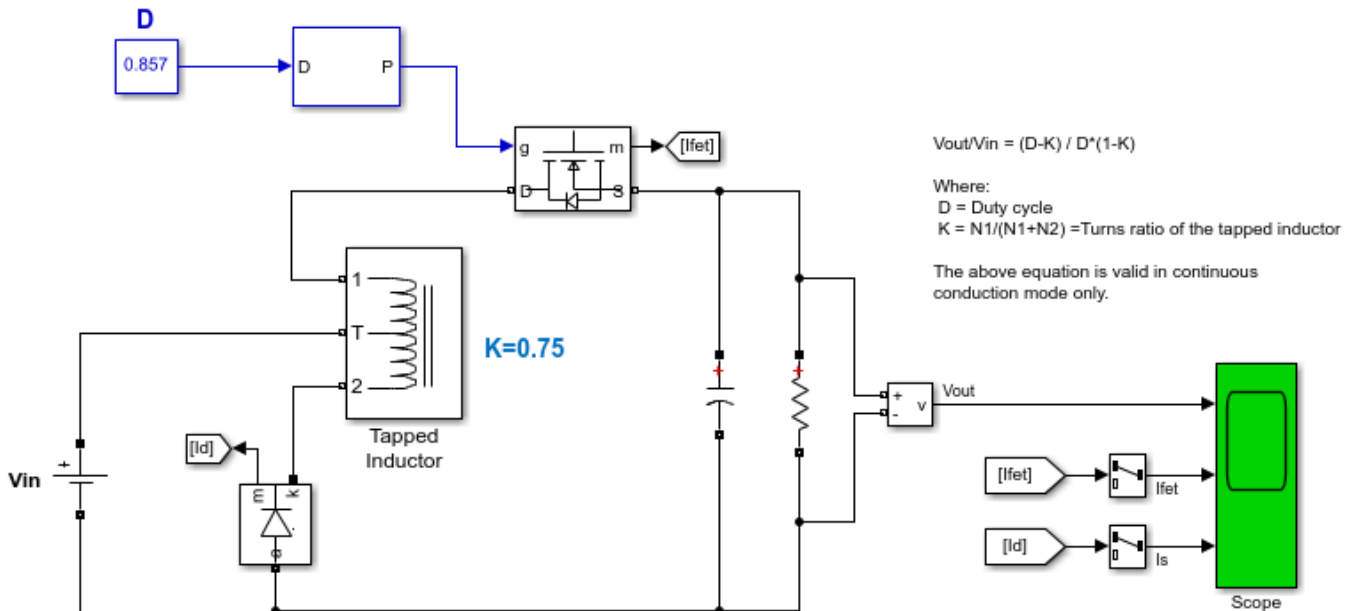
Watkins-Johnson Converter

This example shows the operation of a Watkins-Johnson Converter.

Learn more about this example.

Description

The Watkins-Johnson (WJ) converter employs a tapped-inductor giving an extra-degree of freedom compared to the classical buck converter. The theoretical transfer function of the WJ converter is:

$$Vout/Vin = D - K/D * (1 - K)$$

where D is the Duty cycle and $K = N1/(N1 + N2)$ is the turns ratio of the tapped inductor,

Note that the above equation is valid in continuous conduction mode only. For a giving conversion ratio, the WJ converter has the advantage of employing a larger duty cycle than the traditional classical buck converter. In our example, the tapped inductor is built using the SPS mutual inductance. By double-clicking on the Tapped Inductor block, you can specify the value in henrys and the turns ratio of the tapped inductor.

Simulation

Run the simulation and observe waveforms on Scope. The mean value of the load voltage (Vout) should be very close to the theoretical value of:

Vout = 24 * (0.857-0.75) /(0.855*(1-0.75)) = 12.01 V

Three-Phase SV-PWM Converter

This example shows the open-loop speed control of an induction motor using constant V/Hz principle and a space vector (SV) PWM technique.

Pierre Giroux, Hydro-Quebec (IREQ)
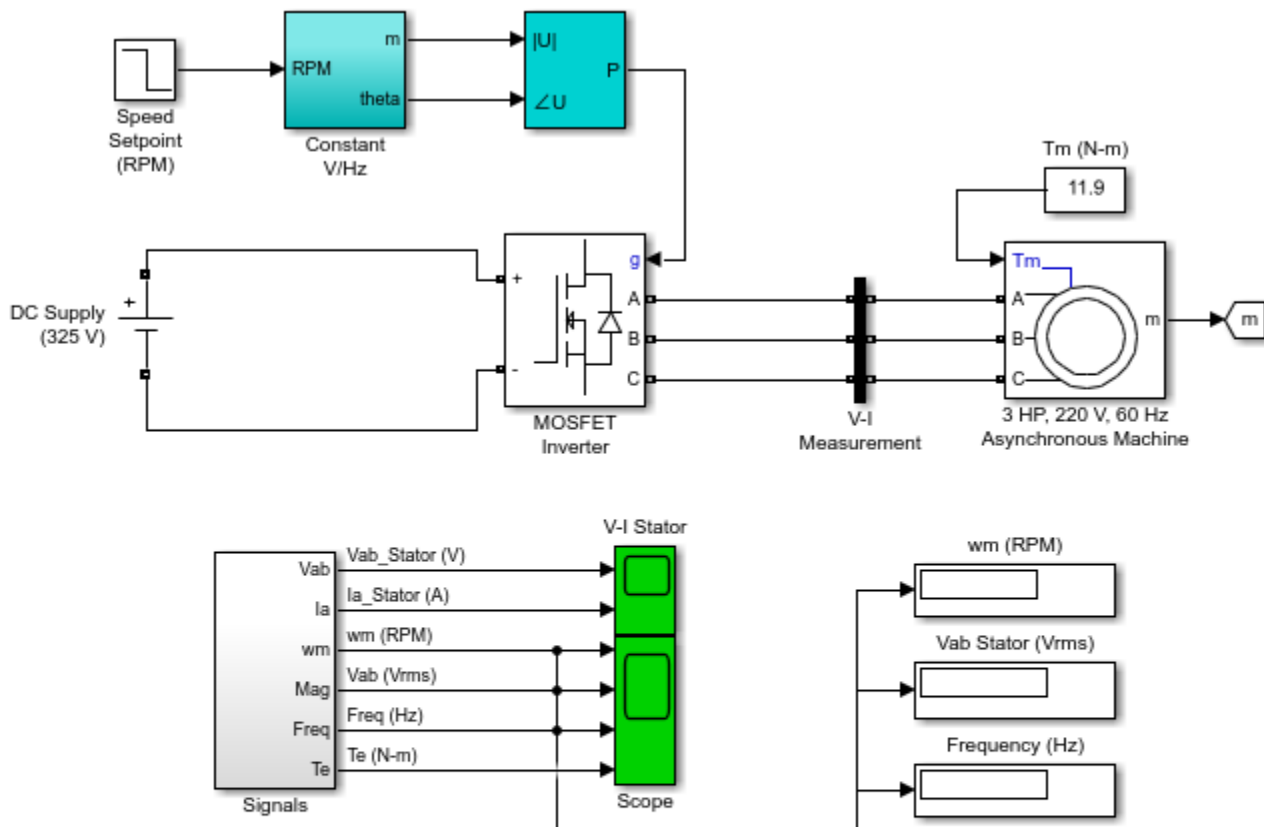
Three-Phase SV-PWM Converter

This example shows the open-loop speed control of an induction motor using constant V/Hz principle and a space vector (SV) PWM technique.

Learn more about this example.

Description

A 3-phase squirrel-cage motor rated 3 HP, 220 V, 60 Hz, 1725 rpm is fed by a 3-phase MOSFET inverter connected to a DC voltage source of 325 V. The inverter is modeled using the "Universal Bridge" block and the motor by the "Asynchronous Machine" block. Its stator leakage inductance Lls is set to twice its actual value to simulate the effect of a smoothing reactor placed between the inverter and the machine. The load torque applied to the machine's shaft is constant and set to its nominal value of 11.9 N.m.

The firing pulses to the inverter are generated by the "Space-Vector PWM modulator" block of the SPS library. The chopping frequency is set to 1980 Hz and the input reference vector to "Magnitude-Angle".

Speed control of the motor is performed by the "Constant V/Hz" block. The magnitude and frequency of the stator voltages are set based on the speed setpoint. By varying the stator voltages magnitude in proportion with frequency, the stator flux is kept constant.

Simulation

Start the simulation. Since the initial states have been automatically loaded, the simulation should start in steady-state. The initial motor speed should be 1720 RPM and the rms value of the stator voltages should be 220V@60Hz.

At 0.1s, the speed setpoint is changed from 1725 to 1300 RPM. You can observe the system dynamic looking inside Scope 1. When the motor reaches a constant speed of 1275 RPM, the stator voltage rms value is down to 165.8V and the frequency to 45.2 Hz.

Stator voltage (phase AB) and phase A current waveforms can be observed in the "V-I Stator" Scope. You can do a FFT of these two quantities using the powergui FFT Analysis.

Regenerate Initial Conditions

The initial states required to start this model in steady state with a 1725 rpm reference speed and a 11.9 N.m load torque have been saved in the "power_svpwm_init.mat" file. When you open this model, the InitFcn callback (in the Model Properties/Callbacks) automatically loads into your workspace the contents of this .mat file ("xInitial" variable).

If you modify this model, or change parameter values of power components, the initial conditions stored in the "xInitial" variable will no longer be valid and Simulink® will issue an error message. To regenerate the initial conditions for your modified model, follow the steps listed below:

1. In the Simulation/Configuration Parameters/Data Import/Export Parameters menu, uncheck the "Initial state" parameter.

2. Double click on the Step block labeled "Speed Setpoint (RPM)" and temporarily disable the change of reference speed by multiplying the Step time by 100.

3. Change the Simulation Mode from "Normal" to "Accelerator".

4. Start simulation. When simulation is completed, verify that steady state has been reached by looking at waveforms displayed on the scopes. The final states which have been saved in the "xFinal" structure with time can be used as initial states for future simulations. Executing the next two commands copies these final conditions in "xInitial" and saves this variable in a new file (myModel_init.mat).

```
>> xInitial=xFinal;
```

```
>> save myModel_init xInitial
```

5. In the File -> Model Properties -> Callbacks -> InitFcn window, change the name of the initialization file from "power_svpwm_init" to "myModel_init". Next time you open this model, the variable xInitial saved in the myModel_init.mat file will be loaded in your workspace.

6. In the Simulation -> Configuration Parameters menu, check "Initial state".

7. Start simulation and verify that your model starts in steady-state.

8. Double click on the Step block labeled "Speed Setpoint (RPM)" and re-enable the change of reference speed at t=0.1 s (remove the 100 multiplication factor in the Step time).

9. Change the Simulation Mode back to Normal.

10. Save your model.

Three-Level NPC Inverter Using Space-Vector PWM with Neutral-Point Voltage Control

This example shows the operation of a 2-MVA, 3-Level NPC inverter using Space-Vector Pulse-Width-Modulation (SVPWM) technique with neutral-point voltage control.

Description

From an ideal 2400-Volt DC source, a 2-MVA, three-phase 3-level inverter delivers power to a 25-kV distribution system. A sine filter is used on the secondary side of the distribution transformer in order mitigate high-frequency harmonics generated by the inverter. The Sw1 ideal switch is used to create a major unbalance on the DC link.

The inverter is controlled in open-loop using an SVPWM 3-Level Generator block. The PWM switching frequency is set to 1620 Hz and the neutral-point voltage control gain is set to 0.04.

Simulation

Run the simulation and observe waveforms on the VIdc Scope. We will notice that the neutral-point voltage control perform correctly when a large unbalance is created at 0.2 sec. Now double-click on the SVPWM generator block and set the proportional gain of the neutral-point voltage control to zero.

Re-run the simulation and note that without neutral-point voltage control, the DC link unbalance increases rapidly.

Five-Cell Multi-Level Converter

This example shows a five-cell multi-level converter driving a static load.

Graham Dudgeon, Senior Consultant, The MathWorks, Inc.
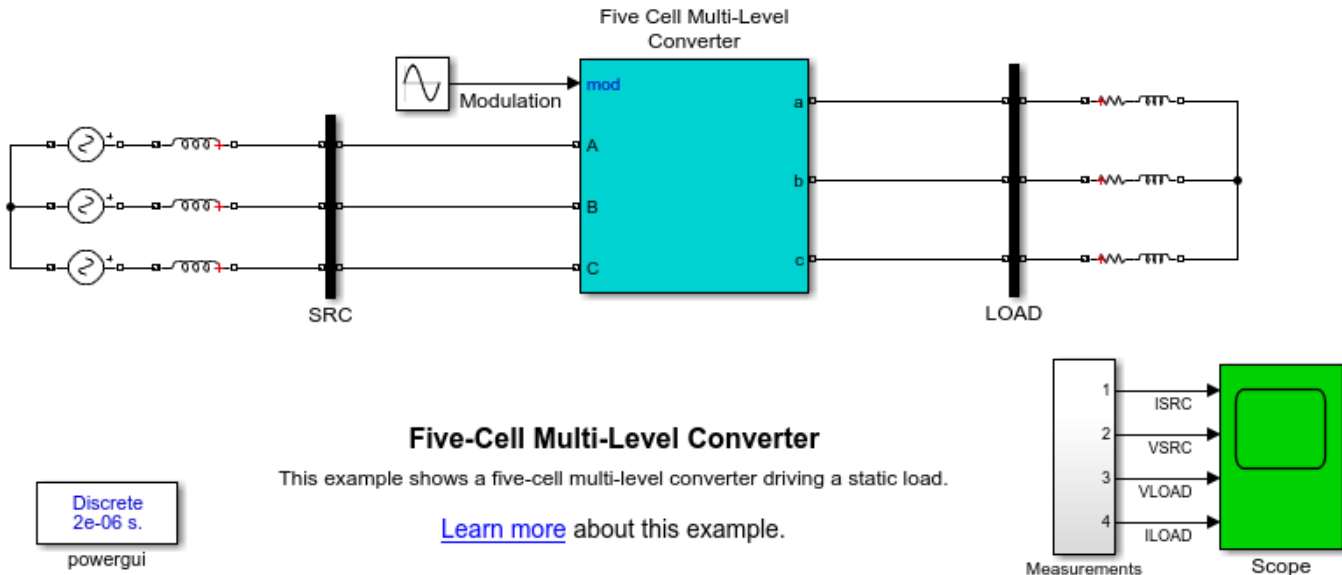
Description

The system consists of a three-phase five-cell multi-level converter (MLC) constructed from cells containing diode rectifiers and IGBT H-bridge inverters. The MLC is supplied by an ideal 60Hz three-phase source and drives a static resistive/inductive load at 100Hz. The architecture of the converter is taken from [1]. The zig-zag input transformers for each cell are phase-shifted by 15 degrees relative to each other to ameliorate source harmonics below the 23rd harmonic. The H-bridge PWM carrier waves are phase shifted by 45 degrees relative to each other to produce the output voltage 'stacking' effect. The modular nature of a MLC is clearly seen by looking under the component mask.

Simulation

Start the simulation. Observe the voltage stacking effect from Scope block. Using the FFT Analysis tool in the powergui, confirm that the source current ISRC has no significant harmonics under the 23rd harmonic.

You can adjust the modulation waveforms to change output voltage amplitude and frequency.

References

[1] Wen, J. and Smedley, K.M., 'Synthesis of Multi-Level Converters Based on Single- and/or Three-Phase Converter Building Blocks', IEEE® Transactions on Power Electronics, Vol. 23, No. 3, May 2008.

Three-Phase 48-Pulse GTO Converter

This example shows the use of three-level converters and zig-zag phase-shifting transformers in a 48-pulse square-wave GTO converter.

P. Giroux and G. Sybille (Hydro-Quebec)
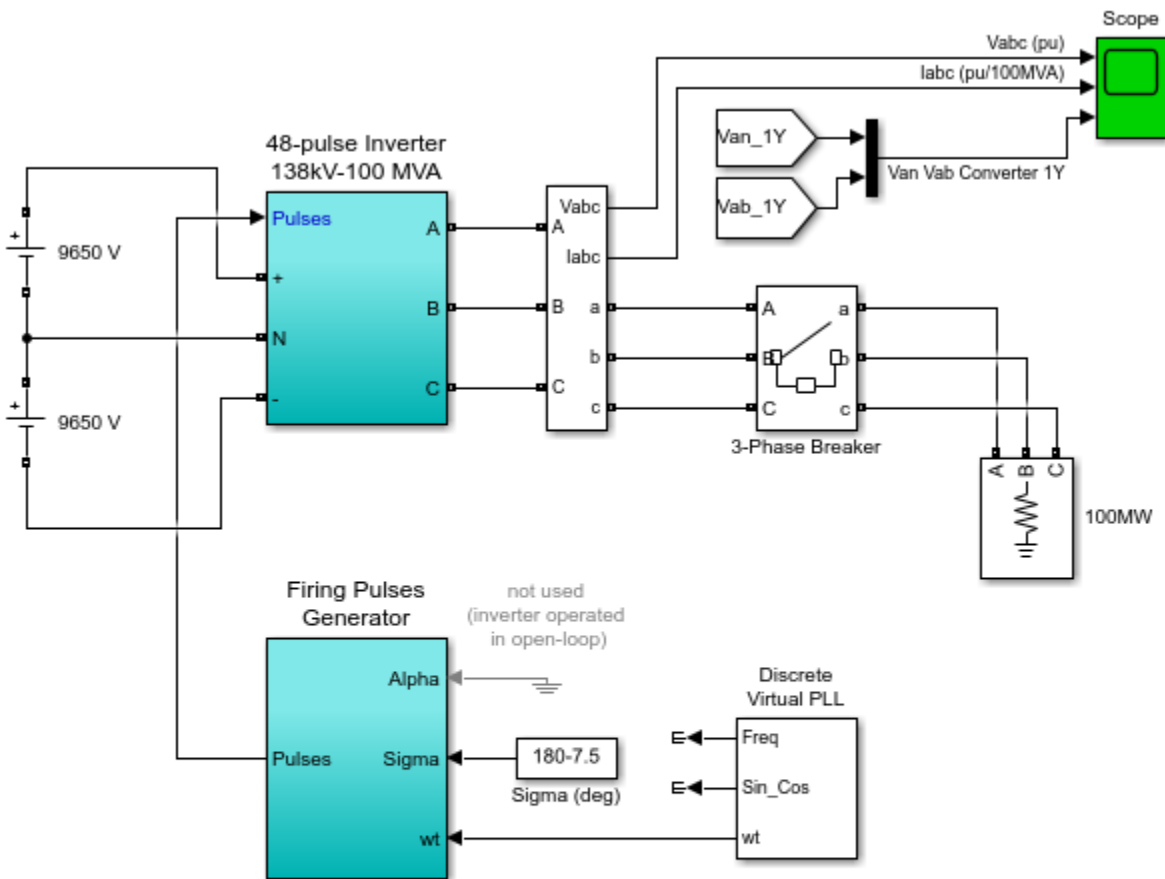
Three-Phase 48-Pulse GTO Converter

This example shows the use of three-level converters and zig-zag phase-shifting transformers in a 48-pulse square-wave GTO converter.

Learn more about this example.

Description

In this example, ideal switches and zig-zag phase shifting transformers are used to build a GTO-type 100 MVA, 138 kV voltage source inverter. This type of converter is used in high-power (up to 200 MVA) Flexible AC Transmission Systems (FACTS) which are used to control power flow on transmission grids. It can be used, for example, to build a model of shunt or series static compensator

(STATCOM or SSSC) or, using two such converters, a combination of shunt and series devices known as Unified Power Flow Controller (UPFC).

The inverter described in this example is a harmonic neutralized, 48-pulse GTO converter described in reference. It consists of four 3-phase, 3-level inverters and four phase-shifting transformers. Open the "48-pulse inverter" subsystem. Notice that the DC bus (Vdc = +/-9650 V) is connected to the four 3-phase inverters.The four voltages generated by the inverters are applied to secondary windings of four zig-zag phase-shifting transformers connected in Wye (Y) or Delta (D). The four transformer primary windings are connected in series and the converter pulse patterns are phase shifted so that the four voltage fundamental components sum in phase on the primary side.

Each 3-level inverter generates three square-wave voltages which can be +Vdc, 0, -Vdc. The duration of the +Vdc or -Vdc level (Sigma) can be adjusted between 0 and 180 degrees from the Sigma input of the Firing Pulse Generator block. Each inverter uses a Three-Level Bridge block where the specified power electronic devices are Ideal Switches. In this model, each leg of the inverter uses 3 ideal switches to obtain the 3 voltage levels (+Vdc, 0, - Vdc). This simple model simulates the behavior of a physical inverter where each leg consists of 4 GTOs, 4 antiparallel diodes and 2 neutral clamping diodes. Despite this simplified switch arrangement, the model still requires 4 pulses per arm as in the physical model. The pulse pattern sent to each leg of a 3-phase inverter is described inside the Firing Pulse Generator.

You can also select GTO/Diodes pairs instead of Ideal Switches as power electronic devices. It would allow you to specify forward voltage drops for GTOs and diodes and to observe currents flowing in GTOs and diodes by means of the Multimeter block.

The phase shifts produced by the secondary delta connections (-30 degrees) and by the primary zig-zag connections (+7.5 degrees for transformers 1Y and 1D, and -7.5 degrees for transformers 2Y and 2D) allows to neutralize harmonics up to 45th harmonic, as explained below:

The 30-degree phase-shift between the Y and D secondaries cancels harmonics 5+12n (5, 17, 29, 41, ...) and 7+12n (7, 19, 31, 43, ...). In addition, the 15-degree phase shift between the two groups of transformers (1Y and 1D leading by 7.5 degrees, 2Y and 2D lagging by +7.5 degrees) allows cancellation of harmonics 11+24n (11, 35, ...) and 13+24n (13, 37, ...). Considering that all the 3n harmonics are not transmitted by the Y and D secondaries, the first harmonics which are not cancelled by the transformers are 23rd, 25th, 47th and 49th. By choosing an appropriate conduction angle for the 3-level inverters (sigma = 180 - 7.5 = 172.5 degrees), the 23rd and 25th can be minimized. The first significant harmonics are therefore the 47th and 49th. This type of inverter generates an almost sinusoidal waveform consisting of 48-steps.

The inverter is operated in open loop at constant DC voltage, therefore, the voltage angle (alpha) which is normally kept close to zero is not used. You can look at the STATCOM (Detailed Model) example that shows the operation of a 48-pulse GTO STATCOM in closed-loop.

Initially, the inverter operates at no load. Then, at t = 0.025 s, a 100 MVA resistive load is connected at the 138-kV terminals.

Simulation

Run the simulation and observe the following waveforms on the Scope block:

Voltages generated by the inverter (trace 1), load currents (trace 2), phase-neutral voltage and phase-phase voltage of one of the four inverters (1Y) superimposed on trace 3. When the inverter is operating at no load, you can observe the three 48-step voltage waveform. When the load is switched

on the voltage becomes smoother because harmonics are filtered by the transformer leakage reactances.

Once the simulation is completed, open the Powergui and select "FFT Analysis" to display the 0-4000 Hz frequency spectrum of signals saved in the two "psb48pulse_str" structure. Select signal labeled 'Vabc (pu)'. The FFT will be performed on a 1-cycle window of phase A voltage starting at t = 0.025-1/60 s (inverter operating at no load). Click on Display and observe the frequency spectrum.

The fundamental component of Voltage (in pu) as well as THD are displayed above the spectrum window. Notice that the first significant harmonics are 47th and 49th (approx. 2%). Notice also that 23rd and 25th are reduced below 0.3%. In order to appreciate the efficiency of harmonic neutralization, you can also observe the frequency spectrum of phase-phase voltage generated by each individual inverter. Select input labeled "Van Vab Converter 1Y" and signal number 2 and click on Display. Observe that THD in the 0 - 4000 Hz frequency range is 25%.

You can also run another simulation by specifying different values of sigma at the input of the pulse generator. You can verify that, in order to cancel a particular harmonic n in the phase-phase voltage of each individual converter, the Sigma value (in degrees) is given by:

$$Sigma = 180 * (1 - 1/n)$$

Verify also that choosing Sigma = 180 degrees is equivalent to using 2-level converters and that the voltage waveform is degraded to 24 pulses.

Reference

Narain G. Hingorani and Laszlo Gyuyi, "Understanding FACTS", IEEE® Press, 2000

Three-Phase Matrix Converter

This example shows a three-phase matrix converter driving a static load and drawing unity power factor at the source.

Graham Dudgeon, Senior Consultant, The MathWorks, Inc.
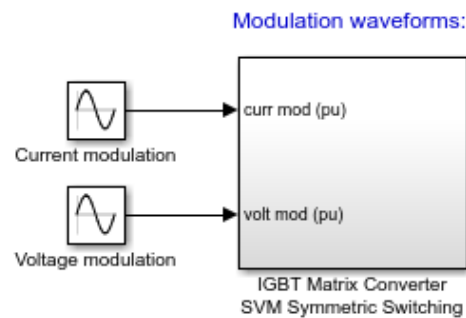
Description

The system consists of a three-phase matrix converter (MC) constructed from 9 back-to-back IGBT switches. The MC is supplied by an ideal 60Hz three-phase source and drives a static resistive load at 60Hz. The switching algorithm is based on an indirect space-vector modulation described in [1] which considers the MC as a rectifier and inverter connected via a DC link with no energy storage. Indirect space-vector modulation allows direct control of input current and output voltage and hence allows

the power factor of the source to be controlled. The switching algorithm utilizes a symmetric switching sequence described in [2].

No filters are included in the model so that the construction of the waveforms can be clearly seen.

Simulation

Start the simulation. Observe from Scope 'Source' that the MC draws unity power factor.

References

[1] Huber, L. and Borojevic, D., 'Space Vector Modulated Three-Phase to Three-Phase Matrix Converter with Input power Factor Correction', IEEE® Transactions on Industry Applications, Vol. 31, No. 6, November/December 1995.

[2] Prasad, V.H., 'Analysis and Comparison of Space Vector Modulation Schemes For Three-Leg and Four-leg Voltage Source Inverters', Masters Thesis, Electrical Engineering, Virginia Tech, May 15, 1997.

Six-Pulse Cycloconverter

This example shows a six-pulse cycloconverter driving a static load.

Graham Dudgeon, Senior Consultant, The MathWorks, Inc. Igor Braverman, Senior Consultant, The MathWorks, Inc.
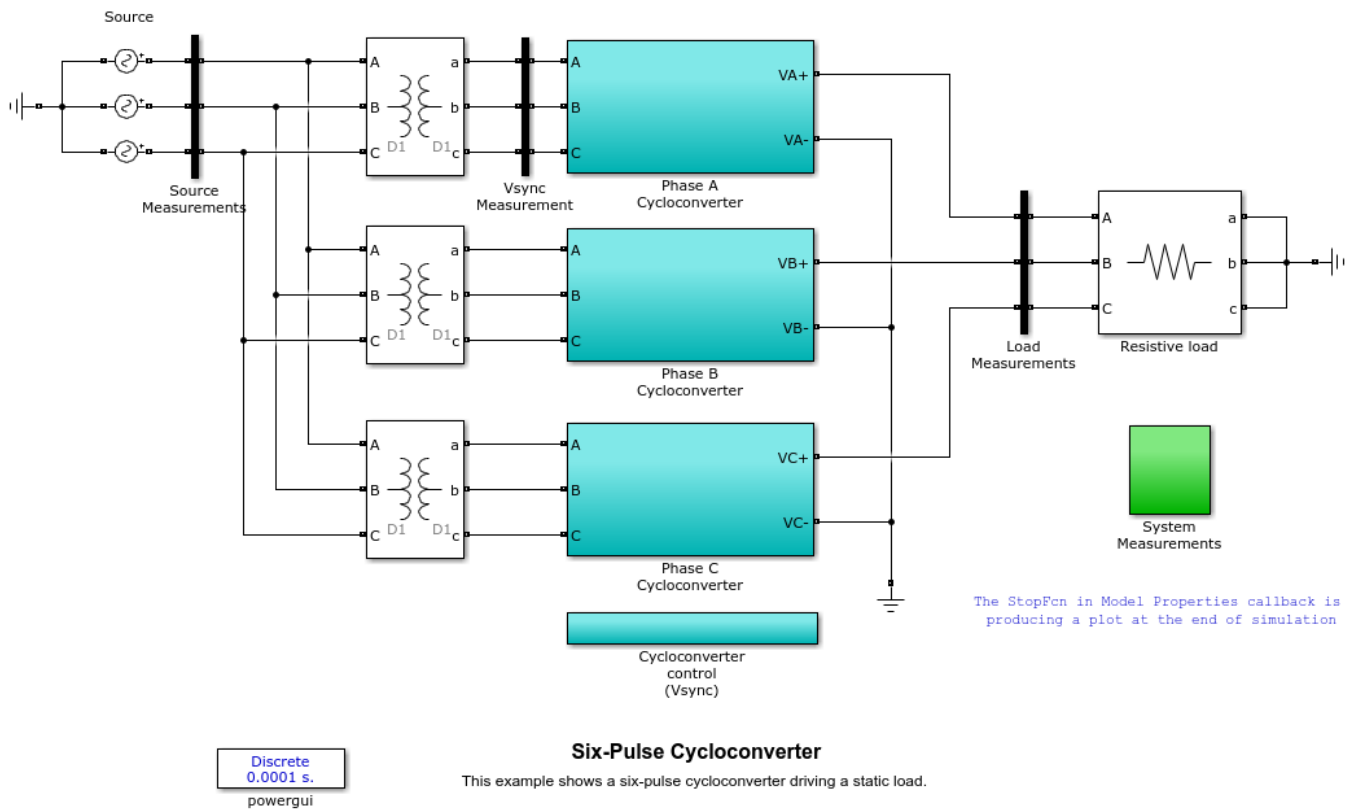
Description

The system consists of three-phase to single-phase cycloconverter modules suitably arranged to implement a three-phase to three-phase architecture. Each module has a positive and negative cycle thyristor bridge connected in anti-parallel. To avoid circulating currents when one bridge is conducting, the anti-parallel bridge is blocked. A 5 degree 'dead zone' exists between the positive and negative cycle commutation. You can modify the dead zone value in **File -> Model Properties -> Callbacks -> InitFcn**. The output voltage waveforms are constructed by suitably modifying the firing angle of the thyristor bridges.

Simulation

Start the simulation. Observe the load voltages from Scope 'Vabc_load' in System Measurements subsystem, and note that a 10Hz output has been constructed from a 60Hz input. A MATLAB® script executes on completion of the simulation to more clearly show the construction of phase-A output voltage from the three-phase input. The user may modify this execution as desired by editing **File ->**

Model Properties -> Callbacks -> StopFcn. The user can adjust the output voltage magnitude and frequency by modifying 'Output voltage (V peak)' and 'Output frequency (Hz)' respectively.

Thyristor Rectifiers

This example shows the operation of thyristor rectifiers.

Description

This example compares the operation of a six-pulse thyristor rectifier to a twelve-pulse thyristor rectifier.

The twelve-pulse bridge is represented by two six-pulse bridge connected in series, with their AC connections connected to a transformer with two secondaries (one wye, one delta) that produces a 30 deg. phase shift between the two bridges. This transformer configuration cancels many of the characteristic harmonics produced by the six-pulse bridges.

Three-Phase Rectifier

This example shows how to use the diode block to simulate a three-phase rectifier.

G. Sybille (Hydro-Quebec)
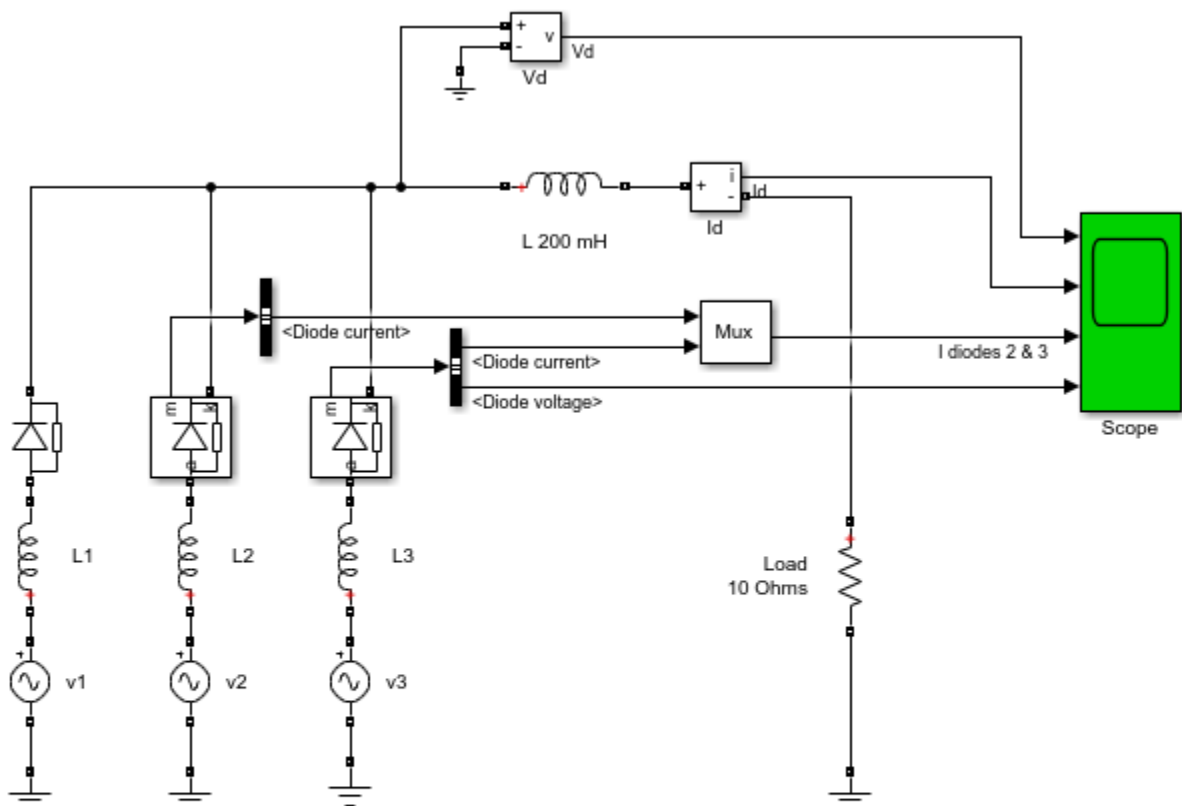
Three-Phase Rectifier

This example shows the diode block to simulate a three-phase rectifier.

Learn more about this example.

Description

A 10 ohm load is fed in DC through a three-phase rectifier from an inductive source (5 mH; 120 V rms). The rectified current is filtered by a 200mH inductance.

Diodes are connected in parallel with RC snubbers (1000 ohms-0.1 uF) The measurement output of Diode2 and Diode3 is used to observe the diode voltage and current.

Simulation

1. Simulation with continuous integration method

Check the simulation parameters in the Simulation -> Configuration Parameters menu. The Variable-step auto (Automatic solver selection) solver is selected. Note that the Simulation type parameter of the Powergui block is not accessible, and it is set to Continuous. Start the simulation and observe waveforms on the four-trace Scope block. Note that Simulink® selected the Ode23tb solver to simulate the model.

After a transient period, the load current Id stabilizes at 12.7 A. Note that most of the third harmonic which can be seen in the rectified voltage Vd (mean value 127 V) is filtered out by the smoothing reactor.

Also observe the commutation period due to the source inductances. The 3.3 degrees overlap is clearly seen on trace 3 of the scope showing currents in diodes 2 and 3.

Zoom on the Diode3 voltage (trace 4) during the conduction period. The voltage magnitude is the sum of the specified forward voltage (0.8 V) and the voltage developed across the diode resistance (0.01 ohm).

2. Simulation with discretized system

Open the Configuration parameters of the model and select the Fixed-step auto (Automatic solver selection) solver. Restart the simulation. Note that the Simulation type parameter of the Powergui is automatically changed to Discrete, with a sample time of 50e-6 usec. Note that Simulink selected the FixedStepDiscrete solver to simulate the model.

Compare waveforms with those obtained with the continuous integration algorithm.

Full-Wave Rectifier

This example shows the operation of a full wave rectifier using ideal diodes.

G. Sybille (Hydro-Quebec)
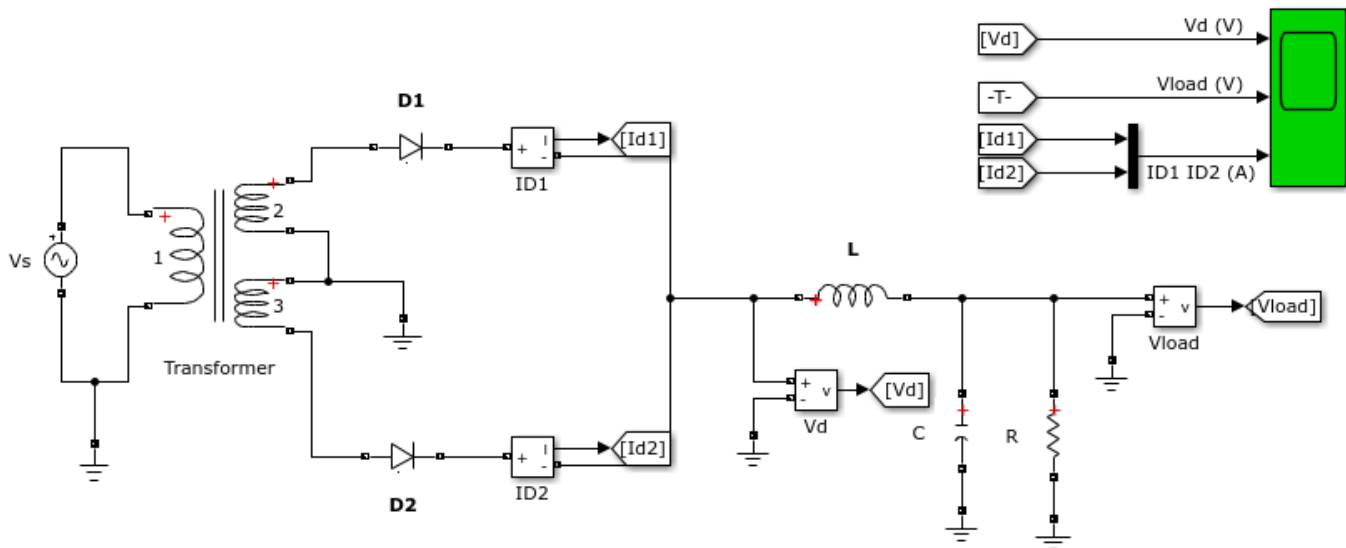
Full-Wave Rectifier
This example shows the operation of a 64 W, 24 Vdc, full-wave rectifier
using ideal diodes.

Learn more about this example.

Description

A convenient way of disabling snubbers of all switches in the model is to select the **Disable snubbers in switching devices** option in powergui Preferences tab. Alternatively, you can individually disable the snubbers of selected switches by specifying Rs = inf in their block menus.

In order to simulate diodes as ideal switches, the **Disable Ron resistances in switching devices** and **Disable forward voltages in switching devices** options in the powergui Preferences tab are also selected.

The elimination of the snubbers reduces the circuit stiffness so that you can use a non-stiff solver such as ode45. This solver produces correct results and good simulation speed.

Current-Controlled Thyristor Rectifier

This example shows the operation of a current-controlled thyristor rectifier.
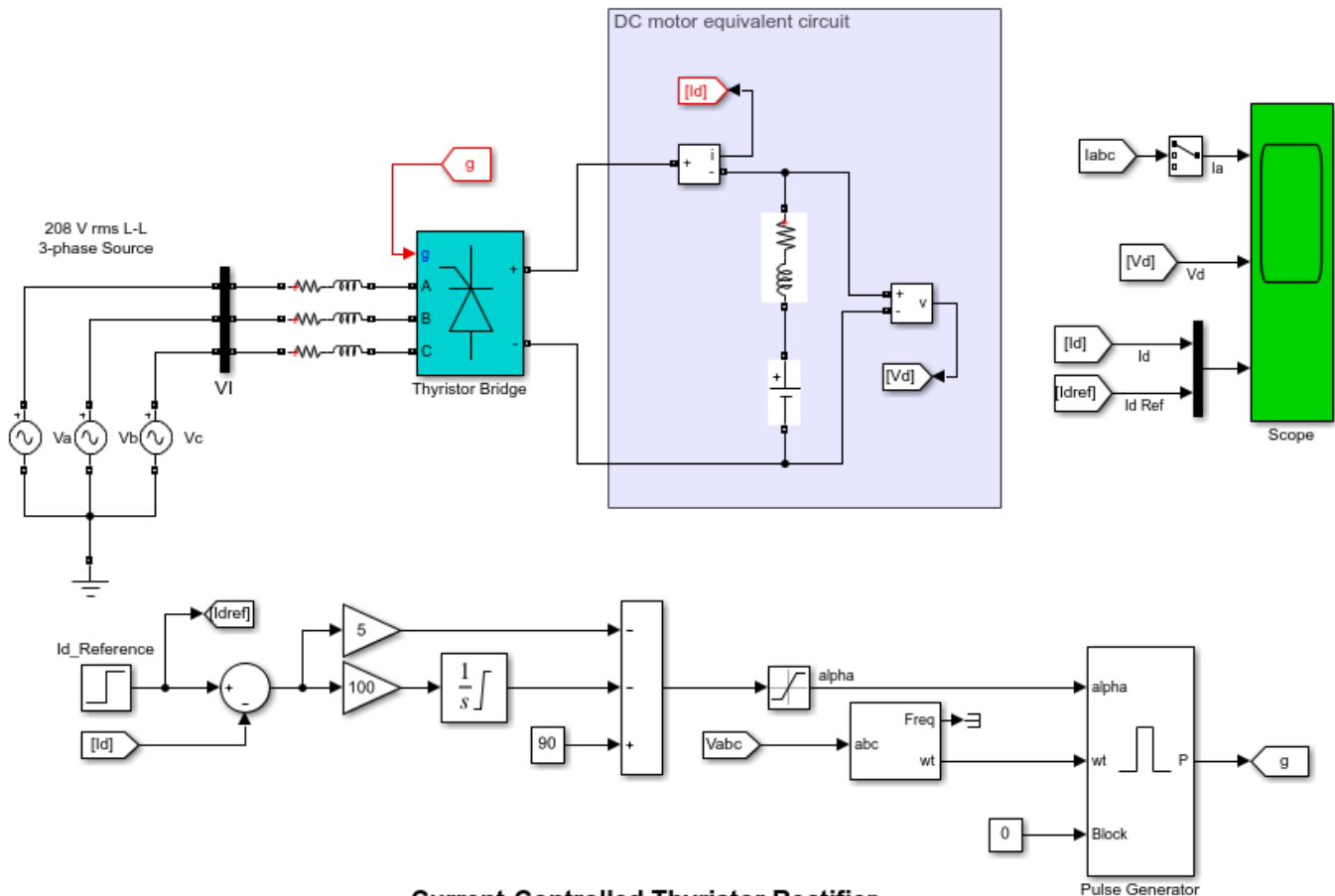
Current-Controlled Thyristor Rectifier

A DC motor represented by a simplified RL-E model is fed from an inductive three-phase source through a six-pulse thyristor bridge. A pulse generator synchronized on the source voltages provides the trigger pulses for the six thyristors.

Learn more about this example.

Description

A DC motor represented by a simplified RL-E model is fed from an inductive three-phase source through a six-pulse thyristor bridge. A pulse generator synchronized on the source voltages provides the trigger pulses for the six thyristors.

The converter output current is controlled by a PI current regulator built with Simulink® blocks. A step signal is applied to the reference input to test the dynamic response of the current regulator.

In order to allow further signal processing, signals displayed on the Scope block are stored in a variable named ScopeData1 (in structure with time format).

Simulation

Start the simulation and observe current and voltage waveforms on the Scope block.

When simulation is completed, open the Powergui and select **FFT Analysis** to display the 0 - 2000 Hz frequency spectrum of signals saved in the ScopeData1 structure. The FFT will be performed on a 2-cycle window starting at t = 0.08 - 2/60 (last 2 cycles of recording). Input labeled Ia should be already selected. Click on Display and observe the frequency spectrum of the last two cycles of Ia. Harmonic currents (6n+/-1) are displayed in % of the fundamental component.

The value of the fundamental current (magnitude = 27.48 A peak) and the Total Harmonic Distortion (THD) are also displayed. Harmonics in the 0 - 2000 Hz range contribute to 29% in THD. If you specify a 0-4000 Hz frequency range, notice that contributions of harmonics above 2000 Hz are negligible in the current THD.

Now select the input labeled Vd and display its spectrum. The DC voltage contains harmonics (6n) and its DC component is 145.4 V.

Zener Diode Regulator

This example shows a model of the zener diode used in a voltage regulator.
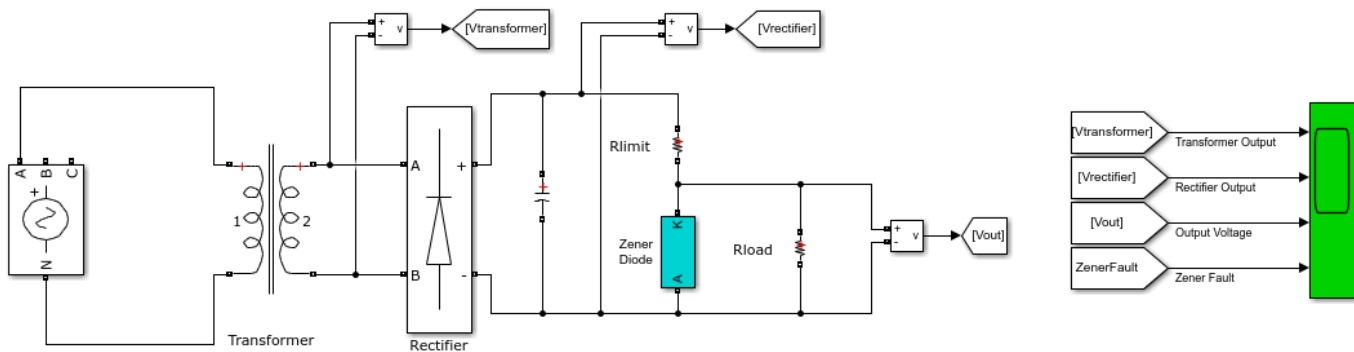
Zener Diode Regulator

This example shows a model of the zener diode used in a voltage regulator and presents a practical implementation that uses parameters commonly provided in datasheets.

Learn more about this example.

Description

The Zener diode block modeled in this example presents a practical implementation that uses parameters commonly provided in datasheets. These parameters are

- (1) Zener Voltage Vz
- (2) Dynamic Impedance Zzt
- (3) Knee Impedance Zzk
- (4) Max Continuous Current Izm
- (5) Forward Voltage Drop Vf
- (6) On Resistance Ron

This block can effectively model three regions of operation of the zener diode I-V characteristics - forward-biased, reverse-biased before breakdown and reverse-biased after breakdown. Beyond the maximum reverse continuous current Izm, the zener is assumed to burn up and is treated as an open circuit.

The implementation of the zener diode can be seen by looking under the mask of the block, and is based on [1].

Zener diodes are commonly employed in applications as voltage regulators. The circuit shows an AC source fed to a step-down transformer. The output of the transformer is then rectified using a diode bridge and smoothened using a capacitive filter. The zener diode then acts to regulate the output voltage to the zener voltage 10V. The input current into the zener is limited by the resistor Rlimit to permissible values.

The programmable voltage source is setup to increase its output voltage at 0.1s. As the source output increases, so does the voltage applied at the input of the zener. However, the zener can regulate the output only as long as its input current is below the maximum specified value. This current increases

as we increase the source voltage and the zener ultimately fails at about 0.112s. With the zener acting as an open-circuit at fault, voltage regulation is lost and the output of the capacitive filter gets applied to the load.

References

1. Wong. S, Hu. C-M, "SPICE macro model for the simulation of zener diode I-V characteristics", Circuits and Devices Magazine, IEEE® Volume 7, Issue 4, Jul 1991 Page(s): 9 - 12, 52

Two- and Three-Level Voltage Source Controllers (VSC)

This example shows the operation of two-level and three-level voltage source controllers.
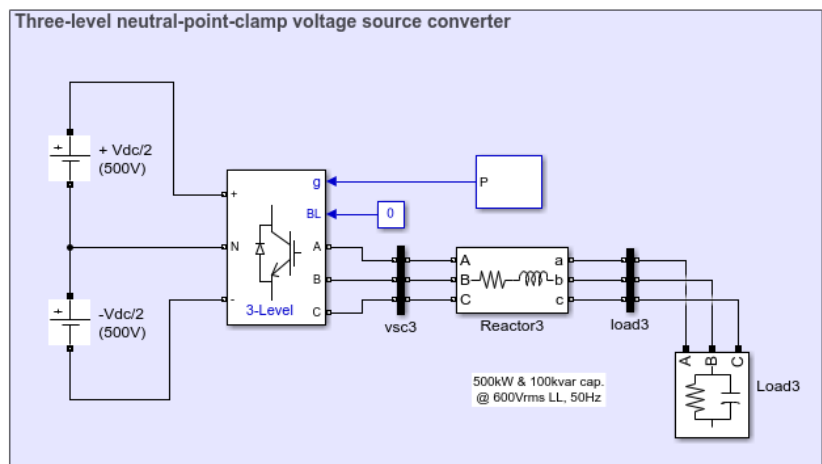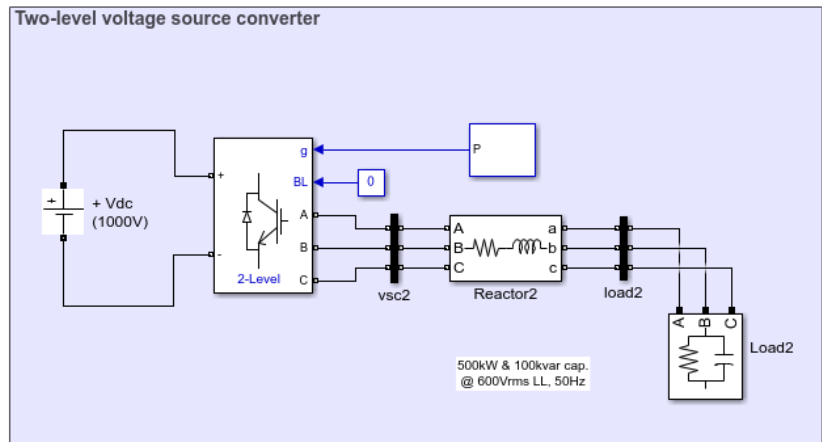
Description

The upper circuit represents a two-level voltage source converter represented by a six pulse IGBT bridge with inverse-parallel diodes. The voltage at the AC terminals of each phase is switched between the positive and negative potential values of the DC terminals.

The lower circuit represents a three-level voltage source converter with neutral-point clamp. This topology can synthesize three voltage levels from positive DC value, zero (neutral), and negative DC value at the AC terminal of each phase. In this neutral-point clamped configuration, the clamping diode valves are replaced by IGBT valves, to allow more control on the generated AC waveforms.

Switching Function Converter Controlled by Averaged Firing Pulses

This example shows the advantages of power electronics converters that can be simulated using one of four selectable modeling techniques

Description

The converter in the circuit 0 is modeled by a switching function controlled by firing pulses produced by a PWM generator operating at 25 microseconds. The circuit 1 also uses a switching function converter and a PWM generator but is sampled at 5 microseconds. The converter in the circuit 2 is modeled by a switching function controlled by firing pulses averaged over a period of 25 microseconds. In this case, the PWM generator is in continuous mode.

Simulation

Run the simulation and look at the simulation results to observe that good performance is obtained with the use of averaged pulses at higher sample time period, when compared to the performance obtained with regular pulses at lower sampling time.

Buck Converter - Increased Accuracy and Simulation Speed Using Interpolation

This example shows how to use the interpolation method on the powergui to preserve model accuracy for simulations with larger time steps.
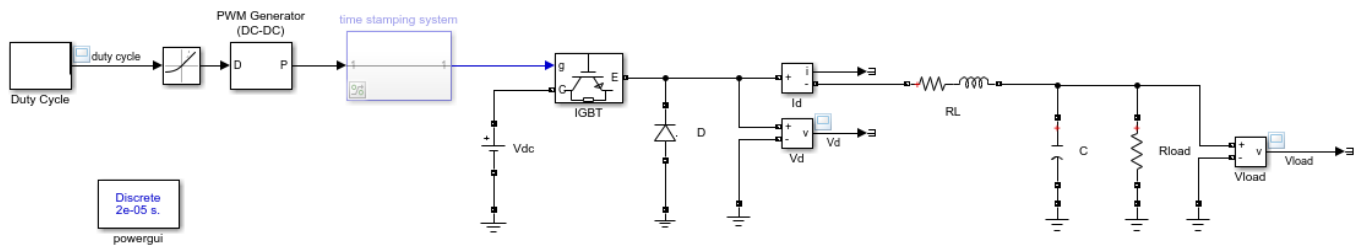
Buck Converter - Increased Accuracy and Simulation Speed Using Interpolation
This example shows how to use the interpolation method on the powergui
to preserve model accuracy for simulations with larger time steps.

Learn more about this example.

Description

This example shows a DC-DC buck converter feeding an RC load from a 200 V source. The PWM frequency is set at 5 kHz and duty cycle varies between 0.1 and 0.8. With this 5 kHz PWM frequency, the sample time needed for a 0.5% resolution on duty cycle using a standard discretization method (Tustin or Backward Euler) is Ts = 1e-6 sec (1 MHz sampling frequency = 200 x PWM_freq -> resolution = 1/200 = 0.5 %). The example shows that using interpolation allows you to run the model with a much larger time step (Ts = 20e-6 sec) while preserving model accuracy. The example also demonstrates the concept of time-stamped gate signals in Specialized Power Systems switching devices.

Simulation

By default, sample time is initialized to 20e-6 sec (inside the Model Properties -> PreloadFcn Callback). Open the powergui block and on the Preferences tab, make sure that the "Discrete solver" parameter is set to Tustin, and that the "Interpolate switching events" option is enabled. Also make sure that the "Use time-stamped gate signals" option is disabled. The Simulation Data Inspector is enabled and the Vload signal is logged.

1) Perform a first simulation with interpolation in service. Note that the subsystem named Time-Stamping System is commented through so that the pulse signal of the PWM Generator block is passed directly to the IGBT block.

2) Now, in the powergui block, enable the "Use time-stamped gate signals" option and uncomment the Time-Stamping System. This subsystem is now computing delays on and delays off for the pulse signal. Three signals (the pulse, the delay on, and the delay off signals) are now passed to the IGBT block. Run the simulation and verify that the simulation results are the same.

3) Now disable interpolation and specify Ts = 1e-6 in the command window. Comment through the Time-Stamping System, and disable the "Use time-stamped gate signals" option. Perform a third simulation run.

4) In the powergui Solver tab, set "Simulation type" to Continuous. Perform a fourth simulation run with the continuous model.

5) Using the Data Inspector, compare the four simulation runs. Vload voltage obtained during the second and third runs (interpolation with Ts = 20e-6 sec and no interpolation with Ts = 1e-6 sec) is very close to the continuous simulation results.

6) Notice that the interpolation solver matches the continuous solver and is even more accurate than the standard discrete solver.

7) Compare simulation speeds of discrete models (interpolation with Ts = 20e-6 sec and no interpolation with Ts = 1e-6 sec). To achieve significant simulation times, increase simulation stop time to 0.5 sec. The Diagnostic Viewer displays the simulation time at the end of each simulation run. The speed increase obtained with the interpolation method is approximately 4X.

Speed Control of a DC Motor Using BJT H-Bridge

This example shows the simulation of an H-bridge used to generate a chopped voltage and to control the speed of a DC motor.

Gilbert Sybille (Hydro-Quebec)
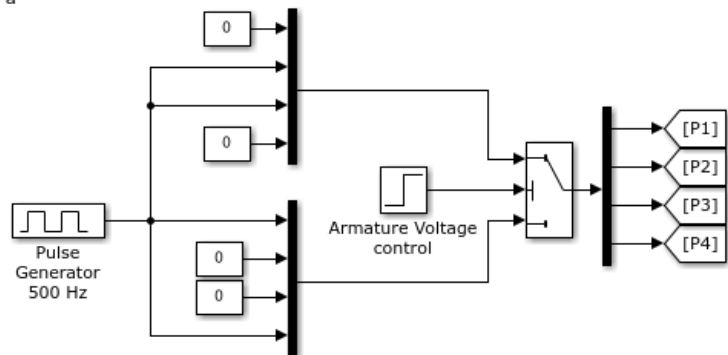
Description

The Bipolar Junction Transistor (BJT) when used for power switching applications, operates as an IGBT. When it is conducting (BJT operating in the saturated region), a forward voltage Vf is developed between collector and emitter (in the range of 1 V). Therefore, the IGBT block can be used to model the BJT device.

The IGBT block does not simulate the gate current controlling the BJT or IGBT. The switch is controlled by a Simulink® signal (1/0). The DC motor uses the preset model (5 HP 24V 1750 rpm). It simulates a fan type load (where Load torque is proportional to square of speed). The armature mean voltage can be varied from 0 to 240 V when the duty cycle (specified in the Pulse Generator block) is varied from 0 to 100%.

The H-bridge consists of four BJT/Diode pairs (BJT simulated by IGBT models). Two transistors are switched simultaneously: Q1 and Q4 or Q2 and Q3. When Q1 and Q4 are fired, a positive voltage is applied to the motor and diodes D2-D3 operate as free-wheeling diodes when Q1 and Q4 are switched off. When Q2 and Q3 are fired, a negative voltage is applied to the motor and diodes D1-D4 operate as free-wheeling diodes when Q2 and Q3 are switched off.

Simulation

The motor starts in the positive direction with a duty cycle of 75% (mean DC voltage of 180V). At t= 0.5 sec., the armature voltage is suddenly reversed and the motor runs in the negative direction.

'Scope' shows motor speed, armature current and load torque and 'Currents' shows currents flowing in BJT Q3 and diode D3.

Inductive Current Chopping

This example shows the effect of current chopping in an inductive circuit.

G. Sybille (Hydro-Quebec)
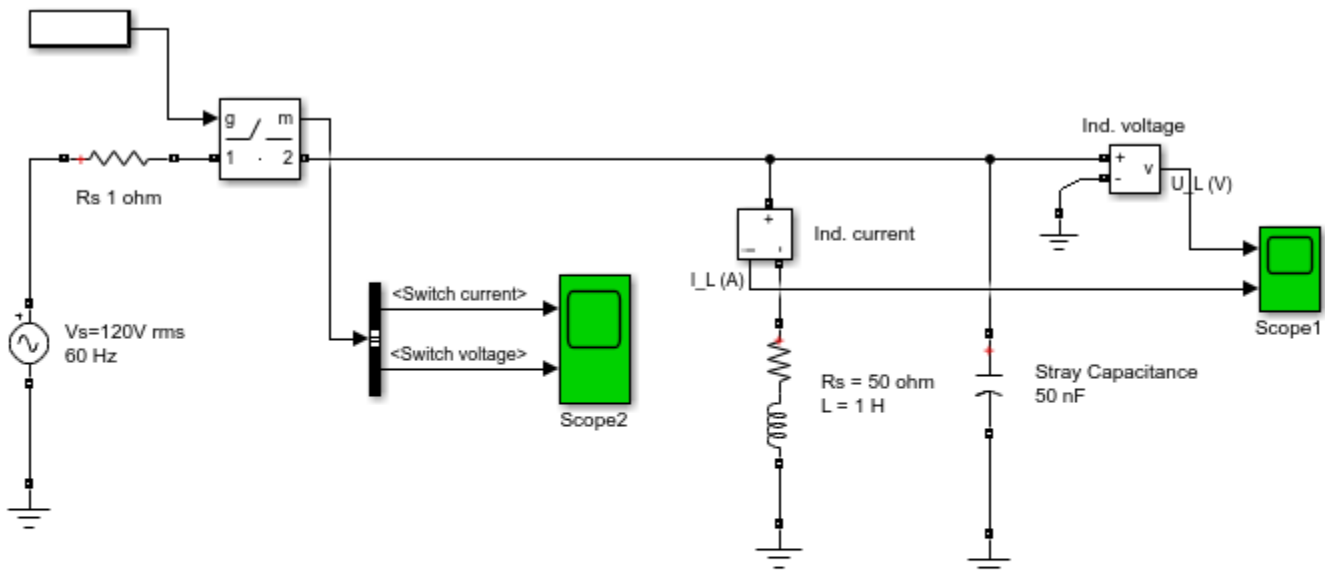
Inductive Current Chopping

This example shows the effect of current chopping in an inductive circuit.

Learn more about this example.

Description

The Ideal Switch is used to switch off and on an inductor (L = 1H, R = 50 ohms) on a 120 Vrms, 60 Hz source. The inductor has a stray capacitance C = 50 nF. The switch resistance is 1e-3 ohm. The switch is initially closed (initial state = 1). It is open at time t = 36ms and then reclosed at time t = 0.15 s.

Simulation

Start the simulation and observe the inductor voltage U_L and inductor current I_L on Scope1. At the opening time, t = 36 ms, the current in the inductor is I_L= 0.187 A. The current chopping in the inductor produces a high frequency overvoltage (711 Hz) across the inductor. The maximum voltage can be calculated from the following equation:

    U_Lmax = I_L*sqrt(L/C) = 0.187*sqrt(1/50e-9) = 836 V

At the reclosing time, t = 0.15 s, the source voltage is zero. Therefore, a full offset is observed in the inductor current. The switch current spike observed on trace 1 of Scope2 at switch reclosing (t = 0.15 s) is due to the capacitor discharge through the source resistance. If you zoom on this current

spike you will observe a current step of 30 A (30V capacitor voltage / 1 ohm source resistance), followed by a fast discharge time constant (RC = 1*50e-9 = 50e-9 s).

Multilevel Multiphase Space-Vector PWM

This example shows the operation of the multilevel multiphase SVPWM and two-level multiphase SVPWM.

Oscar Lopez, Jacobo Alvarez, Jesus Doval-Gandoy and Francisco Freijedo, Electronics Technology Department University of Vigo, Spain. olopez@uvigo.es
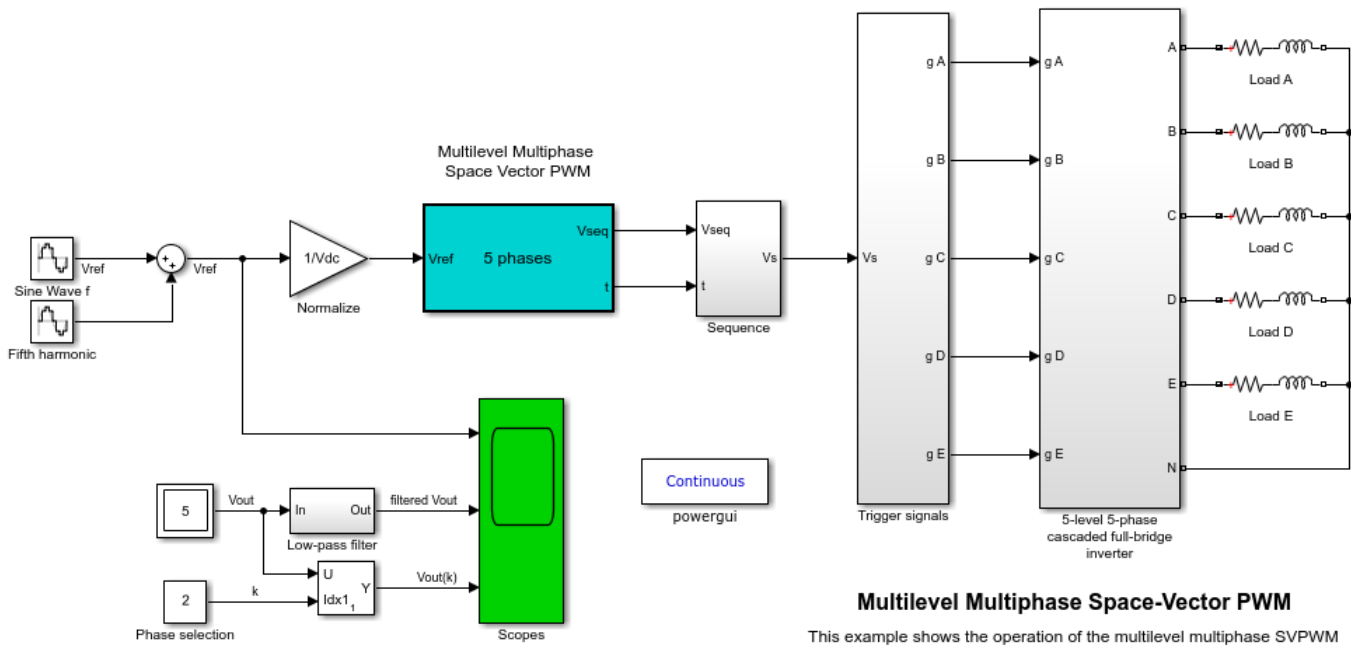
Multilevel Multiphase Space-Vector PWM

This example shows the operation of the multilevel multiphase SVPWM and two-level multiphase SVPWM.

Learn more about this example.

Description

This example shows the operation of the Multilevel multiphase SVPWM and Two-level multiphase SVPWM. It includes a five-level five-phase inverter feeding a passive load. Detailed information about the modulation algorithm, its Simulink® implementation, and the simulated case can be found in [1].

1. The 'Sine Wave f' and 'Fifth harmonic' blocks generate an unbalanced five-phase reference voltage with a fifth harmonic.

2. The 'Normalize' block normalize the reference voltage

3. The 'Multilevel multiphase SVPWM' performs the multilevel multiphase space-vector PWM (SVPWM) algorithm presented in [1]. This block makes use of the block 'Two-level multiphase SVPWM', also described in [1], that can be used alone with two-level multiphase converters. Both blocks require the specification of the number of phases and their outputs are a matrix with the switching vector sequence and a vector with the switching times.

4. The 'Sequence' block provides the time sequence of switching vectors.

5. The 'Trigger signals' generates the proper trigger signals from output levels specified in each switching vector.

6. The '5-level 5-phase cascaded full-bridge inverter' block is the ideal model of the multilevel voltage-source converter.

7. The loads are resistances with a series connected inductances. Load neutral is connected to the inverter neutral.

Simulation

The following signals can be observed:

'Vref' is the sampled voltage reference for each phase.

'filtered Vout' is the output voltage of 'Vout' phase after been filtered with a low-pass filter.

'Vout(k)' is the switched output voltage of phase 'k'. The phase shown in the scope can be selected by means of the 'Phase selection' block.

The filtered output voltage 'filtered Vout' of the converter follows the reference voltage 'Vref'. Therefore, the modulation is working properly.

The output voltage of the inverter 'Vout' has five levels in phases b, c, and d (k=2,3,4) and three levels in phase a and e (k=1,5).

References

[1] Oscar Lopez, Jacobo Alvarez, Jesus Doval-Gandoy and Francisco D. Freijedo "Multilevel Multiphase Space Vector PWM algorithm", IEEE® Transactions on Industrial Electronics, vol. 55, no. 5, pp. 1933-1942, May 2008, doi:10.1109/TIE.2008.918466.

Two-Level PWM Converter and Dead Time

This example shows the impact of dead time on harmonic distortion of a two-level converter used in a 50 kW microturbine.

Harmonic distortion of B2 voltage as function of dead time at nominal output power 50kW PF=1.0; Vdc=900 V

Dead Time (us)	THD (%)	H5 (% of fund.)	H7	H11	15kHz+/-k*60Hz (% of fund.)	Modulation index
0	0.75	0.03	0.03	0.02	0.42	0.87
1	0.85	0.29	0.20	0.10	0.44	0.91
2	1.09	0.57	0.37	0.23	0.45	0.95
3	1.41	0.86	0.55	0.36	0.47	0.98
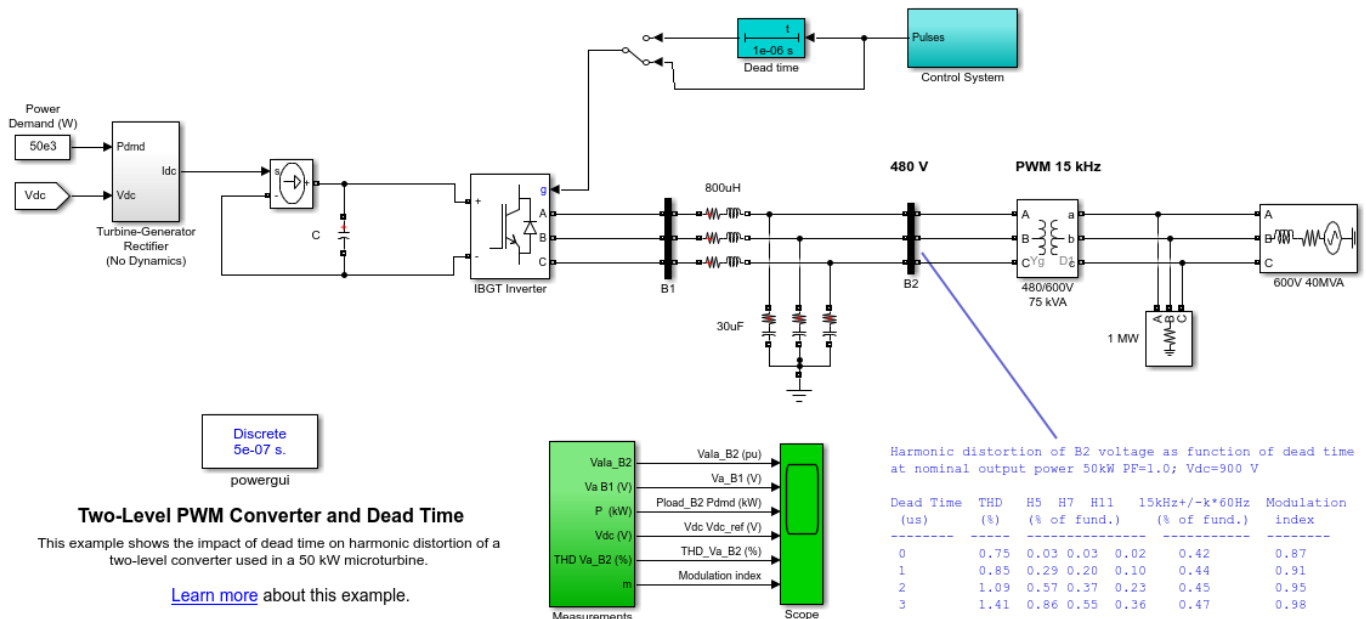
Description

The circuit represents the DC to AC conversion unit of a 480 V, 50 kW microturbine connected to a 600 V power grid. As the purpose of this example is to illustrate the high frequency harmonics generated by the inverter, the slow dynamics of the gas-fired turbine and permanent magnet generator are not represented. The Turbine-Generator-Rectifier group is modeled as a simple DC current source injecting the requested DC power into the DC bus. The inverter uses a PWM, two-level IGBT converter (using the Universal Bridge block). The SPWM modulator uses a carrier frequency of 15 kHz. The control system uses two regulators: an inner current loop controlling the current at bus B2 and an outer DC voltage regulator controlling the DC bus voltage. LC filters are used to reduce harmonic voltages produced at the 480 V bus B2 (harmonic frequencies around multiples of 15 kHz). Note that 1 ohm resistances are connected in series with the 30 uF capacitors of the shunt filters. These resistances are used to damp non characteristic low frequency harmonics resulting from the resonance introduced by the LC filters (around 1.9 kHz).

In a two-level voltage-sourced converter (VSC) using ideal switches, the two pulses sent to the upper and lower IGBT of each arm could be complementary. However, in practical VSCs the turn-off of semiconductor switches is delayed because of the storage effect. Therefore, a time delay of a few microseconds (storage time + safety margin) is required to allow complete extinction of the IGBT which is switched off before switching on the other IGBT. Otherwise, a short-circuit could result on the DC bus. The dead time is modeled by introducing a On/Off Delay block at the pulse input of the converter block. The delay specified in that block is applied on the rising edges of pulses.

In order to get an acceptable accuracy with a 15 kHz switching frequency, a sample time Ts_Power= 0.5 microseconds is used to discretize the circuit. The discrete control system uses a much larger sample time (Ts_Control=50 microseconds)

Simulation

1. Simulation with zero dead time

Start the simulation and observe voltages, current, power and control signals on the Scope block. The simulation has been automatically initialized to start in steady state. The quadrature-axis current Iq_ref is set to zero inside the current controller in order to generate power at unity power factor. Therefore, voltage and current at bus B2 (trace 1) are in phase. The DC voltage (trace 4) is regulated at 900 V. On trace 3, you can observe the power demand (set at nominal power of 50 kW) and the measured output power at bus B2 (49 kW, because of 1 kW losses in converter and filters).

Once the simulation is completed, open the Powergui and select "FFT Analysis" to display the frequency spectrum of signals saved in the ScopeData structure (variable specified in the Scope block). Make sure that input VaIa_B2 (pu), and signal number 1 are selected. The FFT will be performed on the last cycle of phase A voltage at bus B2. Then click Display to observe the 0-50000 Hz frequency spectrum. As expected, harmonics are observed mainly around multiples of the switching frequency (15 kHz). The Total Harmonic Distortion (THD) is displayed above the spectrum (THD=0.75%). THD is also measured during simulation by the "Discrete THD" block (trace 5 of Scope1)

2. Impact of dead time on harmonic distortion

Now move the manual switch to its upper position to apply a dead time of 1 microsecond to the firing pulses. Repeat simulation and FFT analysis for dead times up to 3 microseconds. You will note an increase of THD when the dead time is increased. Results from frequency analysis are summarized in the table shown on the model for dead times comprised between zero and 3 microseconds. This table indicates that the amplitude of the main characteristic frequencies (around 15 kHz) does not vary significantly when the dead time is increased. The THD increase is caused mainly by the introduction of low frequency harmonics (mainly 5th, 7th and 11th). For a 3 us dead time the THD has increases from 0.74 % to 1.75 %. The table also indicates the modulation index increases when the dead time increases, indicating a less efficient use of the DC voltage. With a 900 V DC voltage, if dead time would be increased above 3 us, the modulation index would be higher than 1and additional distortion would be introduced because of overmodulation. You can check harmonic distortion for different power outputs and DC operating voltages by specifying new values in the "Power Demand" constant block and in the "Vdc_ref" constant block (inside the Control System).

Regenerate Initial Conditions

The initial states required to start this model in steady state have been saved in the "power_microturbineDT.mat" file. When you open this model, the InitFcn callback (in the Model Properties/Callbacks) automatically loads into your workspace the contents of this .mat file ("xInitial" variable).

If you modify this model, or change parameter values of power components, the initial conditions stored in the "xInitial" variable will no longer be valid and Simulink® will issue an error message. To regenerate the initial conditions for your modified model, follow the steps listed below:

1. In the Simulation/Configuration Parameters/Data Import/Export Parameters menu, uncheck the "Initial state" parameter.

2. Change the Simulation Stop Time to 1 second. Note that in order to generate initial conditions coherent with the 60 Hz voltage source phase angle, the Stop Time must an integer number of 60 Hz cycles.

3. Change the Simulation Mode from "Normal" to "Accelerator".

4. Start simulation. When simulation is completed, verify that steady state has been reached by looking at waveforms displayed on the scope. The final states which have been saved in the "xFinal" structure with time can be used as initial states for future simulations. Executing the next two commands copies these final conditions in "xInitial" and saves this variable in a new file (myModel_init.mat).

```
>> xInitial=xFinal;
```

```
>> save myModel_init xInitial
```

5. In the File-> Model Properties -> Callbacks -> InitFcn window, change the name of the initialization file from "power_microturbineDT.mat" to "myModel_init.mat". Next time you open this model, the variable xInitial saved in the myModel_init.mat file will be loaded in your workspace.
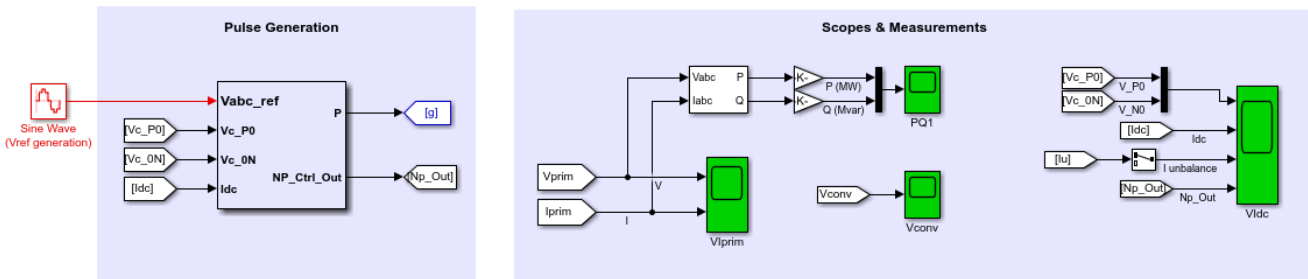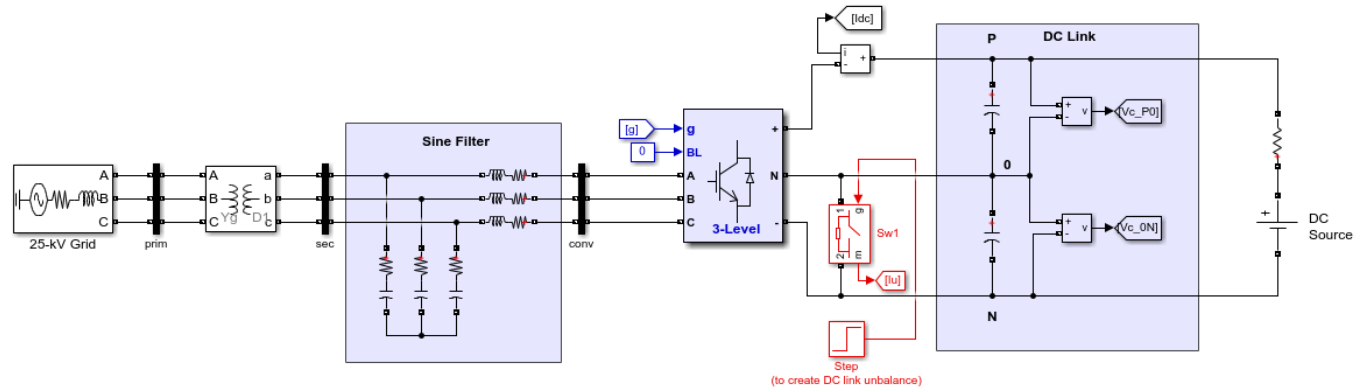
6. In the Simulation -> Configuration Parameters menu, check "Initial state".

7. Start the simulation and verify that your model starts in steady-state.

8. Change the Simulation Stop Time and Simulation Mode back to their original values (0.2 seconds, Normal).

9. Save your model.

Neutral Point Clamp Inverter and Dead Time

This example shows the impact of dead time and semiconductor failure inside a three-level PWM converter.
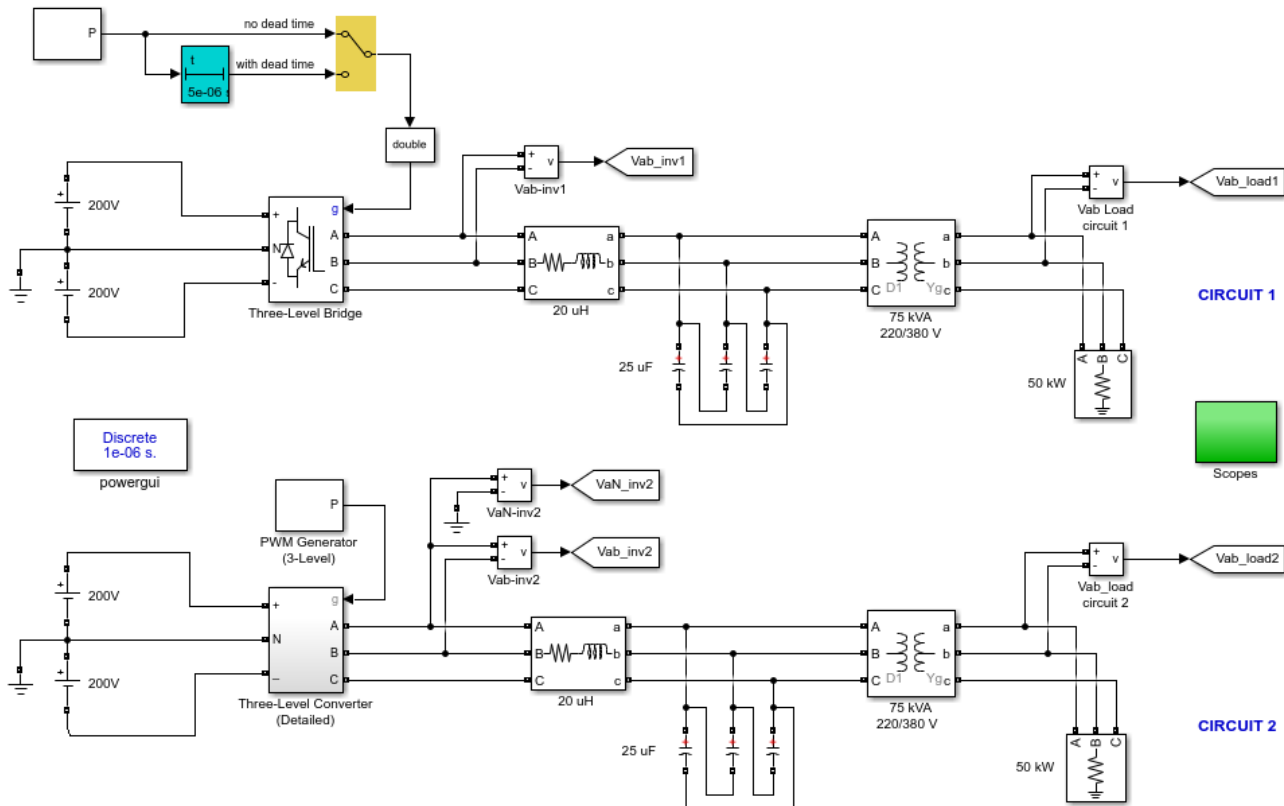
Neutral Point Clamp Inverter and Dead Time

This example shows dead time and semiconductor failure inside a 50 kW, 380 V, 50 Hz, three-phase, three-level PWM converter.

Learn more about this example.

Harmonic Distortion of Load Voltage

Dead time (us)	Vab (Vrms)	THD (%)	H5 (%)	H7 (%)	8 kHz (%)
0	380	1.27	0.21	0.13	0.74
5	359	1.99	1.25	0.73	0.81

Description

This model represents two identical circuits modeling a 50kW, 380V, 50Hz, three-phase, three-level inverter. The IGBT inverter uses SPWM technique, (8kHz carrier frequency) to convert DC power from a +/-200Vdc source to 220V AC, 50Hz. The inverter feeds a 50kW resistive load through a 75kVA 220/380V transformer. L-C filters are used at the converter output to filter out harmonic frequencies Fh generated mainly around multiples of the 8kHz switching frequency (Fh=n*8000 +/- k*50Hz). The 12 inverter pulses required by the inverter are generated by the PWM Generator block. The system operates in open loop at a constant modulation index.

Circuit 1 uses the Three-Level Bridge block to model the inverter. Circuit 2 uses individual IGBT and diode blocks. The dead time is simulated with the On/Off Delay block and it is applied on the circuit 1 only.

Simulation

Comparing simulation results with the Three-Level Bridge block and the user-built converter - no dead time

1. Set the Manual Switch block to its upper position to disable dead time effect on circuit 1. 2. Start the simulation and observe the inverter and load voltages of circuit 1 and circuit 2 which are superimposed on Scope1. 3. Once the simulation is completed, open the Powergui and select FFT Analysis to display the frequency spectrum of signals saved in the ScopeData1 structure. 4. The FFT will be performed on the last cycle of the selected signal. You can analyze either inverter voltages (select input Vab_inv_1_2) or load voltages (select input Vab_load_1_2). Select Signal number 1 or Signal number 2 to analyze voltages of circuit 1 or circuit 2 and click on Display to observe the 0-25000 Hz frequency spectrum.

As expected, at the inverter output, harmonics are observed around multiples of the switching frequency (8 kHz). At the load terminals, these high frequency harmonics are considerably reduced by LC filters. Also, note that the LC filters creates a resonance which produces additional harmonics around 4.4 kHz. The fundamental component and Total Harmonic Distortion (THD) are displayed above the spectrum graph. For both circuits THD is 1.27% on the load side and fundamental voltage is 380 V rms (537.6 V peak).

Impact of dead time on fundamental voltage and harmonic distortion

In a three-level voltage-sourced converter (VSC) using ideal switches, the two pairs of pulses sent to each arm could be complementary. For example, for phase A, IGBT1 is complementary of IGBT3 and IGBT2 is complementary of IGBT4. However, in practical VSCs the turn-off of semiconductor switches is delayed because of the storage effect. Therefore, a time delay of a few microseconds (storage time + safety margin) is required to allow complete extinction of the IGBT which is switched off before switching on the other IGBT. Otherwise, a short-circuit could result on the DC bus.

5. Set the Manual Switch block to its lower position. 6. Run the simulation and compare the two load voltage waveforms on Scope1 and their fundamental values on the two Display blocks. 7. Do the FFT analysis with the Powergui block. You will note that the dead time decreases the fundamental component and increases distortion.

Results from frequency analysis are summarized in the table shown in the model. This table indicates that the amplitude of the main characteristic frequencies (around 8 kHz) increases slightly (from 0.74% to 0.82 %) when the dead time is used. The THD increase is caused mainly by the introduction of non characteristic low frequency harmonics (mainly 5th and 7th).

Simulating a diode failure fault inside the three-level converter

8. Set the manual switch to its upper position to disable the dead time and for the two inverters to generate identical waveforms. 9. Open the Three-Level Converter (Detailed) subsystem. Note that an ideal switch block labeled D5 Open is connected in series with the neutral clamping diode D5 of phase A. 10. Open the Stair Generator block menu and in the Time parameter, change the 100 multiplier to 1. The block will now order a switch opening at t=0.25 s, thus simulating a diode failure in open circuit. 11. Start simulation and compare circuit 1 and circuit 2 waveforms on Scope1.

Diode D5 current and VaN voltage of inverter 2 are displayed on Scope2. When D5 fails open, its clamping effect to neutral is disabled and VaN voltage oscillates between +200V and -200 V (instead of +200V and zero). This unsymmetrical operation of phase A inverter arm introduces low frequency, odd and even harmonics.

Synchronous Buck Converter

This example shows a synchronous buck converter.

P. Giroux (Hydro-Quebec)
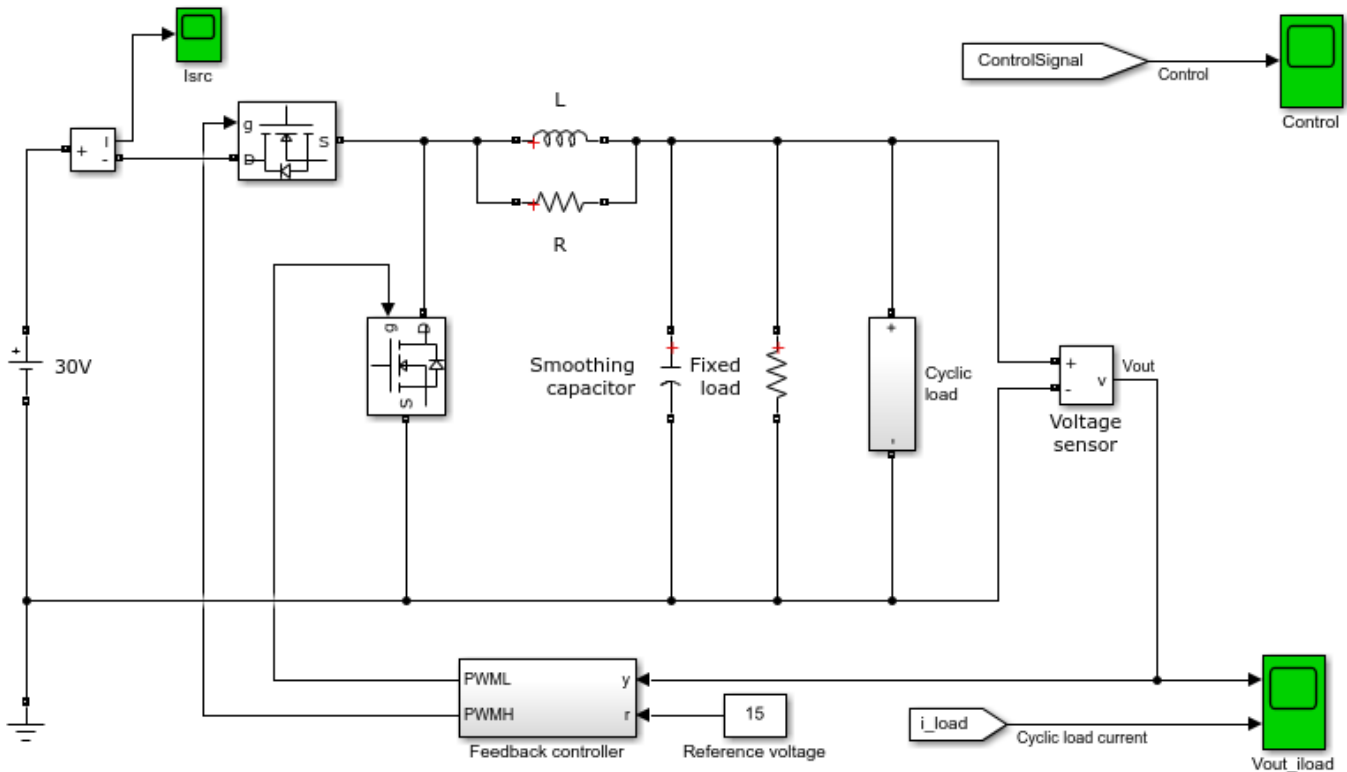
Synchronous Buck Converter
This example shows a synchronous buck converter where in the switched
power supply converts a 30V DC supply into a regulated 15V DC supply.

Learn more about this example.

Description

This switched power supply converts a 30V DC supply into a regulated 15V DC supply. The model can be used to size the inductance L and smoothing capacitor C, as well as to design the feedback controller. By selecting between continuous, discrete time, and fixed-point PI controllers, the impact of discretization and using limited numerical precision can be explored.

Refer to the elec_switching_power_supply example model for a detailed version that uses N-Channel MOSFETS to capture the switch-on/switch-off timing of the devices, this depending primarily on the gate capacitance values and the PWM driver output resistance.

Loss Calculation in a Three-Phase 3-Level Inverter

This example shows how to compute switching losses in a three-phase 3-level inverter, combining Specialized Power Systems and Simscape blocks.

General

From a +/- 1800 volts DC source, a 400-kW, three-phase 3-level inverter delivers variable power to a distribution power system. The inverter output is connected to the 25-kV, 40 MVA, 50-Hz system through a 2200 V / 25 kV transformer. The inverter topology is based on the model described in [1]. Each 3-level leg of the inverter comprises three commercial half-bridge IGBT modules. The IGBT pulsing of module 3 is not required since only the anti-parallel diodes are operating as neutral clamping diodes. The control system contains two PI controllers (one PQ regulator and one current regulator) to generate the required inverter pulses to achieve the reference output power.

The Phase-A leg is implemented using three Half-bridge IGBT with Loss Calculation blocks. Both switching and conduction losses are calculated and injected into a thermal network. The simulation illustrates the achievable output power versus switching frequency for the three-phase, 3-level inverter.

The Half-bridge IGBT With Loss Calculation Block

The half-bridge is modeled by two IGBT/Diode blocks. The upper and lower IGBT/Diode blocks are pulsed from an external pulse generator. The loss calculations are based on the specifications found on the manufacturer's data sheets.

The IGBTs losses are computed as follows:

- Turn-on loss: Pre-switching value of the voltage across the device, post-switching value of the current flowing into the device, and the junction temperature are used to determine the energy losses with the help of a 3-D lookup table. This energy is converted into a power pulse which is injected into the thermal network.

- Turn-off loss: Pre-switching value of the current flowing into the device, post-switching value of the voltage across the device, and the junction temperature are used to determine the energy losses with the help of a 3-D lookup table. This energy is converted into a power pulse which is injected into the thermal network.

- Conduction loss: Value of the current (Ic) flowing in the device and its junction temperature determine what would be the saturation voltage (Vce) across the IGBT using a 2-D look-up table. This Vce is then multiplied by Ic to obtain the losses which are injected into the thermal network.

The diode's losses are computed as follows:

- Reverse recovery loss: Pre-switching value of the current flowing into the device, post switching value of the voltage across the device, and the junction temperature are used to determine the energy losses with the help of a 3-D lookup table. This energy is converted into a power pulse which is injected into the thermal network.

- Conduction loss: Value of the current (If) flowing in the device and its junction temperature determine what would be the on-state voltage (Vf) across the diode using a 2-D look-up table. This Vf is then multiplied by If to obtain the losses which are injected into the thermal network.

The thermal capacitance of the device junction as well as its junction-to-case thermal resistance are represented by a one-cell Cauer network modeled with a Simulink® State-space block.

The Thermal Network

Simscape blocks from the thermal foundation library are used to build a two-cell Cauer network based on the thermal capacitances (case and heat sink) and resistances (case-to-sink and sink-to-ambient). For the sake of simplicity, the two-cell Cauer network uses arbitrary values for the thermal capacitances in order to reduce the time required to simulate the thermal phenomena.

Demonstration

Run the simulation and observe the following operating points:

- From t=0 sec to t=5 sec: the inverter outputs 372 kW (power factor = 0.85) using a switching frequency of 850 Hz. The converter total losses are 2.7 kW and the highest junction temperature (125 C) is observed on IGBT1 of Module 1 (or IGBT2 of Module 2). See Tj(Celsius) Scope block inside the Additional Scopes & Measurements block.

- From t=5 sec to t=12 sec, the inverter outputs 210 kW (power factor = 0.85) using a switching frequency of 1850 Hz. The converter total losses are 2.7 kW and the highest junction temperature (125 C) is still observed on IGBT1 of Module 1.

Reference

[1] Raffael Schnell, Manager Application, ABB Switzerland, "High-Voltage Phase-Leg Modules for Medium Voltage Drives and Inverters".

Modular Multi-Level Converter (MMC)

This example shows the operation of a Modular Multi-Level Converter (MMC).
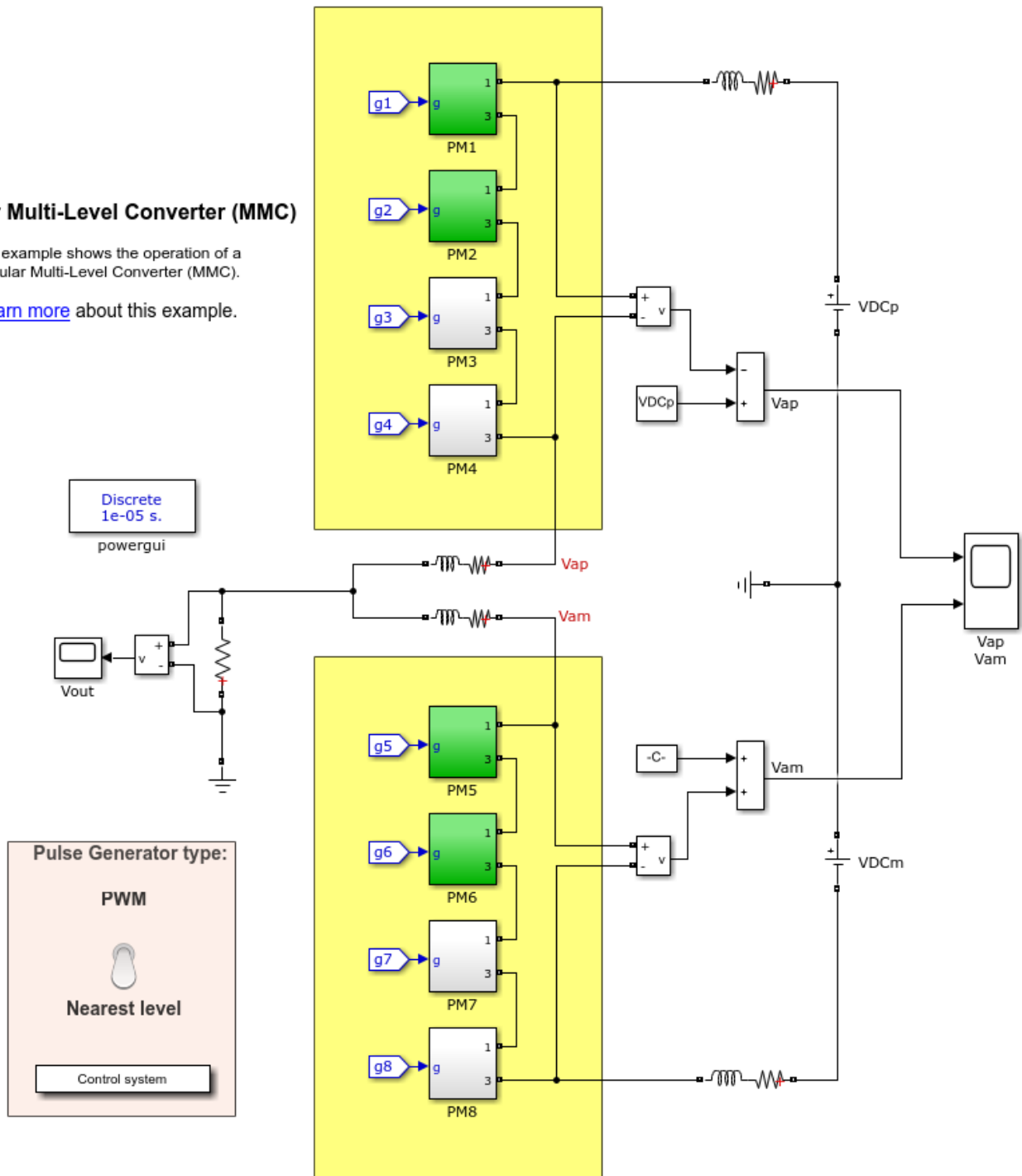
Description

This example uses the half-bridge arm blocks to model an MMC consisting of eight power modules. The control system allows you to choose between two types of pulse generators - PWM and Nearest level. You can toggle the switch to see the impact on the output signals of the converter.

The use of MATLAB® functions within PM1 to PM8 subsystems allows coloring the modules in red when they are activated during the simulation. The use of these functions also allows us to slow down Simulink® in order to observe the activation and deactivation of the modules.

Note that only one half-bridge block can be used to model the positive half-arm and/or negative half-arms of the converter.

Power Converters Modeling Techniques

This example shows the operation of several types of power electronics converters that can be simulated using one of four selectable modeling techniques
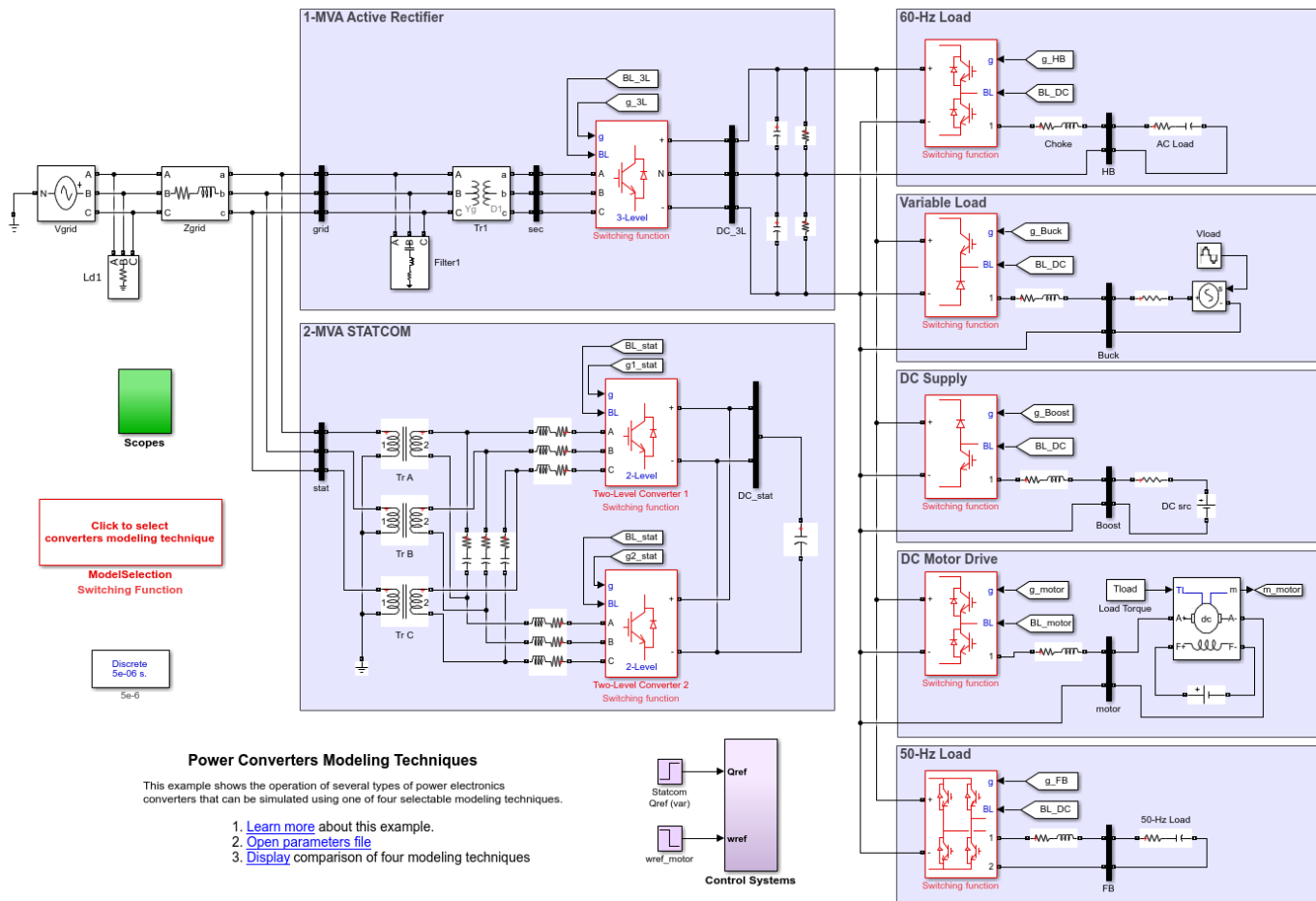
Modeling Techniques Description

You can run this simulation using one of the following modeling techniques:

1. Switching Devices: Converters are modeled using standard SPS power switches and diodes controlled by firing pulses which are produced by the PWM generators.

2. Switching Function: Converters are modeled using a switching-function model controlled by firing pulses which are produced by the PWM generators.

3. Switching Function (PWM averaging): Converters are modeled using a switching-function model controlled by averaging the firing pulses produced by the PWM generators over a specified period.

4. Reference-Voltage (Uref or D-Controlled): Converters are modeled using a switching-function model directly controlled by the reference voltage (Uref) or the duty-cycle (D). PWM generators are not required.

Technique 1 is the most accurate modeling technique, while technique 4 yields to the fastest simulation. Techniques 2 and 3 are well-suited for real-time simulation.

Power Converters Description

The simulation allows you to observe operation of several types of power electronics converters:

1-MVA Active Rectifier: This active rectifier will produce the main DC supply (+/-500V) used by several other converters. The rectifier consists of a three-phase, 3-level NPC converter and a closed-loop control system. It can take or give back power to the grid in order to maintain the specified DC level.

60-Hz Load: The load is modeled using a half-bridge converter controlled by a PWM generator having a carrier frequency of 33*60.

DC Variable Load: The load variation is achieved using a buck converter and a variable DC source at the converter output.

DC Supply: A boost converter transfers power (125 kW) from a 500V DC source to the main DC supply.

DC Motor Drive: The drive consists of a speed-regulated 200-HP motor, a Two-Quadrant DC-DC Converter and a control system.

50-Hz Load: The load is modeled using a full-bridge converter controlled by a PWM generator having a carrier frequency of 1650 Hz and a modulation index of 0.9.

2-MVA STATCOM: This distribution STATCOM consists of two three-phase, 2-level converters (twin topology) and a closed-loop control system. It can generate or absorb 2 Mvars from the grid.

Simulation

During the simulation, the DC variable load will vary from 125 kW to 350 kW at 5 Hz. At 0.5 s, the DC motor speed setpoint is changed from 1200 to 800 RPM. At 0.6 s, the STATCOM reference (Qref) is changed from -1 Mvar to +1.5 Mvar. Run the simulation and observe the resulting signals on the various scopes. Select a different modeling technique and rerun the simulation, comparing results with previous runs.

STATCOM (Detailed MMC Model with 22 Power Modules per Phase)

This example shows a 12 MVA, 34.5 kV Static Synchronous Compensator using 22 power modules per phase

Pierre Giroux (Hydro-Quebec)
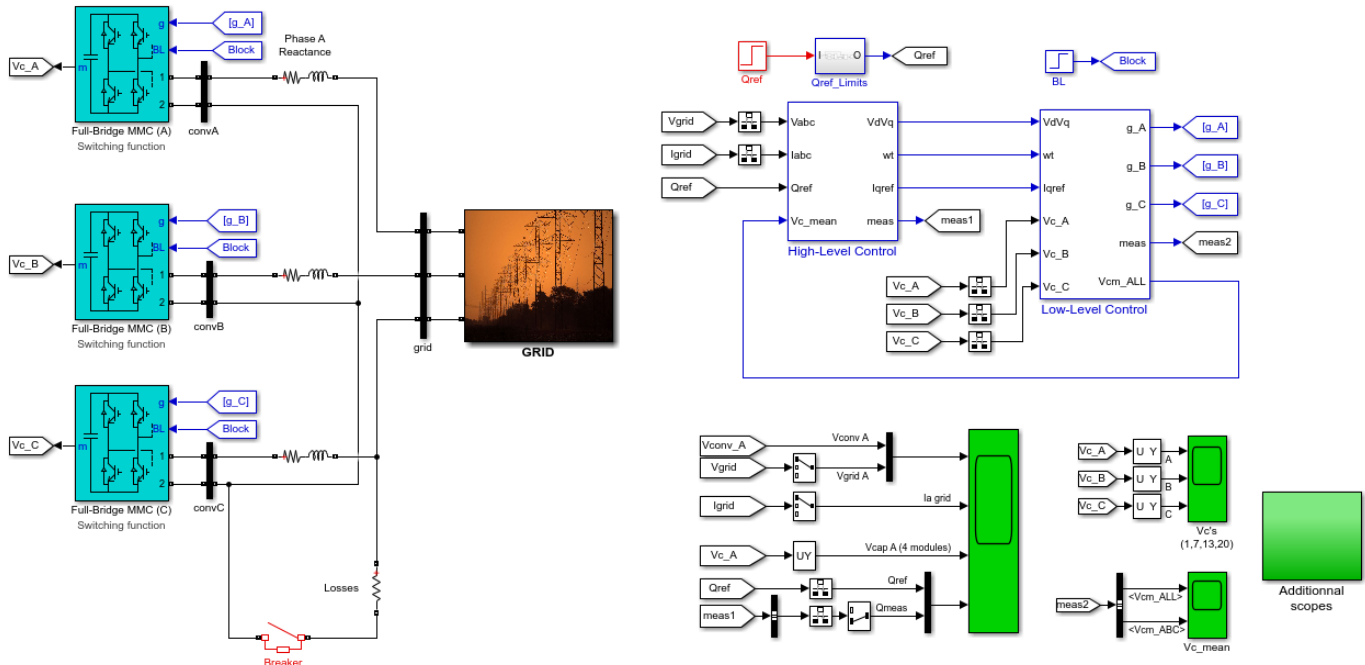
STATCOM (Detailed MMC Model with 22 Power Modules per Phase)

This example shows a 12 MVA, 34.5 kV Static Synchronous Compensator using 22 power modules per phase.

Learn more about this example.

Description

In electrical grids, shunt compensation is often used for reactive power and voltage control. This example models a shunt compensation device that is increasingly used in modern grids: the Modular-Multi-level (MMC) STATCOM. The MMC-STATCOM is built using the Full-Bridge MMC block to represent a power electronics converter of 22 modules per phase. In order to speed-up simulation while keeping simulation fidelity, the Switching Function model is selected.

Principle of Operation

The STATCOM can absorb or generate reactive power. The reactive power transfer is done through the phase reactance. The converter generates a voltage in phase with the grid voltage. When the amplitude of the converter voltage is lower than that of the grid voltage, the STATCOM acts like an inductance absorbing reactive power. When the amplitude of the converter voltage is higher than that of the bus voltage, the STATCOM acts like a capacitor generating reactive power.

Simulation 1: Dynamic Response

Run the simulation and observe waveforms on Scope1. You can see that the simulation starts in steady state and that the STATCOM operates in inductive mode following its set-point Qref (-5 Mvar). At 0.1 second, the set-point changes from -5 to +10 Mvar. The STATCOM control system reacts very rapidly to modify the inverter output voltage in order to generate 10 Mvar of reactive power (capacitive mode).

Simulation 2: Low-Level Control - Capacitor Voltage Balancing

In order to analyze the operation of the low-level control (DC voltage balancing) run the simulation with the following changes: 1. Set the simulation time to 2 seconds. 2. Set the initial value of Qref (red Step block) to 10e6. With this modification, the setpoint will not change during the simulation. 3. Set the Switching time of the red Breaker block to 1 second. This will produce a power unbalance at 1s by switching-in a small resistive load on phase C only. 4. Run the simulation and observe some of the capacitor voltages on the Vc's & Vc_mean Scopes, located inside the green Additional scopes subsystem. You can see that the low-level control system performs well to maintain the capacitor voltages balanced. 5. Double-click on the blue Low-Level Control block and turn-off the Phase Balancing regulator of the DC voltage balancing system. Run the simulation and notice that the STATCOM DC voltages balance between phases is lost when the load is switched-in on phase C. Note: The individual capacitor voltages balancing control generates a small voltage in phase/180 degrees phase-shift with the current flowing into the arm in order to produce/absorb active power from each capacitor. This control system will then not work properly if the current (capacitive or inductive) flowing into the capacitors is too low to produce sufficient active power to charge/discharge them. For this reason, the Qref_Limits subsystem does not permit Qref values between -0.75 and +0.75 Mvar. See reference [2] for more details on this control. Other voltage balancing approaches, such as a sorting algorithm, do not have this limitation. This method is used in the example power_Grid_STATCOM.

References

[1] M. Pereira, Member, IEEE, D. Retzmann, Member, IEEE, J. Lottes, M. Wiesinger, G. Wong, SVC PLUS: An MMC STATCOM for Network and Grid Access Applications 2011 IEEE Trondheim PowerTech

[2] Hirofumi Akagi ; Shigenori Inoue ; Tsurugi Yoshii, Control and Performance of a Transformerless Cascade PWM STATCOM with Star Configuration IEEE Transactions on Industry Applications (Volume: 43, Issue: 4 , July-august 2007)

1.5-MVA Multicell Motor Drive

This example shows the operation of a 1.5-MVA motor drive using a multicell power converter.
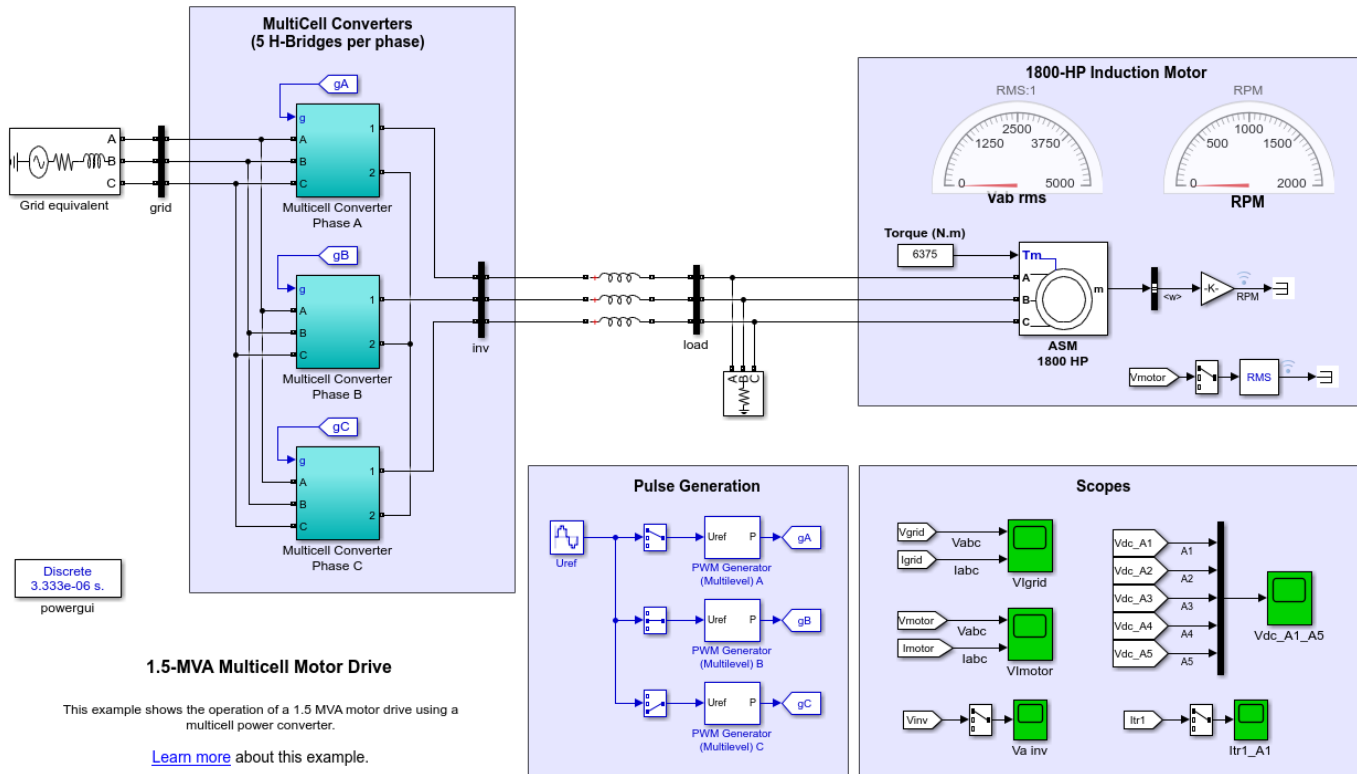
Description

A Full-Bridge MMC with External DC Link block is used to feed a 1800-HP induction motor. this block is programmed to model five H-bridges in series. Phase-shifting carrier-based PWM technique is used to control the H-bridges.

On the rectifier side, a phase-shifting transformer and a three-phase diode bridge is used to feed each individual DC link. Using phase-shifting transformer allows significantly reducing current harmonics injected into the distribution grid.

In order to cancel harmonics produced by the five six-pulse rectifier units in each phase, voltages applied to the AC side of individual rectifiers must be phase shifted by 60/5 = 12 degrees with each others. These phase shifts are specified in the PhaseShift parameter defined in the model pre-load function (PhaseShift = [12,24,36,48,60])

Simulation

Run the simulation and observe waveforms on the various scopes. The harmonics cancellation process using phase-shifting transformers works fairly well.

Now, set PhaseShift=ones(1,5) in the MATLAB® workspace to program the same phase-shifting to all transformers. Rerun the simulation. You will see that the currents injected into the grid now have very high harmonics content.

Six-Phase Synchronous Machine with Post-Fault Operating Strategy

This example shows the use of a 100 KVA Six-Phase Synchronous Machine under normal and fault operation.

J-F. Doyon and Louis-A. Dessaint (Ecole de Technologie Superieure, Montreal)

Description

A 100KVA, 240V, 1800 RPM six-phase synchronous motor is supplied by a three-windings, three-phase transformer connected to a 240V network with a short-circuit level of 10 MVA. Full load is initially applied to the shaft's machine and the excitation voltage is set to 0.6V, which represents the voltage value viewed from the stator so that the machine can produce its nominal phase current at full load.

Simulation

1. The machine starts under normal operation (all healthy phases).

2. At t = 4 seconds, phase A is disconnected. The Ia and Ib currents become dephased by a 180 degrees value to ensure a sum of currents in the stator equal to zero. Such a behavior creates a lot of ripple on the electromagnetic torque. This ripple is eliminated as soon as the ABC stator is disconnected from the circuit.

3. 50 ms later, the ABC stator is entirely disconnected from the circuit to compensate the phase A loss. Also, the load is reduced to half its nominal value and the excitation voltage Vf is decreased to 0.4V so the remaining XYZ stator keeps a nominal phase current value.

4. The system quickly stabilizes.

Stepper Motor Drive

This example shows a model of a hybrid stepper motor drive.

Hoang Le-Huy, Laval University
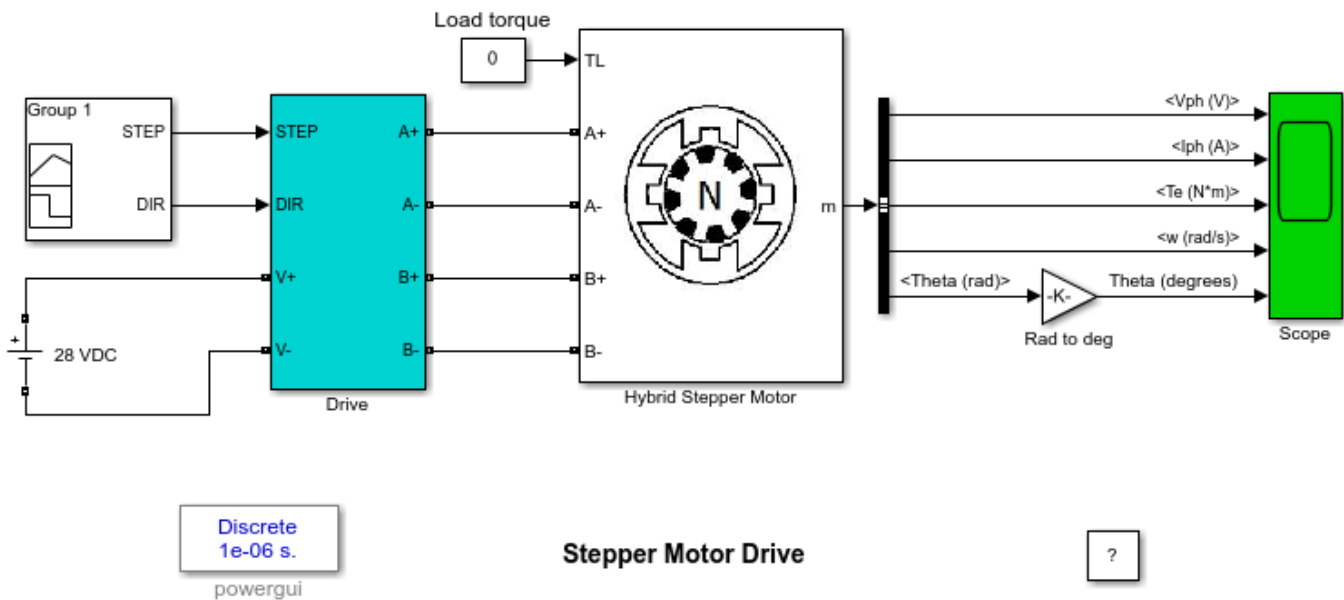
Description

This example presents a stepper motor drive using the Hybrid Two-Phase model selected among the options on the dialog window. The motors parameters are those of a small stepper motor (size 23). The motor phases are fed by two H-bridge MOSFET PWM converters. The DC bus is represent by a 28-V DC voltage source. The motor currents are independently regulated by two hysteresis-based controllers that generate the MOSFETs drive signals by comparing the measured currents with their references. The ripple in the current waveforms is controlled by the hysteresis band of the comparators. The switching frequency is variable and dependent on the motor parameters.

In this example, single-phase-on excitation scheme is used because of its simplicity. Square-wave current references are generated using the current amplitude and the step frequency parameters specified in the dialog window. The movement of the stepper drive is controlled by the STEP and DIR signals received from Signal Builder block.

Simulation

The current amplitude and the stepping rate are selected in the dialog mask to be 2A and 500 step/s, respectively. The STEP signal from the Signal Builder block controls the movement of the stepper drive. A positive value (1.0) will make the motor rotating and a zero value will stop the rotation. The DIR signal controls the rotation direction. A positive value (1.0) will impose the positive direction while a zero value will impose the reverse direction. The stepper motor drive operation is illustrated by the main waveforms (voltages, currents, torque, speed and position) displayed on the Scope block. The simulation is done using a fixed-step solver with a sampling time of 1 us, providing acceptable

accuracy for the PWM. If a high PWM accuracy is required, a smaller time step can be used but the simulation will be slower.

Simplified Synchronous Machine - Speed Regulation

This example shows a load shedding test on a 2000 kVA, 600 V alternator.

G. Sybille (Hydro-Quebec)
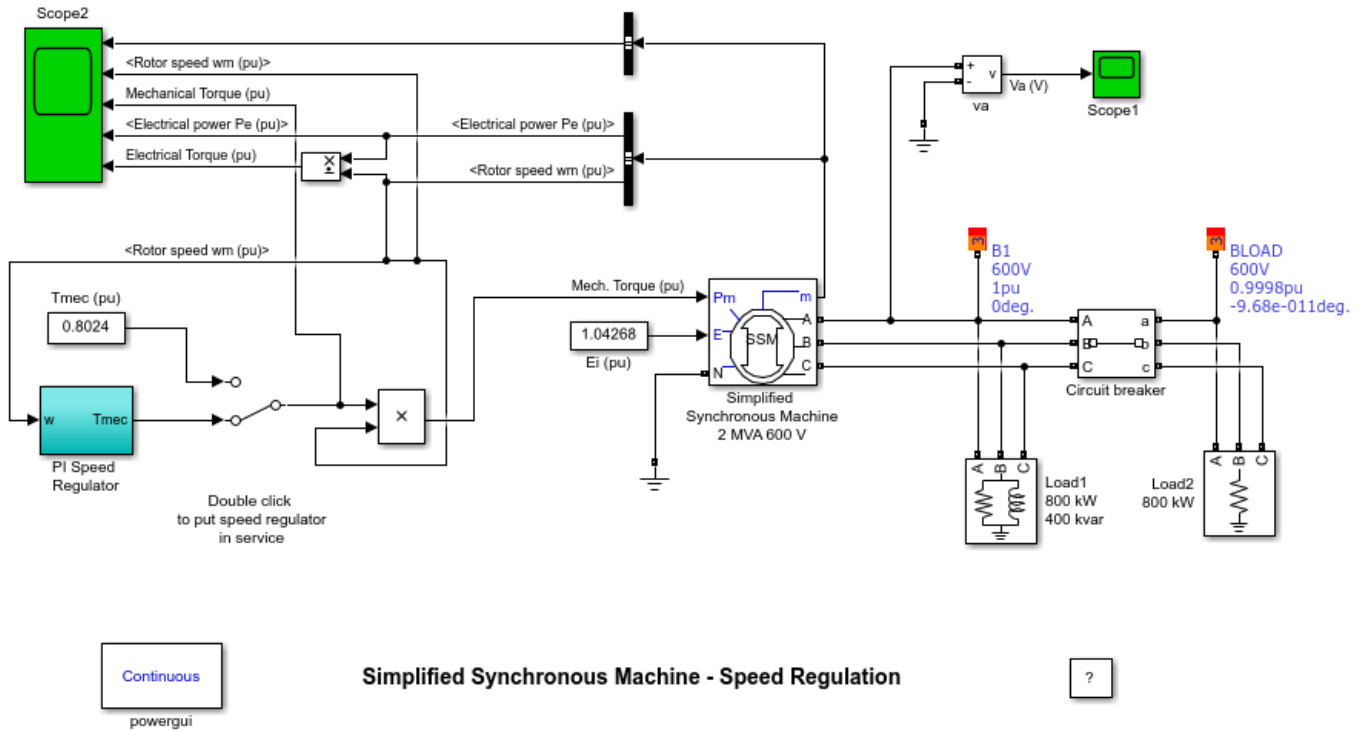
Description

A three-phase, four-wire alternator rated 2000 kVA, 1600 kW, 0.8 power factor, 600 V, 1800 rpm is connected to a 1600 kW, 400 kvar inductive load. The stator neutral point is grounded. The internal impedance of the generator (Zg = 0.0036 + j*0.16 pu) represents the armature winding resistance Ra and direct axis transient reactance X'd. The total inertia constant of the generator and prime mover is H = 0.6 s, corresponding to J = 67.5 kg.m^2. Speed regulation is modeled with Simulink® blocks implementing a PI regulator. The machine is excited with a constant voltage.

A three-phase breaker is used to switch out a 800 kW resistive load. The breaker is initially closed and it is opened at t = 0.2 s, resulting in a 50% load shedding.

Simulation

1. Initializing the machine to start in steady state

Open the powergui and select 'Machine Initialization'. A new window appears. Note that the machine 'Bus type' is set to 'Swing Bus'; this is because the circuit contains no voltage source imposing the reference angle. The initialization is then performed with the machine controlling the voltage and the angle at its terminals. The desired terminal voltage is initialized to 600 V (machine nominal voltage) and the phase angle of UAN angle to 0 degree.

Press the 'Compute and Apply' button. Once the initialization is solved the three line-to-line machine voltages as well as the three machine output currents are updated. The machine active and reactive powers as well as the required mechanical power Pmec and field voltage E are also displayed. You should read the following values: P = 1600kW, Q = 400 kvar, Pmec = 1604.7kW (0.8024 pu), field voltage E = 1.0247 pu. The Machine initialization tool also prompts you with a warning to set initial condition for mechanical power to 0.8024 pu. This is because the Pm input of the machine is not connected to a constant block or to a library block (HTG or STG). Note that the mechanical torque (Tmec block) is set at 0.8024 pu. Disregard this message. Note also that the constant block Ei connected at the E input of the machine is automatically updated (E = 1.04268 pu).

2. Simulation at constant torque - No speed regulator

Make sure that the speed regulator is not in service (Manual Switch is in the upper position) . Start the simulation and observe signals on Scope2. The three Iabc currents should start with steady-state sinusoidal waveforms. Observe that when the circuit breaker opens at t = 0.2 s, the electrical power (trace 4) drops from 0.8 pu to 0.4 pu and that the machine starts to accelerate.The rate of speed increase is dN/dt = 1/2H = 0.833 pu speed/ pu torque / s. As the net electromechanical torque is now Tmec-Telec = 0.8 - 0.4 = 0.4 pu, the speed increases at a rate of 0.833*0.4 = 0.33 pu/s. At t = 1.2s, the expected speed increase is therefore 0.33 pu. In fact, the speed measured at t = 1.2 s is slightly higher than the theoretical value (1.38 pu as compared to 1.33 pu). This is because the electrical torque (trace 5) decreases as speed is increasing, resulting in a net acceleration torque higher than 0.4 pu.

3. Simulation with speed regulator

Now double click on the Manual Switch block in order to put the speed regulator in service. Restart the simulation and observe the dynamic response of the speed regulator on Scope 2. Notice that in order to maintain speed at its reference value (1 pu), the speed regulator has reduced the mechanical torque to 0.4 pu.

Synchronous Machine

This example shows the use of the Machine Initialization tool of Powergui to initialize machine currents.

Louis-A. Dessaint and R. Champagne (Ecole de Technologie Superieure, Montreal)
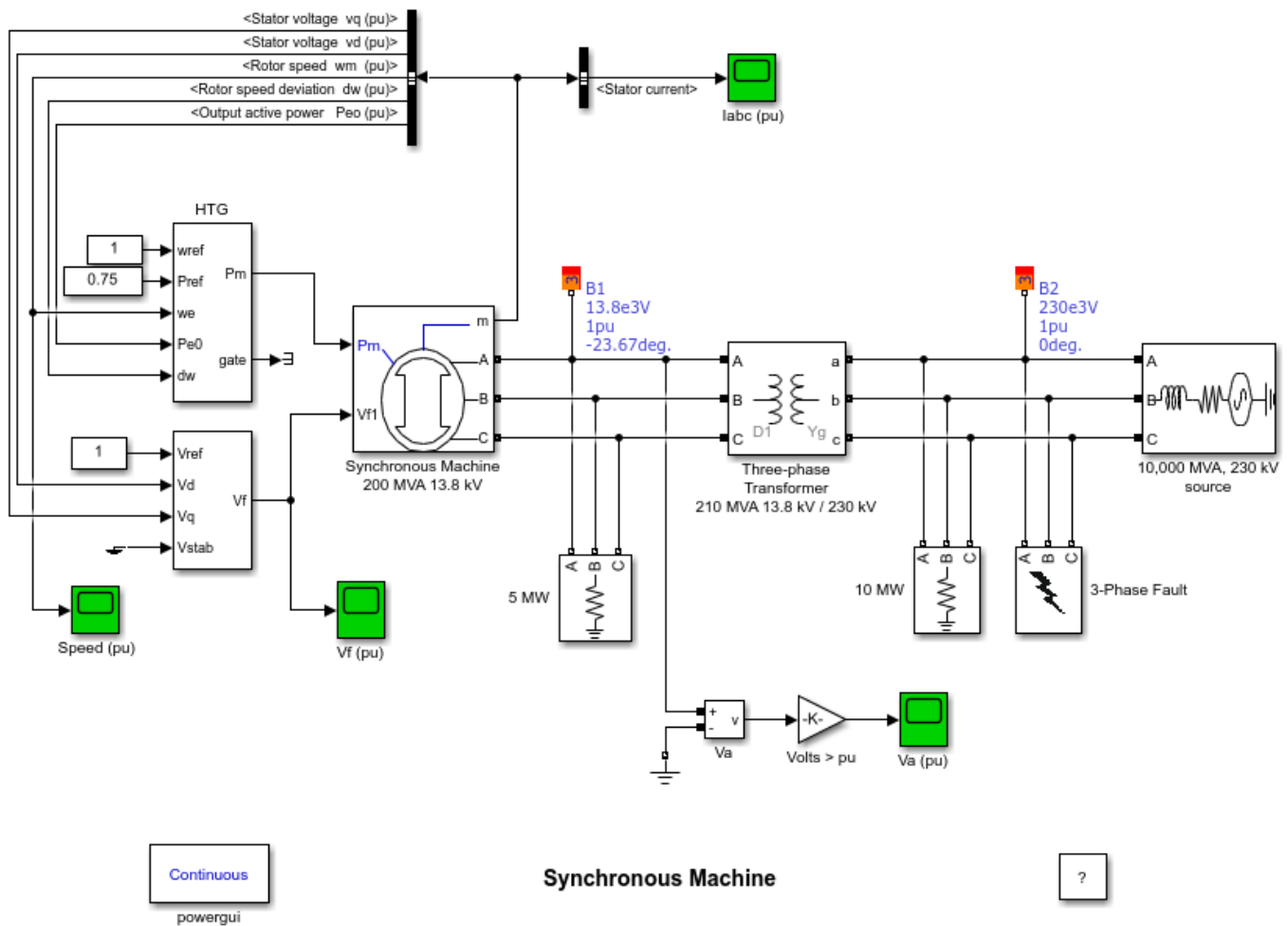
Description

A three-phase generator rated 200 MVA, 13.8 kV, 112.5 rpm is connected to a 230 kV, 10,000 MVA network through a Delta-Wye 210 MVA transformer. At t = 0.1 s, a three-phase to ground fault occurs on the 230 kV bus. The fault is cleared after 6 cycles (t = 0.2 s).

Simulation

1. Open the Powergui and select 'Machine Initialization'. A new window appears. The machine 'Bus type' is initialized as 'PV generator', indicating that the initialization is performed with the machine controlling the active power and its terminal voltage. The desired terminal voltage parameter is set to 13800 and the active Power to 150e6.*

Press the 'Compute and Apply' button. The phasors of AB and BC machine voltages as well as the currents flowing out of phases A and B are updated. The machine reactive power, mechanical power and field voltage requested to supply the electrical power is also be displayed: Q = 3.4 Mvar; Pmec = 150.32 MW (0.7516 pu); field voltage Ef = 1.291 pu.

2. In order to start the simulation in steady state with the HTG and excitation system connected, these two blocks are initialized according to the values calculated by the Machine Initialization tool. This initialization is automatically performed as long as you connect at the Pm and Vf inputs of the machine either Constant blocks or regulation blocks from the machine library (HTG, STG, or Excitation System). Open the HTG block menu and note that the initial mechanical power was set to 0.7516 pu (150.32 MW) by the tool. Open the Excitation System block menu and note that the initial terminal voltage and field voltage have been set respectively to 1.0 and 1.29071 pu.

3. Open the 4 scopes and restart the simulation. Observe that the terminal voltage Va is 1.0 p.u. at the beginning of the simulation. It falls to about 0.4 pu during the fault and returns to nominal quickly after the fault is cleared. This quick response in terminal voltage is due to the fact that the Excitation System output Vf can go as high as 11.5 pu which it does during the fault. The speed of the machine increases to 1.01 pu during the fault then it oscillates around 1 p.u. as the governor system regulates it. The speed takes much longer than the terminal voltage to stabilize mainly because the rate of valve opening/closing in the governor system is limited to 0.1 pu/s.

Starting a Synchronous Motor

This example shows the starting procedure for a synchronous motor.

Richard Gagnon (Hydro-Quebec)
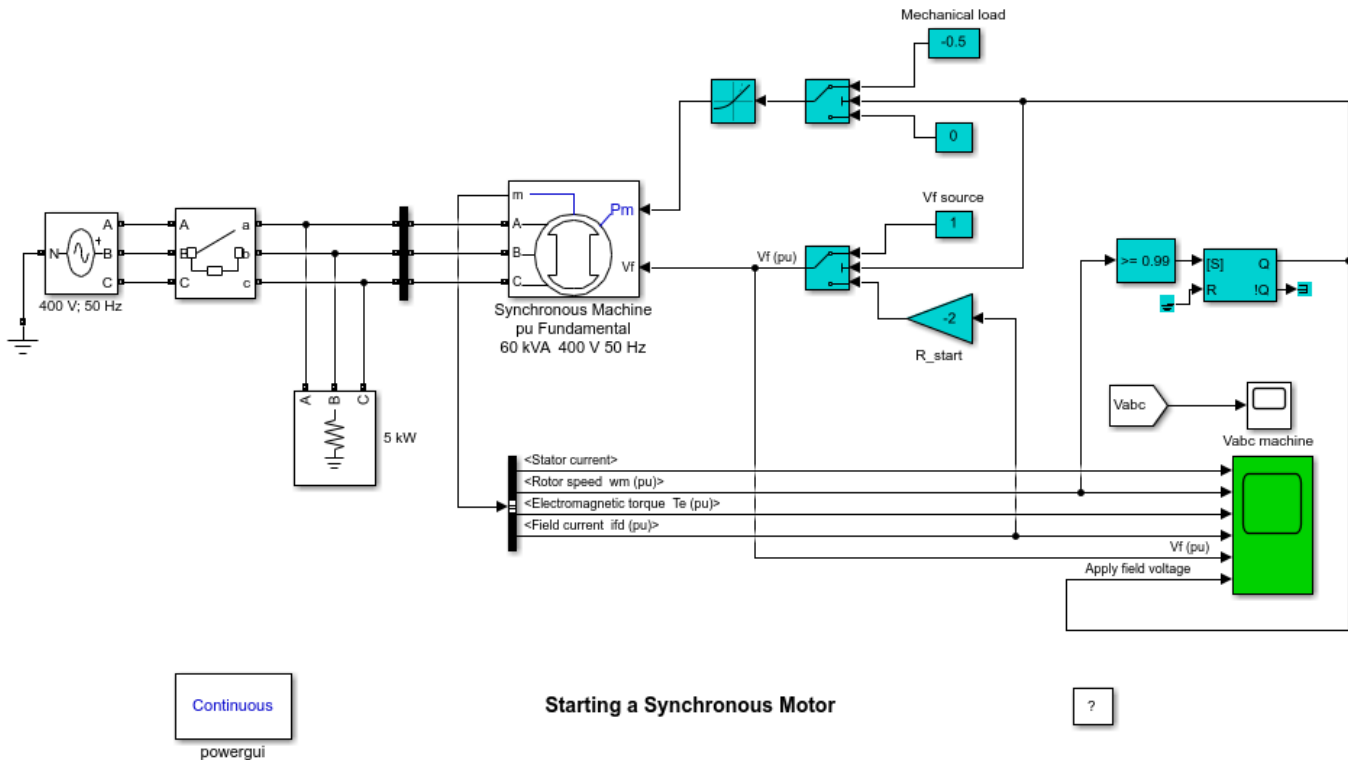
Description

When a synchronous motor is started, the excitation DC voltage is not applied to the field winding. The motor is started in induction machine mode with currents induced in the damper and field windings. A resistor is connected across the field winding in order to produce an acceptable field current and to limit voltage induced across the field winding. Then when speed reaches a preset value near synchronous speed, the field winding is connected to the DC voltage source and the motor synchronizes on the system frequency.

In the synchronous machine model, the field winding terminals are not available. Instead, a Simulink® signal representing the field voltage must be applied at the Vf input of the machine. Therefore if the Vf input is left unconnected, a zero field voltage is applied on the rotor. In other words, the field winding is short-circuited. In this example, the field current (idf) and a gain block (R_start) are used to implement the resistance connected across the field winding.

This model illustrates the starting procedure of a 60-kVA 400-V 50Hz synchronous motor. The motor is started at no load by closing the circuit breaker at t=0.1s. A 2 pu resistor is initially connected across the field winding. When the rotor speed reaches 0.99 pu, the "R_start" resistor is disconnected from the field terminals and it is replaced by the "Vf source" (1 pu). At the same time, the mechanical

power is ramped from zero to 50% of the nominal mechanical power (Pm = -0.5 pu) in one second. The motor locks into step at synchronous speed (1 pu) at approximately t = 1.3 s.

Mechanical Coupling of Synchronous Generator with Exciter System Using the Simscape Mechanical Rotational Port

This example shows a Machine mechanical coupling of an excitation system using Simscape™ mechanical rotational ports.

Gilbert Sybille, IREQ. Jean-Francois Doyon, ETS.
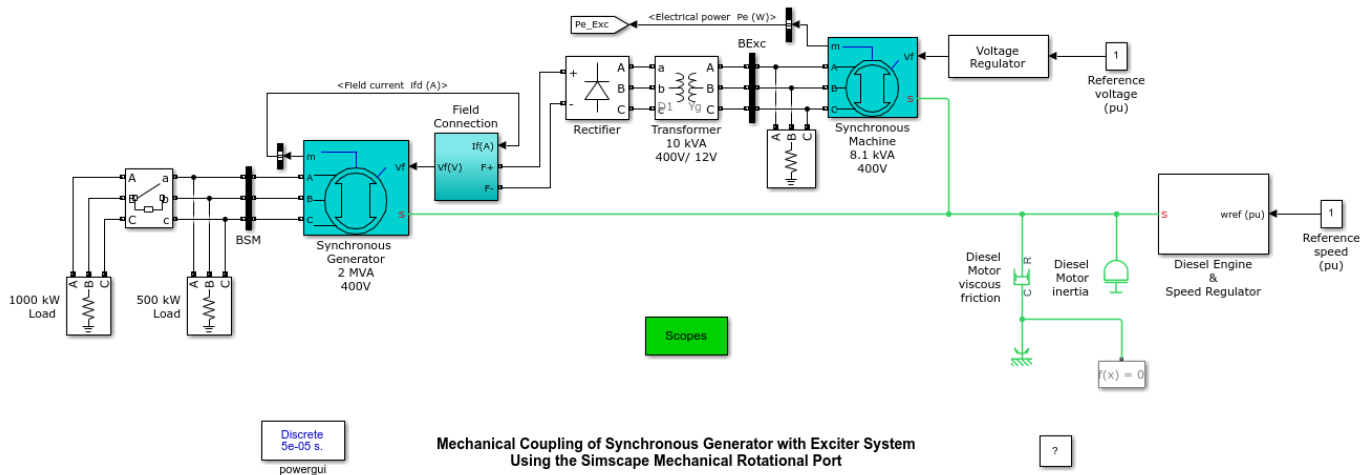
Mechanical Coupling of Synchronous Generator with Exciter System Using the Simscape Mechanical Rotational Port

Description

In large alternators, the excitation system is provided by a small synchronous machine connected to the shaft of the synchronous generator. Current rectification is performed by a rotating diode bridge mounted on the generator shaft, thus avoiding slip rings for providing DC power to the synchronous generator field.

The synchronous generator is driven by a diesel motor with speed regulation. The mechanical coupling of the generator, the exciter system, and the diesel motor is done by using the Simscape mechanical rotational ports of the Synchronous Machine blocks.

This model is very similar to the power_SM_exciter model. The only difference is that the two synchronous Machine blocks and the diesel motor use a mechanical rotational port to connect together and represents the mechanical shaft.

Simulation

Run the model and compare the simulation results with the ones obtained with power_SM_exciter model. They should be identical.

Note that the inertia and viscous friction are specified for every system (in the mask of the Synchronous Machine blocks, and directly to the mechanical shaft for the diesel motor). This is not the case for the power_SM_Excitation model where the total inertia and viscous friction of the three systems need to be specified in the Synchronous Machine block representing the generator.

Mechanical Coupling of Synchronous Generator with Exciter System

This example shows a Machine mechanical coupling of an excitation system.

Gilbert Sybille, IREQ.
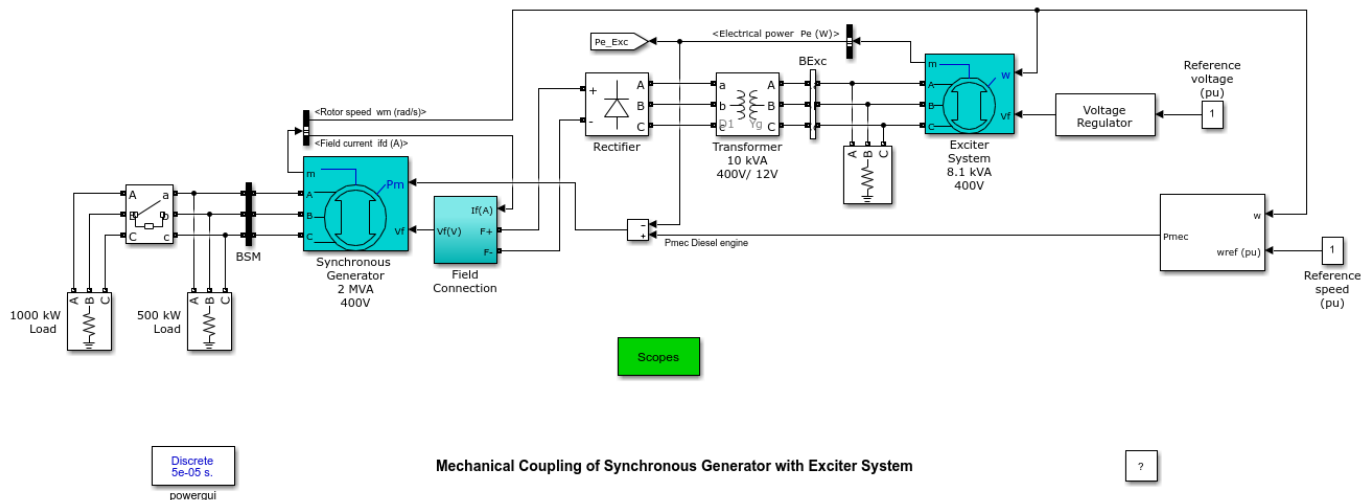
Mechanical Coupling of Synchronous Generator with Exciter System

Description

In large alternators, the excitation system is provided by a small synchronous machine connected on the same shaft as the main synchronous generator. Current rectification is performed by a rotating diode bridge mounted on the synchronous machine shaft, thus avoiding slip rings for providing DC power to the synchronous generator field. Mechanical coupling of the Synchronous generator and the Exciter is done by using speed as mechanical input for the Exciter machine.

The Exciter is a small synchronous machine rated 8.1 kVA, 400 V, 50Hz, 1500 RPM. A 400V / 12 V transformer is used to adapt the 400 V output voltage of the exciter to the rectifier. In real life this transformer would not be used. Instead, the Exciter output voltage would rather be 12 V. The output of the rectifier bridge is connected directly to the synchronous generator field terminals. Filtering is not required because of the large field inductance.

The synchronous generator is a 2 MVA, 400V, 50 Hz, 1500 rpm machine driven by a diesel motor. A nominal field current (Ifn) of 100A specified in the mask parameters allows using the real voltage applied to the rotor (not the field voltage seen from stator). It results in a nominal field voltage of 9.2837 V. The field terminal voltage of the synchronous generator is measured inside the "Field Connections" subsystem. This subsystem is required to interface the input Vf of the synchronous machine and the real field terminals. It uses a current source driven by the DC current output of the bridge which also corresponds to the DC field current. The voltage appearing across this current source corresponds to the field voltage which must be applied to the Vf Synchronous Machine input.

Voltage regulation of the generator is performed by controlling the field voltage of the exciter. The Diesel engine provides the total mechanical power required by the main synchronous machine and the exciter.

Simulation

Run the model and observe signals on the Synchronous Generator Scope block. From 0 to 3 sec the synchronous machine delivers 500 kW (25 % of rating). At t=3 sec, an additional 1000 kW is switched on by closing the circuit breaker. In order to keep voltage at 1 pu, the excitation current increases from 112 A to 185 A (trace 1). The field voltage (trace 2) contains a 300 Hz ripple, but this ripple does not appear in the field current because of the large field inductance. Trace 3 shows that after load switching the synchronous machine terminal voltage resumes to its nominal value after a 3 second transient. Traces 4 and 5 show the mechanical output power of the diesel engine and the speed of the engine-generator set. Speed regulation maintains 1 pu speed and nominal frequency (50 Hz output voltage).

Three-Phase Asynchronous Machine

This example shows the asynchronous machine in an open-loop speed control on a 3 HP 220 V industrial motor.

Louis-A. Dessaint and R. Champagne (Ecole de Technologie Superieure, Montreal)
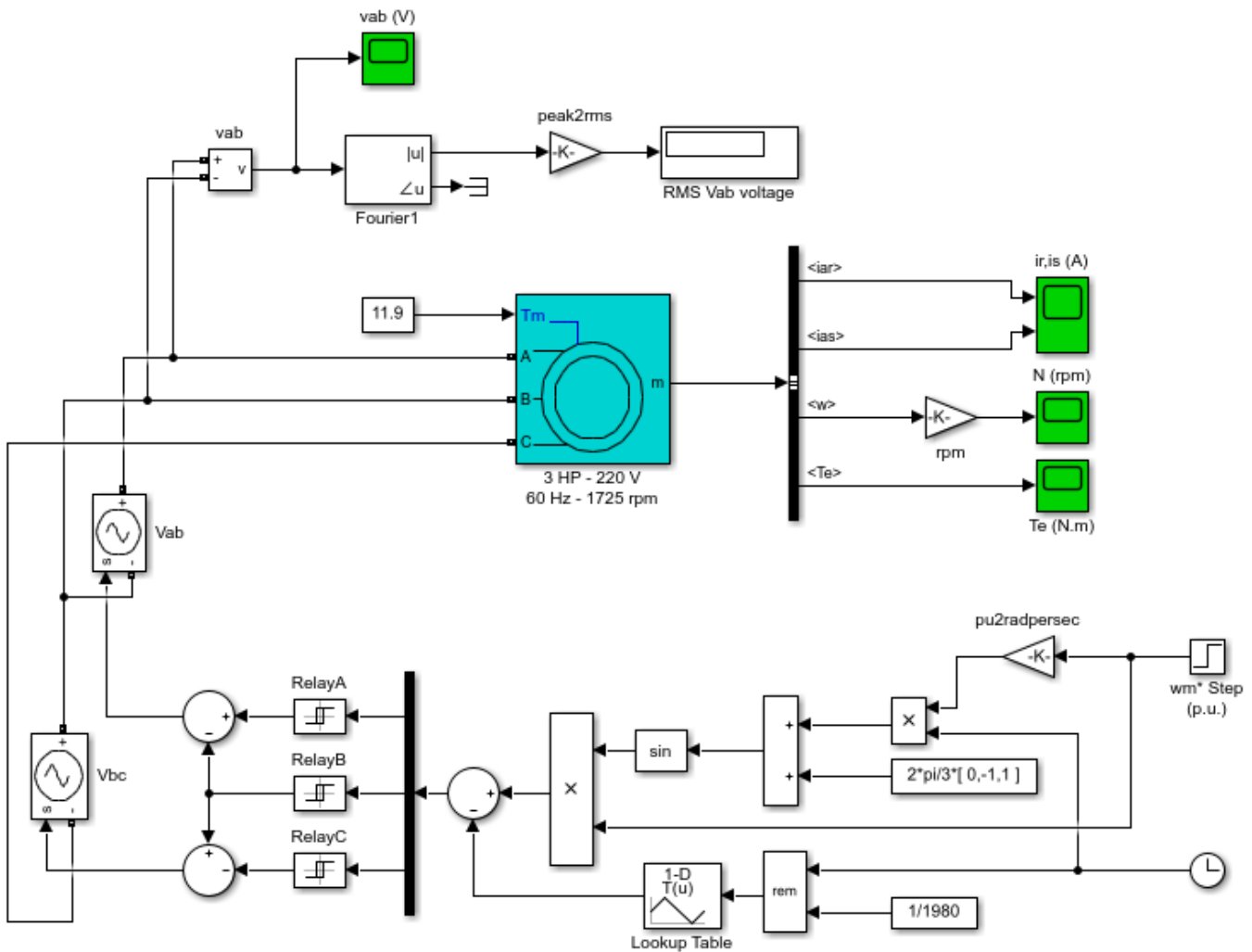
Three-Phase Asynchronous Machine

Description

A three-phase motor rated 3 HP, 220 V, 1725 rpm is fed by a sinusoidal PWM inverter. The base frequency of the sinusoidal reference wave is 60 Hz while the triangular carrier wave's frequency is set to 1980 Hz. The PWM inverter is built entirely with standard Simulink® blocks. Its output goes

through Controlled Voltage Source blocks before being applied to the Asynchronous Machine block's stator windings. The machine's rotor is short-circuited. Its stator leakage inductance Lls is set to twice its actual value to simulate the effect of a smoothing reactor placed between the inverter and the machine. The load torque applied to the machine's shaft is constant and set to its nominal value of 11.9 N.m.

The motor is started from stall. The speed setpoint is set to 1.0 pu, or 1725 rpm. This speed is reached after 0.9 s.

Simulation

Take a look at the simulation parameters. The Maximum time step has been limited to 10 microseconds. This is required due to the relatively high switching frequency (1980 Hz) of the inverter.

Observe that the rotor and stator currents are quite noisy despite the use of a smoothing reactor. The noise introduced by the PWM inverter is also observed in the electromagnetic torque waveform Te. However, the motor's inertia prevents this noise from appearing in the motor's speed waveform.

The RMS value of the fundamental component of the line voltage at the machine's stator terminals is measured with a Fourier block.

Finally, observe the PWM inverter's output. Use the zoom on the vab oscilloscope to zoom in on the waveform.

Saturation in Three-Phase Asynchronous Machine

This example shows the effects of saturation in a three-phase asynchronous motor at various operating conditions.

Jean-Nicolas Paquin and Louis-A. Dessaint (Ecole de Technologie Superieure, Montreal)
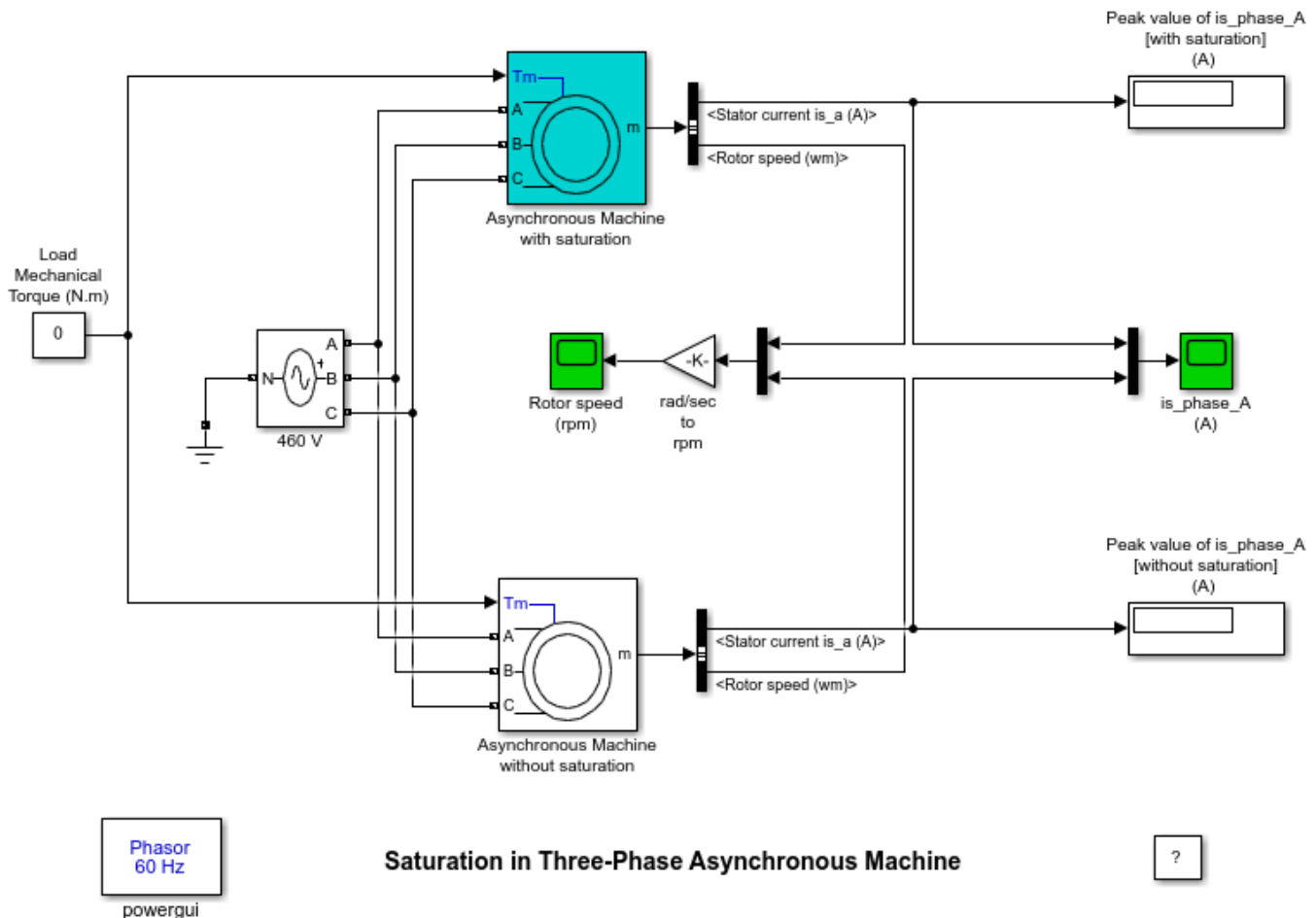
Description

The model shows two identical asynchronous motors rated 50 HP, 460 V, 1800 rpm. In order to compare the linear and saturated model, saturation is implemented in the top motor only. The stator is connected to an ideal three-phase source and load torque is kept constant. Simulation is performed in phasor mode.

Simulation

1. At no load (Tm =0), (three-phase source), simulate and observe the steady-state stator current for different values of stator voltage ranging from 0.5*nominal_voltage (230 Vrms L-L) to 1.5*nominal voltage (690 Vrms L-L). For the saturated motor, you should observe that each operating point lies on the I_versus_V saturation curve specified by the Saturation Parameters. Obviously, a voltage value

below the saturation voltage (first point supplied in the Saturation Parameters) will produce the same current for both motors.

Observe the impact of saturation for different torque values ranging from zero to nominal torque (197 N.m).

Note that during motor start up the 60-Hz component of current observed on the 'is_phase_A' scope model is higher for the saturated model.

2. The locked rotor test is generally used to determine the approximate values of the rotor parameters. In that specific case where the slip is unitary, the impedance of the rotor branch becomes so low that the magnetizing current is negligible compared to the rotor and stator currents. Therefore, the magnetizing branch can be neglected and it can be assumed that the saturation has no influence.

To simulate a locked rotor, specify an infinite inertia (J=inf in the machines' dialog boxes). Simulate with 460V source voltage (nominal voltage) and then 690V (1.5*nominal voltage). Observe both machines' stator currents in steady-state. You should observe stator currents for saturated and unsaturated motors, very close to each other. This shows that when the rotor of an asynchronous motor is blocked, saturation is negligible.

Single-Phase Asynchronous Machine

This example shows the operation of a single phase asynchronous motor in Capacitor-Start and Capacitor-Start-Run operation modes.

H. Ouquelle and Louis-A.Dessaint (Ecole de technologie superieure, Montreal)
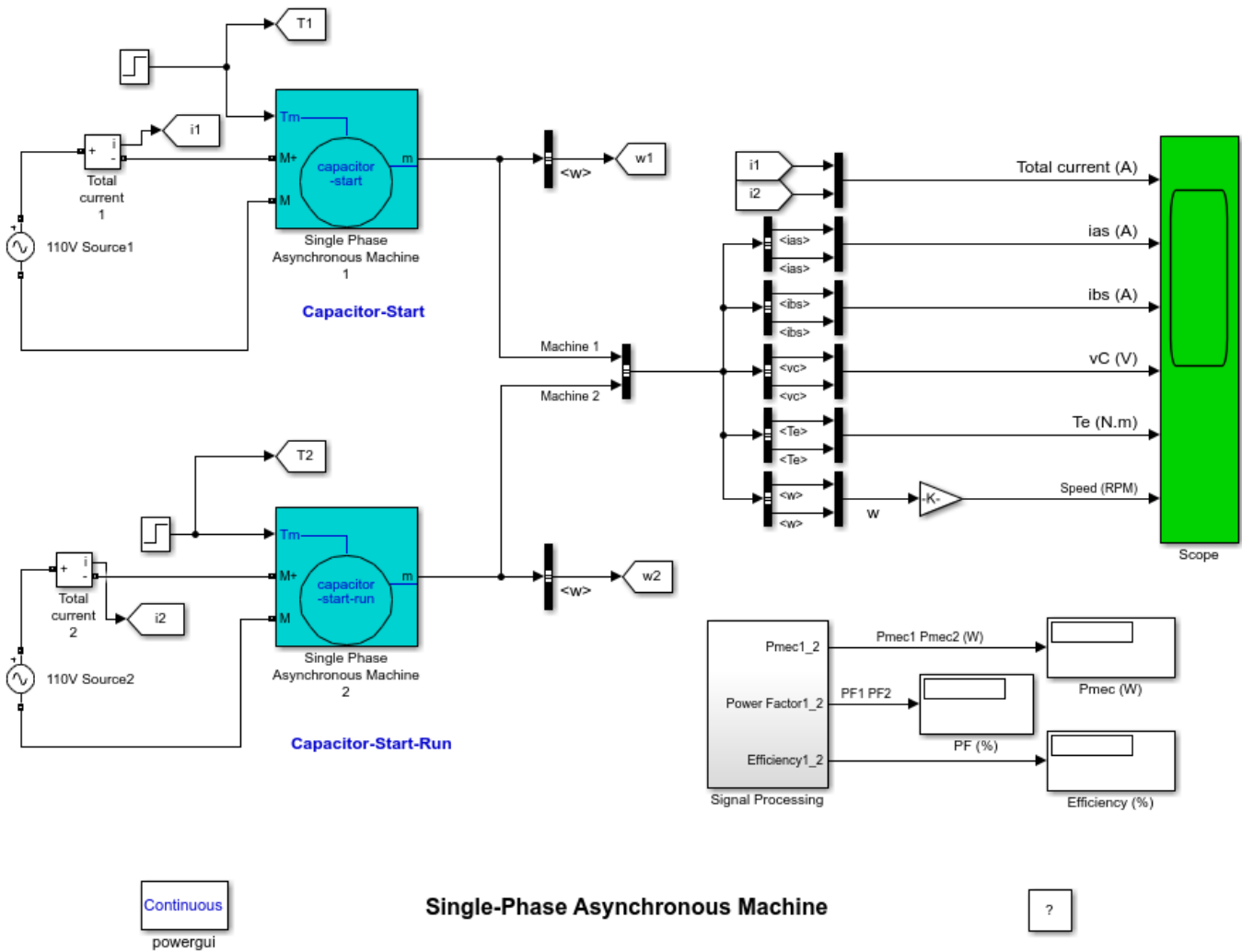
Description

This model uses two single-phase asynchronous motors respectively in Capacitor-Start and Capacitor-Start-Run modes, in order to compare their performance characteristics, such as torque, torque pulsation, efficiency and power factor. The two motors are rated 1/4 HP, 110 V, 60 Hz, 1800 rpm.and they are fed by a 110V single phase power supply. They have identical stator windings (main and auxiliary) and rotor squirrel cages.

Motor 1 motor operates in capacitor-start mode. Its auxiliary winding, in series with a 255 uF starting capacitor, is disconnected when its speed reaches 75% of nominal speed. The starting capacitor is used to provide a high starting torque.

Motor 2 operates in capacitor-start-run mode. This operation mode uses two capacitors: The run and start capacitors. During the starting period, the auxiliary winding is also connected in series with a 255 uF capacitor but, after the disconnection speed has been reached, the auxiliary winding stays connected in series with a 21.1 uF run capacitor. This capacitor value is optimized to mitigate torque pulsations. The motor operates efficiently with a high power factor.

The two motors are first started at no load, at t=0. Then at t=2 sec, once the motors have reached their steady-state regime, a 1 N.m torque (nominal torque) is suddenly applied on the shaft.

Simulation

Start the simulation. The Scope block displays the following signals for the capacitor-start motor (yellow traces) and capacitor-run motor (magenta traces) : total current (main +auxiliary winding), main winding current, auxiliary winding current, capacitor voltage, rotor speed and electromagnetic torque. The mechanical power, power factor and efficiency of motor 1 and motor 2 are computed inside the Signal Processing subsystem and displayed on 3 Display blocks.

During the starting period, as long as the disconnect switch remains closed (from t=0 to t=0.48 s), all waveforms are identical. After opening of the switch, differences are observed as explained below.

1. Capacitor-Start:

Observe the 120 Hz torque pulsations which produce 120 Hz mechanical vibrations of the rotor and decrease the motor efficiency. Peak to peak torque ripple is about 3 N, or 300% of rated load when the motor is operating at no load. Observe that the starting capacitor remains charged at its peak voltage when the auxiliary winding is switched off.

2. Capacitor-Start-Run:

Observe that the torque pulsations are substantially reduced. The run capacitor value has been optimized to minimize torque pulsations at full load. Torque pulsations magnitude is 2 N.m peak to peak (200 % of rated torque) at no load, while it is only 0.04 N.m peak to peak (4 % of rated torque) at full load. Power factor and efficiency at full load (respectively 90 % and 75%) are higher than with the capacitor-start motor (respectively 61 % and 74 %).

Single-Phase Asynchronous Machine - Voltage Control of Auxiliary Winding

This example shows a drive acting on the auxiliary winding of a single-phase synchronous machine.

H.Ouquelle and Louis-A.Dessaint (Ecole de technologie superieure, Montreal)
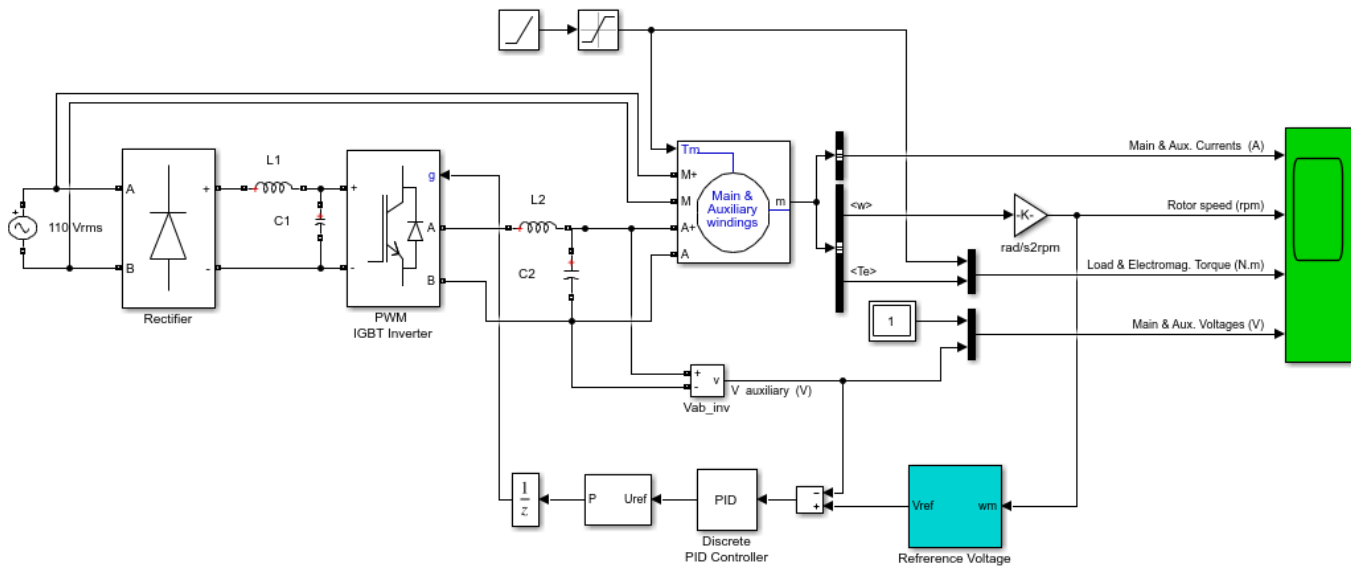
Single-Phase Asynchronous Machine - Voltage Control of Auxiliary Winding

Description

The motor's main winding is directly connected to the 110 V utility supply. The auxiliary winding is fed by a H-bridge IGBT inverter connected to a rectifier.

The reference values of the magnitude and phase of voltage applied to the auxiliary winding are computed by the Reference Voltage block [1]. By varying the auxiliary winding voltage magnitude and phase, the torque ripples are alleviated at all operating points. The voltage control loop uses a proportional-integral-derivative (PID) controller.

Simulation

Start the simulation. The machine starts at no load and then at t=1.0 sec, once the machine has reached its steady state, the load torque is increased to its nominal value (1 N.m) in 0.5 sec.

Observe that torque ripples are substantially mitigated at all operating points. Also observe the auxiliary and winding currents. These currents are in quadrature and their magnitude ratio is equal to the ratio N of number of turns of auxiliary and main windings.

Reference

[1] COLLINS, E.R., PUTTGEN, H.B., and SYLE, W.E.'Single-Phase Induction Motor Adjustable Speed Drive: Drive Phase Angle Control Of the Auxiliary Winding Supply'. IEEE® Industrial Application Society annual meeting conference records, pp.246-252

Single-Phase Asynchronous Machine - Vector Control of AC Drive

This example shows the vector control of an AC drive.

H.Ouquelle and Louis-A.Dessaint (Ecole de technologie superieure, Montreal)
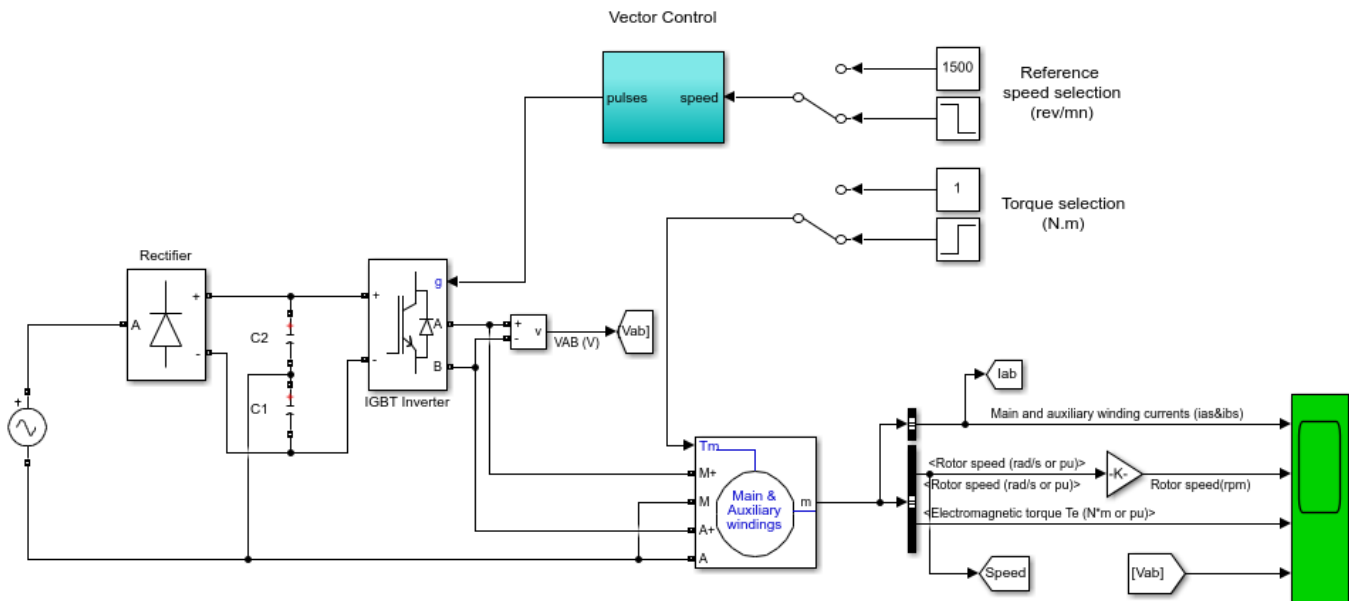
Single-Phase Asynchronous Machine - Vector Control of AC Drive

Description

A single phase asynchronous machine rated 1/4 HP, 110 V, 60Hz is fed by a current-controlled PWM inverter which is built using a Universal Bridge block. The speed control loop uses a proportional-integral-derivative regulator to produce the quadrature-axis current reference iq* which controls the motor torque. The motor flux is controlled by the direct-axis current reference id*. The block DQ-AB is used to convert id* and iq* into current references ia*, and ib* for the current regulator. Current and Voltage Measurement blocks provide signals for visualization purpose.

Simulation

Start the simulation. Observe on the scope the motor currents, voltage, speed, and torque during the starting. Double click on the two Manual Switch blocks to switch from the constant wref=1500 rpm and TL blocks to the Step blocks. (Reference speed wref changed from 1500 to 750 rpm at t = 2 s and load torque changed from 0 to 1 N.m at t= 1s). Restart the simulation and observe the drive response to successive changes in speed reference and load torque.

Permanent Magnet Synchronous Machine

This example shows the Permanent Magnet Synchronous Machine in a closed-loop speed and current control on a 1.1 kW, 3000 rpm industrial motor.
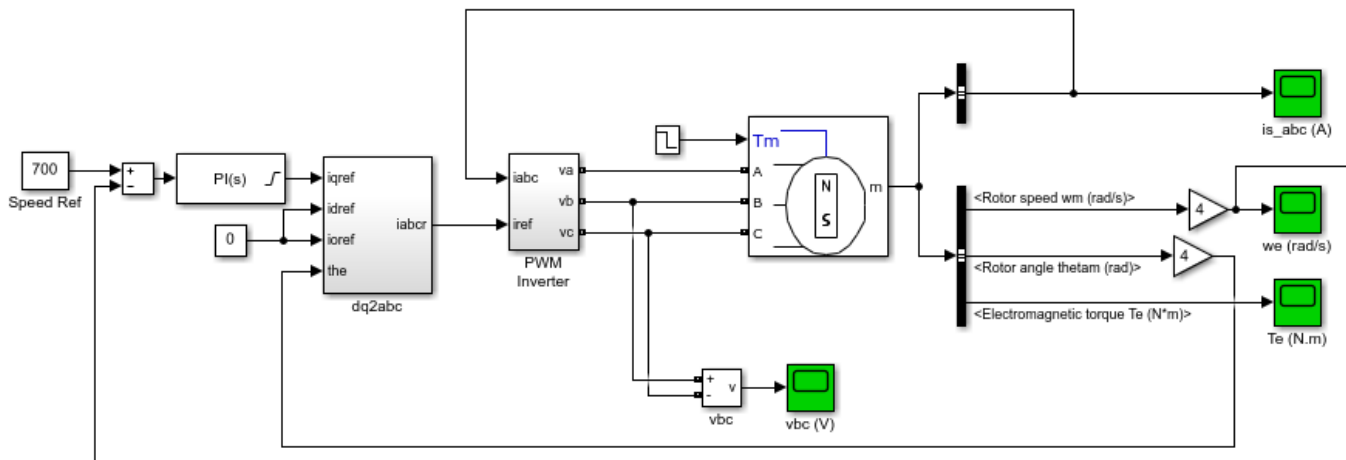
Description

A three-phase motor rated 1.1 kW, 220 V, 3000 rpm is fed by a PWM inverter. The PWM inverter is built entirely with standard Simulink® blocks. Its output goes through Controlled Voltage Source blocks before being applied to the PMSM block's stator windings. The load torque applied to the machine's shaft is originally set to its nominal value (3 N.m) and steps down to 1 N.m at t = 0.04 s.

Two control loops are used. The inner loop regulates the motor's stator currents. The outer loop controls the motor's speed.

Simulation

Observe that the stator currents are quite "noisy," which is to be expected when using PWM inverters. Also, the amplitude of these currents decreases at t = 0.04 s, when the load is decreased. The noise introduced by the PWM inverter is also observed in the electromagnetic torque waveform Te. However, the motor's inertia prevents this noise from appearing in the motor's speed waveform.

Five-Phase Permanent Magnet Synchronous Machine

This example shows the use of the Five-Phase PMSM and the Three-Phase PMSM in a closed-loop speed and current control on two 4.4 kW industrial motors..

J-F. Doyon and Louis-A. Dessaint (Ecole de Technologie Superieure, Montreal)
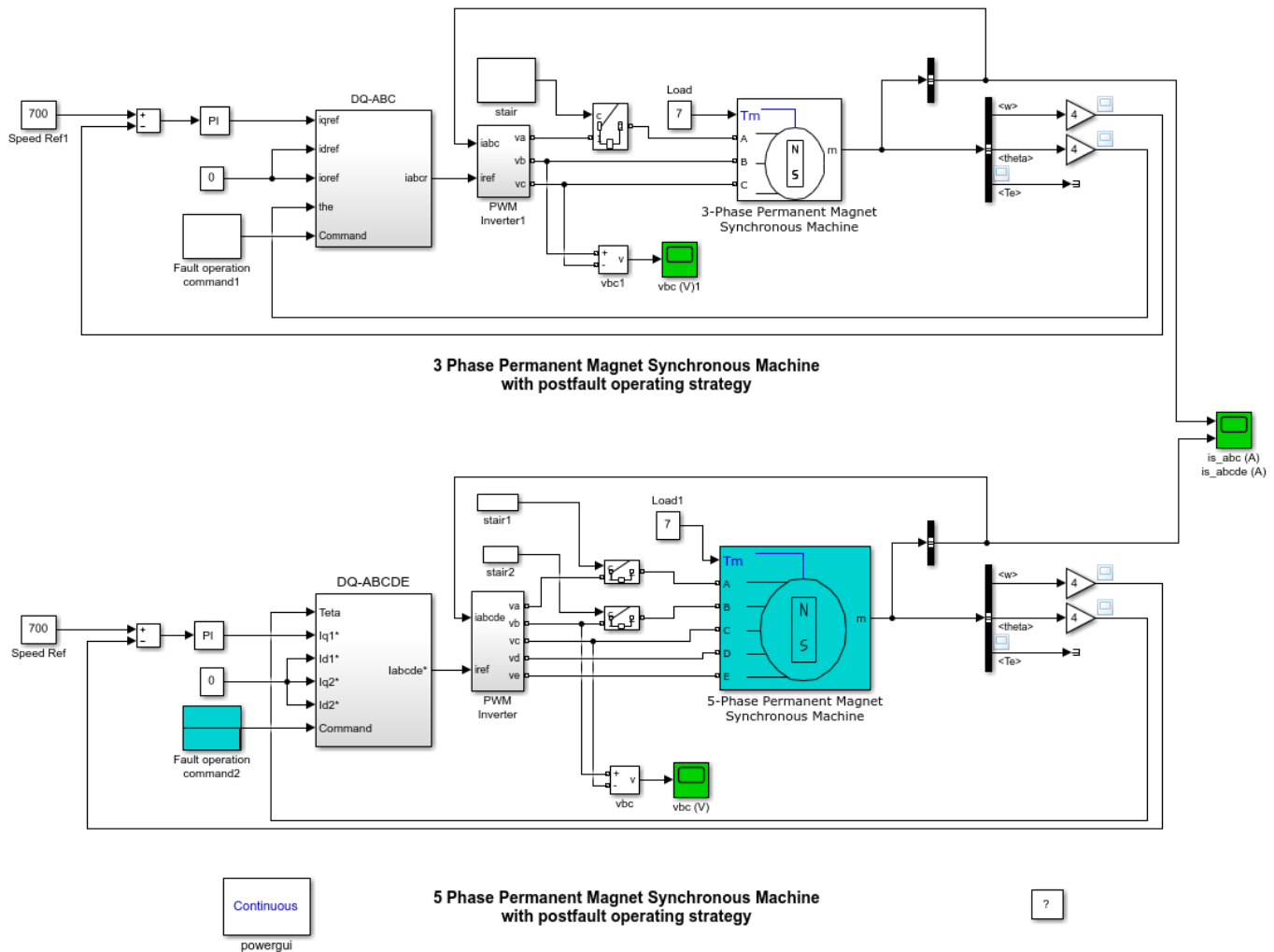
Description

A five-phase motor and a three-phase motor both rated 4.4 kW are each fed by a PWM inverter. The PWM inverter outputs goes through Controlled Voltage Source blocks before being applied to the PMSM block's stator windings. The load torque applied to each machine's shaft is fixed to 7 N.m. Two control loops are used. The inner loop regulates the motor's stator currents. The outer loop controls the motor's speed.

1. Each machine starts under normal operation (all healthy phases).

2. At t = 0.06 second, phase ''A'' of both machines is disconnected.

3. At t = 0.09 second, each controller's current references are varied to compensate for the phase loss.

4. At t = 0.12 second, phase ''B'' is also disconnected from the five-phase machine.

5. At t = 0.15 second, the controller's current references of the five-phase machine are varied to compensate for the two phases loss.

Simulation

Observe that due to the change of current references during the two successive faults, the five-phase machine currents are increased to keep the electromagnetique torque at the same value and to prevent large ripple. Also, good speed regulation is maintained.

Less good results are observed with the three-phase machine. It can be seen that the electromagnetic torque has a lot of ripple which propagates to the regulated speed.

In summary the five-phase machine can be much more fault-tolerant than the three-phase machine.

Starting a DC Motor

This example shows the starting of a 5 HP 240V DC motor with a three-step resistance starter.

G. Sybille (Hydro-Quebec)
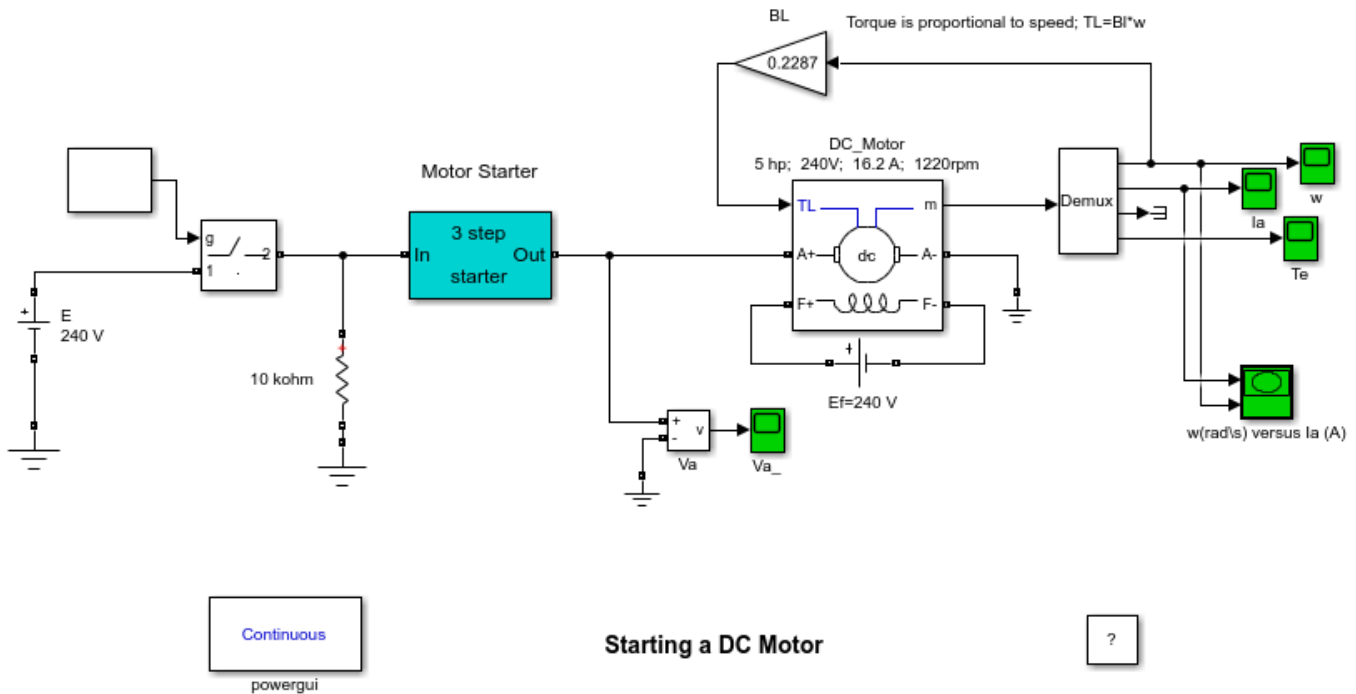
Description

Induced EMF: Eo = 240-16.2*0.6 = 230.3 V

Pe = 230.3*16.2 = 3731 W = 5.0 HP

Field current: If = 240/240 =1 A

Eo = w*Laf*If ---> w = (Eo/La*If) speed w = 230.3/1.8 = 127.7 rad/s = 1220 r/min

Nominal torque: Te = Pe/w = 3731/127.7 = 29.2 N.m

(Krause et al., Reference Analysis of Electric Machinery, pp. 89-92)

Steam Turbine and Governor System - Subsynchronous Resonance

This example shows sub-synchronous resonance (SSR) in Steam Turbine and Governor on a series-compensated network.

R. Champagne and L. Dessaint (Ecole de Technologie Superieure, Montreal)
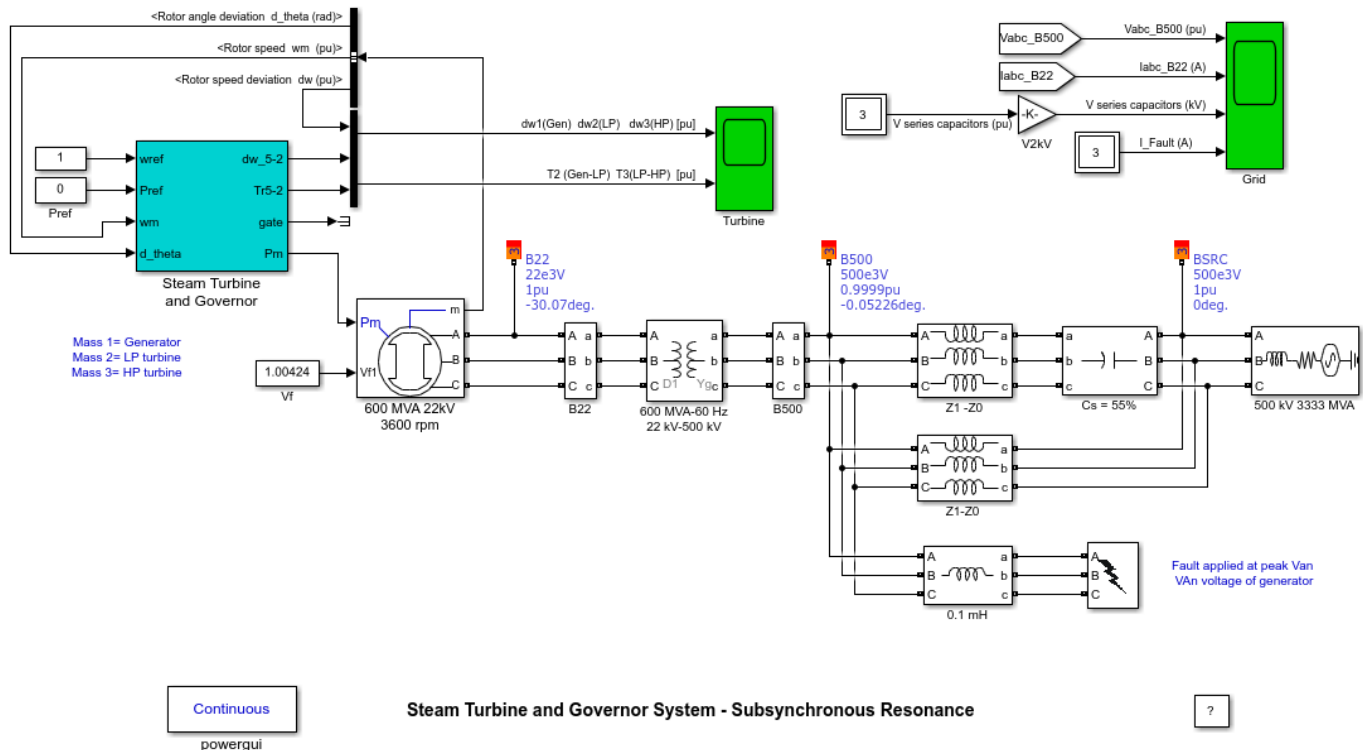
Description

This system is an IEEE® benchmark [1] used to study sub-synchronous resonance and particularly torque amplification after a fault on a series-compensated power system. It consists in a single generator (600 MVA/22kV/60 Hz/3600 rpm) connected to an infinite bus via two transmission lines, one of which is 55% series-compensated. The sub-synchronous mode introduced by the compensation capacitor after a three-phase fault has been applied and cleared excites the oscillatory torsional modes of the multi-mass shaft and the torque amplification phenomenon can be observed. The mechanical system is modeled by 3-masses : mass 1 = generator; mass 2 = low pressure turbine (LP); mass 3 = high pressure turbine(HP).

Simulation

1. In order to start the simulation in steady-state we initialized the synchronous machine with the 'Machine Initialization' tool. Open the Powergui and select "Machine initialization" . The machine "Bus type" should be already initialized as "PV generator", indicating that the initialization will be performed with the machine controlling its active power and terminal voltage. Specify the desired values by entering the following parameters:

*Terminal voltage U AB (Vrms) = 22000, Active power (Watts) = 0 *

Then press the "Compute and Apply" button.

The phasors of AB and BC machine voltages and currents flowing in phases A and B are updated. The SM reactive power, mechanical power and field voltage are displayed : Q = 1.48 Mvar, field voltage Ef = 1.0041 pu; If you open the SM block menu you will observe that the initial conditions have been updated.

2. In order to start the simulation in steady state with the STG connected, this Simulink® block must also be initialized. This initialization is automatically performed as long as you connect at the Pm and Vf inputs of the machine either Constant blocks or regulation blocks from the machine library (HTG, STG, or Excitation System). Open the STG block menu and note that the initial mechanical power and generator rotor angle have been automatically set to Pm0=2.7e-8 pu (corresponding to very low resistive losses in stator windings at zero output active power) and th0 = -120.1 degrees. The Vf constant block connected at the Vf excitation input of the synchronous machine has been automatically set to 1.0046 pu .

3. Open the Scope connected to the STG model. Run the simulation. Observe the speed deviations on the top axis of the Scope and the torques transmitted between the shaft's masses on the bottom axis. A peak torque of over 4 pu is observed for T2 between mass 1 (Generator) and mass 2 (LP turbine) . The frequency of the oscillations observed in the torques and speed deviations is about 27 Hz. The peak values we obtained here agree with those given in [1].

Reference

[1] IEEE Sub-synchronous resonance working group, "Second benchmark model for computer simulation of sub-synchronous resonance," IEEE Transactions on Power Apparatus and Systems, vol. PAS-104, no. 5, 1985, pp. 1057-1066.

Emergency Diesel-Generator and Asynchronous Motor

This example shows the Machine Load Flow tool of Powergui block to initialize an induction motor/diesel-generator system.

G. Sybille (Hydro-Quebec), Tarik Zabaiou (ETS)

Circuit Description

A plant consisting of a resistive and motor load is fed at 2400 V from a distribution 25 kV network through a 6 MVA 25/2 kV Wye-Delta transformer and from an emergency synchronous generator/diesel engine unit. The 25 kV network is modeled by a R-L equivalent source with a short-circuit level of 1000 MVA and with a 5 MW load. A three-phase to ground fault occurs on the 25 kV system, causing opening of the 25kV circuit breaker.

Demonstration

1. To start the simulation in steady-state, the synchronous machine and the asynchronous motor need to be initialized by the Load Flow tool of powergui. The Load flow parameters of the machine and motor are defined in the Load Flow tab of the two blocks:

For the synchronous machine: The "Generator type" parameter is set to "PV", indicating that the load flow is performed with the machine controlling its active power and terminal voltage. The "Active power generation P" parameter is set to 0.

For the asynchronous motor: The "Mechanical Power" parameter is set to 1.492e+006 W (2000 HP).

2. In the Powergui menu, select 'Load Flow'. A new window appears. A summary of the load flow settings is displayed in a table.

3. Press the 'Compute' button to solve the load flow. The table now display the actual machines active and reactive powers.

4. Press the 'Apply' button to apply the load flow solution to the model.

5. Open the SM and ASM blocks and note that the initial values have been updated by the Load Flow tool. The value of the constant block connected to the torque input of the asynchronous motor has also been automatically set to 7964 N.

6. Open the Diesel Engine Governor block. Note that the initial value of mechanical power has been set to 0.00027 pu (844 W) by the Load Flow tool.

7. Open the EXCITATION block and note in the Initial values tab that the initial values of terminal voltage and field voltage are set respectively to 1.0 and 1.4273 pu.

8. Right-click the EXCITATION block then select the type of excitation system you want to simulate.

Note that the initial values of all models have been already presaved with the same initial values. For the ST2A model, an additional line representing the initial value of terminal current It0 is set to 0.2739 pu.

9. Start the simulation. In the scopes check that the simulation start in steady-state.

Simulation Results

Simulation results obtained with the different excitation systems exhibit good stability when the fault is cleared. However, the ST1A and AC1A models provide better stability. The stabilization of the terminal voltage Vt is obtained in less than 2 seconds with the ST1A model and less than 3 seconds with the AC1A model. The results obtained with the AC4A and DC2A models are less efficient: the system takes longer to stabilize, the stabilization of terminal voltage Vt is obtained after 6 seconds. Note that the field voltage Vf reaches its limit without saturating in most of the models.

After fault clearing and islanding, and for all excitations models, the SM mechanical power increases from its initial value of 0 pu to the final value of 0.80 pu required by the resistive and motor load (2.49 MW). The motor speed decreases transiently from 1789 rpm to 1635 rpm, then it recovers close to its normal value.

Performance of Three PSS for Interarea Oscillations

This example shows three Power System Stabilizers (PSS) models using Kundur's four-machine two-area test system.

I. Kamwa (Hydro-Quebec)

Comparison of Three Power System Stabilizer (PSS) Using Kundur's Four-Machine Two-Area Test System

Three PSS are compared using the same settings for all machines:

1) MB-PSS with simplified settings: IEEE® type PSS4B according to IEEE Std 421.5

2) Conventional Delta w PSS from P. Kundur (Ref. [1], pp. 814-815, and Ref. [2])

3) Conventional Acceleration Power (Delta Pa) PSS

Description

The test system consists of two fully symmetrical areas linked together by two 230 kV lines of 220 km length. It was specifically designed in [1,2] to study low frequency electromechanical oscillations in large interconnected power systems. Despite its small size, it mimics very closely the behavior of typical systems in actual operation. Each area is equipped with two identical round rotor generators rated 20 kV/900 MVA. The synchronous machines have identical parameters [1,2], except for inertias which are H = 6.5s in area 1 and H = 6.175s in area 2 [1]. Thermal plants having identical speed regulators are further assumed at all locations, in addition to fast static exciters with a 200 gain [1,2]. The load is represented as constant impedances and split between the areas in such a way that area 1 is exporting 413MW to area 2. Since the surge impedance loading of a single line is about 140 MW [1], the system is somewhat stressed, even in steady-state. The reference load-flow with M2 considered the slack machine is such that all generators are producing about 700 MW each. The results can be seen by opening the Powergui and selecting Machine Initialization. They are slightly different from [1], because the load voltage profile was improved (made closer to unity) by installing 187 Mvar more capacitors in each area. In addition, transmission and generation losses may vary depending on the detail level in line and generator representation.

Simulation

1. Small-signal analysis of the systems

For an initial understanding of the network behavior, we can simulate its open-loop responses (PSSmodel = 0) to a 5%-magnitude pulse, applied for 12 cycles at the voltage reference of M1. This test is activated by opening the timer controlling the voltage reference of M1 and changing the multiplication factor of the transition times vector from 100 to 1. Similarly, the line fault should be deactivated by changing from 1 to 100 the multiplication factor of the transition times vector in the "Fault" device and line breakers "Brk1" and "Brk2". After starting the simulation, the signals responses are visualized by opening the "Machine" and "System" scopes on the main diagram. All signals show undamped oscillations leading to instability. A modal analysis of acceleration powers of the four machines shows three dominant modes:

- (1) An interarea-mode (fn = 0.64Hz , z = -0.026) involving the whole area 1 against area 2: this mode is clearly observable in the tie-line power displayed in "System" scope

- (2) Local mode of area 1 (fn = 1.12Hz, z = 0.08) involving this area's machines against each other

- (3) Local mode of area 2 (fn = 1.16Hz, z = 0.08) involving machine M3 against M4 (i.e.: the smaller the inertia, the greater the local natural frequency)

If one of the two tie-lines is removed by setting the breakers "Brk1" and "Brk2" in an open position, it is possible to achieve another steady-state stable equilibrium point with the same generation and load patterns. This is called a post-contingency network, easy to initialize using the Machine Initialization tool of the Powergui. A modal analysis of this network's responses to the same 5%-magnitude pulse, applied for 12 cycles at the voltage reference of M1 reveals that, while the two local modes remain basically unchanged in both frequency and damping (fn=1.10Hz, z=0.09 in area 1 and fn=1.15Hz, z=0.08 in area 2), the interarea mode shifts to a much lower frequency with still a negative damping (i.e.: unstable): (fn = 0.44Hz, z = -0.015).

2. PSS Tuning

The MB-PSS settings were easily selected by varying the center frequency and gain of each band so as to achieve a nearly flat phase response between 0.1 Hz and 5 Hz. The Delta w PSS settings are from Kundur [1], with two changes: a gain increase from 20 to 30 and the addition of a 15-ms transducer time constant. To see the frequency responses of these PSSs, click on the Show Bode Plot of the PSS icon on the main diagram.
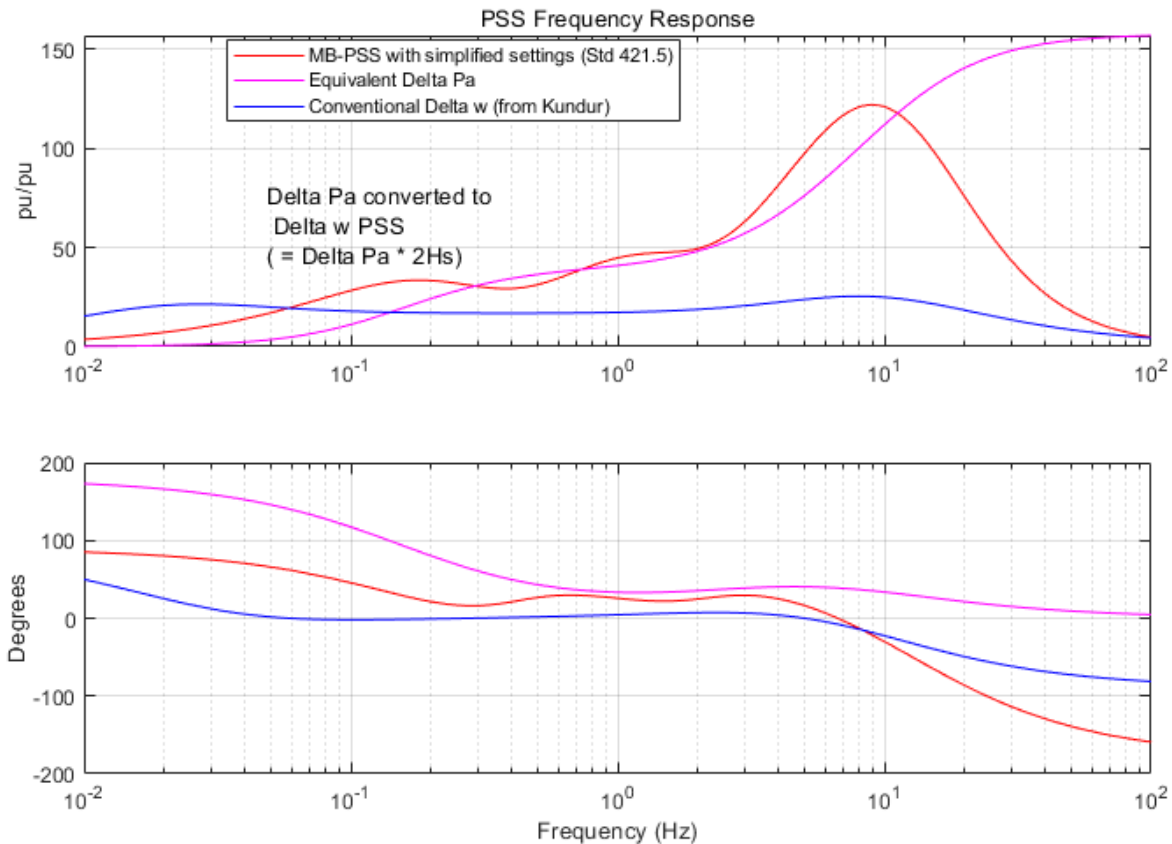
This figure confirms that the MP-BSS phase is effectively flat around 20-40 degrees in the frequency range of interest. The Delta w PSS has an overall poor phase shape, especially around 1-2 Hz, which makes it unable to cope with faster local or intermachine modes in multi-unit power plants. By contrast, the Delta Pa PSS has a good combination of strong gain and phase advance above 0.3 Hz, although it is unpractical at low frequency where it shows a 180 deg phase advance, which actually has a destabilizing effect despite the rather small low-frequency gain. Finally, even though the low-frequency shaping of the Delta w PSS is satisfactory in overall, its DC rejection (washout) is not efficient enough, providing five time less attenuation than the MB-PSS (zoom on the magnitude plot to see this).

3. Small-Signal Performance Assessment (12-cycle pulse on voltage reference of G1)

To simulate the small-signal closed-loop system responses, the transition times vector of the breakers and fault devices on the main diagram should be disabled by multiplying it by 100 as above (section 1). Then the timer block controlling the voltage reference of M1 is activated in the same way by removing any multiplication by 100, i.e. by changing 100 to 1. Select the PSS to be simulated by setting PSSmodel = 1,2,3. Start the simulation and record the variables you want to compare latter.

Most of the useful variables such as the machine speed and terminal voltage are stored in matrices W and Vt (look inside the "Machines" and "System" blocks on the main diagram for other variables.) This process was repeated for the three PSSs. To see the comparisons, double click on icon Show results: Step on vref of M1. The figure contains four plots: the top plot shows the power transfer from area 1 to 2; the second is the M1 speed, then the M1 acceleration power and the bottom plot is the M1 terminal voltage.
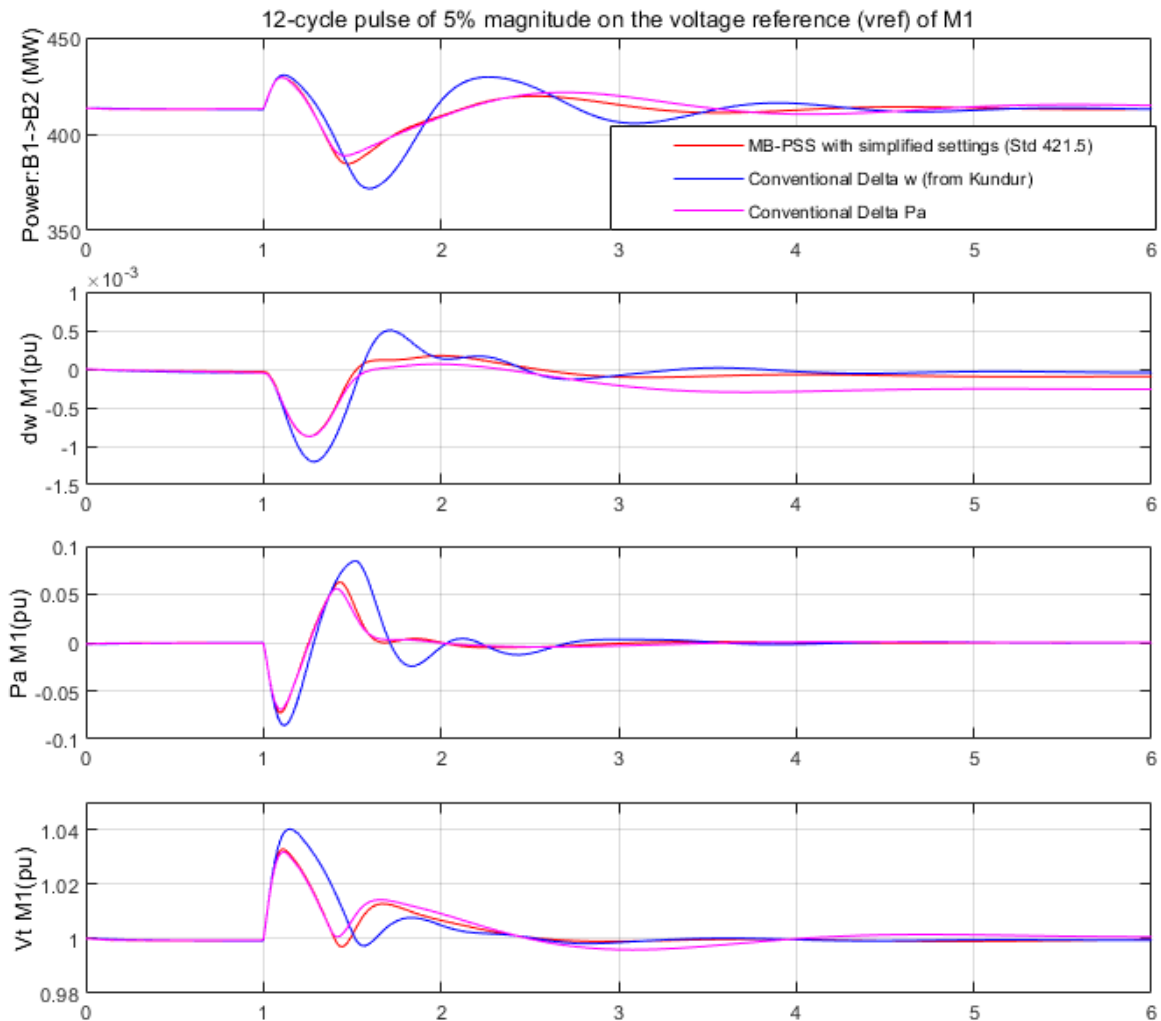
All PSSs do a good job stabilizing the naturally unstable system. However, it is clear that the MB-PSS is superior to the other two PSSs, providing significantly more damping to all modes, especially with respect to the Delta w PSS, whose poor phase/gain shaping above 0.5 Hz highlighted above shows all its limitations. Damping performance of the interarea-mode with two ties: MB-PSS: (fn = 0.50Hz, z = 0.30); Delta w: (fn = 0.64Hz, z = 0.25); Delta Pa: (fn = 0.35Hz, z = 0.30); Damping performance of the interarea-mode with one tie: MB-PSS: (fn = 0.40Hz, z = 0.36); Delta w: (fn = 0.43Hz, z = 0.35); Delta Pa: (fn = 0.23Hz, z = 0.14).

4. Large Signal Performance and Robustness Assessment (8-cycle, three-phase fault with line outage)

In assessing a PSS, small-signal performance is not enough. Good performance during large perturbations and good robustness with respect to changing operating conditions are other criteria of an equal importance. The system responses to a three-phase fault cleared in 8 cycles by opening the breaker "Brk1" and "Brk2" can be simulated following the same procedure as above. Without one tie-line, the system can reach a stable operating point in steady-state, although not every PSS is able to ensure a smooth transition into this new highly stressed operating point. For comparisons, open the icon Show results: 3-phase fault.

8-cycle three-phase fault with the outage of one 230kV line
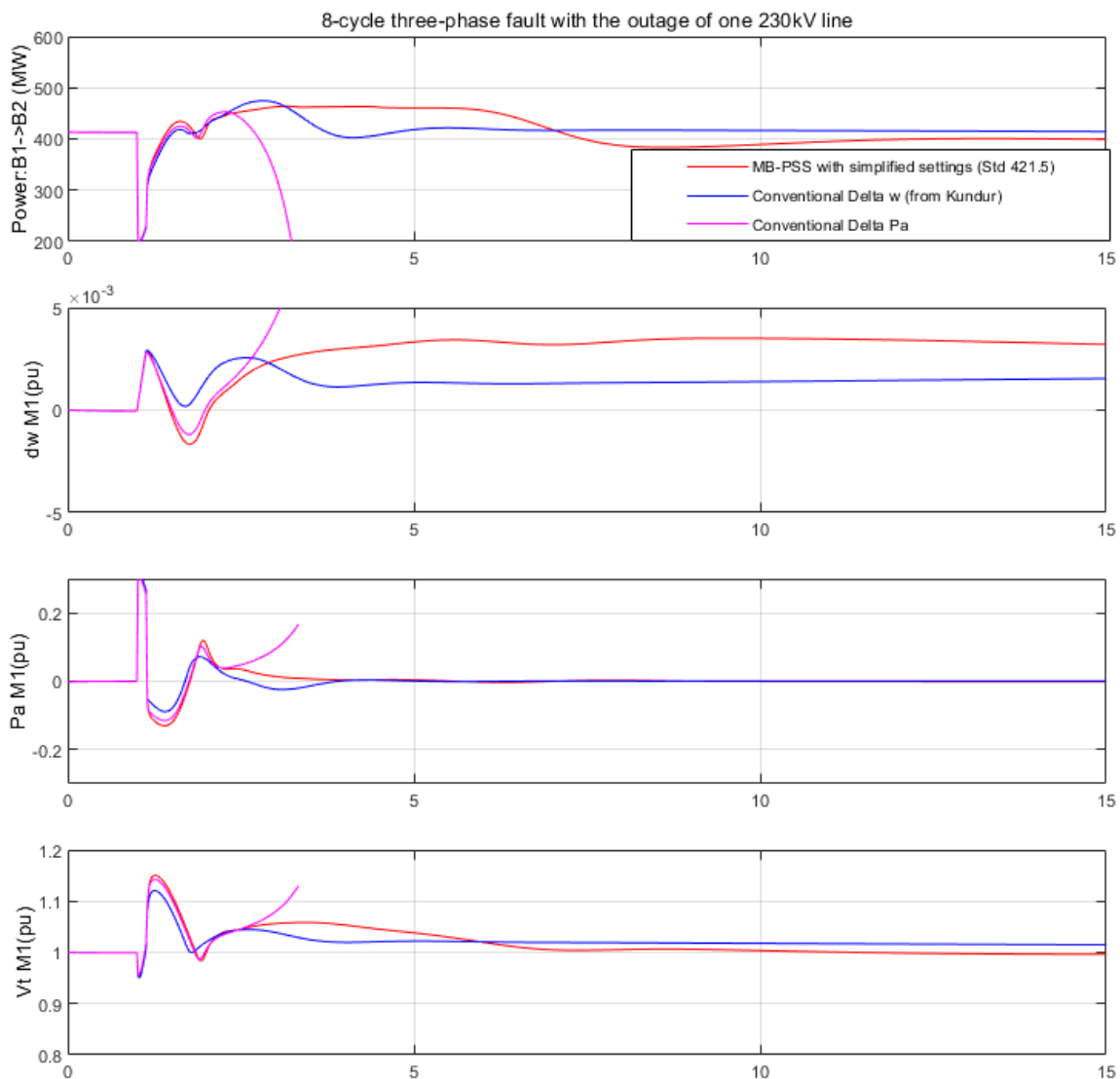
You will see that with the Delta Pa PSS, the system lost its synchronism while the MB-PSS and the Delta W PSS succeed in maintaining stability. The latter are both very effective in damping the oscillation of the power transfer. However, the closed-loop oscillation frequency of the MB-PSS is lower while the Delta w PSS is too slow on recovering the terminal voltage: this is a bad side effect of an inefficient washout. In addition, the power acceleration is more damped with the MB-PSS than any other PSS.

Reference

[1] P. Kundur, Power System Stability and Control, McGraw-Hill, 1994, Example 12.6, p. 813

[2] Klein, Rogers, Moorty and Kundur: "Analytical investigation of factors influencing PSS performance," IEEE Trans. on EC, Vol. 7 , No 3, September 1992, pp.382-390

Switched Reluctance Motor

This example shows a current-controlled 60-kW 6/4 SRM drive using the SRM specific model based on measured magnetization curves. 8/6 and 10/8 preset models are also presented with same control strategy.

Hoang Le-Huy, Laval University
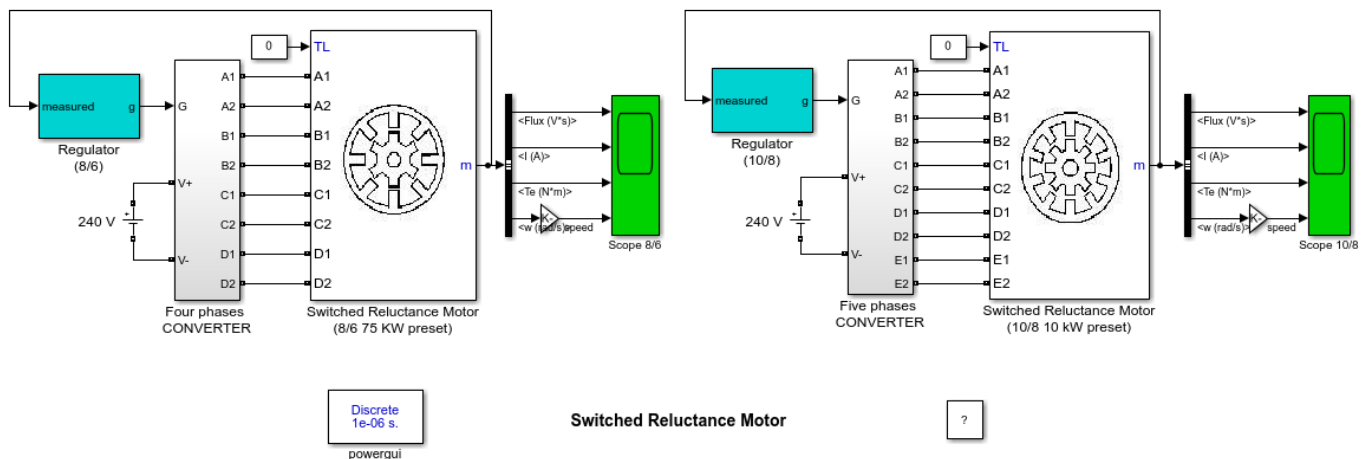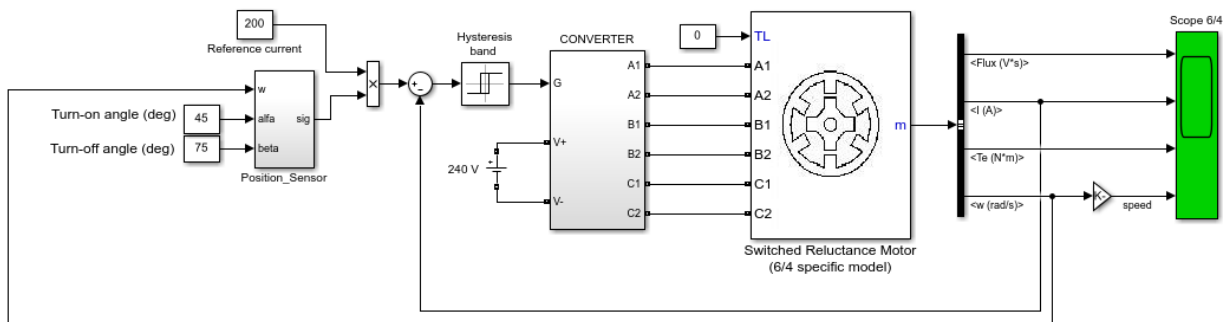
Description (6/4 specific model)

The SRM is fed by a three-phase asymmetrical power converter having three legs, each of which consists of two IGBTs and two free-wheeling diodes. During conduction periods, the active IGBTs apply positive source voltage to the stator windings to drive positive currents into the phase windings. During free-wheeling periods, negative voltage is applied to the windings and the stored energy is returned to the power DC source through the diodes. The fall time of the currents in motor windings can be thus reduced. By using a position sensor attached to the rotor, the turn-on and turn-off angles of the motor phases can be accurately imposed. These switching angle can be used to control the developed torque waveforms. The phase currents are independently controlled by three hysteresis controllers which generate the IGBTs drive signals by comparing the measured currents with the references. The IGBTs switching frequency is mainly determined by the hysteresis band.

Simulation (6/4 specific model)

In this example, a DC supply voltage of 240 V is used. The converter turn-on and turn-off angles are kept constant at 45 deg and 75 deg, respectively, over the speed range. The reference current is 200

A and the hysteresis band is chosen as +-10 A. The SRM is started by applying the step reference to the regulator input. The acceleration rate depends on the load characteristics. To shorten the starting time, a very light load was chosen. Since only the currents are controlled, the motor speed will increase according to the mechanical dynamics of the system. The SRM drive waveforms (phase voltages, magnetic flux, windings currents, motor torque, motor speed) are displayed on the scope. As can be noted, the SRM torque has a very high torque ripple component which is due to the transitions of the currents from one phase to the following one. This torque ripple is a particular characteristic of the SRM and it depends mainly on the converter s turn-on and turn-off angles. In observing the drive's waveforms, we can remark that the SRM operation speed range can be divided into two regions according to the converter operating mode: current-controlled and voltage-fed.

Current-controlled mode

From stand still up to about 3000 rpm, the motor's emf is low and the current can be regulated to the reference value. In this operation mode, the average value of the developed torque is approximately proportional to the current reference. In addition to the torque ripple due to phase transitions, we note also the torque ripple created by the switching of the hysteresis regulator. This operation mode is also called constant torque operation.

Voltage-fed mode

For speeds above 3000 rpm, the motor's emf is high and the phase currents cannot attain the reference value imposed by the current regulators. The converter operation changes naturally to voltage-fed mode in which there is no modulation of the power switches. They remain closed during their active periods and the constant DC supply voltage is continuously applied to the phase windings. This results in linear varying flux waveforms as shown on the scope. In voltage-fed mode, the SRM develops its 'natural' characteristic in which the average value of the developed torque is inversely proportional to the motor speed. Since the hysteresis regulator is inactive in this case, only torque ripple due to phase transitions is present in the torque waveforms.

Optimization of the Torque Characteristic - Adaptive Switching Angle

In SRM drives, both the average torque and torque ripple are affected by the turn-on and turn-off angles and by the current waveforms in the motor phases. And these characteristics change as a function of the motor speed. In many applications, electric vehicle drives for instance, it is highly desirable to have highest torque/ampere ratio and lowest torque ripple and this over a widest speed range possible. The SRM torque characteristic can be optimized by applying appropriated pre-calculated turn-on and turn-off angles in function of the motor current and speed. The optimum values of optimum angles can be stored in a 2-D lookup table.

Brushless DC Motor Fed by Six-Step Inverter

This example shows the use of a Six-Step Switch-on mode for a trapezoidal PMSM motor rated 1kW, 3000 rpm and speed regulated.

Olivier Tremblay, Louis-A. Dessaint (Ecole de technologie superieure, Montreal)
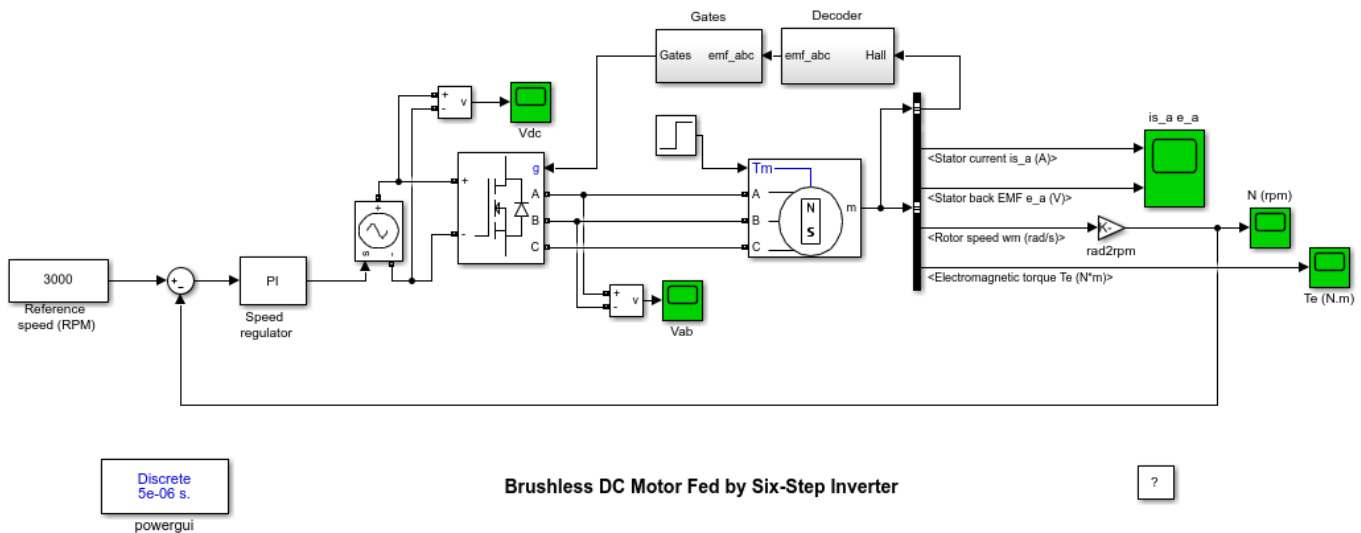
Description

A three-phase motor rated 1 kW, 500 Vdc, 3000 rpm is fed by a six step voltage inverter. The inverter is a MOSFET bridge of the Specialized Power Systems library. A speed regulator is used to control the DC bus voltage. The inverter gates signals are produced by decoding the Hall effect signals of the motor. The three-phase output of the inverter are applied to the PMSM block's stator windings. The load torque applied to the machine's shaft is first set to 0 and steps to its nominal value (3 N.m) at t = 0.1 s.

Two control loops are used. The inner loop synchronises the inverter gates signals with the electromotive forces. The outer loop controls the motor's speed by varying the DC bus voltage.

Simulation

Observe the sawtooth shape of the motor currents. That's caused by the DC bus which applies a constant voltage during 120 electrical degrees to the motor inductances. The initial current is high and decreases during the acceleration to the nominal speed. When the nominal torque is applied, the stator current increases to maintain the nominal speed. The sawtooth waveform is also observed in the electromagnetic torque signal Te. However, the motor's inertia prevents this noise from appearing in the motor's speed waveform.

Change the "Back EMF flat area" of the motor from 120 to 0 and observe the waveform of the electromotive force e_a.

Performance of Frequency Measurement (Phasor)

This example shows the use of two Frequency (Phasor) blocks to measure the frequency variation in a network following a line outage.

Description

The network that is represented corresponds to the Kundur's Four-Machine Two-Area Test System, well known to study low frequency electromechanical oscillations in large interconnected power systems.

In this example, two Frequency (phasor) blocks are used to measure the frequency variation at the B1 bus (Area 1) and B2 bus (Area 2) after the transmission line B is disconnected from the network. The two measured frequencies are compared to the average system frequency. We can observe that the frequency tends to stabilize to 60.14 Hz around 75 sec, where a simple frequency regulation is performed (in open-loop) by reducing the reference power of the machine M1 in the Area 1.

PMU (PLL-based, Positive-Sequence) Kundur's Two-Area System

This example shows the use of the PMU (PLL-Based, Positive-Sequence) block within the Kundur's Two-Area System.
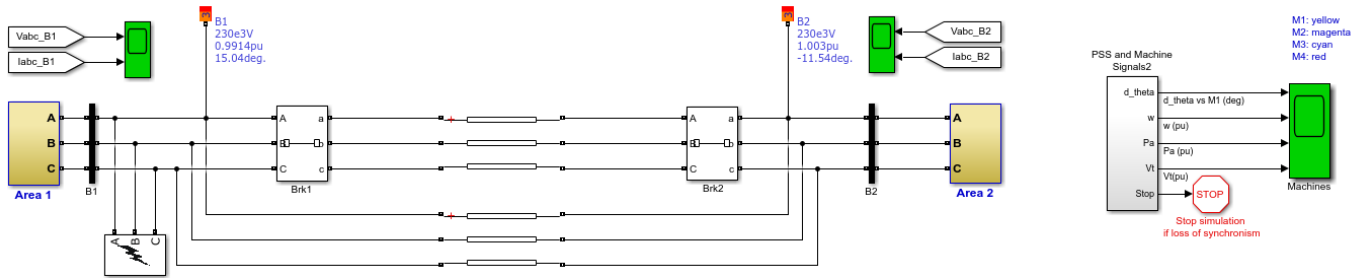
Kundur Two-Area System

This example shows the use of phasor measurement units - PMU (PLL-based, positive sequence) in the Kundur Two-Area System circuit. The system presents eleven buses and two areas, connected by a weak tie between bus B1 and bus B2.

Learn more about this example.
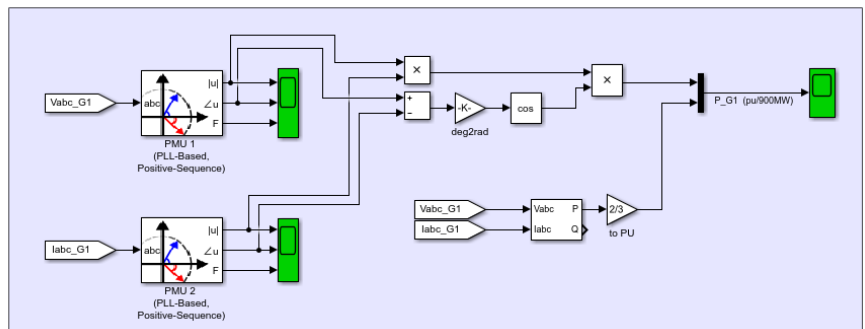
Description

The Kundur's Two-Area System used in this example can be found on page 813 in the textbook 'Power System Stability and Control', written by P. Kundur [1]. Figure below shows basic topology.
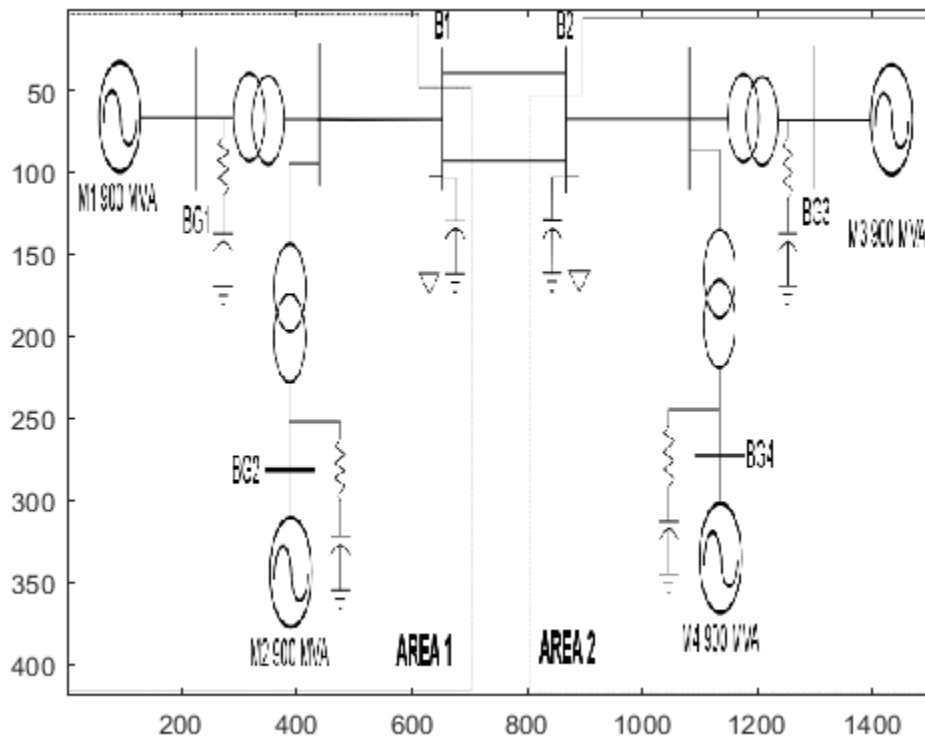
The system presents eleven buses and two areas, connected by a weak tie between bus B1 and bus B2.

Two loads are applied to the system at buses B1 and B2. In addition, two shunt capacitors are also connected to bus B1 and bus B2 as shown in the topology above.

Simulation

In Model Configuration Parameters, select 'Fixed-step' as simulation type and 'Discrete' as solver.

PMU sample time is Ts_PMU = 1/60/64 = 20.42 us. However, the network model sample time Ts = 50 us.

Open the scopes and start the simulation. Zoom on voltage and current phasors right after the fault occurs and notice the transient.

A three-phase fault occurs at 2 seconds of simulation. After 0.10 seconds, the three-phase fault is cleared and a new steady-state is presented until the end of the simulation at 25 seconds, as shown in the plot below.
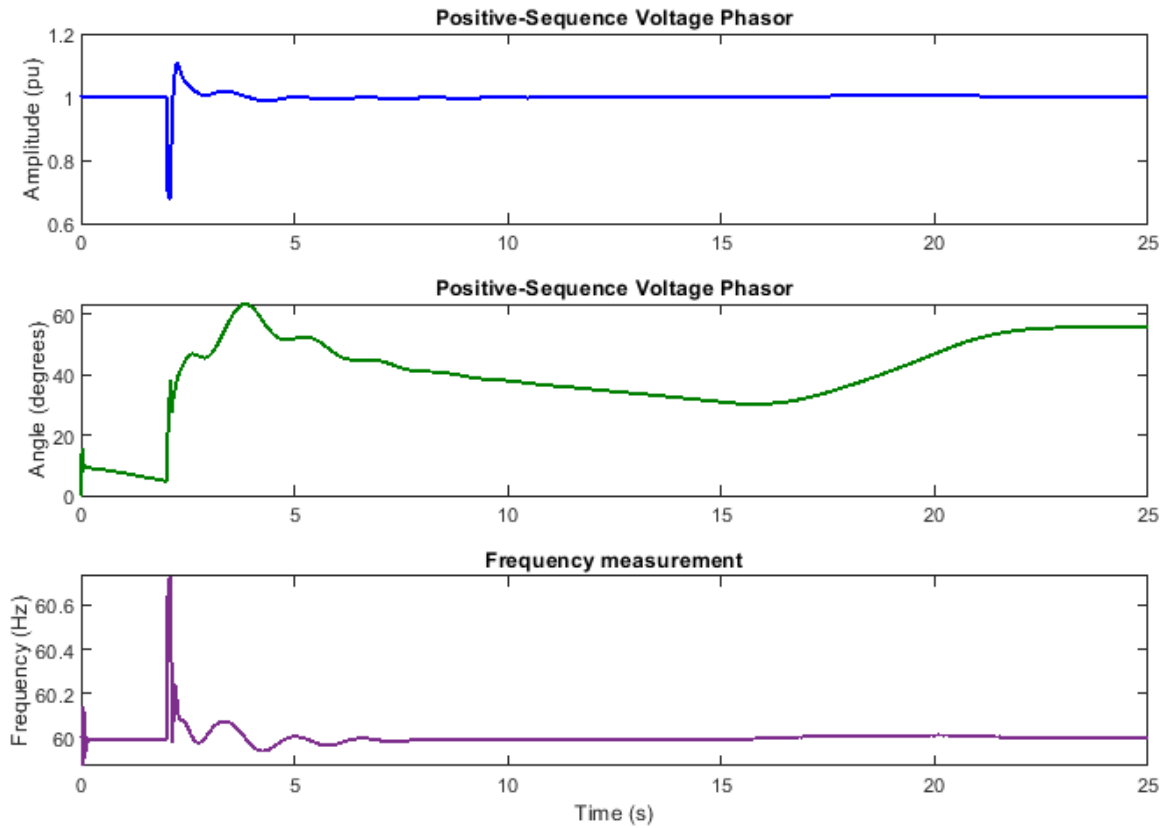
Change the reporting rate factor and the sampling rate. Compare the shape of the waveforms shown on the scope.

Finally, compare the active power computed from the PMU phasors with the three-phase instantaneous power block. The plot below shows how the waveforms match, particularly during fault condition.

Positive-Sequence Voltage Phasor

Positive-Sequence Voltage Phasor
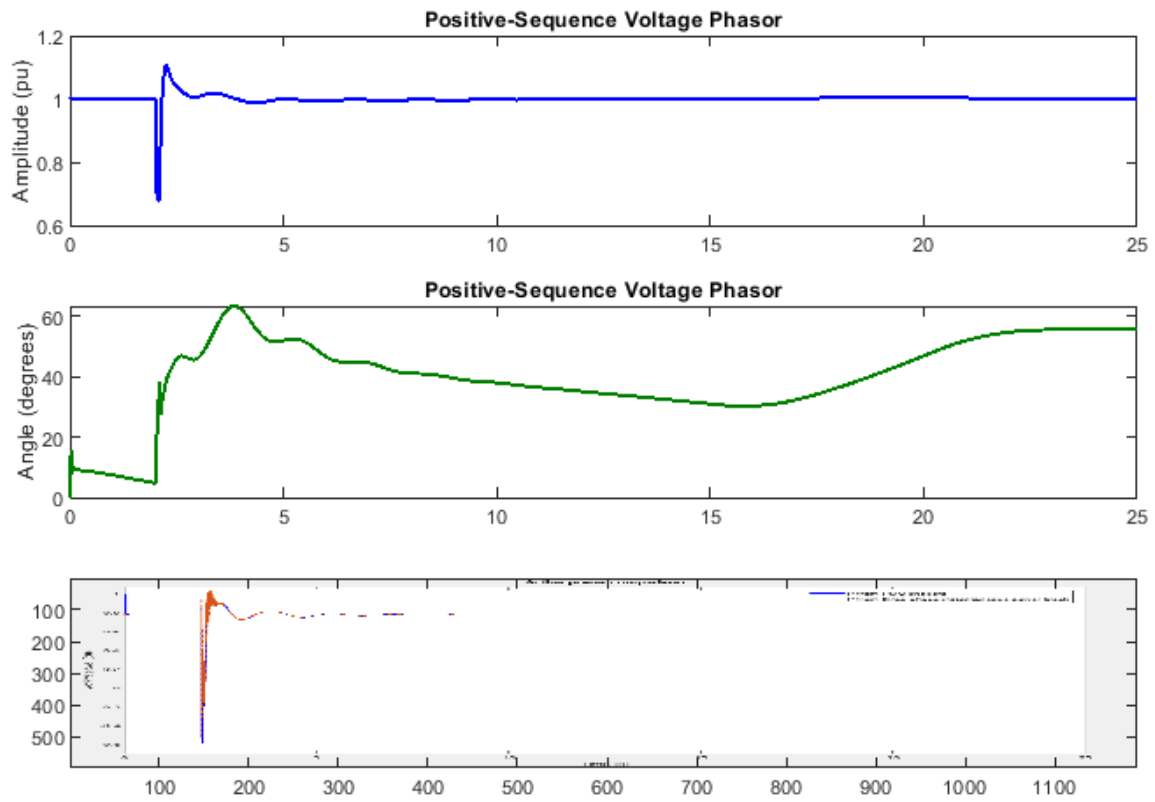
References

[1] P. Kundur, N. J. Balu, and M. G. Lauby, Power system stability and control, vol. 7. McGraw-hi

PMU (PLL-based, Positive-Sequence) Benchmark

This example shows the use of a phasor measurement unit - PMU (PLL-based, Positive-Sequence) in a benchmark circuit.
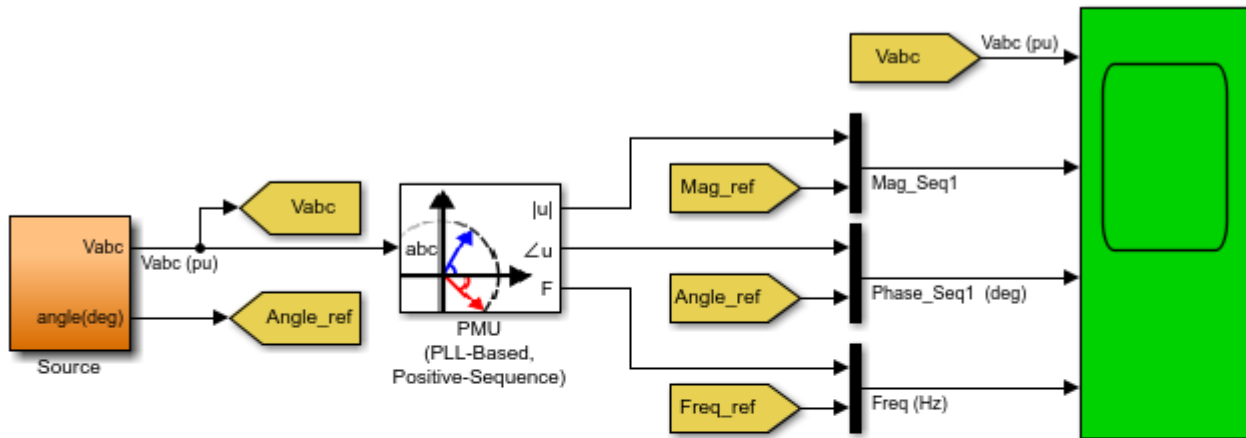
PMU (PLL-Based, Positive-Sequence) Benchmark

This example shows the PMU block that computes the positive-sequence component of a three-phase signal containing a series of events in the input signal (unbalanced condition, harmonics, frequency modulation).

Learn more about this example.

Description

The PMU (PLL-Based, Positive-Sequence) benchmark model computes the positive-sequence component of a three-phase signal containing a series of events in the input signal (unbalanced condition, harmonics, frequency modulation).

It shows that the PMU (PLL-Based, Positive-Sequence) block outputs accurate magnitude, phase and frequency even if the input signal varies during the simulation.

Simulation

In Model Configuration Parameters, select "Variable-step" as simulation type and "Auto (Automatic solver selection)" as the solver.

Sequence of simulated events:

1. t = 0: Positive-sequence voltage = 1 pu, phase 0 deg

2. 0.2 s < t < 0.4 s: Positive-sequence voltage falls to 0 pu

3. t > 0.5 s: Sinusoidal frequency modulation (58/62 Hz Freq = 1 Hz)

4. 1 s < t < 1.5 s: Adding unbalanced condition (0.4 pu of negative-sequence and 0.3 pu of zero-sequence, no harmonics)

5. t = 1.75 s: Phase shift of -90 deg

6. t > 2 s: Adding harmonic condition without unbalance (20% 3rd + 30% 4th)

Change the reporting rate factor and the sampling rate. Compare the shape of the waveforms shown on the scope.

Finally, compare the reference signals (magenta) and the PMU signals (yellow) of frequency, magnitude and phase.

IEEE 13 Node Test Feeder

This example shows the use of the Load Flow tool of Powergui to initialize the IEEE 13 Node Test Feeder circuit.

G. Sybille (Hydro-Quebec)
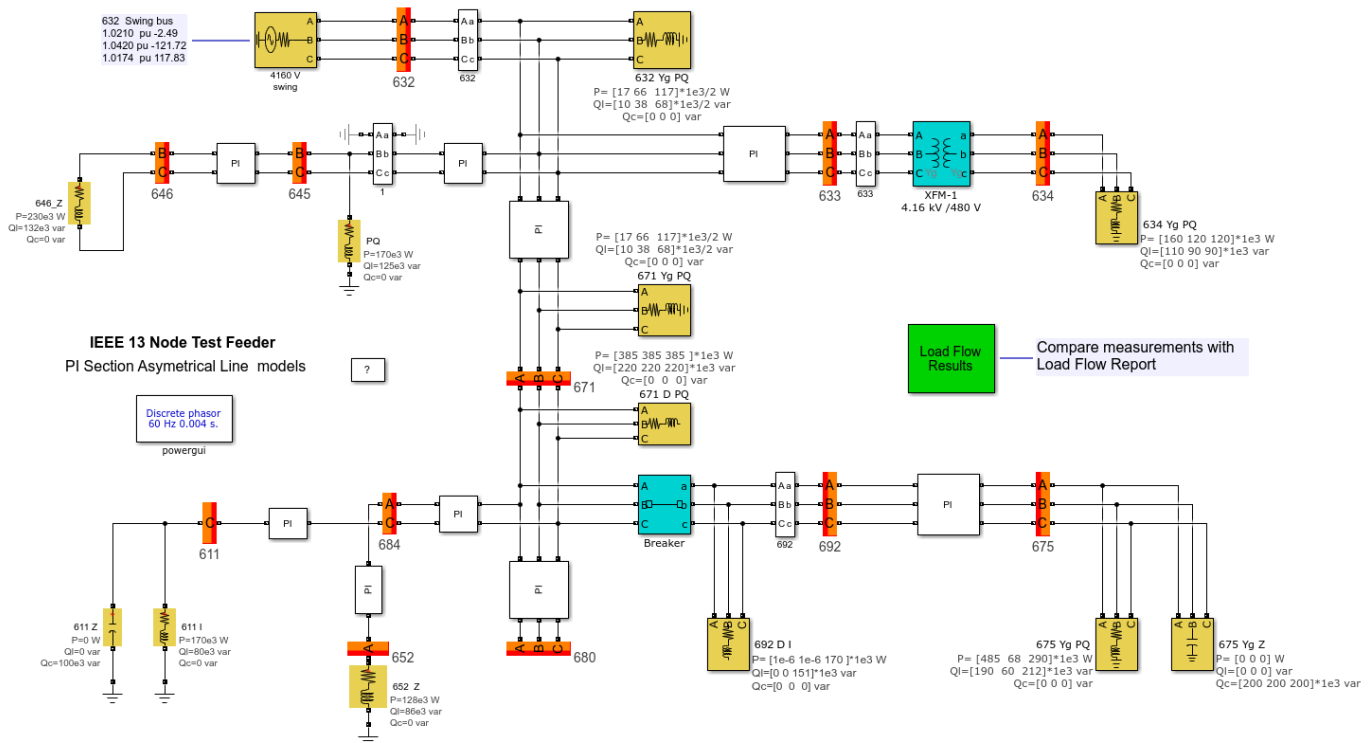
Description

Twelve Load Flow Bus blocks are used to compute an unbalanced load flow on a model representing the IEEE 13 Node Test Feeder circuit, originally published by the IEEE Distribution System Analysis Subcommittee Report. Note that the model does not include the regulating transformer between nodes 650 and 632 of the reference test model.

Simulation

Open the Load Flow Tool of Powergui and press the Compute button. Press the 'Apply' button to apply the load flow solution to the model in order to start the simulation in steady-state. Note that you can view the individual bus voltage magnitude and phase angle values in the corresponding 'Load flow Bus' block's 'Load Flow' tab or you can specify them as block annotations for convenience.

Press the 'Report' button to get a report that shows a load flow summary and detailed load flow results at each bus. Partial load flow report results are reproduced in the Power-Flow Result subsystem in the model (The Green block). Start simulation and check that it starts in steady state, with expected power flow.

Flickermeter Statistical Analysis Module

This example shows a method to compute short-term flicker severity of a phase-to-ground voltage taken from one feeder of a given remote community.
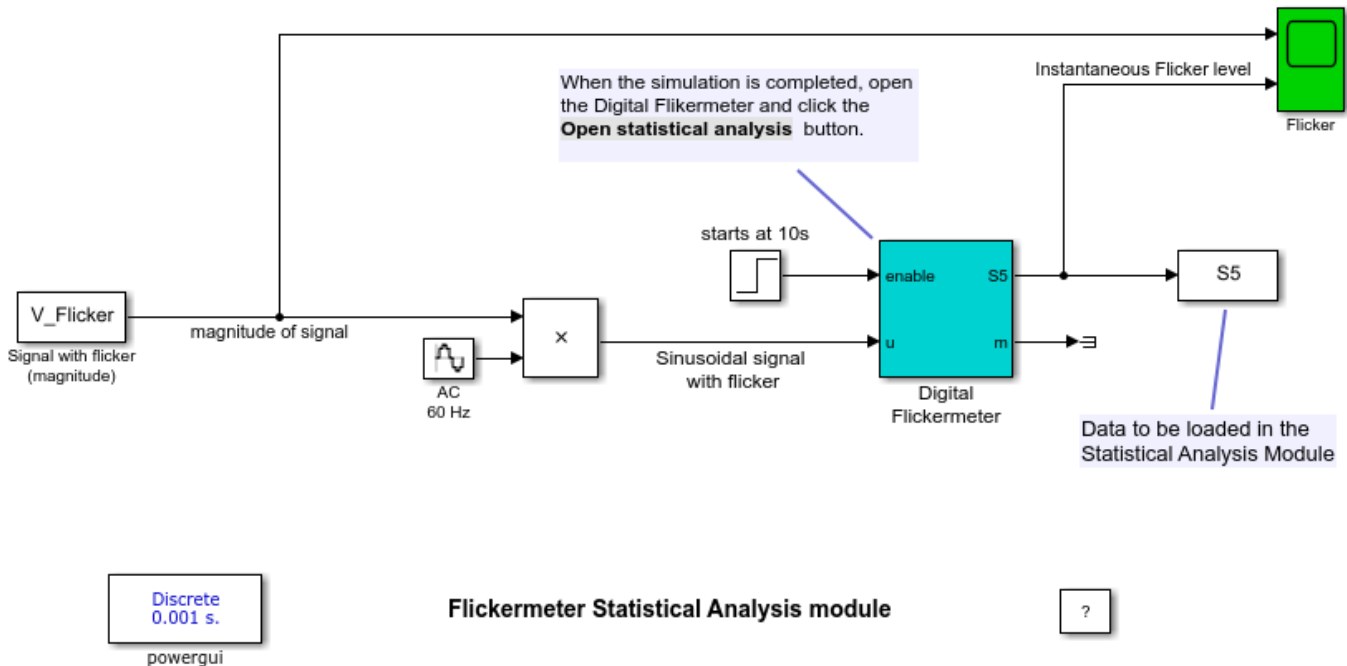
Description

The statistical analysis of instantaneous flicker level is performed on a phase-to-ground voltage taken from one feeder of a given network. The analyzed signal comes from a phasor simulation of 650 sec of duration of a detail model of a remote community (not discussed here).

We can observe in the analyzed signal (Signal with flicker) that a large and relatively slow voltage fluctuation is superimposed on small voltage fluctuations induced by distributed thermal storage systems. It is apparent that the slow moving voltage fluctuation did not induce significant flicker compared to the small voltage fluctuations.

The magnitude of the phasor signal is used to regenerate a 60 Hz sinewave voltage that is connected at the input of the Digital Flickermeter block. Doing so, the block can compute the instantaneous flicker level present in the voltage. The signal is saved in the S5 workspace variable. The statistical analysis module of the flickermeter is used to analyze the S5 signal.

Simulation

1. Run the simulation. When completed, click the Open statistical analysis button of the flickermeter block.

2. In the Statistical Analysis of Instantaneous Flicker Level window, load the S5 signal from workspace. The instantaneous flicker level signal is then displayed in the upper diagram.

3. In the analysis section, set the start time value to 50 seconds to exclude from the analysis the initial spike in amplitude in the instantaneous flicker level.

4. Click the Compute button.

The cumulative probability function (CFP) is then displayed in the lower diagram. The short-term flicker severity value Pst is displayed along with the P0s, P1s, P3s, P10s, and P50s percentiles corresponding to the flicker levels exceeded for 0.1%, 1%, 3%, 10% and 50% of the time during the observed period.

Power Flow Control and Line Deicing Using a Bundle-Controlled Line Impedance Modulator (LIM)

This example shows how power flow control and deicing of a transmission line could be implemented using the Line Impedance Modulator (LIM) technology.
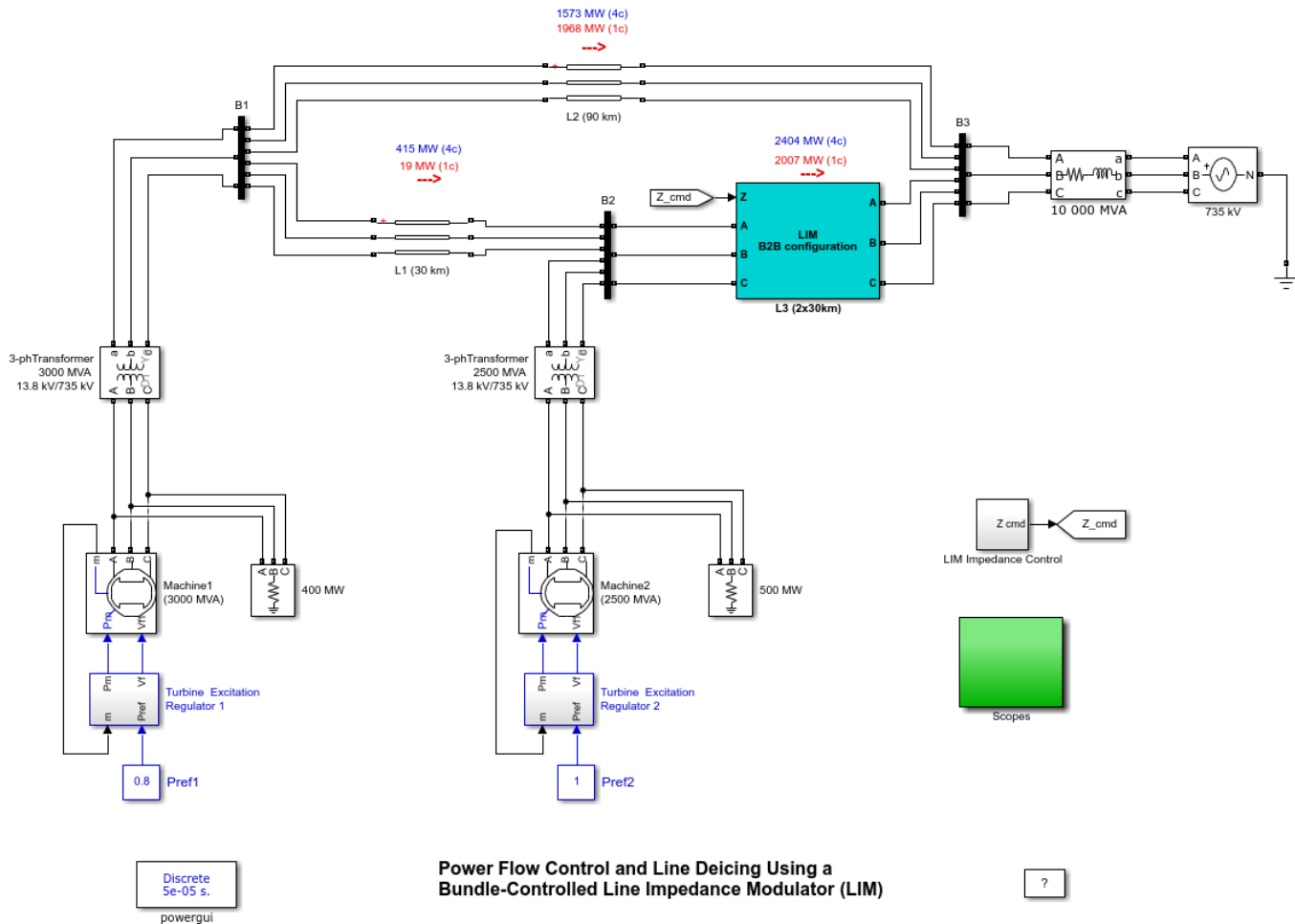
Technology Description

The Bundle-Controlled Line Impedance Modulator (LIM) is a distributed FACTS device that has the capability of increasing the impedance of high-voltage transmission lines. Using switches connected in series with each sub-conductors of a bundle, the LIM also allows concentrating each phase current into one sub-conductor at a time. It is then possible to de-ice each sub-conductor by the Joule effect, one after the other. This example shows how power flow control and line deicing could be implemented with a LIM. More information is available in the papers in reference.

Demonstration

This example shows two generators and a 10,000 MVA equivalent power system interconnected by three 735-kV transmission lines. Lines L1 and L2 are conventional 30- and 90-km long transmission

lines. L3 is a 60-km long line with two switching modules installed at its mid-point. Line L3 with the two line segments forms a back-to-back LIM. Power outputs of generators 1 and 2 are respectively set at 2400 and 2500 MW. Given their respective loads, connected at the 13.8 kV for the sake of simplicity, they each supply 2000 MW to bus B1 and B2 respectively. Line L2 being much longer than L3, its normal power flow is only 1573 MW when the LIM's switches are all closed. Power flows on L1 and L3 are 415 and 2404 MW respectively. The impedance control command transmitted to the LIM is produced by a signal generator inside the LIM impedance Control block. The Z cmd signal ramps between 0.5 and 3 s from its minimum value 1.0 (when all four subconductors are used) to its maximum value 1.642 pu (when only one subconductor per bundle is used) . It then varies in steps after t= 4 s. As shown inside the LIM subsystem, a look-up table associates 58 combinations of 24 switch states to the requested impedance command. As explained in [6], these combinations have been selected to maintain negative- and zero-sequence currents at a level smaller or equal than observed when all switches are closed. The 58 switch combinations used in this example represent a very small subset of the 33752 switch combinations provided by a pair of BCL segments. Each line segment is represented by a 14-conductor Exact-Pi section . Line resistances, inductances and capacitances are provided in the power_LineImpedanceModulator_init.m file. The 14x14 impedance and admittance matrices are automatically loaded in the workspace (see File/Model Properties/ Callbacks/PreloadFcn).

Run this example and observe the following sequence of events in Scope 1.

- At t=0 s, all the LIM's switches are closed. Power flows in each line are annotated in blue in the example next to each transmission line.
- At t=0.5 s, the impedance signal Zcmd ramps from 1 to 1.642 pu as shown by the yellow trace. For each values of Zcmd, the look-up table provides the corresponding switch combinations. The switch combinations transmitted to the switching modules are sampled here every 0.1 s. This gives the discretized impedance signal Zdisc (magenta).
- At t=3 s, the LIM impedance is maximum. Note that power flows in L2 (magenta) and L3 (blue) are almost equal. Power flows at this point are annotated in red in the example.
- At t=4 s, the LIM impedance is set to 1 pu which closes all switches. As a result, power flows in the lines vary abruptly within 1 cycle. This perturbation forces the synchronous generators governors to react and stabilize the power flows back to the initial values prevailing at t=0 s.
- At t=5 s, the LIM impedance signal returns to 1.642 pu which again induces a power flow perturbation.
- At 6.3 s, the LIM impedance is reduced down to 1 pu in three large steps.

Power Flow Control

As shown in the example, when the switches of the LIM are operated, impedance of line L3 progressively increases up to the point where only one switch remains closed per switching module. With only one conductor in service per bundle power flows in line L2 and L3 are nearly equals even though L2 is 50% longer than L3. Power flows in line L1 becomes almost zero. This shows that LIMs have the capability of reducing power flows of overloaded transmission lines. The step changes beyond t=4 s shows that LIMs can also quickly vary the line impedance if required. Scope 2 shows the bus B2 sequence voltages and the sequence currents flowing out of B2 toward line L3. This is the line current of the back-to-back LIM. It can be seen that for all switch combinations used, the negative- and zero-sequence voltages and currents remain lower than the initial values obtained with all switches closed. Hence LIMs can be operated to produce power ramping or power step without increasing negative- and zero-sequence levels. Scope 3 shows that the voltages across the switches of the phase A switching module located on the bus B2 side. It can be seen that transient voltages remain within 35 kV which allows the use of medium voltage switching devices. The maximum rms voltage in steady-state, visible at t = 3.9s, is 13.4 kV.

Line Deicing

Scope 3 also shows the switch currents of the phase A switching module located on the bus B2 side. With all switches closed, switch currents are initially 465 A rms. With all but one switch opened at t=3.9 s, it can be seen that the switch current of subconductor 2 reaches 1533 A rms. Hence, although the power flow in line L3 has been reduced by 17%, the subconductor current has increased by a 3.3 factor. Such a current is large enough for simultaneously deicing by the Joule effect three subconductors (one per phase) in both 30-km BCL segments. Once a first subconductor is de-iced in each bundle of both BCL segments, three other switch combinations can be used to completely de-ice the transmission line. Note that the switch combination table provided in the example is appropriate for power flow control. Other switch combination tables would be used to force specific subconductor deicing sequence and avoid bundle rotation. Note that if line currents had initially been too low for reaching a deicing level, it could have been possible to open the switches of one 30-km segment only. The smaller impedance increase provided by one BCL segment would then lead to a higher current in the sub-conductors to de-ice. Also, accordingly to the concept of the smart power grid where each line would be equipped with switching modules [4-5], impedance of line L2 could be increased to divert even more current into line L3.

References

1 [1] P. Couture, J. Brochu, G. Sybille, P. Giroux and A. O. Barry, "Power flow and stability control using an integrated HV bundle-controlled line-impedance modulator", IEEE Trans. Power Del., vol. 25, no. 4, pp. 2940-2949, Oct. 2010.

2 [2] P. Couture, "Smart Power Line and Photonic de-icer concepts for transmission-line capacity and reliability improvement" Cold Reg. Sci. Technol. 65 (2011), Jan., 13-22.

3 [3] P. Couture, "Switching modules for the extraction/injection of power (without ground or phase reference) from a bundled HV line," IEEE Trans. Power Delivery, vol. 19, No3, pp. 1259-1266, July 2004.

4 [4] P. Couture, "Switching apparatus, control system and method for varying an impedance of a phase line", Patent pending, PCT/CA2011/00850, July 22, 2011.

5 [5] P. Couture, J. Brochu, B. Francoeur, R. Morin, D. H. Nguyen, K. Slimani, A. Turgeon and P. Van Dyke, "Smart Power Line (SPL) experimental research project," CIGRE 2014.

6 [6] J. Brochu and P. Couture, "Load Flow Modeling of the Integrated Bundle-controlled Line Impedance Modulator," IEEE Trans. Power Delivery, accepted and currently available on IEEExplore.

Initializing a 29-Bus, 7-Power Plant Network With the Load Flow Tool of Powergui

This example shows the use of the Load Flow tool of Powergui to initialize a 29-bus 735 kV network with detailed modeling of power plants using hydraulic turbines, speed regulation, excitation systems and power system stabilizers.

G. Sybille (Hydro-Quebec)
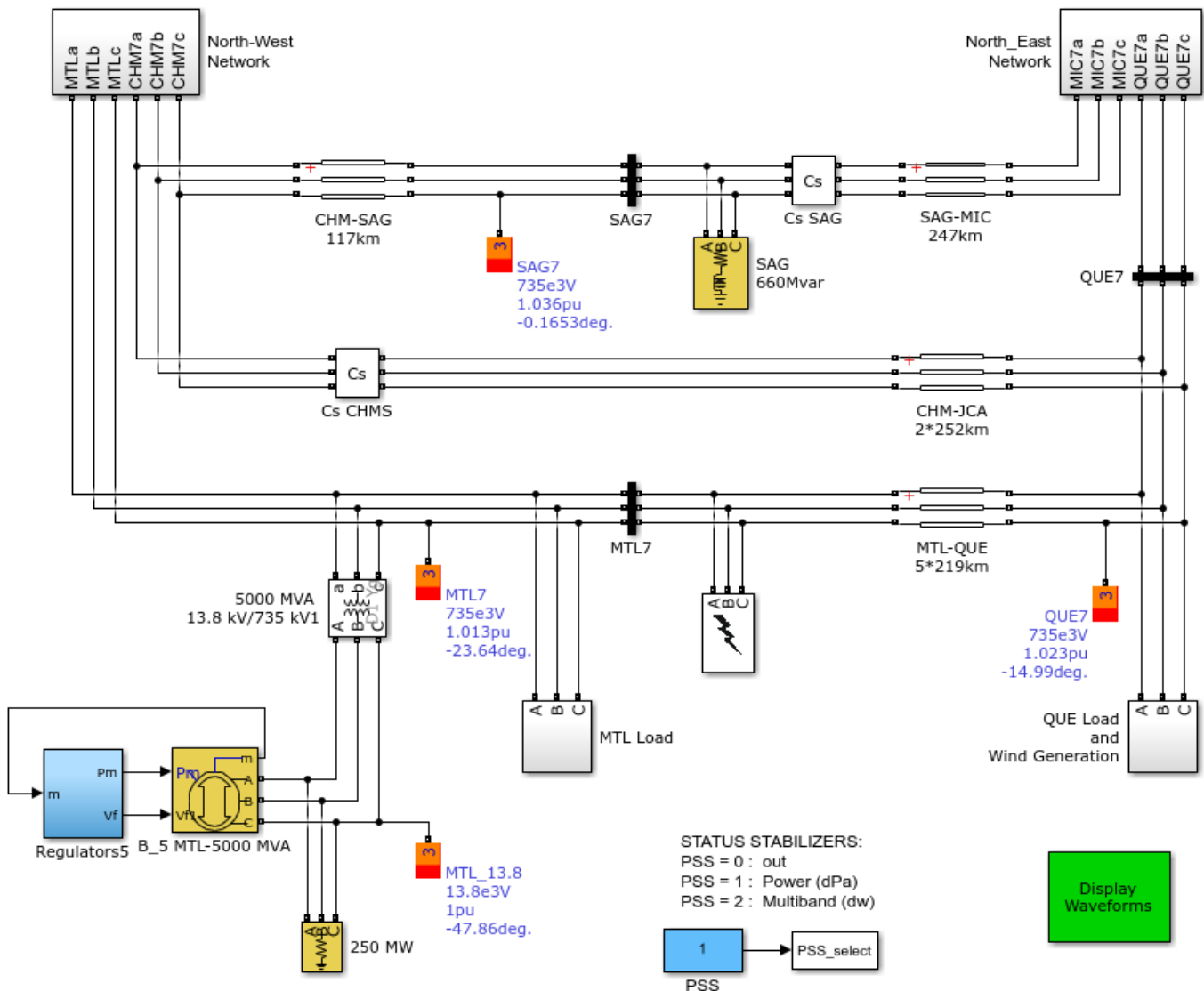
Initializing a 29-Bus Power Plant Network with the Load Flow Tool of Powergui

Description

The model shows a 735-kV transmission network with detailed modeling of seven 13.8 kV power plants (total available generation =26200 MVA) including hydraulic turbines, speed regulation, excitation systems and power system stabilizers. The 735-kV transmission network is both series and shunt compensated using fixed capacitors and inductors. The load is lumped at two buses (MTL7 and QUE7).

The MTL Load subsystem connected to the MTL7 bus consists of four types of load blocks connected on the 25 kV distribution system through 735 kV /230 kV and 230 kV/ 25 kV transformers.

The QUE Load and Wind Generation subsystem uses a 6000 MW load (constant Z and constant PQ) connected on the 120 kV bus. A 9 MW wind farm using an asynchronous generator is connected to the 120 kV bus through a 25-kV feeder and a 25 kV/120 KV transformer.

The model is discretized using a 50 usec. sample time. A six-cycle three-phase fault is programmed at MTL7 bus. Look at waveforms in the scopes (inside the Display Waveforms green subsystem) and notice that simulation starts in steady state.

Simulation

In the Powergui menu, select 'Load Flow'. A new window appears. A summary of the load flow settings is displayed in a table. Press the 'Compute' button to solve the load flow. The table now displays the actual active and reactive powers of the load flow blocks of the model.

Press the 'Apply' button to apply the load flow solution to the model in order to start the simulation in steady-state. Note that the Load flow Bus block displays the bus voltage magnitude and phase angle as block annotations.

Press the 'Report' button to get a report that shows a load flow summary and detailed load flow results at each bus.

Finally, start simulation and check that it starts in steady state.

Initializing a 5-Bus Network with the Load Flow Tool of Powergui

This example shows the use of the Load Flow tool of Powergui to initialize a simple 5-bus network consisting of a wind farm connected to a 25-kV distribution feeder and exporting power to a 120 kV network.

G. Sybille (Hydro-Quebec)
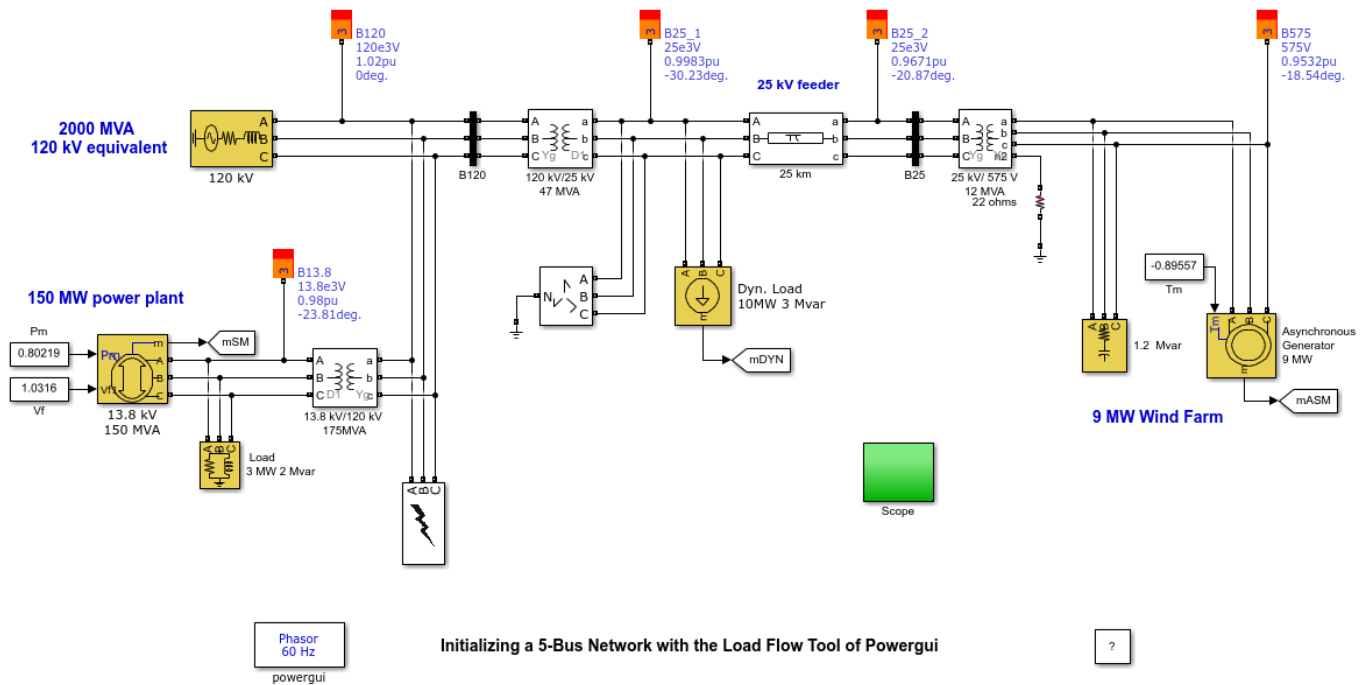
Description

The model shows a 9 MW wind farm using asynchronous generators and exporting power to a 120 kV network through a 25-kV distribution feeder. The 120 kV network is modeled by a simple inductive voltage source (short circuit power of 1200 MVA) using the Three-Phase Source block. A 150 MW power plant using a 13.8 kV synchronous generator is connected at the 120 kV bus through a 13.8 kV/ 120 kV transformer.

Simulation

The five Load Flow Bus blocks are used to specify the bus base voltages and to specify the voltage at PV bus and the voltage and angle of the swing bus.

The Load flow parameters are defined in the Load Flow tab of the Synchronous and Asynchronous machine blocks, Three-Phase Source block, Three-Phase Dynamic Load block, and the Three-Phase RLC Load blocks.

Press the 'Apply' button to apply the load flow solution to the model in order to start the simulation in steady-state. Note that the Load flow Bus block displays the bus voltage magnitude and phase angle as block annotations.

Press the 'Report' button to get a report that shows a load flow summary and detailed load flow results at each bus.

Finally, start simulation and check that it starts in steady state.

Flickermeter on a Distribution STATCOM

This example shows a flickermeter model designed according to functional specifications of the international standard IEC 6100-4-15 [1].

Silvano Casoria (Hydro-Quebec)
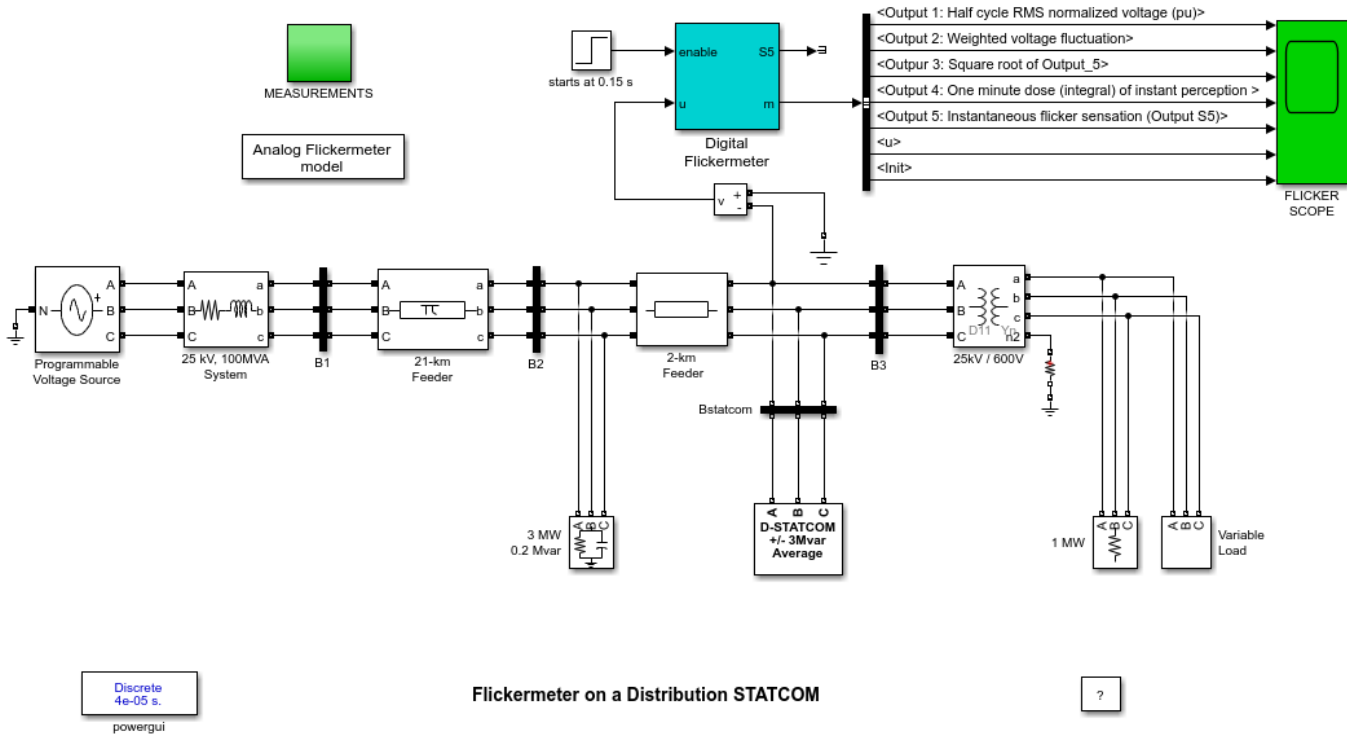
Description

A flickermeter device is used to measure the instantaneous flicker sensation on terminal voltage of a STATCOM unit.

The flickermeter model is designed according to functional specifications of the international standard IEC 6100-4-15 [1]. The flickermeter is a standardized instrument for measuring the flicker obtained by simulation and by statistical analysis of the response of the lamp-eye-brain chain to the input voltage fluctuations. Reference [2] gives a detailed description of the standard block diagram. The statistical analysis (module 5 in standard block diagram) is not modeled in the block.

In order to minimize the initial transient response, the initial conditions are defined for the different transfer functions. A fixed initialization period (Tini = 0.3 s) is required between the instant the model is enabled (input ON = 1) and the computation of the results. The initial conditions calculation are evaluated in the mask initialization section of the block.

The model should be enabled (input ON > 0) after the analyzed signal (input U) is stable. The flickermeter internal outputs of the various modules are available in output m.

In practice output S (instantaneous flicker sensation) stabilizes within 2 seconds after the initialization period is started (Init signal in output m) and Output_4 (One minute dose of instant perception signal in output m) stabilizes after 2 minutes.

Module 1 contains a signal generator for checking the flickermeter setting in the field and a circuit for normalizing (in pu) the RMS value of the input voltage at network frequency (50 or 60 Hz). The generator adds a modulating voltage (fluctuating sinusoidal or triangular signal) to the fundamental (120 Vrms/60 Hz or 230 Vrms/50 Hz). Different relative amplitude (in %) and frequency of the fluctuation are defined in the standard. They should produce at Output_5 an instantaneous flicker sensation of 1.

References

[1] EC Standard 61000-4-15 Ed. 1.1 (2003) Electromagnetic compatibility (EMC)-Part4 Testing and measurement techniques-Section15 Flickermeter: Functional and design specifications [2] Bertola A.; Lazaroiu, G.C.; Roscia, M.; Zaninelli, D.;"A MATLAB®-Simulink® flickermeter model for power quality studies", IEEE® PES 11th International Conference on Harmonics and Quality of Power, September 2004, Lake Placid, USA.

Single-Phase Series Compensated Network

This example shows frequency-domain and time-domain analysis of a series-compensated transmission system.

G. Sybille (Hydro-Quebec)
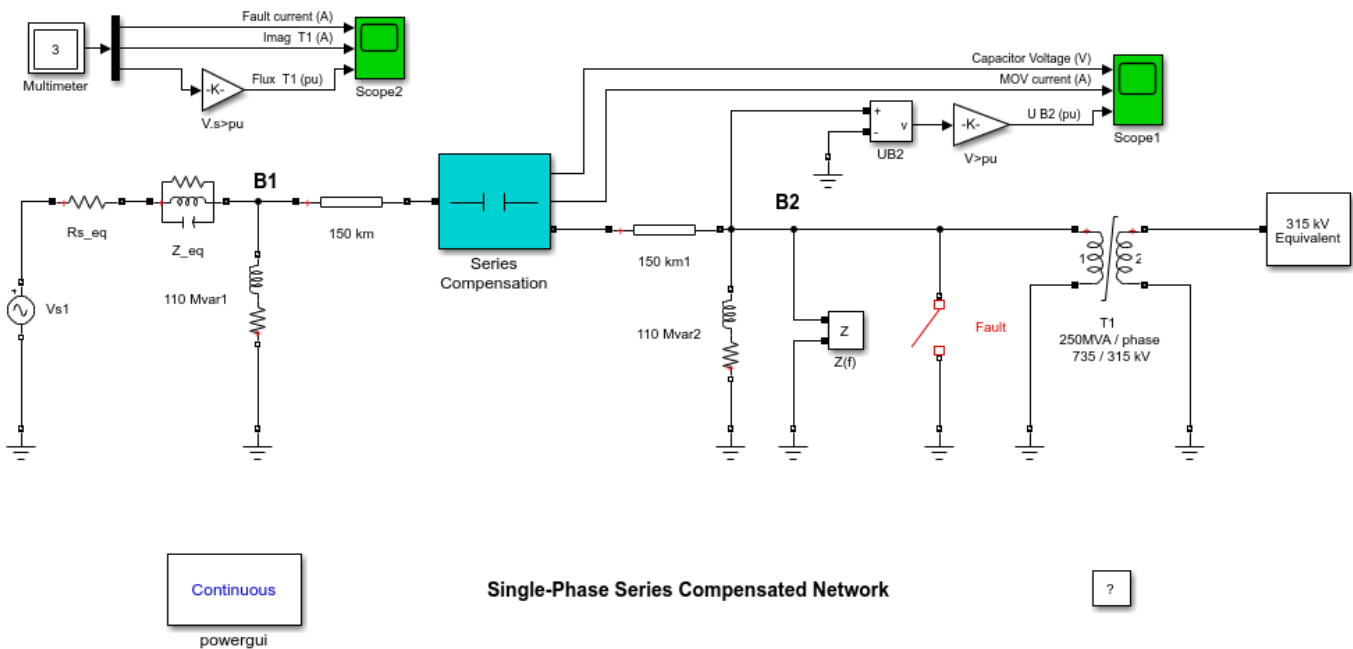
Description

A 735 kV, 300 km line is used to transmit power from bus B1 (735 kV equivalent system) to bus B2 (315 kV equivalent). In order to simplify, only one phase of the system has been represented.

In order to increase the transmission capacity, the line is series compensated at its center by a capacitor representing 40% of the line reactance. The line is also shunt compensated at both ends by a 330 Mvar shunt reactance (110 Mvar /phase). Open the Series Compensation subsystem. Notice that the series capacitor is protected by a metal oxide varistor (MOV) simulated by the Surge Arrester block. The 250 MVA, 735 kV / 315 kV transformer is a Saturable Transformer block simulating one phase of the three-phase 750 MVA transformer. A Multimeter block is used to monitor the fault current as well as the flux and magnetizing current of the transformer.

Simulation

Transient performance of this circuit can be studied when a 6-cycle fault is applied at node B2. The fault is simulated by the Breaker block. Switching times are defined in the Breaker block menu (closing at t = 3 cycles and opening at t = 9 cycles).

Frequency Analysis

In order to understand the transient behavior of this series-compensated network, a frequency analysis is first performed by measuring the Impedance at node B2. This measurement is performed by the Impedance Measurement block connected at node B2. Open the Powergui and in the Tools

menu select 'Impedance vs Frequency Measurement'. Click on Display to compute and display the impedance for the 0 - 500 Hz range. The impedance curves show two main parallel resonances (impedance maxima and phase inversion), corresponding to 15 Hz and 300 Hz modes. The 15 Hz mode is due to a parallel resonance of the series capacitance and the two shunt reactances. The 300 Hz mode is mainly due to resonance of shunt line capacitance and series reactance of the transmission system. These two modes are likely to be excited at fault clearing.

Time Domain Simulation - Fault at Bus B2

Start the simulation and observe waveforms on the two Scopes. At t = 3 cycles, a line-to-ground fault is applied and the fault current reaches 10 kA (trace 1 of Scope2). During the fault, the MOV conducts at every half cycle (trace 2 of Scope1) and the voltage across the capacitor (trace 1 of Scope1) is limited to 263 kV. At t = 9 cycles, the fault is cleared. The 15 Hz mode is clearly seen on the capacitor voltage (trace 1 of Scope1) and bus B2 voltage (trace 3 of Scope1). During fault the flux in the transformer is trapped to around 1 pu. At fault clearing the flux offset and 15 Hz component cause transformer saturation (flux > 1.2 pu, trace 3 of Scope2), producing magnetizing current pulses (trace 2 of Scope2).

Three-Phase Series Compensated Network

This example shows the use of three-phase blocks to study transients on a series-compensated 735-kV transmission system

G. Sybille (Hydro-Quebec)
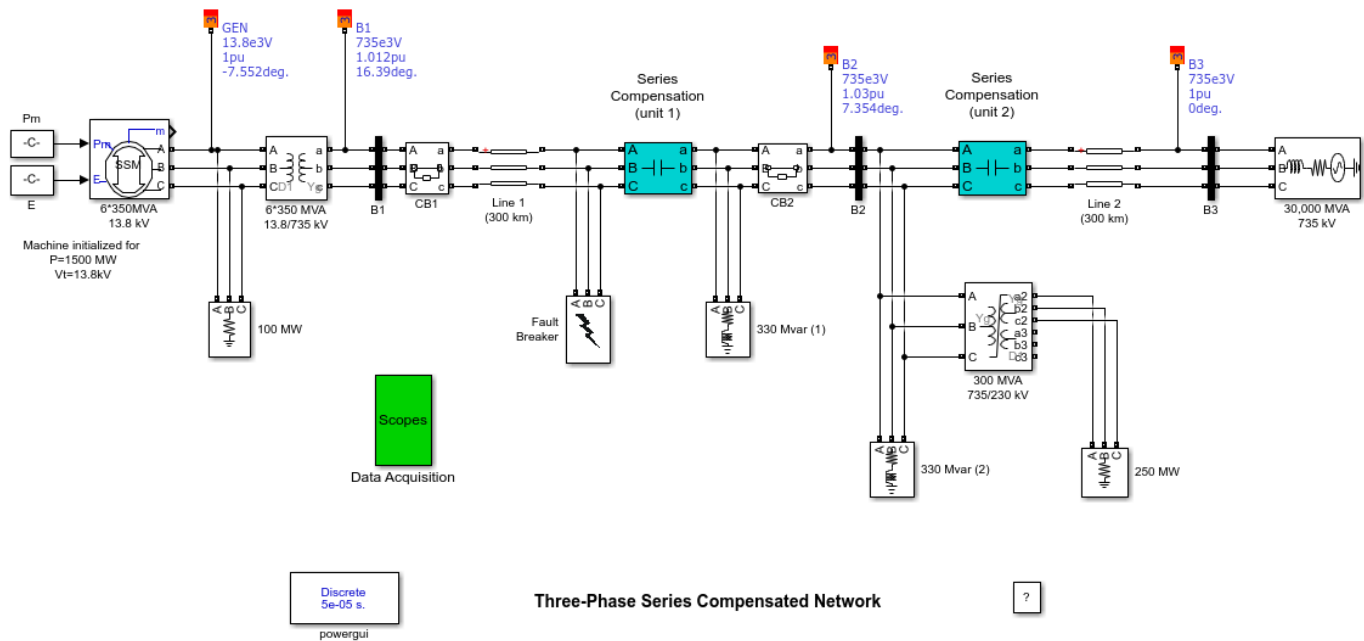
Description

A three-phase, 60 Hz, 735 kV power system transmitting power from a power plant consisting of six 350 MVA generators to an equivalent network through a 600 km transmission line. The transmission line is split in two 300 km lines connected between buses B1,B2, and B3. In order to increase the transmission capacity, each line is series compensated by capacitors representing 40% of the line reactance. Both lines are also shunt compensated by a 330 Mvar shunt reactance. The shunt and series compensation equipments are located at the B2 substation where a 300 MVA 735/230 kV transformer with a 25 kV tertiary winding feeds a 230 kV, 250 MW load. The series compensation subsystems are identical for the two lines. For each line, each phase of the series compensation module contains the series capacitor , a metal oxide varistor (MOV) protecting the capacitor, and a parallel gap protecting the MOV. When the energy dissipated in the MOV exceeds a threshold level of 30 MJ, the gap simulated by a circuit breaker is fired. CB1 and CB2 are the two line circuit breakers .

The generators are simulated with a Simplified Synchronous Machine block. Universal transformer blocks (two-windings and three-windings) are used to model the two transformers. Saturation is implemented on the transformer connected at bus B2. Voltages and currents are measured in B1, B2, and B3 blocks. These blocks are Three-phase V-I Measurement blocks where voltage and current signals are sent to the Data Acquisition block through Goto blocks.

Fault and Line Switching

You now study the transient performance of this circuit when a line-to-ground and three-phase-to-ground faults are applied on line 1. The fault and the two line circuit breakers CB1 and CB2 are

simulated with blocks from the three-phase library. Open the dialog boxes of CB1 and CB2. See how the initial breaker status and switching times are specified. A line-to-ground fault is applied on phase A at t = 1cycle. The two circuit breakers which are initially closed are then open at t = 5 cycles, simulating a fault detection and opening time of 4 cycles. The fault is eliminated at t = 6 cycles, one cycle after line opening.

Simulation

Notice that this system contains the Powergui block. In addition, when you start the system the 'power_3phseriescomp' model, the sampling time Ts = 50e-6 is automatically set in your workspace. The system is therefore be discretized using a 50 microseconds sample time.

Line-to-Ground Fault

Double click the Data Acquisition block and open the three scopes. Start the simulation. As the system has already been initialized (1500 MW generation at the 13.8 kV bus) with the Load Flow utility of the Powergui, the simulation starts in steady state. At t = 1 cycle a line-to-ground fault is applied and the fault current reaches 10 kA . During the fault, the MOV conducts at every half cycle and the energy dissipated in the MOV builds up to 13 MJ.

At t = 5 cycles the line protection relays (not simulated) open breakers CB1 and CB2 and the energy stays constant at 13 MJ. As the maximum energy does not exceed the 30 MJ threshold level, the gap is not fired. After breaker opening the fault current drops to a small value and the line and series capacitance start to discharge through the fault and the shunt reactance. The fault current extinguishes at the first zero crossing after the opening order given to the fault breaker (t = 6 cycles). Then, the series capacitor stops discharging and its voltage oscillates around 220 kV .

Three-Phase-to-Ground Fault

Change the fault type to a three-phase-to-ground fault by checking Phases A, B, and C in the Fault Breaker block. Restart the simulation. Notice that during the fault the energy dissipated in the MOV builds up faster that in the case of a line-to-ground fault. The energy reaches the 30 MJ threshold level after 3 cycles, one cycle before opening of the line breakers. As a result, the gap is fired and the capacitor voltage quickly discharges to zero through the damping circuit.

Simple 6-Pulse HVDC Transmission System

This example shows the steady-state and transient performance of a simple 500 MW (250 kV-2kA) HVDC transmission system.

Simple 6-Pulse HVDC Transmission System 500 MW (250kV- 2kA)
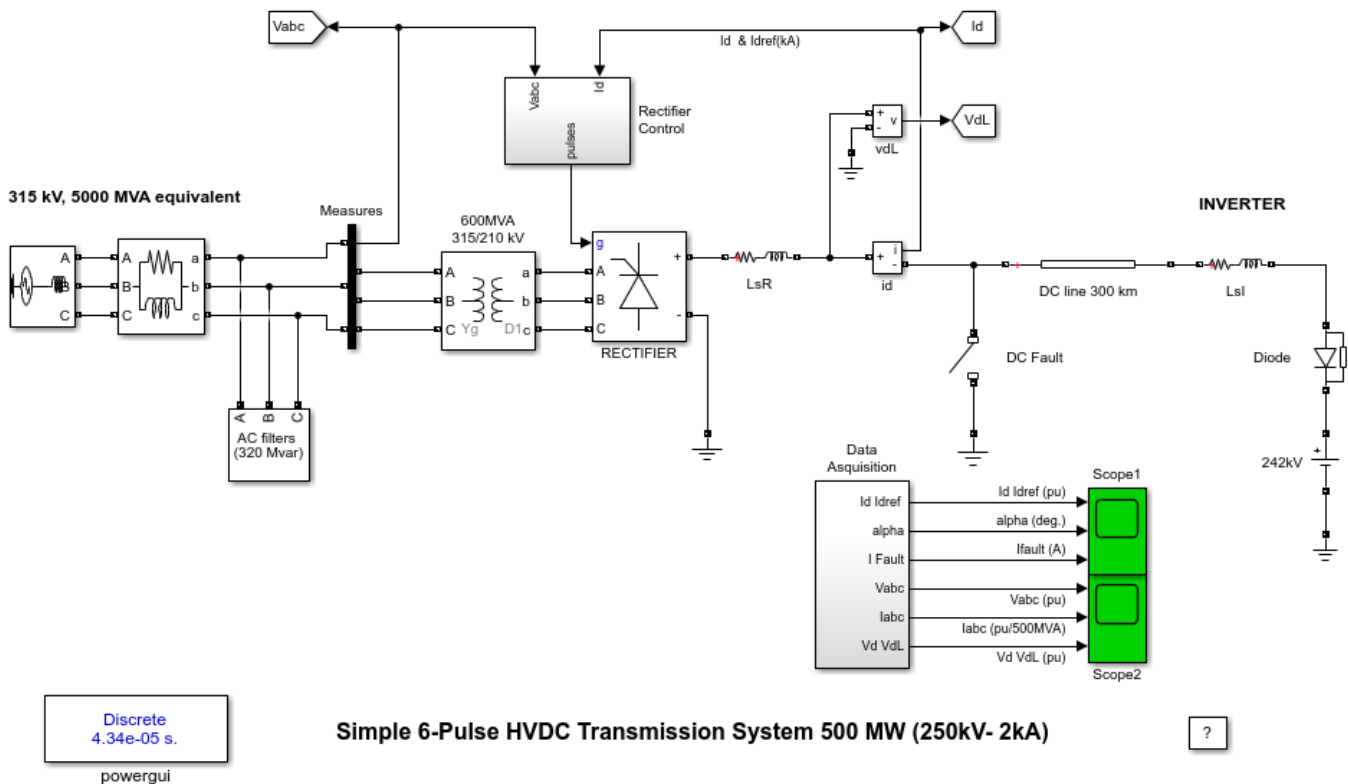
Description

A 500 MW (250 kV, 2 kA) DC interconnection is used to transmit power from a 315 kV, 5000 MVA AC network. The network is simulated by a LLR damped equivalent (impedance angle of 80 degrees at 60 Hz and 3rd harmonic). The converter transformer and the rectifier are modelled respectively with the Universal Transformer and Universal Bridge blocks The converter is a 6-pulse rectifier. It is connected to a 300 km distributed parameter line through a 0.5 H smoothing reactor LsR. The inverter is simulated by a simple DC voltage source in series with a diode (to force unidirectional conduction) and smoothing reactor LsI. The reactive power required by the converter is provided by a set of filters (C bank plus 5th, 7th and high pass filters; total 320 Mvar). Open the AC filter subsystem to see the filter topology. A circuit breaker facilitates the application of a DC line fault on the rectifier side.

Voltages sent to the synchronization system are filtered by 2nd order band pass filters. The whole control system is discretized (Sample time = 1/360/64 = 43.4 us).

The DC line current at the output of the rectifier is compared with a reference. The PI regulator tries to keep the error at zero and outputs the alpha firing angle required by the synchronizing unit. Inputs 3 and 4 of the current regulator allow to bypass the regulator action and to impose the alpha firing angle.

Simulation

Notice that the system is discretized (sample time 1/360/64 = 43.4 us). Setting, the sample time in to zero, will change to continuous integration for the power system.

The system is programmed to start and reach a steady state. Then, a step is applied on the reference current to observe the dynamic response of the regulator. Finally a DC fault is applied on the line.

Start the simulation and observe the following events on Scope1 :

0 < t < 0.3 s

Trace 1 shows the reference current (magenta) and the measured Id current (yellow). The reference current is set to 0.5 pu (1 kA). The DC current starts from zero and reaches a steady-state in 0.1 s. Trace 2 shows the alpha firing angle required to obtain 0.5 pu of current (30 degrees).

0.3 < t < 0.5 s

At t = 0.3 s, the reference current is increased from 0.5 pu (1 kA) to the nominal current 1pu (2 kA). The current regulator responds in approximately 0.1 s (6 cycles). The alpha angle decreases from 30 degrees to 15 degrees.

0.5 < t < 0.55 s

At t = 0.5 s, a DC fault is applied on the line. The fault current (trace 3) increases to 5 kA and the Id current increases to 2 pu (4 kA) in 10 ms. Then, the fast regulator action lowers the current back to its reference value of 1 pu.

0.55 < t <0.57 s

At t = 0.55 s, the alpha angle is forced by the protection system (not simulated) to reach 165 degrees when the Forced_alpha input of the current regulator goes high (1). The rectifier thus passes in inverter mode and sends the energy stored in the line back to the 345 kV network. As a result, the arc current producing the fault rapidly decreases. The fault is cleared at t = 0.555 s when the fault current zero crossing is reached.

0.57 < t < 0.8 s

At t = 0.57 s, the regulator is released and it starts to regulate the DC current again. The steady-state 1 pu current is reached at t = 0.75 s.

Frequency analysis of AC and DC voltages and currents

In order to allow further signal processing, signals displayed on Scope2 have been saved in a variable named 'psbvdc_str'(structure with time). These signals are: AC voltages(input 1), AC currents(input 2) and DC voltages on rectifier and line side of the smoothing reactor(input 3).

Open the Powergui and select 'FFT Analysis'. In the FFT window select structure 'psbhvdc_str'. The 0 - 2000 Hz FFT will be performed on a 2-cycle window starting at t = 0.8 - 2/60 (last 2 cycles of recording). Select input labeled 'Iabc'. By default Phase a current (signal number 1) is selected. Press on 'Display' and observe the frequency spectrum of AC current. Harmonic currents (order 6n+/-1 , n = 1,2,3, ...for a 6-pulse converter) are displayed in % of the fundamental component. If you analyze AC voltage (input Vabc) you should notice that the highest harmonics generated by the converter (5th and 7th) are filtered out by the two filters tuned at the 5th and 7th harmonics. Finally, select input

labeled 'Vd VdL (pu)' and then input 1 or input 2. You will obtain harmonic content (order 6n) of the DC voltage Vd (rectifier side) or VdL (line side) expressed in % of the DC component.

Three-Phase Harmonic Filters

This example shows three-phase harmonic filters in HVDC installations.
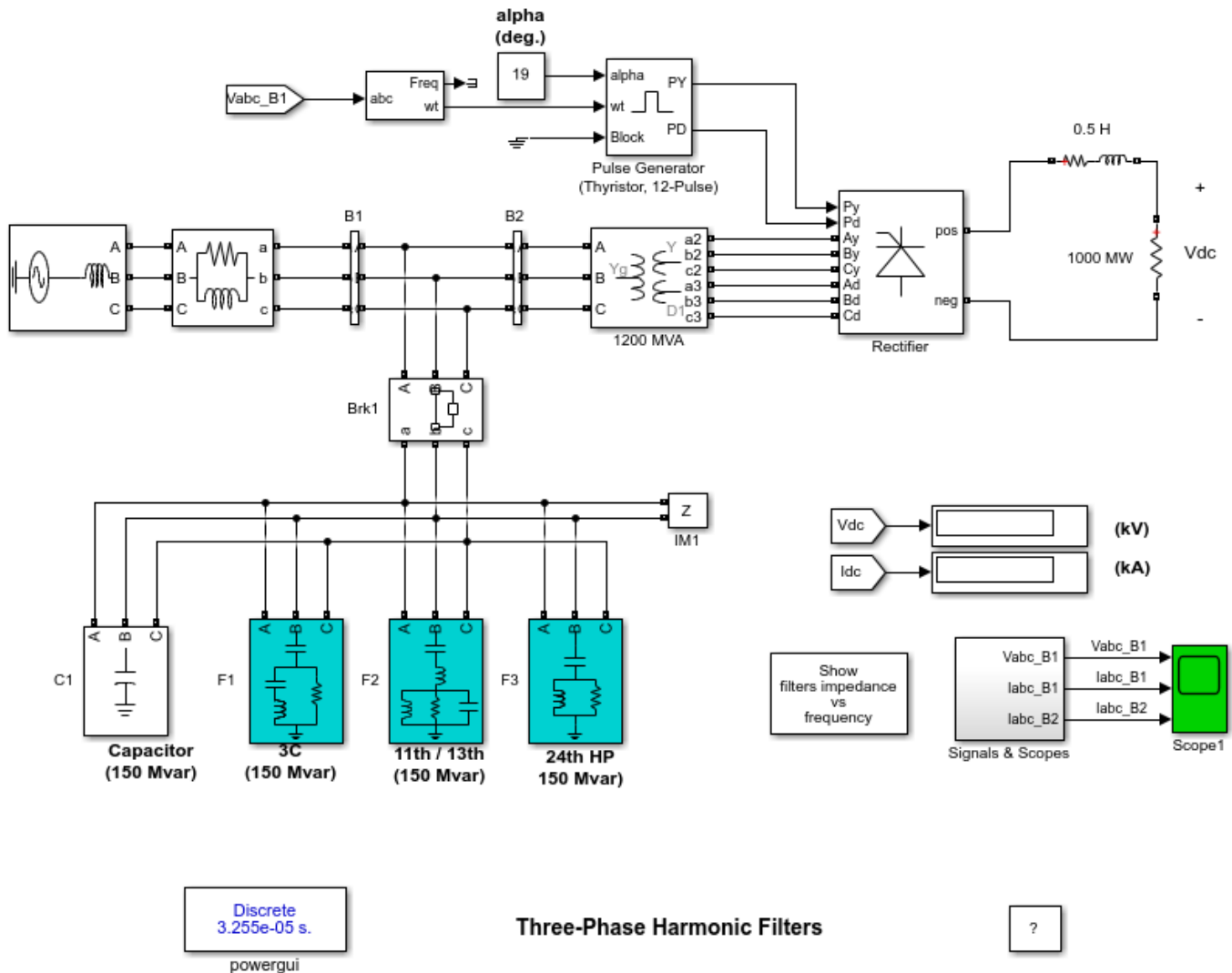
Description

In HVDC installations, AC harmonic shunt filters are used to:

1) reduce harmonic voltages and currents in the power system,

2) supply the reactive power consumed by the converter. To illustrate these concepts, a 1000-MW (500 kV, 2kA) HVDC rectifier is simulated.

The HVDC rectifier is built up from two 6-pulse thyristor bridges connected in series. The converter is connected to the system with a 1200-MVA Three-Phase transformer (three windings). A 1000-MW resistive load is connected to the DC side through a 0.5 H smoothing reactor. The filters set is made of the following four components of the powerlib/Elements library:

- one capacitor banks (C1) of 150 Mvar modeled by a "Three-Phase Series RLC Load",

- three filters modeled using the "Three-Phase Harmonic Filter"

(1) one C-type high-pass filter tuned to the 3rd (F1) of 150 Mvar

(2) one double-tuned filter 11/13 th (F2) of 150 Mvar

(3) one high-pass filter tuned to the 24th (F3) of 150 Mvar

The total Mvar rating of the filters set is then 600 Mvar. A three-phase circuit breaker (Brk1) is used to connect the filters set on the AC bus.

Simulation

Time-domain simulation

Run a first simulation with an alpha firing angle of 19 degrees. You should get a DC voltage level of 500 kV. Now, look inside Scope1. Compare the currents flowing into Bus B1 (Iabc_B1, axis 2) with those flowing into Bus B2 (Iabc_B2, axis 3). You can see that the harmonic filters almost eliminate the harmonics generated by the converter. If you use the FFT tool of the powergui, you will find that the harmonic filters reduce the THD of the current injected in the system from 9% to 0.7%. You can also perform other simulations with various values of alpha. Notice the impact on the DC level and on the generated harmonics.

Frequency response

You will now plot the impedance vs frequency of the harmonic filters:

1) Disconnect the filters from the AC bus. To do so, double-click on the breaker Brk1, select 'open' for initial status of breakers and click on the OK button.

2) Open the powergui and select "Impedance vs Frequency Measurement".

3) Click on the Display/Save button. Specialized Power Systems will compute and display the filters frequency response.

4) Double-click on the block "Show filters impedance vs frequency". A second figure, showing the pre-computed filters frequency response will be displayed. The impedance data of the two figures should be identical.

5) If you zoom the figure (using the "Tool" menu), you should find an impedance of 417 ohms capacitive (-90deg.) at 60 Hz. This value confirms that the total reactive power of the filters at 60 Hz is: Qc =V^2/Xc = 500e3^2/417 = 600 Mvar.

Three-Phase Active Harmonic Filter

This example shows the use of a shunt active harmonic filter (AHF) to minimize the harmonic content propagated to the source from a non-linear load.

Graham Dudgeon, Senior Consultant, The MathWorks, Inc.
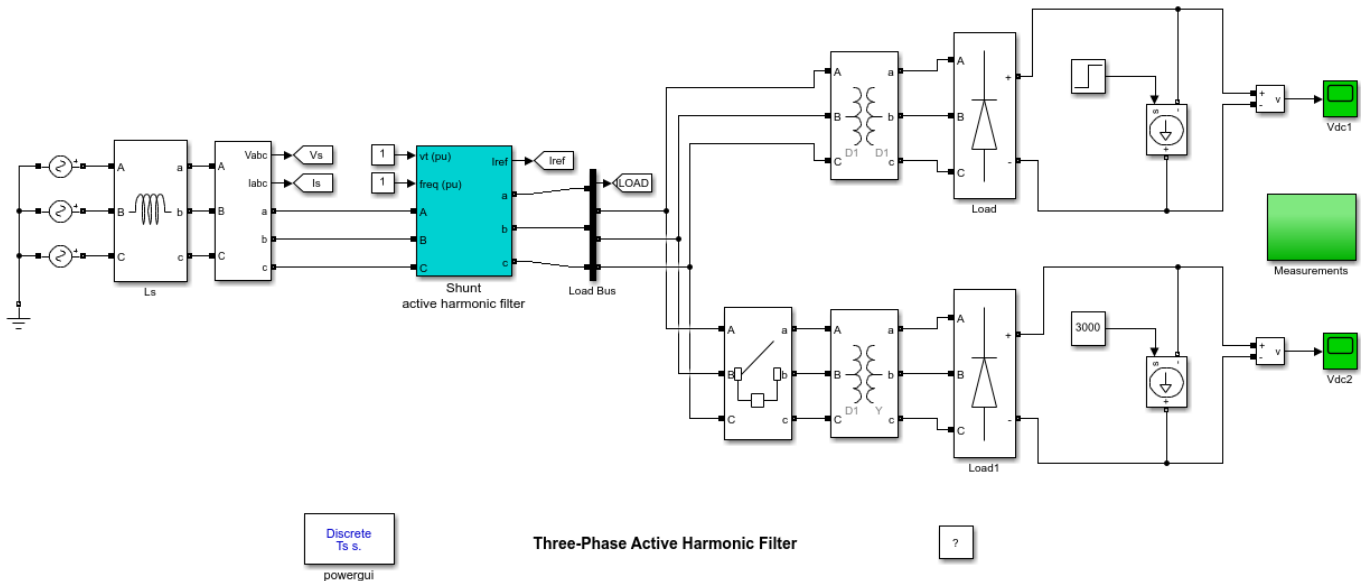
Description

The circuit models a standard shunt AHF with IGBT inverter and series inductor on the AC side and DC capacitor energization. The load consists of two diode rectifiers which are phase-shifted by 30 degrees. The Delta-Y connected rectifier is connected after 10 cycles to change the load from 6-pulse to 12-pulse.

The AHF uses a PLL to generate a reference sinusoidal source current which is in-phase and has the same RMS gain as the load current. The current error between the load current and the reference current is generated by the IGBT Bridge through hysteresis switching. The AHF aims to inject this current error at the point of common coupling in order to match the source current as closely as possible with the reference current.

Simulation

Start the simulation. You can observe phase A reference and source current on scope 'Iref_Isource' and phase A source and load current on scope 'Isource_Iload'. If you look under the mask of the AHF, further responses specific to the operation of the AHF are available.

At t=0s, only the delta/delta connected diode rectifier is in-circuit. The DC current is set at 2000A. Observe from 'Iref_Isource' that the AHF captures the reference current within 1 cycle.

FFT Analysis tool of Powergui show an FFT analysis of the load and source phase A current on the 3rd cycle. It is seen that the AHF has effectively reduced the THD from 22.41% to 0.69%.

At t=5/60s, the DC current is increased from 2000A to 3000A. Observe that the AHF effectively responds to this change in load and captures the new reference current within one cycle.

At t=10/60s, the delta/wye diode rectifier is connected, thus producing a 12-pulse load. Observe that the AHF again captures the new reference current within one cycle. FFT Analysis tool of Powergui shows the load and source phase A current on the 13th cycle. It is seen that the AHF has effectively reduced the THD from 4.80% to 0.93%.

Notes

The circuit has been discretized with a 2us time step. This time step may be increased but a decrease in accuracy of the simulation will be observed.

Three-Phase Line - Single-Pole Reclosing

This example shows the use of three-phase blocks to study phase-to-ground fault and single-pole reclosing of a 735-kV transmission line

G. Sybille (Hydro-Quebec)
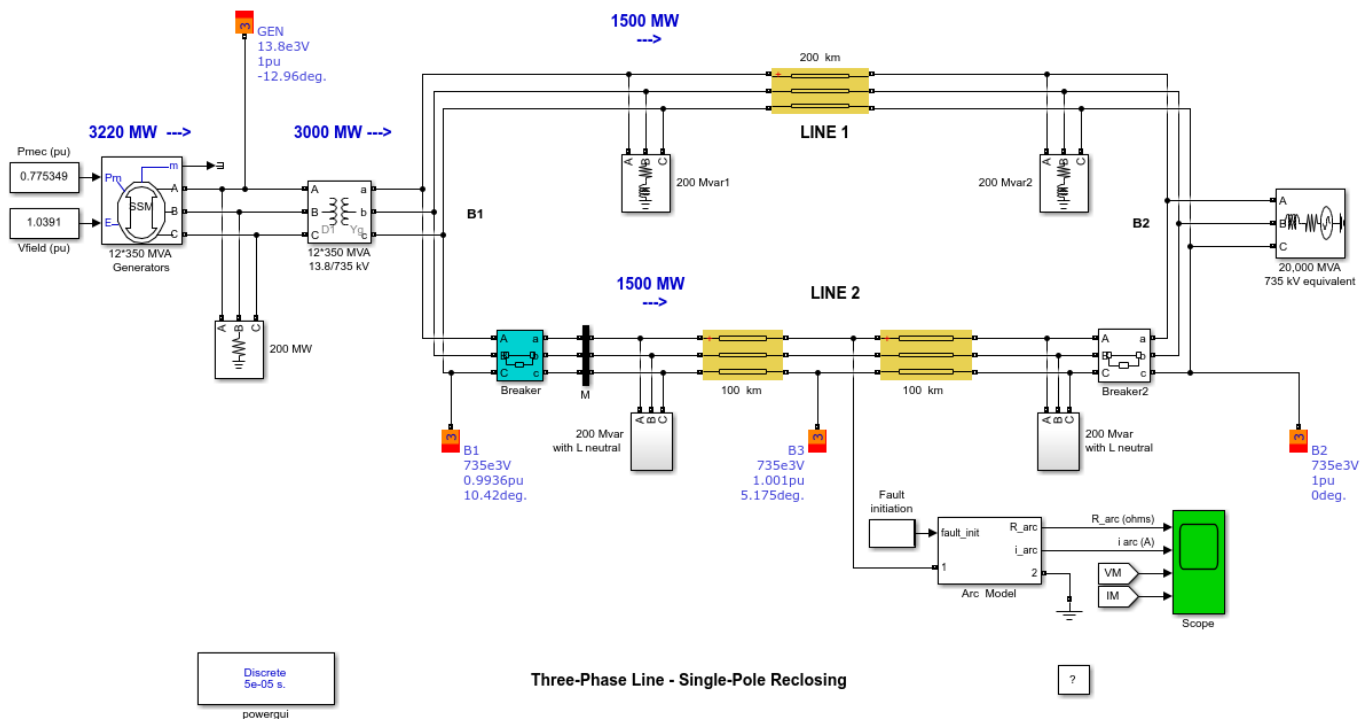
Description

Two 735 kV parallel lines, 200 km long, transmit 3000 MW of power from a generation plant (12 generators of 350 MVA) to to an equivalent network having a short circuit level of 20 GVA.

The generation plant is simulated with a simplified synchronous machine (sub-transient reactance of 0.22 pu). The machine is connected to the transmission network through a 13.8 kV/ 735 kV Wye-Delta transformer.

The line models are distributed parameter lines. The lines are assumed to be transposed and their parameters R,L,C /km are specified in positive- and zero-sequence components. Each line is shunt compensated by two shunt reactors of 200 Mvars each, connected at line ends. Applying single pole reclosing at this voltage level is made possible by the use of neutral inductances for the two shunt reactances of line 2. Otherwise, the 'secondary arc current' which is induced into the fault (mainly because of the capacitive coupling between the two sound phases and the faulted phase) would be too high to allow arc extinction after opening of line breakers on the faulted phase. If you open the two shunt reactance blocks of line 2, observe how the optimum neutral inductance is computed.

Fault and Line Switching

A phase-to-ground fault is applied at the middle of line 2. In order to apply the fault along the line, this line is simulated in two sections of 100 km. As soon as the fault is detected by the protection

relays (not simulated here) , an opening command is sent to the two line breakers of the faulted phase. The breaker are kept open during a certain 'dead time' , usually around 0.5 s, during which the arc normally extinguishes, then the two breakers are reclosed

When the two line breakers are tripped on the faulted phase, the fault current is interrupted but a small current will continue to flow through the arc. If this secondary arc current is too large (typically above 50 A), the arc cannot extinguish and the breaker will reclose on the fault.

Arc Model

The arc is modeled by a fixed or non-linear resistance R = f(Iarc_rms). The arc extinguishes when its rms current falls below a threshold value (typically 50 A) defined in the arc model block. Open the Arc Model block and look at the arc parameters. The mean arc resistance is programmed as an exponential function of the rms current. The mean arc resistance increases when the rms arc current decreases so that the time for arc current to decay below the threshold value is shortened . (With the specified parameters, Rarc = 0.1ohm and 30 ohms respectively for currents of 1kA and 100A). The Arc Model block is a masked block. Use 'Look under mask' to see how the arc model is implemented.

Open the blocks simulating the two line breakers. See how the line opening/reclosing sequence is programmed. The fault is applied at t = 1 cycle. Then, the opening command is sent to both breakers at t = 4 cycles (3 cycles detection + opening time). The two breakers are reclosed at t = 34 cycles after a dead time of 30 cycles, during which the arc creating the fault should extinguish.

Simulation

Start the simulation and observe the voltage and current waveforms on the 4-trace oscilloscope.

Observe the three phase-to-ground voltages and currents at sending end of line 2 and the current flowing into the fault. The machine has been initialized to deliver 3220 MW at 1 pu voltage so that a net power of 3000 MW is flowing into Line 1 and Line 2. The line currents flowing into each line are therefore 15pu/100 MVA as observed on trace 2.

The fault current (I_arc) is measured in amperes. It reaches 22 kA during the first cycle, then it drops to a very small value after 3 cycles when the two line breakers open. Use Y-zoom to look at the secondary arc current. It contains a slowly decaying DC component and a fundamental component (12 A peak). As its rms value is below 50 A, the arc extinguishes at the first current zero crossing.

Now, open the Arc Model block menu and change the arc model to a fixed resistance (0.1 ohm). Restart the simulation. Notice that the DC component of the arc current prevents arc extinction, so that the line is now reclosed on a fault. You can also suppress the neutral reactance in the two shunt reactance blocks and see the impact on the secondary arc current.

Sequence and abc_to_dq0 Transformations

This example shows the use of the Three-Phase Programmable Source, abc_dq0, and Sequence Analyzer blocks.

G. Sybille (Hydro-Quebec)
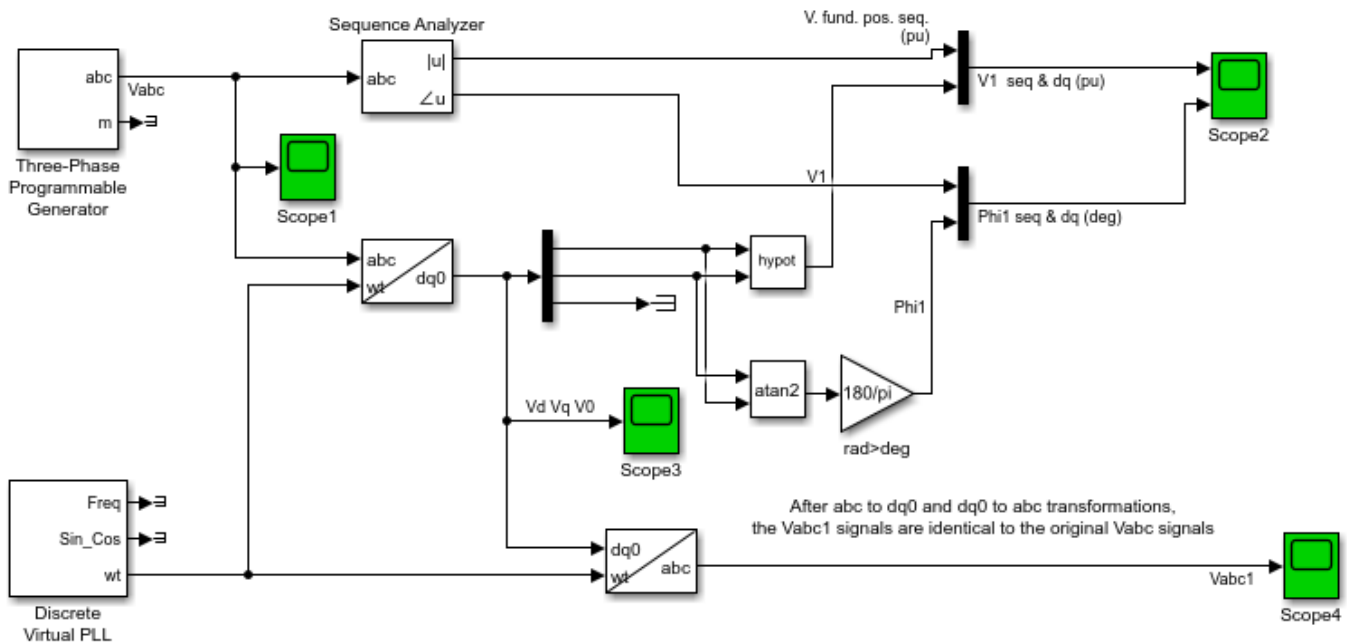
Sequence and abc_to_dq0 Transformations

Description

A Discrete 3-Phase ProgrammableSource block is used to generate a 1 pu, 15 degrees positive sequence voltage. At t = 0.05 s the positive sequence voltage is increased to 1.5 pu. At t = 0.1 s an unbalance is introduced by adding a 0.3 pu negative sequence component with a phase of -30 degrees.

The magnitude and phase of the positive-sequence component is evaluated in two different ways: 1) Sequence calculation of phasors using Fourier analysis; and 2) abc to dq0 transformation.

Math Function and Trigonometric Function blocks are used to evaluate the magnitude and phase of the positive sequence from the d and q components:

V1 = sqrt(Vd^2 + Vq^2) , Phi1 = atan(Vq/Vd))

Simulation

Choose Simulation/Start and observe the instantaneous signals Vabc on Scope, the signal magnitude (V1) and phase (Phi1) returned by the Sequence Analyzer (Scope2) and the d, q and 0 components displayed on Scope3.

Note that the Sequence Analyzer using Fourier analysis is immune to harmonics and unbalance (yellow traces of Scope2). However, its response to a step is a one-cycle ramp.

The abc_dqo transformation is instantaneous. However, the imbalance starting at 0.1 s produces a ripple at the V1 and Phi1 outputs (magenta traces of Scope2).

Three-Phase Programmable Source, V-I Measurement and Sequence Analyzer

This example shows the use of the 3-Phase Programmable Voltage Source, 3-Phase V-I Measurement, and Sequence Analyzer blocks.

G. Sybille (Hydro-Quebec)

Description

A 25kV, 100 MVA short-circuit level, equivalent network feeds a 5MW, 2 Mvar capacitive load.

The internal voltage of the source is controlled by the Discrete 3-phase Programmable Voltage Source block.

Open the source dialog box and look at the parameters controlling the voltage. A positive-sequence of 1.0 pu, 0 degrees is specified. At t = 0.05 s a step of 0.5 pu is applied on positive-sequence voltage magnitude, then, at t = 0.1 s, 0.08 pu of 5th harmonic in negative sequence is added on the 1.5 pu voltage.

In order to start simulation in steady state, the three voltage sources are initialized with a positive-sequence voltage of 25 kV, 0 degree, 60 Hz.

The Three-Phase V-I Measurement block allows to monitor the three load voltages and currents. Open its dialog box and see how this block allows to output the voltages and currents in pu.

Two Discrete 3-phase Sequence Analyzer blocks are used to monitor the positive-sequence of the three fundamental voltages Va, Vb, Vc and the negative-sequence component of the 5th harmonic.

The whole system, (power network, programmable source, and sequence analyzers) is discretized at a 50 us sample time.

Simulation

Choose Simulation/Start and observe the voltage and current waveforms.

The simulation starts in steady state with 1 pu voltage. At t = 0.05 sec, the internal voltage is increased to 1.5 pu. At t = 0.10 sec. 0.08 pu of 5th harmonic internal voltage is added.

Observe the fundamental component (pos. seq.) and the 5th harmonic component (neg. seq.) measured by the two sequence analyzers. The 0.08 pu 5th harmonic internal voltage is amplified at 0.14 pu at the load terminals. As the sequence analyzers use Fourier analysis, their response time is one cycle.

Three-Phase Programmable Source, PLL, Voltage and Power Measurement

This example shows the use of the 3-Phase Programmable Voltage Source, PLL and Variable-Frequency Positive-Sequence Voltage and Power Measurement blocks.

Description

A 25kV, 100 MVA short-circuit level, equivalent network feeds a 5 MW, 5 Mvar capacitive load. The internal voltage of the source is controlled by the Discrete 3-Phase Programmable Voltage Source block.

Open the programmable source dialog box and look at the parameters controlling the voltage and frequency. A 60 Hz, positive-sequence of 1.0 pu, 45 degrees is specified.

At t = 0.5 s, a sinusoidal modulation of the frequency (amplitude 3 Hz, frequency 0.4 Hz) is started. The modulation stops at t =3 s so that a full cycle of modulation can be observed.

The Three-Phase V-I Measurement block is used to monitor the three load voltages and currents. Open its dialog box and see how this block allows to output the three voltages and currents in p.u.

The Discrete 3-Phase PLL block measures the frequency and generates a signal (wt output) locked on the variable frequency system voltage. The PLL drives two measurement blocks taking into account the variable frequency: one block computing the fundamental value of the positive-sequence load voltage and another one computing the load active and reactive powers. These two blocks and the PLL are initialized in order to start in steady state.

The whole system, (power network, PLL and measurement blocks) is discretized at a 50 us sample time.

Simulation

Start the simulation.

Observe on Scope1 block how the PLL tracks the changing system frequency (trace 1). As expected, the system frequency varies between 57 Hz and 63 Hz with a maximum rate of change of 7.5 Hz/s. Traces 2 and 3 show the variations of magnitude and phase of positive-sequence voltage as the frequency is changing. Traces 4 and 5 show the variations of active and reactive powers.

Observe on Scope2 that the (wt) ramp output of the PLL stays locked on zero crossings of phase A voltage.

FFT Analysis During Simulation

This example shows how to do FFT analysis during a simulation.

P.Dahler, ABB® Turgi

Spectrum (harmonics 0-19 of signals 1 2 3)

93.81	187.62	281.42
50.00	100.00	150.00
70.71	141.42	212.13
36.06	72.11	108.17
79.37	158.75	238.12

Shows RMS quantities of signals 1 2 3
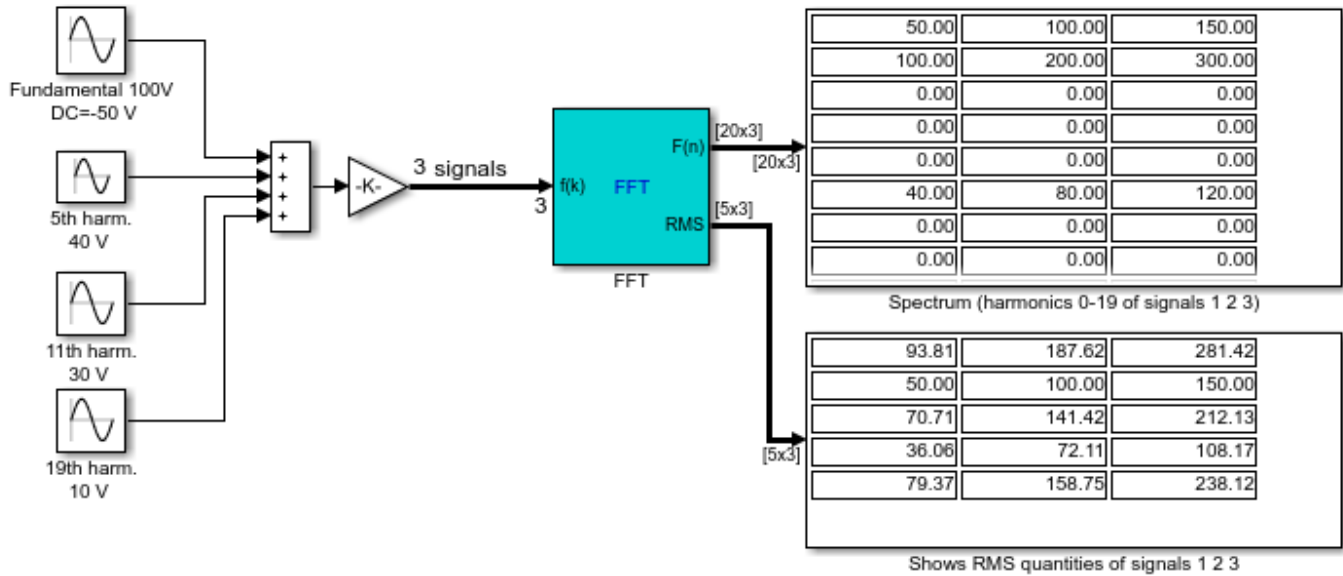
FFT Analysis During Simulation

P.Dahler, ABB Turgi
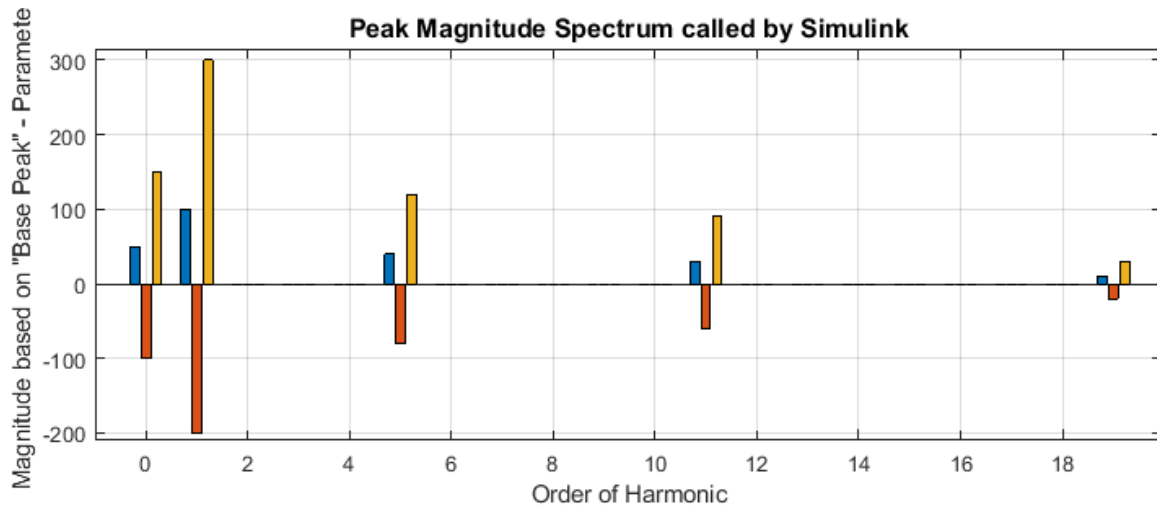
12.8 V, 40 Ah, Lithium-Ion (LiFePO4) Battery Aging Model (1000 h Simulation)

This example shows the impact of aging (due to cycling) on a 12.8 V, 40 Ah, Lithium-Ion battery module.

Souleman Njoya M., Louis-A. Dessaint (Ecole de technologie superieure, Montreal)
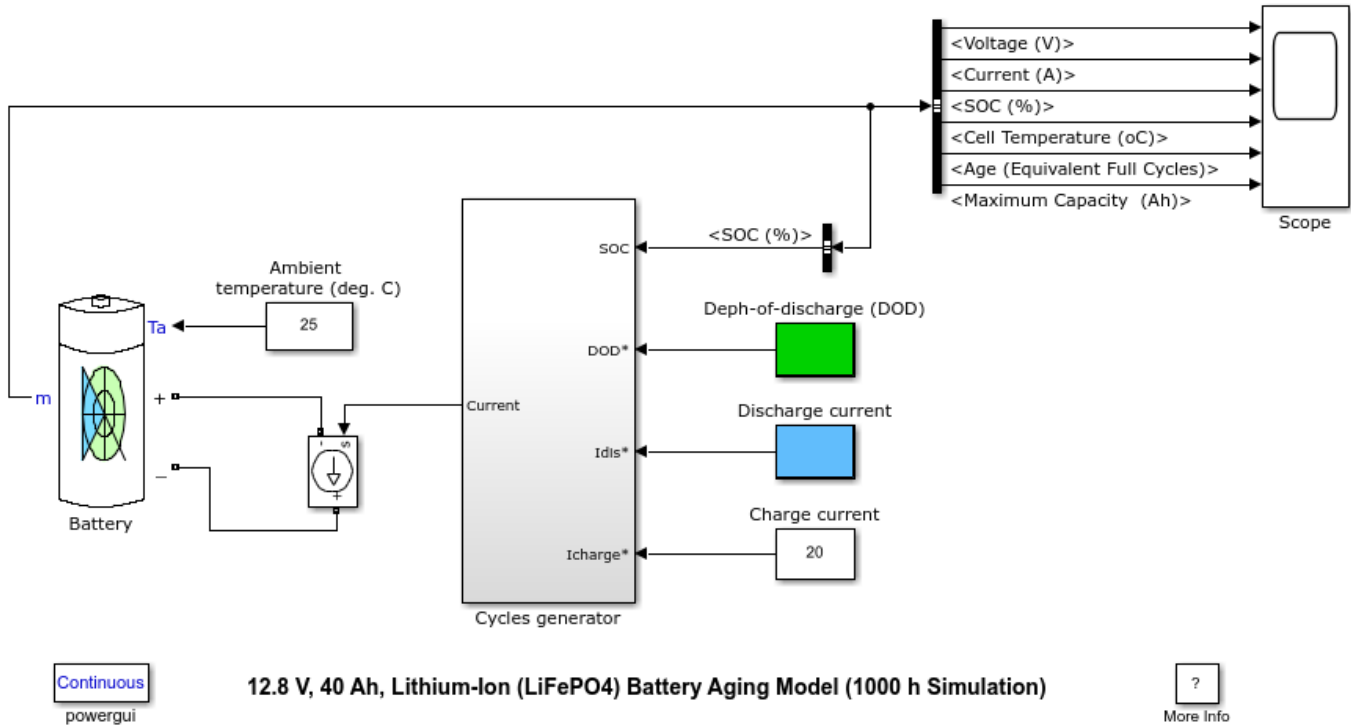
Circuit Description

This demo illustrates the effect of aging (due to cycling) on the performance of a 12.8 V, 40 Ah Lithium-Ion battery model. The battery is submitted for 1000 hours, to several discharge-charge cycles at ambient temperature of 25 degrees C , and at various depths of discharge (DOD) and discharge rates. As observed from the Scope, the impact of DOD and discharge rate on the battery life is as expected. As the DOD or discharge rate increases, the battery ages rapidly, which quickly reduces the battery capacity.

Simulation

The demonstration shows the life cycle performance of a 12.8 V, 40 Ah, Lithium-Ion battery during shallow and deep discharges, at various discharge rates. At first, the battery is cycled to a DOD of 20 % at 0.5 C discharge rate. Afterwards, the DOD and discharge rate are increased to 80 % and 2 C, respectively. Start the Simulation and open the Scope to view all signals.

At t = 0 s, the battery cycling starts with a discharge current of 20 A (0.5 C-rate), at ambient temperature of 25 degrees C. The initial SOC is 100 %. The discharge goes on until the SOC reaches 80 % (DOD of 20%). Afterwards, the battery is charged to 100 % SOC with a charge current of 20 A.

As this cycle is repeated, the battery age increases whereas its capacity decreases. The internal cell temperature also increases from 25 degrees C to 33 degrees C.

At t = 200 h, the battery is now discharged to 20 % SOC (DOD of 80 %) and charged to 100 % SOC. This cycle is repeated for another 200 hours. As observed, the battery age starts to increase rapidly.

At t = 400 h, the cycling DOD is brought back to 20 % for another 200 hours. This slows down the aging process of the battery.

At t = 600 h, the discharge current is increased to 80 A (2 C-rate). This causes the internal cell temperature to rise from 33 degrees C to 43 degrees C. As this cycle is repeated, the battery ages more rapidly, which quickly reduces the battery capacity.

At t = 800 h, the discharge current is brought back to 0.5 C-rate for another 200 hours, which slows down the aging process of the battery.

References

1. O. Tremblay, L.-A. Dessaint, A.-I. Dekkiche, A Generic Battery Model for the Dynamic Simulation of Hybrid Electric Vehicles, 2007 IEEE® Vehicle Power and Propulsion Conference, September 9-13, 2007, Arlington/Texas, USA.

2. N. Omar, M. A. Monem, Y. Firouz, J. Salminen, J. Smekens, O. Hegazy, H. Gaulous, G. Mulder, P Van den Bossche, T. Coosemans, J. Van Mierlo, Lithium iron phosphate based battery - Assessment of the aging parameters and development of cycle life model, Applied Energy, Volume 113, January 2014, Pages 1575-1585.

Lithium-Ion Temperature Dependent Battery Model

This example shows the impact of temperature on a 7.2 V, 5.4 Ah, Lithium-Ion battery module.

Souleman Njoya M., Louis-A. Dessaint (Ecole de technologie superieure, Montreal)

Circuit Description

This demo illustrates the effect of temperature on the performance of a 7.2 V, 5.4 Ah Lithium-Ion battery model. The model (which includes the impact of cell/ambient temperature on the voltage, capacity and resistance) is submitted to a variable ambient temperature during a discharge and charge process. Its performance is compared to the case where the impact of temperature is neglected. As observed from the Scope, the temperature dependent battery model performs close to reality. As the cell/internal temperature increases/decreases due to charge(or discharge) heat losses and ambient temperature variations, the output voltage and capacity also increase/decrease.

Simulation

The demonstration shows the performance of the temperature dependent Lithium-Ion battery model (Battery A) when the ambient temperature is varied from 20 degrees C to -20 degrees C and then to 0 degrees C. Battery B represents the case where the effect of temperature is neglected. Start the Simulation and open the Scope to view all signals.

At t = 0 s, the Battery A and B are discharged with 2 A at ambient temperature of 20 degrees C.

At t = 150 s, the internal temperature has increased to its steady state value of 29.2 degrees due to heat losses from the discharge process. This causes the output voltage of Battery A to slightly increase, while battery B output voltage continues to decrease.

At t = 1000 s, the ambient temperature is decreased to -20 degrees C. This causes the output voltage of Battery A to greatly decrease as the internal temperature decreases rapidly. Also the SOC of Battery A decreases due to the reduction of battery capacity. The battery B output voltage continues to decrease slowly to its steady state voltage.

At t = 2000 s, the ambient temperature is increased from -20 degrees C to 0 degrees C. As the internal temperature increases, the output voltage of Battery A increases. Also, as the capacity increases, the SOC of Battery A increases. The Battery B output voltage remains constant to its steady state value.

At t = 2500 s, the Battery A and B are charged with 3 A at ambient temperature of 0 degrees C. This causes the internal temperature to increase due to heat losses during the charge process, which increases the charging voltage of Battery A. Afterwards, Battery A and B continue to charge up until fully charged.

References

1. O. Tremblay, L.-A. Dessaint, A.-I. Dekkiche, A Generic Battery Model for the Dynamic Simulation of Hybrid Electric Vehicles, 2007 IEEE Vehicle Power and Propulsion Conference, September 9-13, 2007, Arlington/Texas, USA.

3. Cong Zhu, Xinghu Li, Lingjun Song, Liming Xiang, Development of a theoretically based thermal model for lithium ion battery pack, Journal of Power Sources, Volume 223, 1 February 2013, Pages 155-164.

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Shaft Coupling (Simscape model)

This example shows a Simscape™ model of mechanical coupling shaft.

Ernesto Vilchez-Ynca, Louis-A. Dessaint (Ecole de technologie superieure, Montreal)

Mechanical Coupling

Description

The model outputs the transmitted torque through the shaft regarding the speed difference between the driving side and the loaded side of the shaft.

The shaft has a stiffness of 17190 N.m and an internal damping factor of 600 N.m.s. This shaft is designed to have 0.1 degree of angular deflection for a 30 N.m load torque.

The shaft is driven by a variable speed source and is connected to a load. The load has an inertia of 0.35 kg.m2 and a viscous friction term of 0.006 N.m.s

Simulation

Start the simulation. You can observe the driving and the load speeds, the speed difference, the angular deflection and the transmitted torque values on the scope.

At t = 0 s, the driving speed starts climbing to 1750 rpm with a 500 rpm/s acceleration ramp. The angular deflection jumps to about 0.06 degree and the shaft transmits about 18.5 Nm to the load in order to accelerate it. At t = 0.2 s, the driving and load speeds tend to equalize. During the acceleration phase, the angular deflection increases slowly in order to transmit a higher torque to compensate the viscous friction increase.

At t = 3.5 s, the driving speed settles at 1750 rpm. This reduces the angular deflection and also the transmitted torque which settles around 1.1 Nm to compensate the viscous friction of the load.

At t = 5 s, the driving speed lowers towards 0 rpm with a -500 rpm/s deceleration ramp. The angular deflection becomes negative and so does the transmitted torque in order to decelerate the load.

During the deceleration phase, the angular deflection increases in order to transmit a higher deceleration torque to compensate the reduction of viscous friction.

At t = 8.5 s, the driving speed stabilizes at 0 rpm. This causes the angular deflection to reduce to 0 degree, the transmitted torque becomes null and the load stops.

Notes

1) The shaft has been discretized with a 10 us time step.

2) In order to reduce the number of points stored in the scope memory, a decimation factor of 10 is used.

Gear Speed Reducer (Simscape model)

This example shows a Simscape™ model representing a gear speed reducer device connected between a high-speed shaft and a low-speed shaft.

Ernesto Vilchez-Ynca, Louis-A. Dessaint (Ecole de technologie superieure, Montreal)

Gear Speed Reducer (Simscape model)

Description

The speed reducer is driven by a variable speed source and is connected to a load. The load has an inertia of 30 kg.m2 and a viscous friction term of 0.5 N.m.s.

The reduction device has a reduction ratio of 10 and it's inertia with respect to the high-speed side is 0.0005 kg.m2. The reduction ratio being quite low, the efficiency is high and worth 0.95.

The high-speed shaft has a stiffness of 17190 N.m and an internal damping factor of 600 N.m.s. This shaft is designed to have 0.1 degree of angular deflection for a 30 N.m load torque. The low-speed shaft, having a higher torque to transmit, has a stiffness of 171900 N.m and an internal damping factor of 6000 N.m.s. This shaft is designed to have 0.1 degree of angular deflection for a 300 N.m load torque.

Simulation

Start the simulation. You can observe the driving (high-speed) and load speeds (low-speed), the torque transmitted by the high-speed shaft and the torque transmitted by the low-speed shaft on the scope.

At t = 0 s, the driving speed starts climbing to 1750 rpm with a 500 rpm/s acceleration ramp. This causes the transmitted torque of the high-speed shaft to jump to about 18 N.m. Because of the

reduction device, the torque transmitted to the load by the low-speed shaft is a lot bigger and is worth about 170 N.m.

During the accelerating phase, both torques keep increasing in order to compensate the viscous friction of the load. Notice that the load accelerates with a ramp of +50 rpm/s due to the reduction ratio of the speed reducer.

At t = 3.5 s, the driving speed settles at 1750 rpm. Since no more accelerating torque is needed, the input and output torques decrease and stabilize to 0.965 N.m and 9.16 N.m respectively at t = 4 s. The load speed is now equal to 175 rpm.

Energy Management Systems for a Hybrid Electric Source (Application for a More Electric Aircraft)

This example shows energy management systems for a fuel cell hybrid electric source.

Souleman Njoya M., Louis-A. Dessaint (Ecole de technologie superieure, Montreal) and Susan Liscouet-Hanke (Bombardier Aerospace)
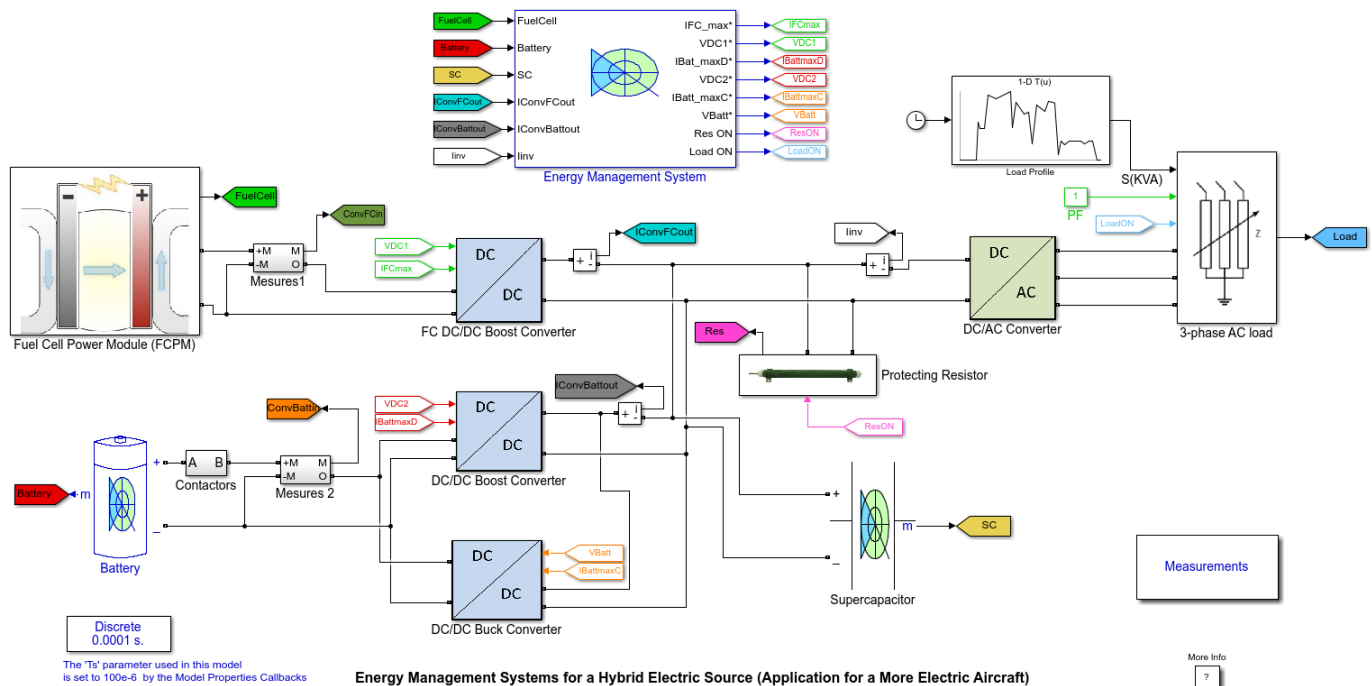
Circuit Description

This example illustrates a simulation model of a fuel cell based emergency power system of More Electric Aircraft (MEA). As the landing-gear and flight control systems become more electric in MEA, the peak electrical load seen by the conventional emergency power system (ram air turbine or air-driven generator) increases. Consequently, there is a potential risk of overloading the ram air turbine (RAT)/air-driven generator (ADG) at lower aircraft speeds, where the produced power is nearly zero. A more robust emergency power system is needed to ensure a safe landing of MEA. This model presents an alternative emergency power system based on fuel cells, lithium-ion batteries and supercapacitors. The demo also features different energy management systems for a fuel cell hybrid electric source.

The fuel cell hybrid power system is designed based on a representative emergency flight profile of a Bombardier aircraft and consists of the following:

- A 12.5 kW (peak), 30-60 V PEM (proton exchange membrane) fuel cell power module (FCPM), with nominal power of 10 kW.
- A 48 V, 40 Ah, Li-ion battery system.
- A 291.6 V, 15.6 F, supercapacitor system (six 48.6v cells in series)
- A 12.5 kW fuel cell DC/DC boost converter, with regulated output voltage and input current limitation.

- Two DC/DC converters for discharging (4 kW boost converter) and charging (1.2 kW buck converter) the battery system. These converters are also output voltage regulated with current limitation. Normally, a single bidirectional DC/DC converter can also be used to reduce the weight of the power system.
- A 15 kVA, 270 V DC in, 200 V AC, 400 Hz inverter system.
- A 3 phase AC load with variable apparent power and power factor, to emulate the MEA emergency load profile.
- A 15 kW protecting resistor to avoid overcharging the supercapacitor and battery systems.
- An energy management system, which distributes the power among the energy sources according to a given energy management strategy. Five types of energy management strategies are implemented, which are:

1 The state machine control strategy
2 The Classical PI control strategy
3 The frequency decoupling and state machine control strategy
4 The equivalent consumption minimization strategy (ECMS)
5 The external energy maximization strategy (EEMS)

Demonstration

The demonstration shows the performance of the fuel cell hybrid emergency power system during a five minutes emergency landing scenario. In this scenario, the fuel cell hybrid power system supplies the essential loads during the following events:

- Instantly when the main generators are lost (this is normally assumed by the Avionic and APU battery system till the RAT/ADG is fully deployed).
- Emergency hydraulic pump start-ups.
- Motion of Flaps/Slats and gear down.
- Taxiing and passengers evacuation (also normally assumed by the Avionic and APU battery system as the RAT/ADG becomes unavailable).

Depending on the type of energy management strategy selected, the energy management system controls the power of each energy source devices through the reference signals (output voltage and maximum current) of the fuel cell and battery DC/DC converters. Double click on the **Energy Management System** block and select for example the **State Machine Control Strategy**. Start the simulation. Double click on the **Measurements** block. Open the **Power** scope (showing the power distribution referred to the 270 V dc bus) together with the **Fuel Cell**, **Battery**, **SuperCap** and **Load** scopes. The following explains what happens during this simulated emergency landing scenario:

At t = 0 s, the essential loads are supplied by the main generators and the fuel cell hybrid power system is turned ON to prepare for an unlikely emergency landing situation.

At t = 5 s, the fuel cell begins to recharge the battery with its optimal power (around 1 kW).

At t = 40 s, all generators are lost. The fuel cell hybrid power system takes over the essential loads. At this time the extra load power required is instantly supplied by the supercapacitor due to its fast dynamics, while the fuel cell power increases slowly.

At t = 45 s, the supercapacitor is discharged below the required DC bus voltage (270 V) and the battery starts providing power to regulate the bus voltage back to 270 V.

At t = 48.5 s, the DC bus or supercapacitor voltage reaches 270 V and the battery reduces its power slowly to zero. The fuel cell provides the total load power and continues to recharge the supercapacitor.

At t = 60 s, an emergency hydraulic pump is started, and the supercapacitor provides the extra transient load power, while the fuel cell power increases slowly.

At t = 61.5 s, the battery comes online to regulate the DC bus voltage to 270 V and helps the fuel cell by providing the extra load power required.

At t = 70 s, the fuel cell reaches its maximum power (the FCPM power was limited to 9 kW due to its DC/DC converter input voltage range) and the extra load power is provided by the battery.

At t = 110 s, the battery also reaches its maximum power (4 kW) and the supercapacitor provides the extra load power.

At t = 125 s, the load power reduces below the fuel cell maximum power. Due to the slow fuel cell dynamics, the extra fuel cell power during transients is transferred to the supercapacitor.

At t = 126 s, the DC bus voltage reaches 270 V and the battery power drops to zero.

At t = 130 s, a second emergency hydraulic pump is turned ON and the fuel cell hybrid power system behavior is similar to when the first hydraulic pump was turned ON.

At t = 170 s, the load power reduces below the fuel cell maximum power and the extra fuel cell power is transferred to both the battery and supercapacitor.

At t = 180 s, the load is suddenly increased due to the motion of Flaps/Slats and landing gears. Once again, the supercapacitor responds quickly by providing the extra load power.

At t = 185 s, the battery discharges to regulate the DC bus voltage and helps the fuel cell with the extra load power required.

At t = 235 s, the aircraft has landed and the load power decreases suddenly. The extra fuel cell energy is stored in the battery and supercapacitor.

At t = 250 s, the aircraft is taxiing and the fuel cell supplies nearly the total load power required.

At t = 330 s, the passengers have been evacuated and the load power reduces to zero. The fuel cell reduces its power slowly to its optimal power and recharges the battery.

Notes

1. In order to reduce the amount of memory used, a decimation factor of 100 is used for all scopes, except for the **Load** scope (which uses a decimation factor of 10).

2. Average-value models of DC/DC and DC/AC converters are used to speed up the simulation.

3. Select a different energy management strategy in the **Energy Management System** block and compare its performance in terms of hydrogen consumption, storage (battery/supercapacitor) energy used and overall efficiency.

References

1. S. Njoya Motapon, L.A. Dessaint and K. Al-Haddad, "A Comparative Study of Energy Management Schemes for a Fuel Cell Hybrid Emergency Power System of More Electric Aircraft," IEEE Transactions on Industrial Electronics, 2013 (IEEE Early access).

Supercapacitor Model

This example shows the supercapacitor model during charge and discharge.

Pierre Clement Blaud, L.-A. Dessaint (Ecole de technologie superieure, Montreal)

Supercapacitor Model
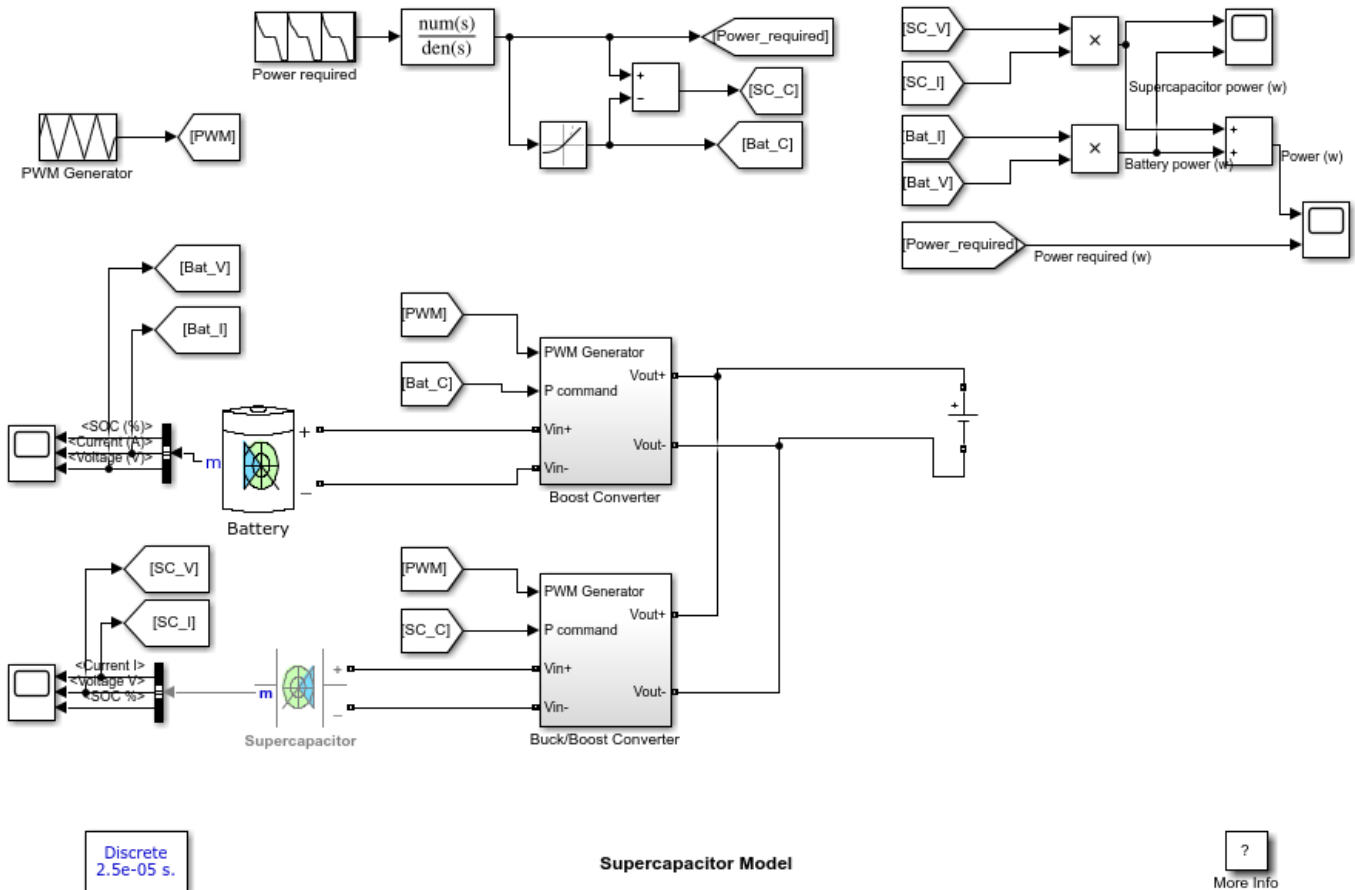
Description

The circuit illustrates a simple hybridization of a supercapacitor with a battery. The supercapacitor is connected to a Buck/Boost converter and the battery is connected to a Boost converter. Power of the battery is limited by a rate limiter block, therefore the transient power is supplied to the DC bus by the supercapacitor.

6 kW 45 Vdc Fuel Cell Stack

This example shows the Proton Exchange Membrane (PEM) Fuel Cell Stack model feeding an average value 100Vdc DC/DC converter.

Olivier Tremblay, Njoya Motapon Souleman, Louis-A. Dessaint (Ecole de technologie superieure, Montreal)

6 kW 45 Vdc Fuel Cell Stack
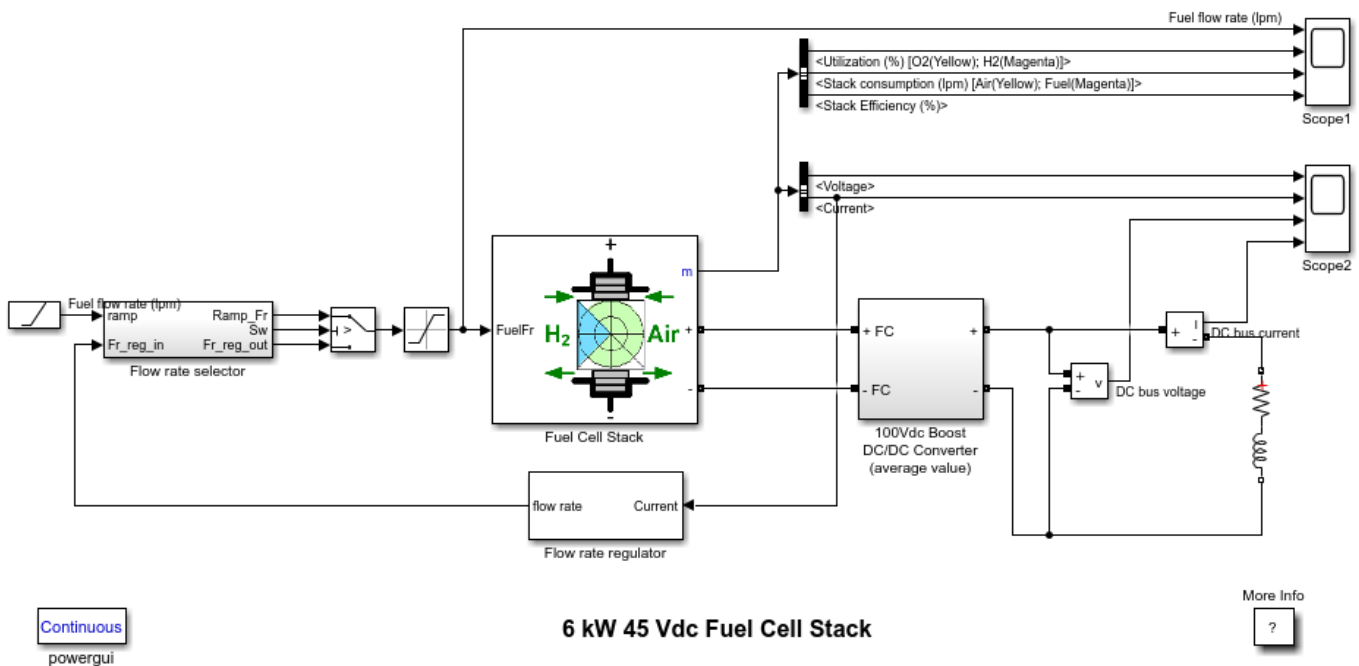
Description

The nominal Fuel Cell Stack voltage is 45Vdc and the nominal power is 6kW. The converter is loaded by an RL element of 6kW with a time constant of 1 sec. During the first 10 secs, the utilization of the hydrogen is constant to the nominal value (Uf_H2 = 99.56%) using a fuel flow rate regulator. After 10 secs, the flow rate regulator is bypassed and the rate of fuel is increased to the maximum value of 85 lpm in order to observe the variation in the stack voltage. That will affect the stack efficiency, the fuel consumption and the air consumption.

Fuel cell voltage, current, DC/DC converter voltage and DC/DC converter current signals are available on the Scope2. Fuel flow rate, Hydrogen and oxygen utilization, fuel and air consumption, and efficiency are available on the Scope1.

Simulation

At t = 0 s, the DC/DC converter applies 100Vdc to the RL load (the initial current of the load is 0A). The fuel utilization is set to the nominal value of 99.56%. The current increases to the value of 133A. The flow rate is automatically set in order to maintain the nominal fuel utilization. Observe the DC bus voltage (Scope2) which is very well regulated by the converter. The peak voltage of 122Vdc at the beginning of the simulation is caused by the transient state of the voltage regulator.

At t = 10 s, the fuel flow rate is increased from 50 liters per minute (lpm) to 85 lpm during 3.5 s reducing by doing so the hydrogen utilization. This causes an increasing of the Nernst voltage so the

fuel cell current will decrease. Therefore the stack consumption and the efficiency will decrease (Scope1).

Notes

1) You can vary more than 7 signals by checking the checkbox in the "Signal variation" tab of the Simulink® mask. Refer to documentation for more information.

2) A simplified version of the model allows simulating the fuel cell with less parameters using only the V-I curves. Select "Model detailed Level" to "Simplified". Note that the signal variation is not available in this mode; hence the Fuel flow rate input is not available anymore. Also the utilization, the stack consumption and the efficiency are 0 for the "Simplified" mode.

Ni-MH Battery Model

This example shows a 200 V, 6.5 Ah Ni-MH battery model during charge and discharge process.

Olivier Tremblay, Louis-A. Dessaint (Ecole de technologie superieure, Montreal)

Ni-MH Battery Model
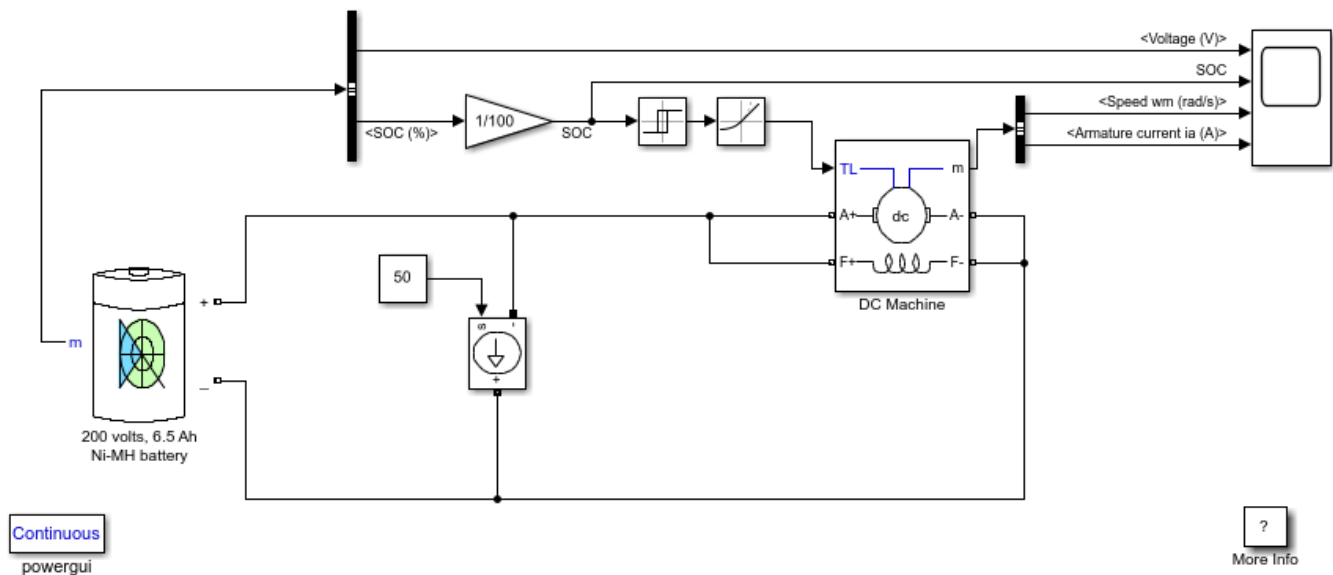
Description

The battery is connected to a constant load of 50 Amps. The DC machine is connected in parallel with the load and operates at no load torque. When the State-Of-Charge (SOC) of the battery goes under 0.4 (40%), a negative load torque of 200 Nm is applied to the machine so it acts as a generator to recharge the battery. When the SOC goes over 80%, the load torque is removed so only the battery supplies the 50 amps load.

Battery voltage, SOC, Motor speed and Motor current signals are available at the output of the block.

Simulation

At t = 0 s, the DC machine is started with the battery power. The speed increases to 120 rad/s. The battery is also discharged by the constant DC load of 50 amps. At t = 280 s, the SOC drops under 40%. A mechanical torque of -200 Nm is applied to the machine so it acts as a generator and provides a current of 100 amps. Hence, 50 amps goes to the load and 50 amps goes to recharge the battery. At t = 500 s, the SOC goes over 80%. The mechanical torque is removed and the machine operates free. And the cycle restarts.

References

1. O. Tremblay, L.-A. Dessaint, A.-I. Dekkiche, "A Generic Battery Model for the Dynamic Simulation of Hybrid Electric Vehicles", 2007 IEEE® Vehicle Power and Propulsion Conference, September 9-13, 2007, Arlington/Texas, USA

AC1 - Six-Step VSI Induction 3HP Motor Drive

This example shows the AC1 Six-Step VSI Induction Motor Drive during speed regulation.

H.Blanchette, L.-A. Dessaint (Ecole de technologie superieure, Montreal)
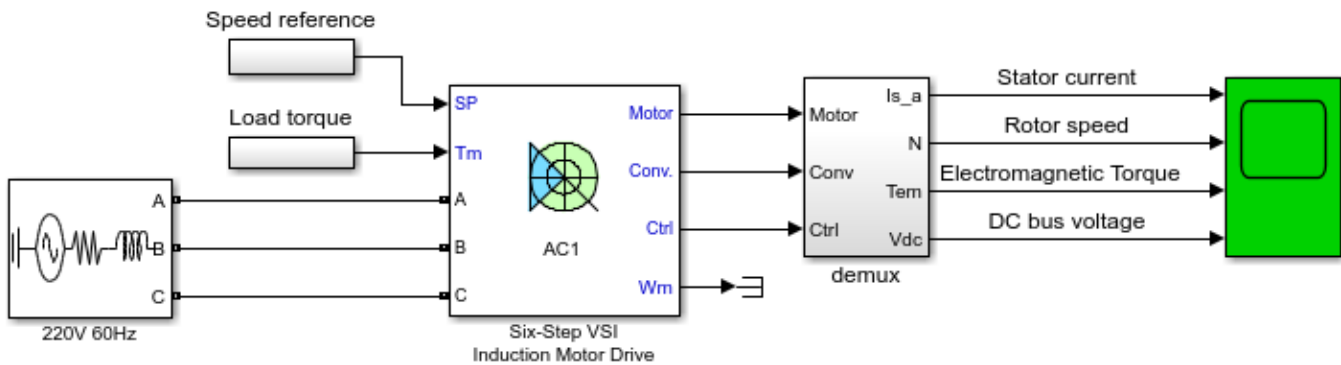
Description

The induction motor is fed by a voltage source inverter, which is built using the Universal Bridge Block. The DC bus voltage is produced by a thyristor rectifier and regulated using a PI controller in order to maintain a constant volts per hertz ratio. A braking chopper limits the DC bus voltage increase during motor deceleration or when the load torque tends to accelerate the motor. The motor drives a mechanical load characterized by inertia J, friction coefficient B, and load torque TL.

Motor current, speed, and torque signals are available at the output of the block.

Simulation

A speed reference step from 0 to 1800 rpm is applied at t = 0. The speed set point doesn't go instantaneously at 1800 rpm but follows the acceleration ramp. The motor reaches steady state at t = 1.3 s.

At t = 2 s, an accelerating torque is applied on the motor's shaft. You can observe a speed increase. Since the rotor speed is higher than the synchronous speed, the motor is working in the generator mode. The braking energy is transferred to the DC link and the bus voltage tends to increase. However the over voltage activates the braking chopper which causes the voltage to reduce. In this example, the braking resistance is not big enough to avoid a voltage increase but the bus is maintained within tolerable limits.

At t = 3 s, the torque applied to the motor's shaft steps from -11 N.m to +11 N.m .You can observe a DC bus voltage and speed drop. At this point, the DC bus controller switches from braking to motoring mode.

At t = 4 s, the load torque is removed completely and the electromagnetic stabilizes around zero shortly after.

Notes

1) The power system has been discretised with a 20us time step

AC2 - Space Vector PWM VSI Induction 3HP Motor Drive

This example shows a PWM VSI induction motor drive with a braking chopper for a 3HP AC motor.

H.Blanchette, L.-A. Dessaint (Ecole de technologie superieure, Montreal)
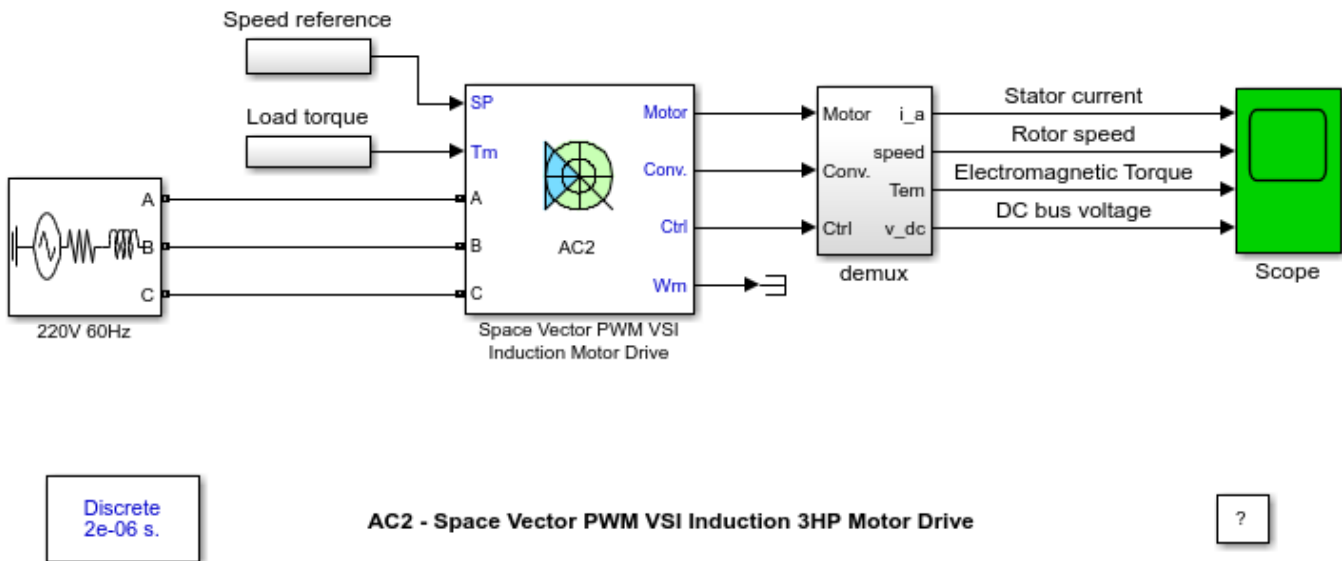
Description

The induction motor is fed by a PWM inverter, which is built using a Universal Bridge Block. The speed controller consists in a PI regulator that produces a slip compensation, which is added to the rotor speed in order to derive the commanded stator voltage frequency. A constant volts per hertz ratio is also applied to the motor. The motor drives a mechanical load characterized by inertia J, friction coefficient B, and load torque TL.

Motor current, speed, and torque signals are available at the output of the block.

Simulation

Start the simulation. You can observe the motor stator current, the rotor speed, the electromagnetic torque and the DC bus voltage on the scope. The speed set point and the torque set point are also shown.

At time t = 0 s, the speed set point is 1000 rpm. As shown in Figure 0-10, the speed follows precisely the acceleration ramp.

At t = 0.5 s, the full load torque is applied to the motor shaft while the motor speed is still ramping to its final value. This forces the electromagnetic torque to increase to a high value and then to stabilize at 11 N.m once the speed ramping is completed and the motor has reached 1000 rpm.

At t = 1 s, the speed set point is changed to 1500 rpm and the electromagnetic torque reaches again a high value so that the speed ramps precisely at 1800 rpm/s up to 1500 rpm under full load.

At t = 1.5 s, the mechanical load passed from 11 N.m to -11 N.m , which causes the electromagnetic torque to stabilize at approximately at -11 N.m shortly after. Note that the DC bus voltage increases since the motor is in the braking mode. This increase is limited by the action of the braking chopper.

Notes

1) The power system has been discretised with a 2 us time step. The speed controller uses a 100 us sample time and the space vector modulator uses a 20 us sample time in order to simulate a microcontroller control device.

2) In order to reduce the number of points stored in the scope memory, a decimation factor of 25 is used.

3) A simplified version of the model using average-value inverter can be used by selecting 'Average' in the 'Model detail level' menu of the graphical user-interface. The time step can then be increased up to the control system sample time value.This can be done by typing 'Ts = 20e-6' in the workspace in the case of this example. See also ac2_example_simplified model.

AC3 - Field-Oriented Control Induction 200 HP Motor Drive

This example shows the Field-Oriented Control Induction Motor Drive during speed regulation.

H.Blanchette, L.-A. Dessaint (Ecole de technologie superieure, Montreal)

Description

This circuit uses the AC3 block of Specialized Power Systems library. It models a field-oriented control (FOC) induction motor drive with a braking chopper for a 200HP AC motor.

The induction motor is fed by a PWM voltage source inverter, which is built using a Universal Bridge Block. The speed control loop uses a PI controller to produce the flux and torque references for the FOC controller. The FOC controller computes the three reference motor line currents corresponding to the flux and torque references and then feeds the motor with these currents using a three-phase current regulator.

Motor current, speed, and torque signals are available at the output of the block.

Simulation

Start the simulation. You can observe the motor stator current, the rotor speed, the electromagnetic torque and the DC bus voltage on the scope. The speed set point and the torque set point are also shown.

At time t = 0 s, the speed set point is 500 rpm. Observe that the speed follows precisely the acceleration ramp.

At t = 0.5 s, the full load torque is applied to the motor shaft while the motor speed is still ramping to its final value. This forces the electromagnetic torque to increase to the user-defined maximum value (1200 N.m) and then to stabilize at 820 N.m once the speed ramping is completed and the motor has reached 500 rpm.

At t = 1 s, the speed set point is changed to 0 rpm. The speed decreases down to 0 rpm by following precisely the deceleration ramp even though the mechanical load is inverted abruptly, passing from 792 N.m to - 792 N.m, at t = 1.5 s. Shortly after, the motor speed stabilizes at 0 rpm.

Finally, note how well the DC bus voltage is regulated during the whole simulation period.

Notes

1) The power system has been discretised with a 2 us time step. The speed controller uses a 100 us sample and the vector controller uses a 20us is sample time in order to simulate a microcontroller control device.

2) A simplified version of the model using average-value inverter can be used by selecting 'Average' in the 'Model detail level' menu of the graphical user-interface. The time step can then be increased up to 60 us. This can be done by typing 'Ts = 60e-6' in the workspace and by changing the speed controller sampling time to 120e-6 and the vector controller sampling time to 60e-6. See also ac3_example_simplified model.

AC3 - Comparison Between Detailed and Simplified Models

This example shows the AC3 detailed model and the AC3 average model during speed regulation. The comparison is performed for normal condition and for inverter saturation condition.

O. Tremblay, L.-A. Dessaint (Ecole de technologie superieure, Montreal)
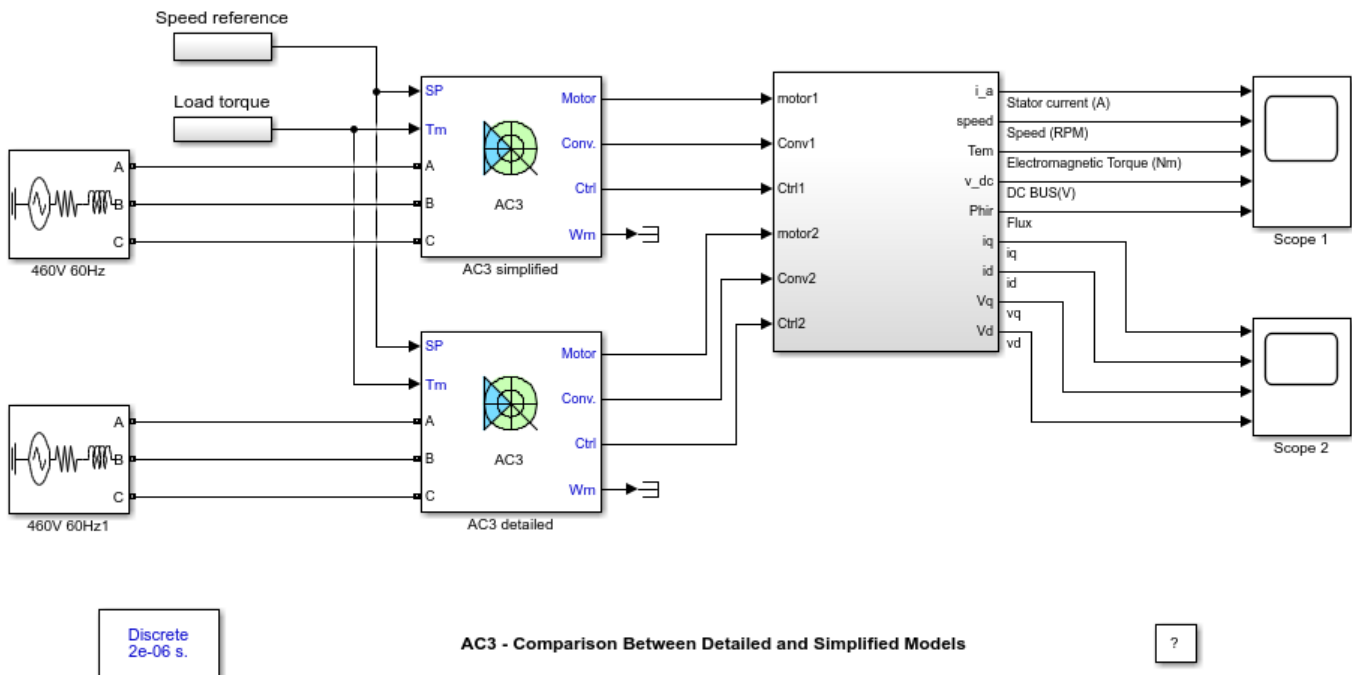
Description

This circuit uses two AC3 blocks of the Specialized Power Systems electric drives library. The AC3 block models a field-oriented control (FOC) induction motor drive with a braking chopper for a 200 HP AC motor. The first AC3 block is set to average value model and the second AC3 block is set to detailed model.

For the detailed model, the induction motor is fed by a PWM voltage source inverter, which is built using a Universal Bridge Block. The average value model uses ideal voltages and currents sources to feed the induction motor. The speed control loop uses a PI regulator to produce the flux and torque references for the FOC controller. The FOC controller computes the three reference motor line currents corresponding to the flux and torque references and then feeds the motor with these currents using a three-phase current regulator.

Motor currents, voltages, speed, and torque signals are available at the output of the block.

Simulation

Start the simulation. You can observe the motor stator current, the rotor speed, the electromagnetic torque, the DC bus voltage and the magnitude of the rotor flux on the first scope. The speed set point and the torque set point are also shown. On the second scope, the dq currents and voltages are displayed. Note that all signals are multiplexed to compare the two models.

At time t = 0 s, the speed set point is 4000 rpm. Observe that the speed follows precisely the acceleration ramp.

At t = 0.5 s, the full load torque is applied to the motor shaft while the motor speed is still ramping.

At t = 0.65 s, the inverter is saturated due to the limited DC bus voltage. You can observe loss of current tracking which decreases the motor torque.

At t = 1.5 s, the speed set point is changed to -4000 rpm.

At t = 2 s, the deceleration ramp reaches the motor speed. The inverter comes back to normal mode.

At t = 2.5 s, the mechanical load passes from 792 N.m to -792 N.m.

At t = 3.45 s, the inverter is saturated due to insufficient DC bus voltage. You can observe loss of current tracking which decreases the motor torque.

Finally, notice that the results of the average-value model are similar to those of the detailed model except that the higher frequency signal components are not represented with the average-value converter.

Notes

To evaluate the speed gain of the average value model, see ac3_example_simplified and compare the simulation speed with ac3_example.

AC3 - Sensorless Field-Oriented Control Induction Motor Drive

This example shows the Sensorless Field-Oriented Control Induction Motor Drive during speed regulation.

Souleman Njoya M., Louis-A. Dessaint (Ecole de technologie superieure, Montreal))
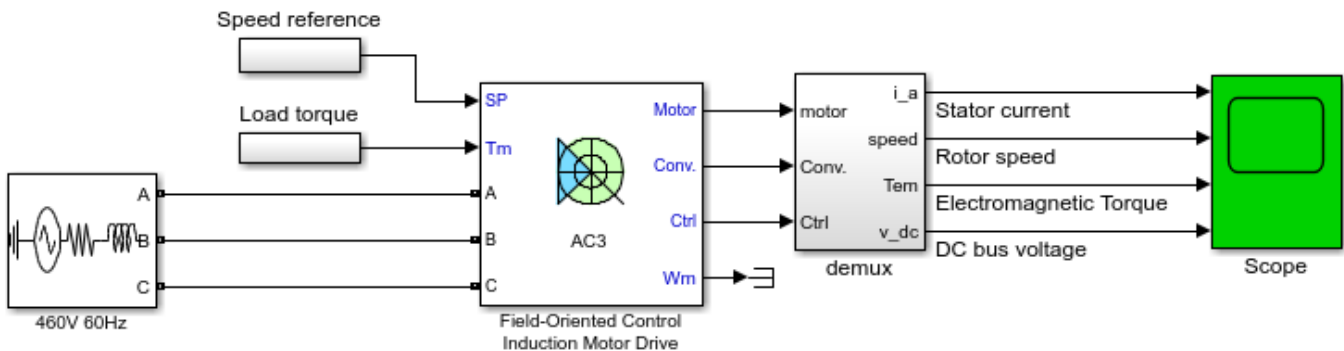
Description

This circuit uses a modified version of the AC3 block of Specialized Power Systems library. It models a sensorless field-oriented control (FOC) induction motor drive with a braking chopper for a 200HP AC motor. The motor speed is estimated from terminal voltages and currents based on the MRAS (Model Referencing Adaptive System) technique [1]. Consequently, the speed sensor (necessary in AC3) is no more required.

The induction motor is fed by a PWM voltage source inverter, which is built using a Universal Bridge Block. The speed control loop uses a PI controller to produce the flux and torque references for the FOC controller. The FOC controller computes the three reference motor line currents corresponding to the flux and torque references and then feeds the motor with these currents using a three-phase current regulator.

Motor current, speed (reference, real and estimated), and torque signals are available at the output of the block.

Simulation

Start the simulation. You can observe the motor stator current, the rotor speed, the electromagnetic torque and the DC bus voltage on the scope. The speed set point and the torque set point are also shown.

At time t = 0 s, the speed set point is 500 rpm. Observe that the speed follows precisely the acceleration ramp.

At t = 0.5 s, the full load torque is applied to the motor shaft while the motor speed is still ramping to its final value. This forces the electromagnetic torque to increase to the user-defined maximum value (1200 N.m) and then to stabilize at 820 N.m once the speed ramping is completed and the motor has reached 500 rpm.

At t = 1 s, the speed set point is changed to 0 rpm. The speed decreases down to 0 rpm by following precisely the deceleration ramp even though the mechanical load is inverted abruptly, passing from 792 N.m to - 792 N.m, at t = 1.5 s. Shortly after, the motor speed stabilizes at 0 rpm.

Finally, note how well the DC bus voltage is regulated during the whole simulation period.

Notes

1) The power system has been discretised with a 2 us time step. The speed controller uses a 100 us sampling time and the vector controller uses a 20us sampling time in order to simulate a microcontroller control device.

2) A simplified version of the model using average-value inverter can be used by selecting 'Average' in the 'Model detail level' menu of the graphical user-interface. The time step can then be increased up to 40 us. This can be done by typing 'Ts = 40e-6' in the workspace and by changing the speed controller sampling time to 120e-6 and the vector controller sampling time to 40e-6. See also ac3_sensorless_simplified model.

References

1. Bose, Bimal K., "Modern Power Electronics And AC Drives", Prentice-Hall, Inc., Upper Saddle River, NJ 07458, 2002.

AC4 - DTC Induction 200 HP Motor Drive

This example shows the AC4 DTC Induction Motor Drive during speed regulation.

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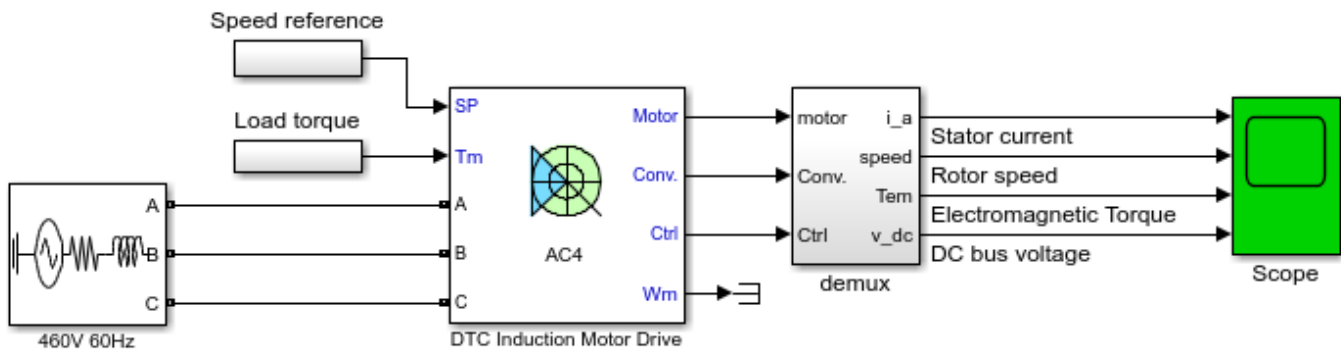
Description

This circuit uses the AC4 block of Specialized Power Systems library. It models a direct torque control (DTC) induction motor drive with a braking chopper for a 200HP AC motor.

The induction motor is fed by a PWM voltage source inverter which is built using a Universal Bridge Block. The speed control loop uses a proportional-integral controller to produce the flux and torque references for the DTC block. The DTC block computes the motor torque and flux estimates and compares them to their respective reference. The comparators outputs are then used by an optimal switching table which generates the inverter switching pulses.

Motor current, speed, and torque signals are available at the output of the block.

Simulation

Start the simulation. You can observe the motor stator current, the rotor speed, the electromagnetic torque and the DC bus voltage on the scope. The speed set point and the torque set point are also shown.

At time t = 0 s, the speed set point is 500 rpm. Observe that the speed follows precisely the acceleration ramp.

At t = 0.5 s, the full load torque is applied to the motor shaft while the motor speed is still ramping to its final value. This forces the electromagnetic torque to increase to the user-defined maximum value (1200 N.m) and then to stabilize at 820 N.m once the speed ramping is completed and the motor has reached 500 rpm.

At t = 1 s, the speed set point is changed to 0 rpm. The speed decreases down to 0 rpm by following precisely the deceleration ramp even though the mechanical load is inverted abruptly, passing from 792 N.m to - 792 N.m, at t = 1.5 s. Shortly after, the motor speed stabilizes at 0 rpm.

Finally, note how well the DC bus voltage is regulated during the whole simulation period.

Notes

The power system has been discretised with a 2 us time step. The speed controller uses a 140 us sample and the DTC controller uses a 20 us sample time in order to simulate a microcontroller control device.

AC4 - Space Vector PWM-DTC Induction 200 HP Motor Drive

This example shows a Space Vector PWM DTC Induction Motor Drive during speed regulation.

C.Semaille, O. Tremblay, L.-A. Dessaint (Ecole de technologie superieure, Montreal)
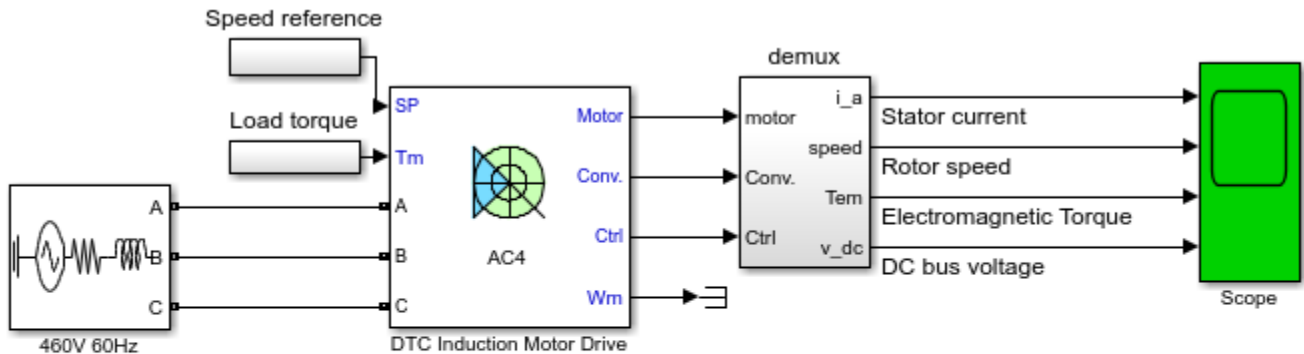
Description

This circuit uses a modified version of the AC4 block of the Specialized Power Systems electric drives library. It models a direct torque control (DTC) induction motor drive with space vector pulse width modulation. The particularity of this modified version is that the DTC is no longer based on hysteresis regulation that implies switching at variable frequency but on a fixed frequency PMW inverter. As in AC4 it uses a braking chopper for a 200HP AC motor.

The induction motor is fed by a PWM voltage source inverter which is built using a Universal Bridge Block. The speed control loop uses a proportional-integral controller to produce the flux and torque references for the DTC block. The DTC block computes the motor torque and flux estimates and compares them to their respective reference. The torque and flux are then controlled by independent PI regulators that compute a reference voltage vector. The voltage source inverter is then controlled by the space vector modulation method in order to output the desired reference voltage.

Motor current, speed, and torque signals are available at the output of the block.

Simulation

Start the simulation. You can observe the motor stator current, the rotor speed, the electromagnetic torque and the DC bus voltage on the scope. The speed set point and the torque set point are also shown.

At time t = 0 s, the speed set point is 500 rpm. Observe that the speed follows precisely the acceleration ramp.

At t = 0.5 s, the full load torque is applied to the motor shaft while the motor speed is still ramping to its final value. This forces the electromagnetic torque to increase to the user-defined maximum value (1200 N.m) and then to stabilize at 820 N.m once the speed ramping is completed and the motor has reached 500 rpm.

At t = 1 s, the speed set point is changed to 0 rpm. The speed decreases down to 0 rpm by following precisely the deceleration ramp even though the mechanical load is inverted abruptly, passing from 792 N.m to - 792 N.m, at t = 1.5 s. Shortly after, the motor speed stabilizes at 0 rpm.

Finally, note how well the DC bus voltage is regulated during the whole simulation period.

Notes

The power system has been discretised with a 2us time step. The speed controller uses a 140 us sample and the DTC controller uses a 20 us sample time in order to simulate a microcontroller control device. The inverter switching frequency is set to 5 kHz.

AC5 - Self-Controlled Synchronous 200 HP Motor Drive

This example shows the AC5 Self-Controlled Synchronous motor drive during speed regulation.

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Description

This circuit uses the AC5 block of Specialized Power Systems library. It models a self-controlled synchronous motor drive with active front-end rectifier for a 200HP motor.

The synchronous motor is fed by a PWM voltage source inverter, which is built using a Universal Bridge Block. The speed control loop uses a PI regulator to produce the flux and current references for the vector controller block. The vector controller computes the three reference motor line currents corresponding to the torque reference and feeds the motor with these currents using a three-phase current regulator. The vector controller also computes the flux estimate and compares it with the desired value in order to generate the field excitation voltage.

Since the field flux dynamics of the synchronous machine are relatively slow, it is advisable to establish first the field flux to its nominal value before feeding the stator with three-phase currents. In this example, a high magnetization voltage of 600V is applied to the rotor field during the first 0.2 s of the simulation in order to speed up the rotor field increase. Once the field flux has reached its nominal value of 1.0 weber, the three-phase current regulator associated to the motor stator is switched on.

Motor current, speed, and torque signals are available at the output of the block.

Simulation

Start the simulation. You can observe the motor stator current, the rotor speed, the electromagnetic torque and the DC bus voltage and the motor magnetic flux on the scope. The speed set point and the torque set point are also shown.

At time t = 1.5 s, the speed set point is 200 rpm. Observe that the speed follows precisely the acceleration ramp and that the stator current amplitude and frequency increase gradually.

At t = 3.0 s, a resistive torque of the nominal value is applied to the motor shaft. Recall that this type of torque tends to decelerate the motor. This explains why the motor speed slightly undershoots. Then, the motor reaches 200 rpm.

At t = 4.0 s, the speed set point is changed to 0 rpm. This forces the motor to produce a lower electric torque. The speed decreases down to 0 rpm following precisely the deceleration ramp. At t = 6.0 s, the speed setpoint reaches 0 rpm.

At t = 5.5 s, the sign of the load torque applied to the motor shaft is reversed. Observe the corresponding small overshoot in the motor speed and the stabilization of the electric torque at its nominal value.

Finally, note how well the DC bus voltage is regulated during the whole simulation period.

Notes

1) The power system has been discretised with a 2us time step. The speed controller uses a 140 us sample and the vector controller uses a 20 us sample time in order to simulate a microcontroller control device.

2) The torque sign convention of the synchronous machine is different from the one of the asynchronous and PM synchronous machines. That is, the synchronous machine is in the motor operation mode when the electric torque is negative and in the generator operation mode when the electric torque is positive.

3) A simplified version of the model using average-value inverter and rectifier can be used by selecting 'Average' in the 'Model detail level' menu of the graphical user-interface. The time step can then be increased up to 50 us. This can be done by typing 'Ts = 50e-6' in the workspace and by changing the speed controller sampling time to 150e-6, the DC bus controller sampling time to 50e-6 and the vector controller sampling time to 50e-6. See also ac5_example_simplified model.

AC5 - Comparison Between Detailed and Simplified Models

This example shows the AC5 detailed model and the AC5 average model during speed regulation. The comparison is performed for normal condition and for inverter-rectifier saturation condition.

O. Tremblay, L.-A. Dessaint (Ecole de technologie superieure, Montreal)
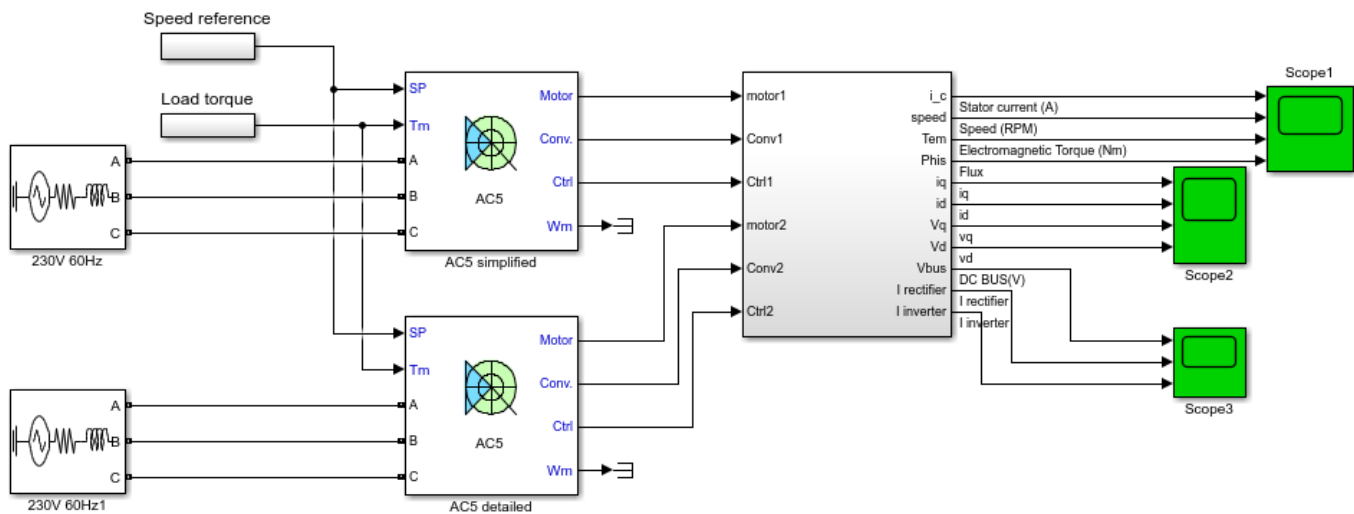
Description

This circuit uses two AC5 blocks of Specialized Power Systems electric drives library. The AC5 block models a self-controlled synchronous motor drive with active front-end rectifier for a 200 HP motor.

The first AC5 block is set to average value model and the second AC5 block is set to detailed model.

For the detailed model, the synchronous motor is fed by a PWM voltage source inverter, which is built using a Universal Bridge Block. The average value model uses ideal voltages and currents sources to feed the synchronous motor. The speed control loop uses a PI regulator to produce the flux and current references for the vector controller block. The vector controller computes the three reference motor line currents corresponding to the torque reference and feeds the motor with these currents using a three-phase current regulator. The vector controller also computes the flux estimate and compares it with the desired value in order to generate the field excitation voltage.

Since the field flux dynamics of the synchronous machine are relatively slow, it is advisable to establish first the field flux to its nominal value before feeding the stator with three-phase currents. In this example, a high magnetization voltage of 600V is applied to the rotor field during the first 0.2 s of the simulation in order to speed up the rotor field increase. Once the field flux has reached its nominal value of 1.0 weber, the three-phase current regulator associated to the motor stator is switched on.

Motor currents, voltages, speed, and torque signals are available at the output of the block.

Simulation

Start the simulation. You can observe the motor stator current, the rotor speed, the electromagnetic torque and the magnitude of the rotor flux on the first scope. The speed set point and the torque set point are also shown. On the second scope, the dq currents and voltages are displayed. Finally, on the third scope, the DC bus voltage, the rectifier current and the inverter current are shown. Note that all signals are multiplexed to compare the two models.

At time t = 1.5 s, the speed set point is 1750 rpm. Observe that the speed follows precisely the acceleration ramp and that the stator current amplitude and frequency increase gradually.

At t = 3.0 s, a resistive torque of -792 N.m is applied to the motor shaft while the motor speed is still ramping. Observe the rectifier saturation on the third scope.

At t = 3.5 s, the inverter is saturated due to the limited DC bus voltage. You can observe loss of current tracking which decreases the motor torque.

At t = 5 s, the speed set point is changed to -1750 rpm.

At t = 6 s, the deceleration ramp reaches the motor speed. The inverter and the rectifier come back to normal mode.

At t = 6.5 s, the mechanical load passes from -792 Nm to 792 Nm.

At t = 9.5 s, observe on the third scope the rectifier saturation.

At t = 10.5 s, the inverter is saturated due to the limited DC bus voltage. You can observe loss of current tracking which decreases the motor torque.

Finally, note how the simplified model reacts well compared to the detailed model.

Notes

To evaluate the speed gain of the average value model, see ac5_example_simplified and compare the simulation speed with ac5_example.

AC6 - PM Synchronous 3HP Motor Drive

This example shows the AC6 PM Synchronous Motor Drive during speed regulation.

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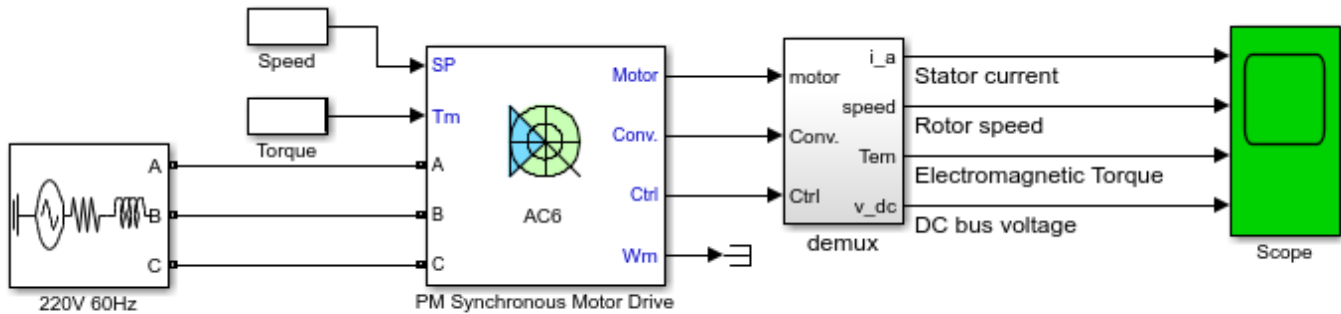
Description

This circuit uses the AC6 block of Specialized Power Systems library. It models a permanent magnet (PM) synchronous motor drive with a braking chopper for a 3HP motor.

The PM synchronous motor is fed by a PWM voltage source inverter, which is built using a Universal Bridge Block. The speed control loop uses a PI regulator to produce the flux and torque references for the vector control block. The vector control block computes the three reference motor line currents corresponding to the flux and torque references and then feeds the motor with these currents using a three-phase current regulator.

Motor current, speed, and torque signals are available at the output of the block.

Simulation

Start the simulation. You can observe the motor stator current, the rotor speed, the electromagnetic torque and the DC bus voltage on the scope. The speed set point and the torque set point are also shown.

At time t = 0 s, the speed set point is 300 rpm. Observe that the speed follows precisely the acceleration ramp.

At t = 0.5 s, the full load torque is applied to the motor. You can observe a small disturbance in the motor speed, which stabilizes very quickly.

At t = 1 s, the speed set point is changed to 0 rpm. The speed decreases down to 0 rpm following precisely the deceleration ramp.

At t = 1.5 s., the mechanical load passes from 11 Nm to -11 Nm. The motor speed stabilizes very quickly after a small overshoot.

Finally, note how well the DC bus voltage is regulated during the whole simulation period.

Notes

1) The power system has been discretised with a 2 us time step. The speed controller uses a 140 us sample and the vector controller uses a 20 us sample time in order to simulate a microcontroller control device.

2) A simplified version of the model using average-value inverter can be used by selecting 'Average' in the 'Model detail level' menu of the graphical user-interface. The time step can then be increased up to 75 us. This can be done by typing 'Ts = 75e-6' in the workspace, by setting Vector control sample time to 75e-6 and by setting Speed controller sample time to 150e-6 in the case of this example. See also ac6_example_simplified model.

AC6 - Comparison Between Detailed and Simplified Models

This example shows the AC6 detailed model and the AC6 average model during speed regulation. The comparison is performed for normal condition and for inverter saturation condition.

O. Tremblay, L.-A. Dessaint (Ecole de technologie superieure, Montreal)
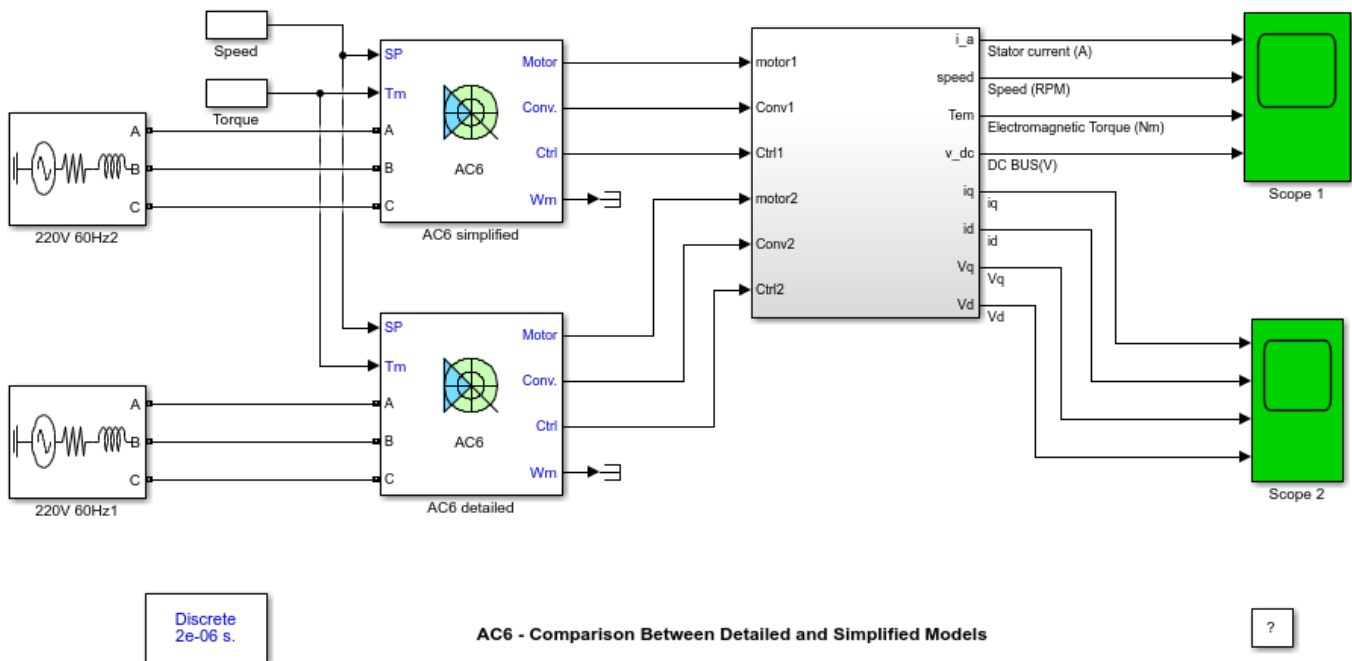
Description

This circuit uses two AC6 blocks of Specialized Power Systems library. It models a permanent magnet (PM) synchronous motor drive with a braking chopper for a 3HP motor. The first block is set to average value model and the second is set to detailed model.

For the detailed model, the PM synchronous motor is fed by a PWM voltage source inverter, which is built using a Universal Bridge Block. The average value model uses ideal voltages and currents sources to feed the PM synchronous motor. The speed control loop uses a PI regulator to produce the flux and torque references for the vector control block. The vector control block computes the three reference motor line currents corresponding to the flux and torque references and then feeds the motor with these currents using a three-phase current regulator.

Motor currents, voltages, speed, and torque signals are available at the output of the block.

Simulation

Start the simulation. You can observe the motor stator current, the rotor speed, the electromagnetic torque and the DC bus voltage on the first scope. The speed set point and the torque set point are also shown. On the second scope, the dq currents and voltages are displayed. Note that all signals are multiplexed to compare the two models.

At time t = 0 s, the speed set point is 2400 rpm and the full load torque is applied to the motor. Observe that the speed follows precisely the acceleration ramp.

At t = 0.7 s, the inverter is saturated due to insufficient inverter voltage. You can observe loss of current tracking which decreases the motor torque.

At t = 1.2 s, the speed set point is changed to -2400 rpm.

At t = 1.5 s., the deceleration ramp reaches the motor speed. The inverter comes back to normal mode.

At t = 2 s., the mechanical load passes from 11 Nm to -11 Nm.

At t = 3.15 s., the inverter is saturated due to insufficient inverter voltage. You can observe loss of current tracking which decreases the motor torque.

Finally, note how the simplified model reacts well compared to the detailed model.

Notes

To evaluate the speed gain of the average value model, see ac6_example_simplified and compare the simulation speed with ac6_example.

AC6 - 100 kW Interior Permanent Magnet Synchronous Motor Drive

This example shows Vector Control for an interior Permanent Magnet Synchronous Motor (PMSM) during torque regulation.

In the ac6_example model, it was assumed that the PMSM had its permanent magnets mounted on the surface of the rotor. This type of PMSM has therefore a uniform air gap and no saliency, hence Ld = Lq. It is assumed that the PMSM has an interior permanent magnets rotor. The impact of the buried-magnet configuration is rotor saliency that makes Lq > Ld and introduces a reluctance torque term into the PMSM torque equation. To take advantage of the reluctance torque, the Id current component is no longer set to zero has it is for the PMSM with surface mounted permanent magnets.

Olivier Tremblay, Louis-A. Dessaint (Ecole de Technologie Superieure, Montreal).

AC6 - 100 kW Interior Permanent Magnet Synchronous Motor Drive

* The library link of this block is intentionally broken
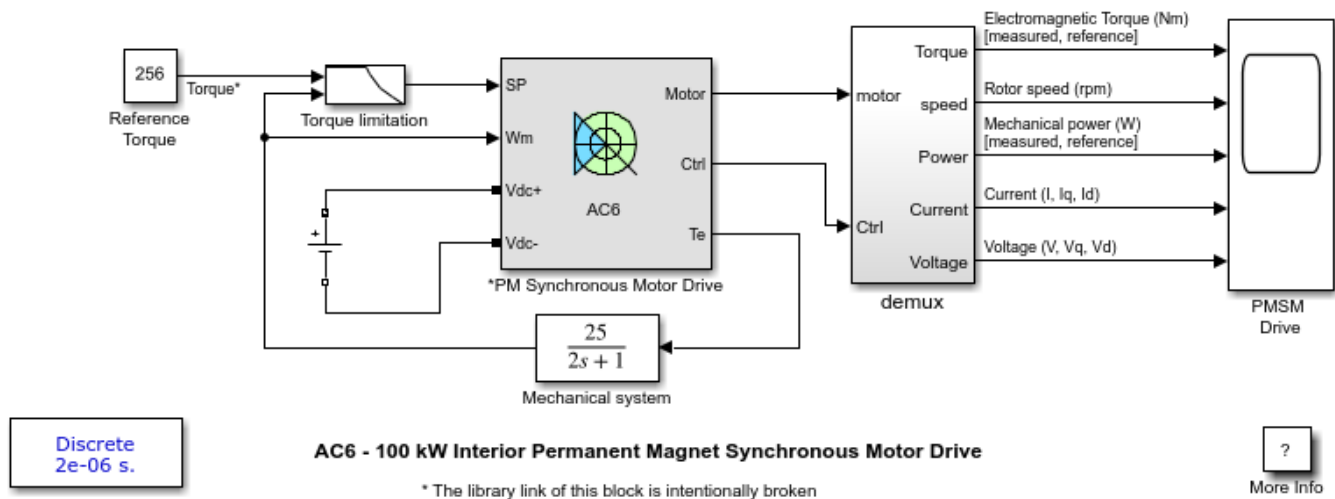
The 'Ts' parameter used in this model
is set to 2e-6 by the Model Properties Callbacks

Description

This circuit uses a modified version of the AC6 block of the Specialized Power Systems electric drives library. It models a flux weakening vector control for a 100 kW, 12500 rpm, salient pole PMSM powered by a 288 Vdc source. The mechanical system is represented externally. That's why the input of the motor is the speed and the output is the electromagnetic torque.

The **PM Synchronous Motor Drive** is composed of four main parts: The electrical motor, the Three-phase Inverter, the VECT controller and the Speed Controller.

- The electrical motor is a 288 Vdc, 100 kW PMSM. This motor has 8 pole and the magnets are buried (salient rotor's type).
- The Three-phase Inverter is a voltage source inverter, controlled by PWM. This block is built using the Universal Bridge Block.
- The VECT controller block computes the three reference motor line currents corresponding to the flux and torque references and then generates a corresponding PWM using a three-phase current

regulator. When the nominal flux is required, an optimal control is used in order to minimise the line current amplitude for the required torque. When a flux weakening is needed, the amplitude and the phase of the current are changed to extend the torque-speed operating range.

- The Speed Controller is used in torque regulation mode. The normalized flux value is computed with the speed of the machine in order to perform a flux weakening control.

The Torque limitation block is used to prevent the limitation due to the torque-speed characteristic of this motor for a 288 Vdc source. When the internal machine's voltage reaches the inverter voltage (because the desired torque is too high for the motor's speed), the inverter becomes in saturation mode (the desired current cannot flow anymore into motor). After this point, there will be a loss of current tracking which will decrease the motor current. This block is used to reduce the reference torque as a function of the motor's speed and the torque-speed characteristic in order to never operate in inverter saturation mode.

Motor torque, speed, power, currents and voltages signals are available at the output of the block.

Simulation

Start the simulation. You can observe the motor torque (electromagnetic and reference), the rotor speed, the mechanical power (electromagnetic and reference), the stator currents (magnitude, Iq and Id), and the stator voltages (magnitude, Vq and Vd)

- At t = 0 s, the torque set point is set to 256 Nm (the nominal torque of the motor). The electromagnetic torque reaches rapidly the reference.

- At t = 0.104 s, the rotor speed exceeds the nominal speed of 3000 rpm. Hence, a flux weakening is performed in order to limit the back electromotive force (BEMF) of the motor; therefore the Id current component is increased (negatively). Also the reference torque is limited (due to the torque-speed characteristic of the motor) to prevent inverter saturation, causing a decrease in the Iq current component. Note that the magnitude of the current is constant; only the angle changes.

Now change the **Reference Torque** to 100 Nm and observe the results:

- At t = 0 s, the torque set point is set to 100 Nm. The current amplitude is optimal for this torque.

- At t = 0.28 s, the rotor speed exceeds the nominal speed of 3000 rpm. Hence, a flux weakening is performed in order to limit the back electromotive force (BEMF) of the motor; therefore the Id current component is increased (negatively).

- At t = 1.06 s, the reference torque is limited (due to the torque-speed characteristic of the motor) to prevent inverter saturation, causing a decrease in the Iq current component. The magnitude of the current is maintained at a constant value, but the phase of current changes.

Note that the electromagnetic torque follows precisely the reference torque even in the flux weakening region.

Notes

1) The power system has been discretised with a 2 us time step. The speed controller uses a 140 us sample and the vector controller uses a 20 us sample time in order to simulate a micro controller control device.

See Also

- AC6 - PM Synchronous 3HP Motor Drive

AC7 - Brushless DC Motor Drive During Speed Regulation

This example shows the AC7 Brushless DC Motor Drive during speed regulation.

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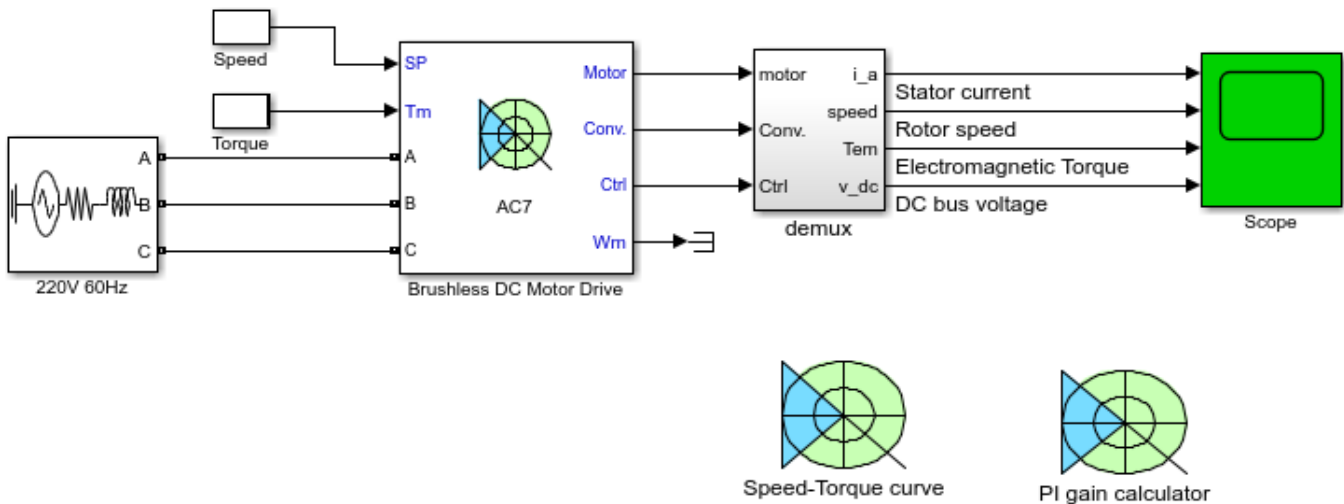
Description

This circuit uses the AC7 block of Specialized Power Systems library. It models a brushless DC motor drive with a braking chopper for a 3HP motor.

The permanent magnet synchronous motor (with trapezoidal back-EMF) is fed by a PWM voltage source inverter, which is built using a Universal Bridge Block. The speed control loop uses a PI regulator to produce the torque reference for the current control block. The current control block computes the three reference motor line currents, in phase with the back electromotive forces, corresponding to the torque reference and then feeds the motor with these currents using a three-phase current regulator.

Motor current, speed, and torque signals are available at the output of the block.

Simulation

Start the simulation. You can observe the motor stator current, the rotor speed, the electromagnetic torque and the DC bus voltage on the scope. The speed set point and the torque set point are also shown.

At time t = 0 s, the speed set point is 300 rpm. Observe that the speed follows precisely the acceleration ramp.

At t = 0.5 s, the full load torque is applied to the motor. You can observe a small disturbance in the motor speed, which stabilizes very quickly.

At t = 1 s, the speed set point is changed to 0 rpm. The speed decreases down to 0 rpm following precisely the deceleration ramp.

At t = 1.5 s., the mechanical load passes from 11 Nm to -11 Nm. The motor speed stabilizes very quickly after a small overshoot.

Finally, note how well the DC bus voltage is regulated during the whole simulation period.

Notes

1) The power system has been discretised with a 2 us time step. The speed controller uses a 140 us sample and the current controller uses a 20 us sample time in order to simulate a microcontroller control device.

2) To calculate automatically the speed regulator gains, double-click on the PI gain calculator icon. Enter the mechanical parameters of the machine and click on the "Enter specification" checkbox. Enter the desired specifications and click on the "Calculate Kp and Ki" checkbox.

If you don't know the specifications, uncheck the "Enter specification" checkbox and enter the electrical parameters of the machine. Click on the "Machine natural frequency" checkbox to obtain the open-loop machine response. After, click on the "Enter specification" checkbox to enter a response time faster than the open loop response time and click on the "Calculate Kp and Ki" checkbox.

3) To obtain the operation regions of the drive system, double-click on the Speed-Torque curve icon. Enter the machine parameters, the DC bus voltage, the output torque limit and the acceleration speed ramp and click on the "Plot Speed-Torque curve" checkbox.

4) A simplified version of the model using average-value inverter can be used by selecting 'Average' in the 'Model detail level' menu of the graphical user-interface. The time step can then be increased up to 40 us. This can be done by typing 'Ts = 40e-6' in the workspace, by setting current control sample time to 40e-6 and by setting Speed controller sample time to 120e-6 in the case of this example. See also ac7_example_simplified model.

AC7 - Comparison Between Detailed and Simplified Models

This example shows the AC7 detailed model and the AC7 average model during speed regulation. The comparison is performed for normal condition and for inverter saturation condition.

O.Tremblay, L.-A. Dessaint (Ecole de technologie superieure, Montreal)
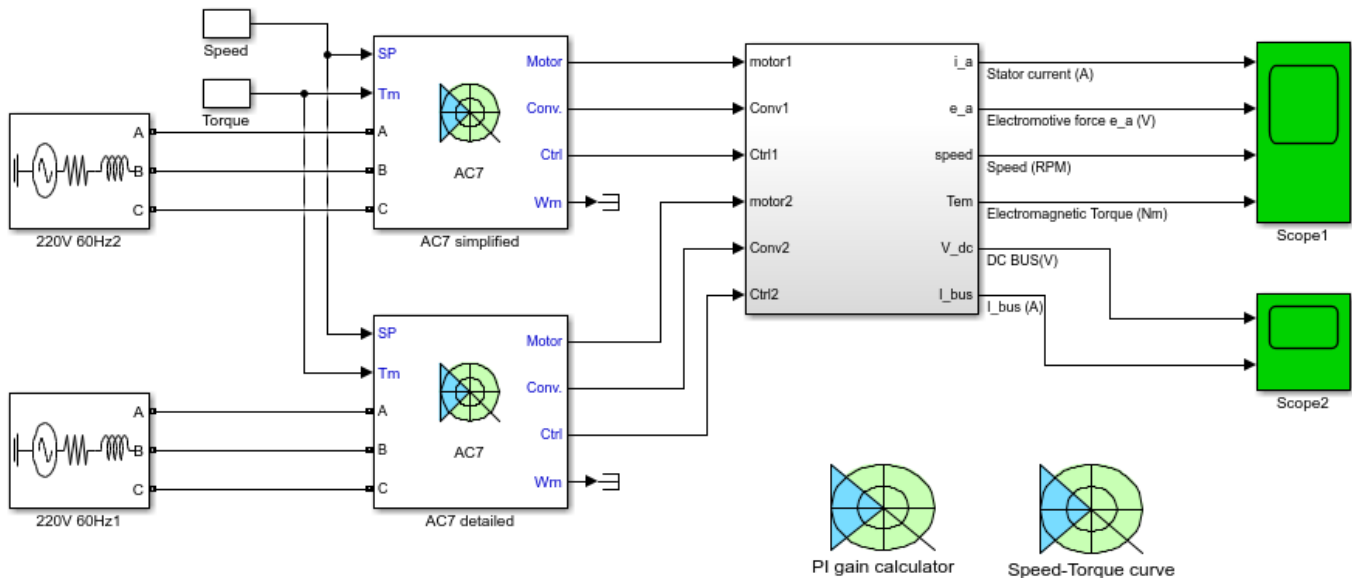
Description

This circuit uses two AC7 blocks from the Specialized Power Systems library. It models a brushless DC motor drive with a braking chopper for a 3HP motor. The first block is set to average value model and the second is set to detailed model.

For the detailed model, the permanent magnet (PM) synchronous motor (with trapezoidal back-EMF) is fed by a PWM voltage source inverter, which is built using a Universal Bridge Block. The average value model uses ideal voltages and currents sources to feed the PM synchronous motor. The speed control loop uses a PI regulator to produce the torque reference for the current control block. The current control block computes the three reference motor line currents, in phase with the back electromotive forces, corresponding to the torque reference and then feeds the motor with these currents using a three-phase current regulator.

Motor currents, voltages, speed, and torque signals are available at the output of the block.

Simulation

Start the simulation. You can observe the motor stator current, the back EMF, the rotor speed and the electromagnetic torque on the first scope. The speed set point and the torque set point are also shown. On the second scope, the DC bus voltage and the bus current are displayed. Note that all signals are multiplexed to compare the two models.

At time t = 0 s, the speed set point is 2400 rpm and the full load torque is applied to the motor. Observe that the speed follows precisely the acceleration ramp.

At t = 0.6 s, the inverter is saturated due to insufficient inverter voltage. You can observe loss of current tracking which decreases the motor torque. This saturation point occurs when the speed is about 1200 rpm. That's the same point as predicted by the operation region during acceleration (when load torque is 11 Nm) given by the Speed-Torque curve tool (double-click on the 'Speed-Torque curve' tool to check the operating regions).

At t = 1.2 s, the speed set point is changed to -2400 rpm.

At t = 1.6 s., the deceleration ramp reaches the motor speed. The inverter comes back to normal mode.

At t = 2 s., the mechanical load passes from 11 Nm to -11 Nm.

At t = 3 s., the inverter is saturated due to insufficient inverter voltage. You can observe loss of current tracking which decreases the motor torque.

Finally, note how the simplified model reacts well compared to the detailed model.

Notes

To evaluate the speed gain of the average value model, see ac7_example_simplified and compare the simulation speed with ac7_example.

AC7 - Sensorless Brushless DC Motor Drive During Speed Regulation

This example shows the AC7 Sensorless Brushless DC Motor Drive during speed regulation.

Souleman Njoya M., Louis-A. Dessaint (Ecole de technologie superieure, Montreal))
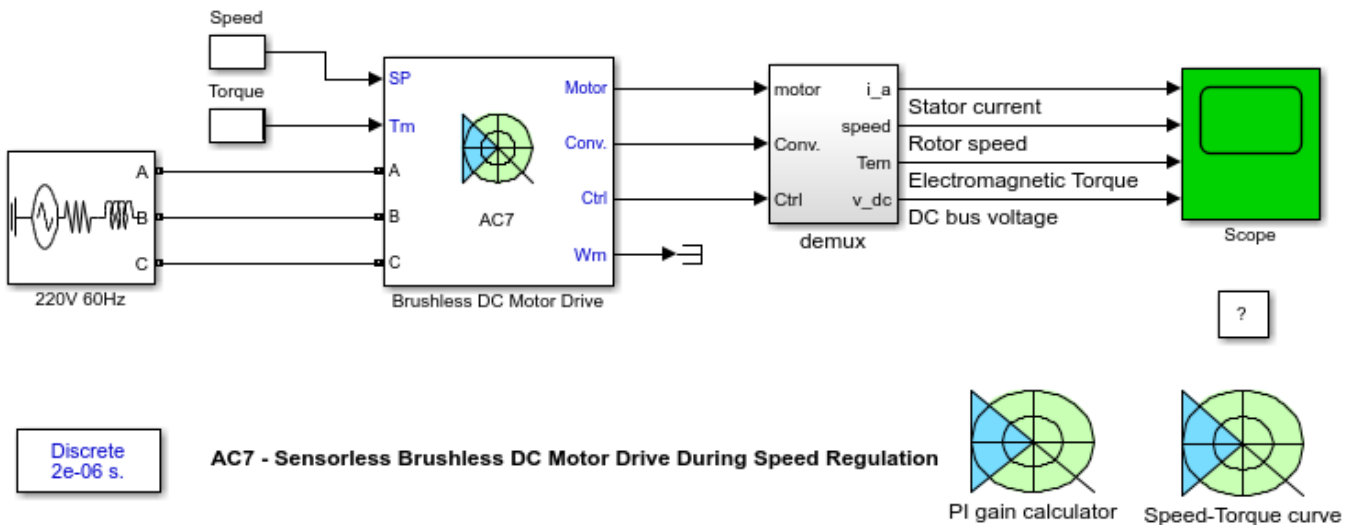
Description

This circuit uses a modified version of the AC7 block of Specialized Power Systems library. It models a sensorless brushless DC motor drive with a braking chopper for a 3HP motor. The AC7 which requires speed and hall sensors is made sensorless. The motor speed and position are estimated from terminal voltages and currents using a back-emf observer [1]. The commutations signals (equivalent to hall effect signals) are generated from the rotor position every 60 electrical degrees.

The permanent magnet synchronous motor (with trapezoidal back-EMF) is fed by a PWM voltage source inverter, which is built using a Universal Bridge Block. The speed control loop uses a PI regulator to produce the torque reference for the current control block. The current control block computes the three reference motor line currents, in phase with the back electromotive forces, corresponding to the torque reference and then feeds the motor with these currents using a three-phase current regulator.

Motor current, speed (reference, real and estimated), and torque signals are available at the output of the block.

Simulation

Start the simulation. You can observe the motor stator current, the rotor speed, the electromagnetic torque and the DC bus voltage on the scope. The speed set point and the torque set point are also shown.

At time t = 0 s, the speed set point is 300 rpm. Observe that the speed follows precisely the acceleration ramp.

At t = 0.5 s, the full load torque is applied to the motor. You can observe a small disturbance in the motor speed, which stabilizes very quickly.

At t = 1 s, the speed set point is changed to 0 rpm. The speed decreases down to 0 rpm following precisely the deceleration ramp.

Notes

1) The power system has been discretised with a 2 us time step. The speed controller uses a 140 us sample and the current controller uses a 20 us sample time in order to simulate a microcontroller control device.

References

1. TS Kim, BG Park, DM Lee, JS Ryu, DS Hyun, "A New Approach to Sensorless Control Method for Brushless DC Motors", International Journal of Control, Automation, and Systems, vol. 6, no. 4, pp. 477-487, August 2008.

AC8 - PM Synchronous 3HP Motor Drive

This example shows the AC8 Five-Phase PM Synchronous Motor Drive during speed regulation.

J.-F. Doyon, L.-A. Dessaint (Ecole de technologie superieure, Montreal)
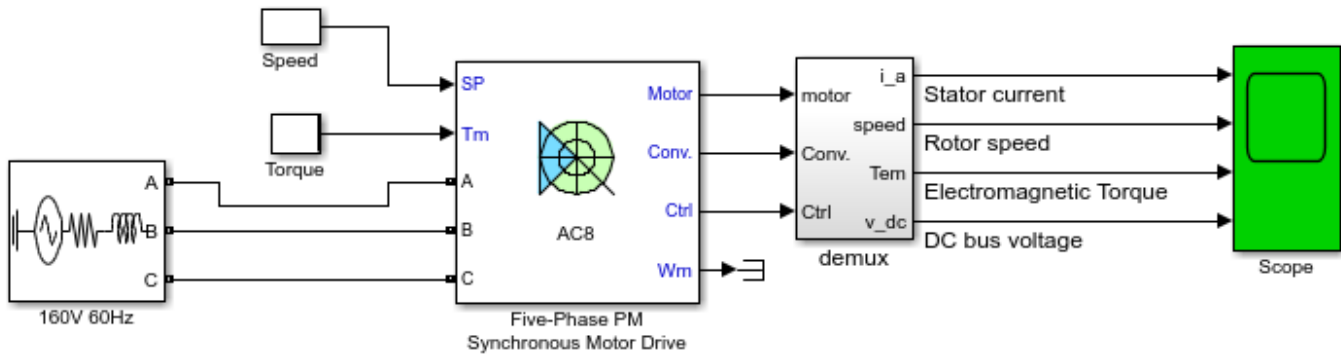
Description

This circuit uses the AC8 block of Specialized Power Systems library. It models a five-phase permanent magnet (PM) synchronous motor drive with a braking chopper for a 4.4kW five-phase motor.

The PM synchronous motor is fed by a PWM voltage source inverter, which is built using a Universal Bridge Block. The speed control loop uses a PI regulator to produce the flux and torque references for the vector control block. The vector control block computes the five reference motor line currents corresponding to the flux and torque references and then feeds the motor with these currents using a five-phase current regulator.

Motor current, speed, and torque signals are available at the output of the block.

Simulation

Start the simulation. You can observe the motor stator current, the rotor speed, the electromagnetic torque and the DC bus voltage on the scope. The speed set point and the torque set point are also shown.

At time t = 0 s, the speed set point is 300 rpm. Observe that the speed follows precisely the acceleration ramp.

At t = 0.5 s, the full load torque is applied to the motor. You can observe a small disturbance in the motor speed, which stabilizes very quickly.

At t = 1 s, the speed set point is changed to 0 rpm. The speed decreases down to 0 rpm following precisely the deceleration ramp.

At t = 1.5 s., the mechanical load passes from 11 Nm to -11 Nm. The motor speed stabilizes very quickly after a small overshoot.

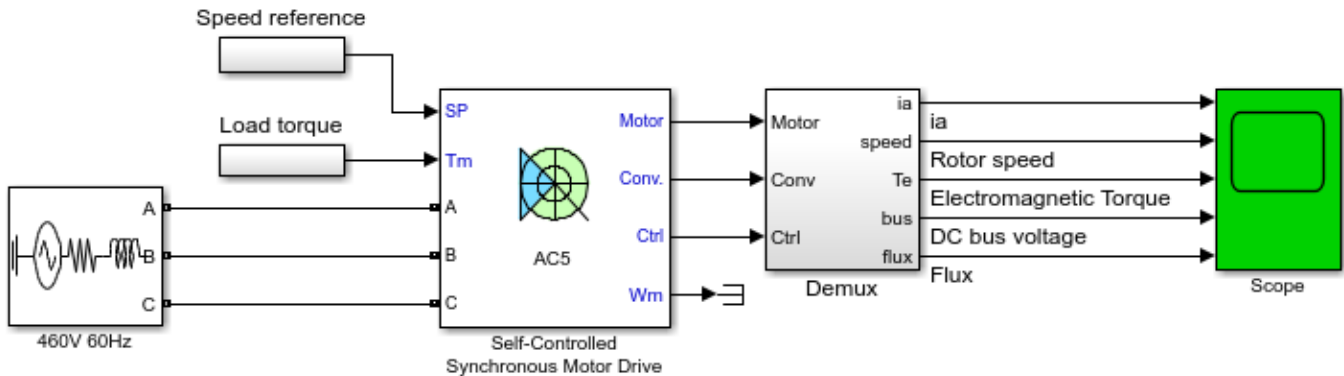Finally, note how well the DC bus voltage is regulated during the whole simulation period.

Notes

1) The power system has been discretized with a 2 us time step. The speed controller uses a 80 us sample and the vector controller uses a 8 us sample time in order to simulate a micro-controller control device.

2) A simplified version of the model using average-value inverter can be used by selecting 'Average' in the 'Model detail level' menu of the graphical user-interface. The time step can then be increased up to 35 us. This can be done by typing 'Ts = 35e-6' in the workspace, by setting Vector control sample time to 35e-6 and by setting Speed controller sample time to 70e-6 in the case of this example. See also ac8_example_simplified model.

AC8 - Comparison Between Detailed and Simplified Models

This example shows the AC8 detailed model and the AC8 average model during speed regulation.

J-F. Doyon, L.-A. Dessaint (Ecole de technologie superieure, Montreal)

Description

This circuit uses two AC8 blocks of Specialized Power Systems library. It models a five-phase permanent magnet (PM) synchronous motor drive with a braking chopper for a 4.4kW motor. The first block is set to average value model and the second is set to detailed model.

For the detailed model, the five-phase PM synchronous motor is fed by a PWM voltage source inverter, which is built using a Universal Bridge Block. The average value model uses ideal voltages and currents sources to feed the PM synchronous motor. The speed control loop uses a PI regulator to produce the flux and torque references for the vector control block. The vector control block computes the five reference motor line currents corresponding to the flux and torque references and then feeds the motor with these currents using a five-phase current regulator.

Motor currents, voltages, speed, and torque signals are available at the output of the block.

Simulation

Start the simulation. You can observe the motor stator current, the rotor speed, the electromagnetic torque and the DC bus voltage on the first scope. The speed set point and the torque set point are also shown. On the second scope, the dq currents and voltages are displayed. Note that all signals are multiplexed to compare the two models.

At time t = 0 s, the speed set point is 900 rpm and a 25 Nm load torque is applied to the motor. Observe that the speed follows precisely the acceleration ramp.

At t = 0.45 s, the speed set point is reached by both drives.

At t = 1.2 s, the speed set point is changed to -900 rpm.

At t = 2 s., the mechanical load passes from 25 Nm to -25 Nm. You can observe a small disturbance in the motor speed, which stabilizes very quickly.

At t = 2.1 s., the speed set point is reached by both drives.

Finally, note how the simplified model reacts well compared to the detailed model.

Notes

To evaluate the speed gain of the average value model, see ac8_example_simplified and compare the simulation speed with ac8_example.

AC9 - Single-Phase Induction Motor Drive

This example shows the Single-Phase Induction Motor Drive during speed regulation.

E. Vilchez, L.-A. Dessaint (Ecole de technologie superieure, Montreal)

AC9 - Single-Phase Induction Motor Drive

This example shows the Single-Phase Induction Motor Drive
during speed regulation.

Learn more about this example.
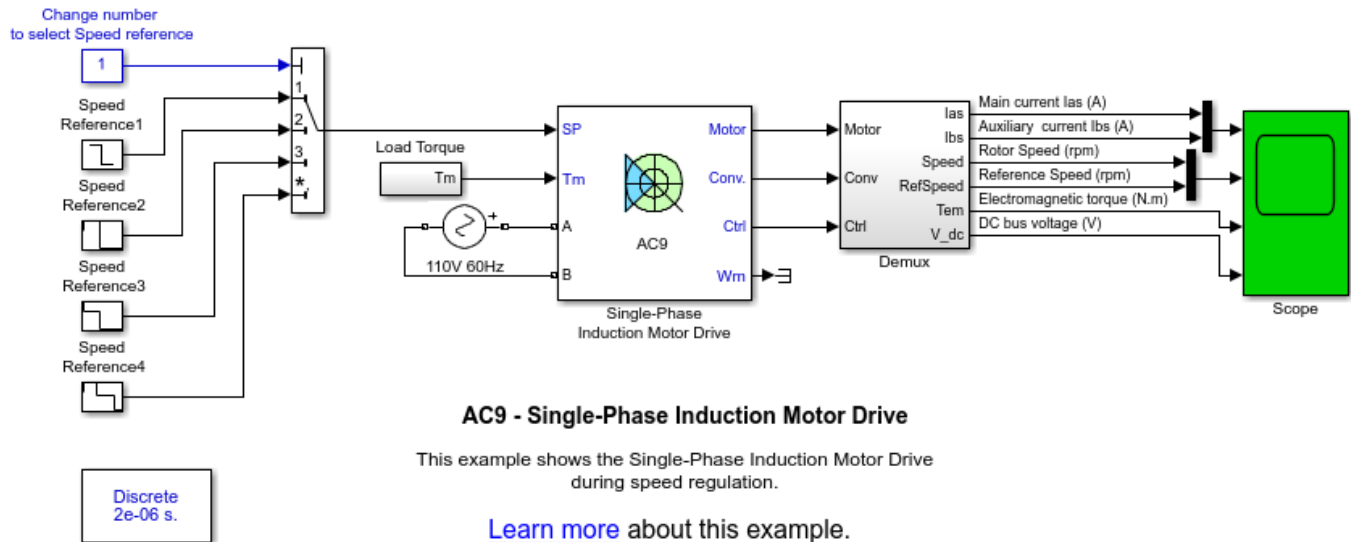
Description

This circuit uses the AC9 block of Specialized Power Systems library. It models a vector-controlled single-phase machine drive for a 1/4 HP, 110 V, 60Hz single phase AC motor with its auxiliary and main windings accessible (Main & auxiliary windings operation mode).

The single-phase induction motor is fed by a two-leg inverter, which is built using a Universal Bridge Block. The speed control loop uses a PI controller to produce the flux and torque references for the block controller. In this, two vector control strategies are implemented: Field oriented control (FOC) and direct torque control (DTC) and the modulation is hysteresis-based.

Motor current, speed, and torque signals are available at the output of the block.

Simulation

Select one of the four speed references provided. Start the simulation. You can observe the main and auxiliary windings stator currents, the rotor speed, the electromagnetic torque and the DC bus voltage on the Scope block. The speed set point is also shown. Restart the simulation and observe the drive response to successive changes in speed reference and control type (FOC or DTC)

DC1 - Two-Quadrant Single-Phase Rectifier 5 HP DC Drive

This example shows the DC1 two-quadrant single-phase rectifier DC drive during speed regulation.

C.Semaille, Louis-A. Dessaint (Ecole de technologie superieure, Montreal)

Description

This circuit uses the DC1 block of Specialized Power Systems. It models a two-quadrant single-phase rectifier drive for a 5 HP DC motor.

The 5 HP DC motor is separately excited with a constant 150 V DC field voltage source. The armature voltage is provided by a single-phase rectifier controlled by two PI regulators. The rectifier is fed by a 220 V AC 50 Hz voltage source followed by a linear transformer to boost the voltage up to a sufficient value.

The regulators control the firing angle of the rectifier thyristors. The first regulator is a speed regulator, followed by a current regulator. The speed regulator outputs the armature current reference (in p.u.) used by the current controller in order to obtain the electromagnetic torque needed to reach the desired speed. The speed reference change rate follows acceleration and deceleration ramps in order to avoid sudden reference changes that could cause armature over-current and destabilize the system. The current regulator controls the armature current by computing the appropriate thyristor firing angle. This generates the rectifier output voltage needed to obtain the desired armature current.

A 150 mH smoothing inductance is placed in series with the armature circuit to reduce armature current oscillations.

Simulation

Start the simulation. You can observe the motor armature voltage and current, the rectifier firing angle and the motor speed on the scope. The current and speed references are also shown.

The speed reference is set at 1750 rpm at t = 0 s. Initial load torque is 15 N.m.

Observe that the motor speed follows the reference ramp accurately (+250 rpm/s) and reaches steady state around t = 8.5 s. The armature current follows the current reference very well, and the firing angle stays below 90 degrees, the converter being in rectifier mode (first quadrant operating mode). The lower limit of the firing angle has been set to 20 degrees.

At t = 8.75 s, the load torque passes from 15 N.m to 20 N.m. The motor speed recovers fast and is back at 1750 rpm at t = 10 s. The current reference rises to about 17.5 A to generate a higher electromagnetic torque to maintain the needed speed. As observed before, the armature current follows its reference perfectly.

Notes

1) The power system has been discretized with a 25 us time step. The control system (regulators) uses a 100 us time step in order to simulate a microcontroller control device.

2) In order to reduce the number of points stored in the scope memory, a decimation factor of 20 is used.

3) A simplified version of the model using an average-value rectifier can be used by selecting 'Average' in the 'Model detail level' menu of the graphical user-interface. The time step can then be increased up to the control system sample time value.This can be done by typing 'Ts = 100e-6' in the workspace in the case of this example. See also dc1_example_simplified model.

DC1 - Two-Quadrant Single-Phase Rectifier 5 HP DC Drive with Regenerative Braking System

This example shows a two quadrant single-phase rectifier DC drive with regenerative braking system.

C.Semaille, Louis-A. Dessaint (Ecole de technologie superieure, Montreal)

Description

This circuit is based on the DC1 block of Specialized Power Systems. It models a two-quadrant single-phase rectifier drive for a 5 HP DC motor. A braking unit block has been added in order to simulate regenerative braking (quadrant IV operating mode).

The 5 HP DC motor is separately excited with a constant 150 V DC field voltage source. The armature voltage is provided by a single-phase rectifier controlled by two PI regulators. The rectifier is fed by a 220 V AC 50 Hz voltage source followed by a linear transformer to boost the voltage up to a sufficient value.

The regulators control the firing angle of the rectifier thyristors. The first regulator is a speed regulator, followed by a current regulator. The speed regulator outputs the armature current reference (in p.u.) used by the current controller in order to obtain the electromagnetic torque needed to reach the desired speed. The speed reference change rate follows acceleration and deceleration ramps in order to avoid sudden reference changes that could cause armature over-current and destabilize the system. The current regulator controls the armature current by computing the appropriate thyristor firing angle. This generates the rectifier output voltage needed to obtain the desired armature current.

The braking unit block is managed by a finite state machine with two states (normal operating mode and braking operating mode). When the system passes in braking mode, armature switches are

activated and allow reversal of the armature current flow. This generates a reverse electromagnetic braking torque for fast speed deceleration. The reversal of the current flow is initiated when the armature current flowing through the switches equals 0 A. This avoids destructive arcs in the switches during commutation.

A 150 mH smoothing inductance is placed in series with the armature circuit to reduce current oscillations.

Simulation

Start the simulation. You can observe the motor voltage, current, speed and rectifier firing angle on the scope. The speed and current references are also shown.

The initial speed reference is set to 800 rpm. Load torque is 10 N.m. Observe that the motor speed follows the acceleration reference ramp accurately (+350 rpm/s) and reaches steady-state after about 3.5 s. The armature current follows the current reference very well and stays below nominal current. During this phase, the average firing angle value stays below 90 degrees, the thyristor bridge being in rectifier mode (first quadrant operating mode).

At t = 4 s, the speed reference drops to 200 rpm and the system passes in braking mode. The armature switches are activated when the armature current reaches 0 A and reversal of the current flow through the motor takes place (the current flow direction through the bridge is of course unchanged). Observe again that the motor speed follows the deceleration ramp as wanted. The deceleration ramp has been set to a high value (-1250 rpm/s) to clearly show the effect of the braking electromagnetic torque. During this period of time, the bridge works in inverter mode (second quadrant operating mode).

At t = 4.5 s, motor speed is slightly lower than speed reference and the armature current flow through the motor is reversed back to normal. The bridge operates in rectifier mode and motor speed reaches 200 rpm around t = 5.5 s.

Notice the current overshoot during commutation. This is due to the sudden voltage reversal at the bridge output caused by armature switching. The bridge output voltage cannot follow instantaneously this voltage reversal. This sudden voltage difference between bridge and motor creates the current overshooting. However, the overshoot peak is of reasonable value and is not damageable.

Notes

1) The power system has been discretized with a 10 us time step. The control system (regulators and braking unit) uses a 100 us time step in order to simulate a microcontroller control device.

2) In order to reduce the number of points stored in the scope memory, a decimation factor of 20 is used.

DC2 - Four-Quadrant Single-Phase Rectifier 5 HP DC Drive

This example shows the DC2 four-quadrant single-phase rectifier DC drive with circulating current during speed regulation.

C.Semaille, Louis-A. Dessaint (Ecole de technologie superieure, Montreal)
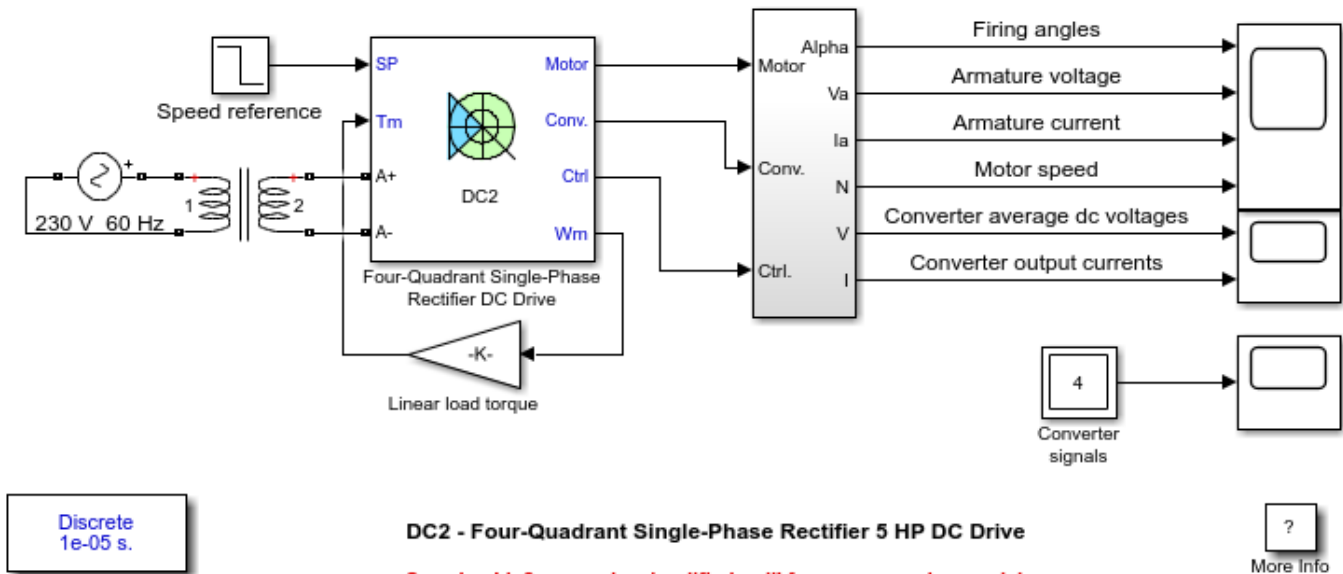
Description

This circuit uses the DC2 block of Specialized Power Systems. It models a four-quadrant single-phase rectifier (dual-converter topology) drive for a 5 HP DC motor.

The 5 HP DC motor is separately excited with a constant 150 V DC field voltage source. The armature voltage is provided by two single-phase anti-parallel connected converters controlled by two PI regulators. This allows bidirectional current flow through the DC motor armature circuit and thus four-quadrant operation. The converters are fed by a 230 V AC 60 Hz voltage source followed by a linear transformer to boost the voltage up to a sufficient value.

The regulators control the firing angles of both converter thyristors. The first regulator is a speed regulator, followed by a current regulator. The speed regulator outputs the armature current reference (in p.u.) used by the current controller in order to obtain the electromagnetic torque needed to reach the desired speed. The speed reference change rate follows acceleration and deceleration ramps in order to avoid sudden reference changes that could cause armature over-current and destabilize the system. The current regulator controls the armature current by computing the appropriate thyristor firing angles. This generates the converter output voltages needed to obtain the desired armature current.

Both converters operate simultaneously and the two firing angles are controlled so that their sum gives 180 degrees. This produces opposite average voltages at the converter dc output terminals and

thus identical average voltages at the DC motor armature, the converters being connected in anti-parallel. One converter is working in rectifier mode while the other is in inverter mode.

The circulating current produced by the instantaneous voltage difference at the terminal of both converters is limited by 80 mH inductors connected between these terminals. A 50 mH smoothing inductance is placed in series with the armature circuit to reduce armature current oscillations.

Simulation

Start the simulation. You can observe the motor armature voltage and current, the converter firing angles and the motor speed on the scope. The current and speed references are also shown. A second scope allows you to visualize the converter average output voltages and output currents.

During this simulation, the motor is coupled to a linear load, which means that the mechanical torque produced by the load is proportional to the speed.

The speed reference is set at 1200 rpm at t = 0 s. Observe that the firing angles are symmetrical around 90 degrees and that the converter average output DC voltages are of opposite signs. The armature current is supplied by converter 1, and the total current in this converter is the sum of load current and circulating current. Converter 2 simply carries the circulating current.

Observe that the motor speed follows the reference ramp accurately (+250 rpm/s) and reaches steady state after 5.5 s. The armature current follows the current reference very well and stabilizes around 12 A.

At t = 6 s, speed reference drops to -600 rpm. The current reference decreases to reduce the electromagnetic torque, and the load torque causes the motor to decelerate. Around t = 10.4 s, the armature current becomes negative and the electromagnetic torque reverses in order to brake the motor down to 0 rpm, the load torque being insufficient to decelerate the motor. At t = 10.8 s, the motor reaches 0 rpm and the load torque becomes negative. The electromagnetic torque now produces an accelerating torque to allow the motor to follow the negative speed ramp (-250 rpm/s). The armature current is now provided by converter 2, converter 1 only handling the circulating current.

At t = 13.2 s, speed stabilizes at -600 rpm.

Notes

1) The power system has been discretized with a 10 us time step. The control system (regulators) uses a 100 us sample time in order to simulate a microcontroller control device.

2) In order to reduce the number of points stored in the scope memory, a decimation factor of 20 is used.

3) A simplified version of the model using average-value rectifiers can be used by selecting 'Average' in the 'Model detail level' menu of the graphical user-interface. The time step can then be increased up to the control system sample time value.This can be done by typing 'Ts = 100e-6' in the workspace in the case of this example. See also dc2_example_simplified model.

DC3 - Two-Quadrant Three-Phase Rectifier 200 HP DC Drive

This example shows the DC3 two-quadrant three-phase rectifier DC drive during torque regulation.

C.Semaille, Louis-A. Dessaint (Ecole de technologie superieure, Montreal)
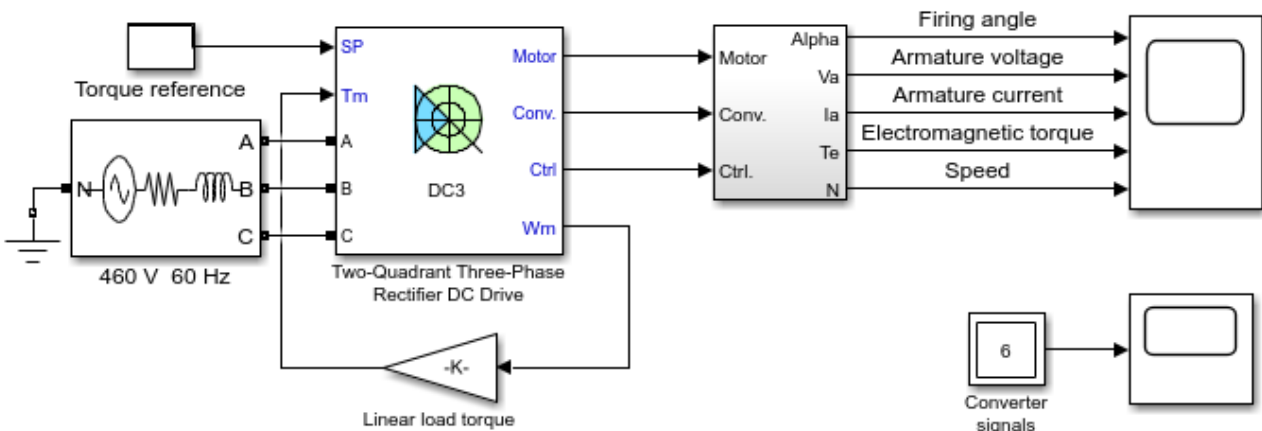
Description

This circuit uses the DC3 block of Specialized Power Systems. It models a two-quadrant three-phase rectifier drive for a 200 HP DC motor.

The 200 HP DC motor is separately excited with a constant 310 V DC field voltage source. The armature voltage is provided by a three-phase rectifier controlled by two PI regulators. The rectifier is fed by a 460 V AC 60 Hz voltage source.

The regulators control the firing angle of the rectifier thyristors. The first regulator is a speed regulator, followed by a current regulator. Since we are here in torque regulation mode, the speed regulator is disabled and only the current regulator is used. The current regulator controls the armature current by computing the appropriate thyristor firing angle. This generates the rectifier output voltage needed to obtain the desired armature current and thus the desired electromagnetic torque.

The current controller takes two inputs. The first one is the current reference (in p.u). This current reference is computed from the torque reference provided by the user. The second input is the armature current flowing through the machine.

A 15 mH smoothing inductance is placed in series with the armature circuit to reduce armature current oscillations.

Simulation

Start the simulation. You can observe the motor armature voltage and current, the rectifier firing angle, the electromagnetic torque and the motor speed on the scope. The current and torque references are also shown.

The motor is coupled to a linear load, which means that the mechanical torque of the load is proportional to the speed.

The initial torque reference is set to 0 N.m and the armature current is null. No electromagnetic torque is produced, and the motor stays still.

At t = 0.05 s, the torque reference jumps to 800 N.m. This causes the armature current to rise to about 305 A. Notice that the armature current follows the reference quite accurately, with fast response time and small overshooting. The 15 mH smoothing inductance keeps the current oscillations quite small. Observe also that the average firing angle value stays below 90 degrees, the converter being in rectifier mode.

The electromagnetic torque produced by the armature current flow causes the motor to accelerate. The speed rises and starts to stabilize around t = 5 s at about 1450 rpm, the sum of the load and viscous friction torques beginning to equalize the electromagnetic torque.

At t = 5 s, the torque reference is set to 400 N.m and the armature current jumps down to about 155 A. This causes the load torque to decelerate the motor.

At t = 10 s speed starts to stabilize around 850 rpm.

Notes

1) The power system has been discretized with a 20 us time step. The control system (regulators) uses a 100 us time step in order to simulate a microcontroller control device.

2) In order to reduce the number of points stored in the scope memory, a decimation factor of 20 is used.

3) A simplified version of the model using an average-value rectifier can be used by selecting 'Average' in the 'Model detail level' menu of the graphical user-interface. The time step can then be increased up to the control system sample time value.This can be done by typing 'Ts = 100e-6' in the workspace in the case of this example. See also dc3_example_simplified model.

DC4 - Four-Quadrant Three-Phase Rectifier 200 HP DC Drive

This example shows the DC4 four-quadrant three-phase rectifier DC drive with circulating current during torque regulation.

C.Semaille, Louis-A. Dessaint (Ecole de technologie superieure, Montreal)
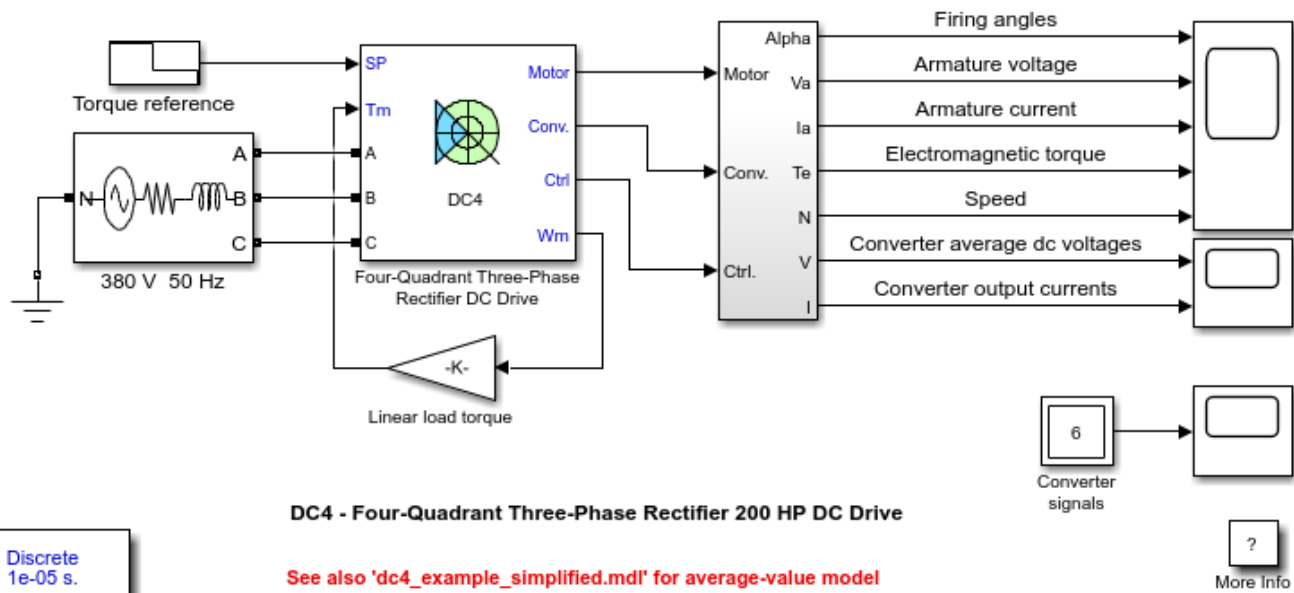
Description

This circuit uses the DC4 block of Specialized Power Systems. It models a four-quadrant three-phase rectifier (dual-converter topology) drive for a 200 HP DC motor.

The 200 HP DC motor is separately excited with a constant 310 V DC field voltage source. The armature voltage is provided by two three-phase anti-parallel connected converters controlled by two PI regulators. This allows bidirectional current flow through the DC motor armature circuit and thus four-quadrant operation. The converters are fed by a 380 V AC 50 Hz voltage source.

The regulators control the firing angles of both converter thyristors. The first regulator is a speed regulator, followed by a current regulator. Since we are here in torque regulation mode, the speed regulator is disabled and only the current regulator is used. The current regulator controls the armature current by computing the appropriate thyristor firing angles. This generates the rectifier output voltages needed to obtain the desired armature current and thus the desired electromagnetic torque.

Both converters operate simultaneously and the two firing angles are controlled so that their sum gives 180 degrees. This produces opposite average voltages at the converter dc output terminals and thus identical average voltages at the DC motor armature, the converters being connected in anti-parallel. One converter is working in rectifier mode while the other is in inverter mode.

The circulating current produced by the instantaneous voltage difference at the terminal of both converters is limited by 5 mH inductors connected between these terminals. No smoothing inductance is placed in series with the armature circuit, the armature current oscillations being quite small due to the three-phase voltage source.

Simulation

Start the simulation. You can observe the motor armature voltage and current, the converter firing angles, the electromagnetic torque and the motor speed on the scope. The current and torque references are also shown. A second scope allows you to visualize the converter average output voltages and output currents.

The motor is coupled to a linear load, which means that the mechanical torque of the load is proportional to the speed.

The initial torque reference is set to 0 N.m and the armature current is null. No electromagnetic torque is produced and the motor stays still.

At t = 0.2 s, the torque reference jumps to 600 N.m. This causes the armature current to rise to about 180 A. The armature current is supplied by converter 1, and the total current in this converter is the sum of load current and circulating current. Converter 2 simply carries the circulating current. Notice that the armature current follows the reference current quite accurately, with fast response time and small overshooting. Observe also that the firing angles are symmetrical around 90 degrees and that the converter average output DC voltages are equal but of opposite signs.

The electromagnetic torque produced by the armature current flow causes the motor to accelerate. The speed rises and starts to stabilize around t = 4 s at about 560 rpm, the sum of the load and viscous friction torques beginning to equalize the electromagnetic torque.

At t = 4 s, the torque reference is set to 0 N.m and the load torque causes the motor to decelerate. Notice that the four reactors keep the current oscillations quite small.

At t = 8 s, the torque reference is set to -300 N.m. The armature current jumps down to -90 A and is now delivered by converter 2 while converter 1 only handles the circulating current. Converter 2 is now working in rectifier mode and converter 1 in inverter mode.

The negative electromagnetic torque produced allows the motor to accelerate in the negative speed plane.

At t = 12 s, speed starts to stabilize around -290 rpm.

Notes

1) The power system has been discretized with a 10 us time step. The control system (regulators) uses a 100 us sample time in order to simulate a microcontroller control device.

2) In order to reduce the number of points stored in the scope memory, a decimation factor of 20 is used.

3) A simplified version of the model using average-value rectifiers can be used by selecting 'Average' in the 'Model detail level' menu of the graphical user-interface. The time step can then be increased up to the control system sample time value.This can be done by typing 'Ts = 100e-6' in the workspace in the case of this example. See also dc4_example_simplified example.

DC4 - Four-Quadrant Three-Phase Rectifier 200 HP DC Drive with No Circulating Current

This example shows a four-quadrant three-phase rectifier DC drive with no circulating current.

C.Semaille, Louis-A. Dessaint (Ecole de technologie superieure, Montreal)
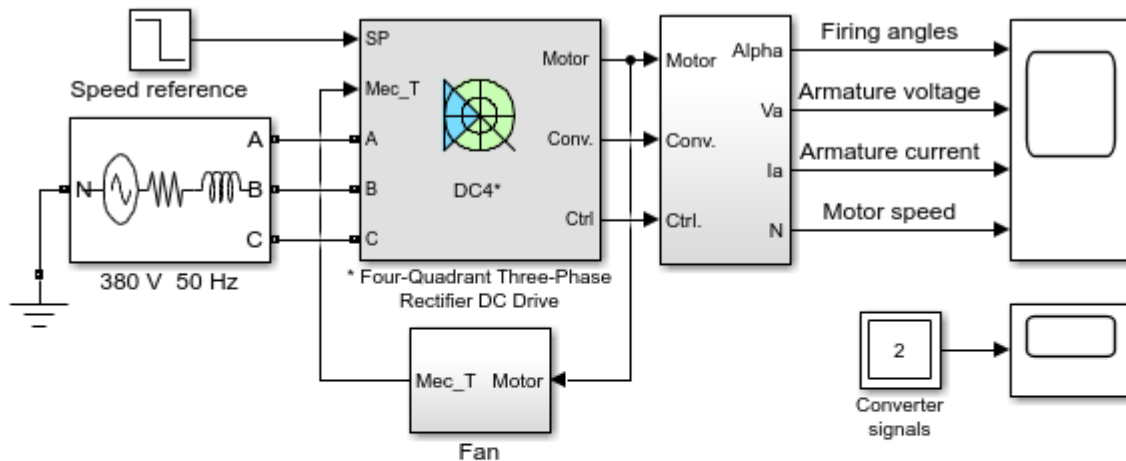
* The library link of this block is intentionally broken

DC4 - Four-Quadrant Three-Phase Rectifier 200 HP DC Drive with no Circulating Current

Description

This circuit is based on the DC4 block of Specialized Power Systems. It models a four-quadrant three-phase rectifier (dual-converter topology) drive with no circulating current for a 200 HP DC motor.

The 200 HP DC motor is separately excited with a constant 310 V DC field voltage source. The armature voltage is provided by two three-phase anti-parallel connected converters controlled by two PI regulators. This allows bidirectional current flow through the DC motor armature circuit and thus four-quadrant operation. The converters are fed by a 380 V AC 50 Hz voltage source.

The regulators control the firing angles of both converter thyristors. The first regulator is a speed regulator, followed by a current regulator. The speed regulator outputs the armature current reference (in p.u.) used by the current controller in order to obtain the electromagnetic torque needed to reach the desired speed. The speed reference change rate follows acceleration and deceleration ramps in order to avoid sudden reference changes that could cause armature over-current and destabilize the system. The current regulator controls the armature current by computing the appropriate thyristor firing angles. This generates the converter output voltages needed to obtain the desired armature current.

Compared with the original DC4 block, this circuit models a four-quadrant drive with no circulating current. During this regulation process, the flow of circulating current is completely inhibited by

automatic control of the firing pulses. By enabling only one of the two firing pulses needed for the two thyristor converters, only one converter operates at a time and carries the load current. The other converter is temporarily blocked. The firing pulse control is completely managed by the "bridge driver" module. By sensing the reference and load currents, this module determines when converter crossover has to take place by enabling the appropriate converter's firing pulses.

The two firing angles are controlled so that their sum gives 180 degrees. This allows smoother bridge transition. Since circulating current is inhibited, no more inductors are needed to limit the value of this current. However, a 10 mH smoothing inductance is placed in series with the armature circuit to reduce armature current oscillations.

Simulation

Start the simulation. You can observe the motor armature voltage and current, the converter firing angles and the motor speed on the scope. The current and speed references are also shown. A second scope has been added inside the main block to allow you to visualize the converter output currents. The "block_1" and "block_2" signals, controlled by the "bridge driver" module are also visible.

During this simulation the motor is coupled to a fan. The mechanical torque of this type of load is proportional to the square of the speed.

The initial speed reference is set to 1184 rpm, nominal speed. Observe that the motor speed follows the acceleration reference ramp accurately (+320 rpm/s) and reaches steady-state after about 4 s. The slow acceleration is due to the high inertia of the load.

The armature current follows the current reference very well and stabilizes around 330 A. During the acceleration phase, the armature current rises progressively (and thus also the electromagnetic torque produced) the mechanical torque opposed by the load rising with the speed. Only converter 1 is working (Block_1 is low) and converter 2 is inhibited (Block_2 is high). Consequently the output current of converter 1 is equal to the load current and converter 2 outputs no current. Notice that the 10 mH smoothing inductance keeps the armature current oscillations quite small.

At t = 4.5 s, the speed reference drops to -600 rpm and the armature current lowers with the speed to reduce the electromagnetic torque in order to decelerate following the negative speed ramp (-320 rpm/s).

Around t = 5.3 s, the armature current reaches 0 A and bridge crossover takes place to let the armature current become negative. Converter 1 is disabled and converter 2 enabled (Block_1 becomes high and Block_2 becomes low). To avoid simultaneous conduction of both converters (and thus to avoid circulating current) during the crossover, the incoming converter is enabled a few milliseconds after disabling Converter 1. Converter 2 then outputs the load current and the output current of converter 1 is nul. Notice that the current waveforms keep smooth during bridge transition. This negative current now generates a braking torque to keep the fan slowing down.

At t = 8.2 s, the speed reaches 0 rpm and the armature current now generates an accelerating torque to allow the fan to accelerate in the negative speed plane.

At t =11 s, speed and armature current stabilize around -600 rpm and 90 A respectively.

Notes

1) The power system has been discretized with a 5 us time step. The control system (regulators and bridge driver module) uses a 100 us time step in order to simulate a microcontroller control device.

2) In order to reduce the number of points stored in the scope memory, a decimation factor of 10 is used.

DC5 - One-Quadrant Chopper 5 HP DC Drive

This example shows the DC5 one-quadrant chopper DC drive during speed regulation.

C.Semaille, Louis-A. Dessaint (Ecole de technologie superieure, Montreal)
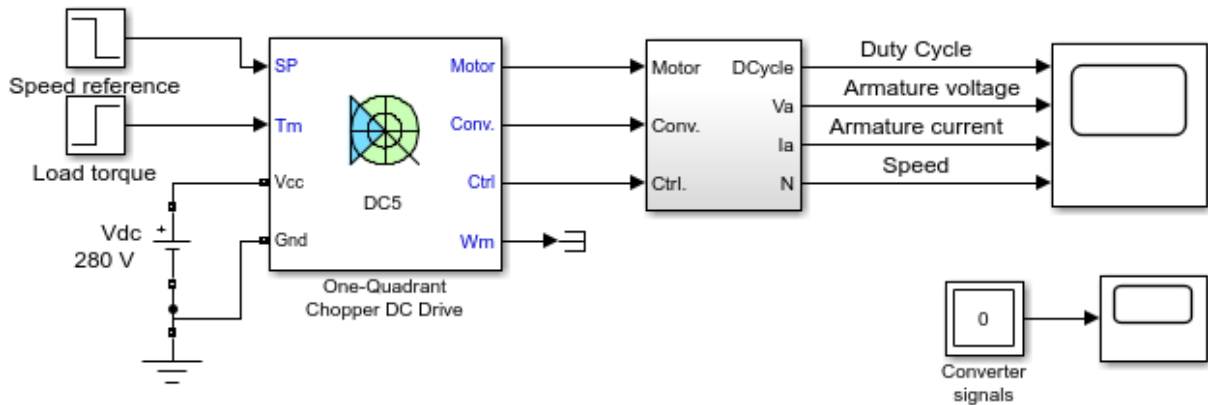
Description

This circuit uses the DC5 block of Specialized Power Systems. It models a one-quadrant chopper (buck converter) drive for a 5 HP DC motor.

The 5 HP DC motor is separately excited with a constant 150 V DC field voltage source. The armature voltage is provided by an IGBT buck converter controlled by two PI regulators. The buck converter is fed by a 280 V DC voltage source.

The first regulator is a speed regulator, followed by a current regulator. The speed regulator outputs the armature current reference (in p.u.) used by the current controller in order to obtain the electromagnetic torque needed to reach the desired speed. The speed reference change rate follows acceleration and deceleration ramps in order to avoid sudden reference changes that could cause armature over-current and destabilize the system. The current regulator controls the armature current by computing the appropriate duty ratio of the IGBT 5 kHz pulses (Pulse Width Modulation). This generates the average armature voltage needed to obtain the desired armature current. In order to limit the amplitude of the current oscillations, a smoothing inductance is placed in series with the armature circuit.

Simulation

Start the simulation. You can observe the motor armature voltage and current, the IGBT pulses and the motor speed on the scope. The current and speed references are also shown.

The speed reference is set at 500 rpm at t = 0 s. Initial load torque is 15 N.m.

Observe that the motor speed follows the reference ramp accurately (+250 rpm/s) and reaches steady state around t = 2.5 s. The armature current follows the current reference very well, with fast response time and small ripples. Notice that the current ripple frequency is 5 kHz.

At t = 2.5 s, the load torque passes from 15 N.m to 20 N.m. The motor speed recovers fast and is back at 500 rpm at t = 3 s. The current reference rises to about 16.7 A to generate a higher electromagnetic torque to maintain the speed reference. As observed before, the armature current follows its reference perfectly.

At t = 3 s, the speed reference jumps down to 350 rpm. The armature current lowers in order for the speed to decrease following the negative speed slope (-250 rpm/s) with the help of the load torque.

At t = 4 s, the speed stabilizes around its reference.

Notes

1) The power system has been discretized with a 1us time step. The speed and current controllers use a 100 us and 20 us sampling time respectively in order to simulate a microcontroller control device.

2) In order to reduce the number of points stored in the scope memory, a decimation factor of 25 is used. Some transitions may thus not appear on the scope. To view detailed simulation results, reduce the decimation factor to 1.

3) A simplified version of the model using an average-value converter can be used by selecting 'Average' in the 'Model detail level' menu of the graphical user-interface. The time step can then be increased up to the smallest control system sample time value. This can be done by typing 'Ts = 20e-6' in the workspace in the case of this example. See also dc5_example_simplified model.

DC5 - One-Quadrant Chopper 5 HP DC Drive with Hysteresis Current Control

This example shows a one-quadrant chopper DC drive with hysteresis current control during speed regulation.

C.Semaille, Louis-A. Dessaint (Ecole de technologie superieure, Montreal)

Description

This circuit is based on the DC5 block of Specialized Power Systems. It models a one-quadrant chopper (buck converter) drive for a 5 HP DC motor.

The 5 HP DC motor is separately excited with a constant 150 V DC field voltage source. The armature voltage is provided by an IGBT buck converter controlled by two regulators. The buck converter is fed by a 280 V DC voltage source.

The first regulator is a PI speed regulator, followed by a hysteresis current regulator. The speed regulator outputs the armature current reference (in p.u.) used by the current controller in order to obtain the electromagnetic torque needed to reach the desired speed. The speed reference change rate follows acceleration and deceleration ramps in order to avoid sudden reference changes that could cause armature over-current and destabilize the system. The current regulator controls the armature current by delivering the correct pulses to the IGBT device in order to keep the armature current inside a user-defined hysteresis band. The switching frequency of the IGBT device is limited by the motor inductance and an external inductance placed in series with the armature circuit.

Simulation

Start the simulation. You can observe the motor armature voltage and current, the IGBT pulses and the motor speed on the scope. The current and speed references are also shown.

The speed reference is set at 500 rpm at t = 0 s. Initial load torque is 15 N.m.

Observe that the motor speed follows the reference ramp accurately (+250 rpm/s) and reaches steady-state around t = 2.5 s. The armature current follows the current reference very well and stays limited inside it's hysteresis band.

At t = 2.5 s, the load torque passes from 15 N.m to 20 N.m. The motor speed recovers fast and is back at 500 rpm at t = 3 s. The current reference rises to about 16.7 A to generate a higher electromagnetic torque to maintain the speed reference. As observed before, the armature current follows its reference perfectly.

At t = 3 s, the speed reference jumps down to 350 rpm. The armature current lowers in order for the speed to decrease following the negative speed slope (-250 rpm/s) with the help of the load torque.

At t = 4 s, the speed stabilizes around its reference.

Notes

1) The power system has been discretized with a 4 us time step. The control system (regulators) uses a 100 us time step in order to simulate a microcontroller control device.

2) In order to reduce the number of points stored in the scope memory, a decimation factor of 5 is used.

DC6 - Two-Quadrant Chopper 200 HP DC Drive

This example shows the DC6 two-quadrant chopper DC drive during speed regulation.

C.Semaille, Louis-A. Dessaint (Ecole de technologie superieure, Montreal)
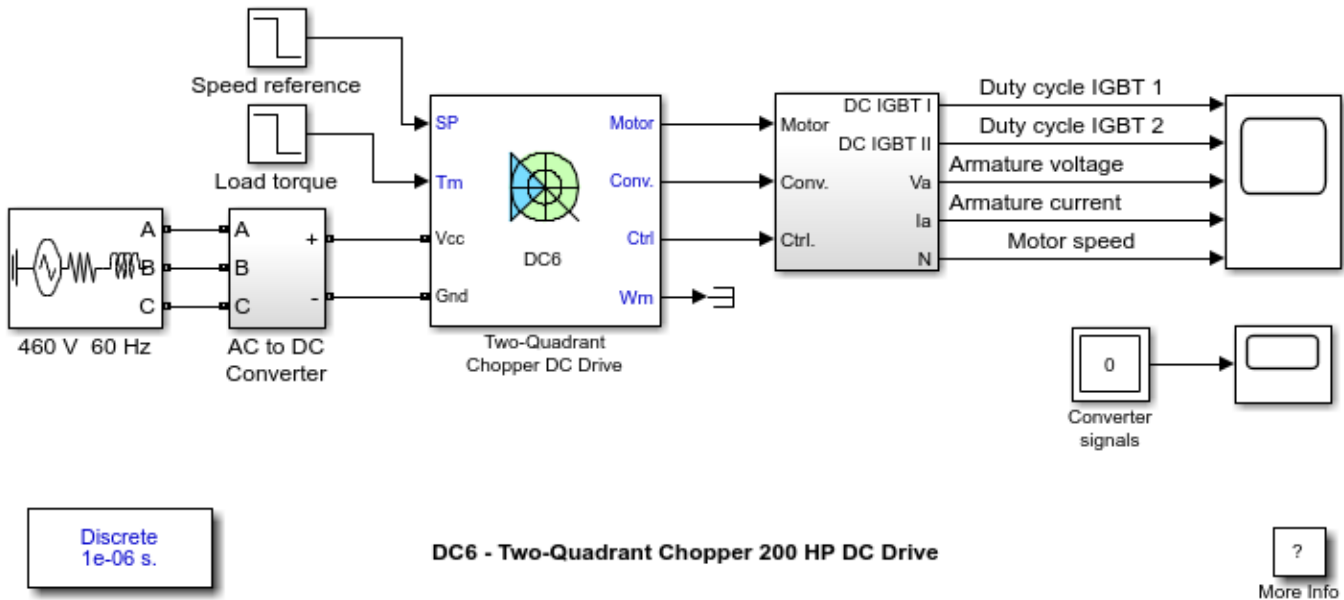
Description

The 200 HP DC motor is separately excited with a constant 150 V DC field voltage source. The armature voltage is provided by an IGBT buck-boost converter controlled by two PI regulators. The buck-boost converter is fed by a 630 V DC bus obtained by rectification of a 460 V AC 60 Hz voltage source. In order to limit the DC bus voltage during dynamic braking mode, a braking chopper has been added between the diode rectifier and the DC6 block.

The first regulator is a speed regulator, followed by a current regulator. The speed regulator outputs the armature current reference (in p.u.) used by the current controller in order to obtain the electromagnetic torque needed to reach the desired speed. The speed reference change rate follows acceleration and deceleration ramps in order to avoid sudden reference changes that could cause armature over-current and destabilize the system. The current regulator controls the armature current by computing the appropriate duty ratios of the 5 kHz pulses of the two IGBT devices (Pulse Width Modulation).For proper system behaviour, the two IGBT devices have opposite instantaneous pulse values.This generates the average armature voltage needed to obtain the desired armature current. In order to limit the amplitude of the current oscillations, a smoothing inductance is placed in series with the armature circuit.

Simulation

Before starting the simulation, set the initial bus voltage to 630 V via the GUI block ('Initial States Setting' button and 'Cbus' variable).

Start the simulation. You can observe the motor armature voltage and current, the two IGBT pulses and the motor speed on the scope. The current and speed references are also shown.

The speed reference is set at 400 rpm at t = 0 s. Initial load torque is 814 N.m.

Observe that the motor speed follows the reference ramp accurately (+250 rpm/s) and reaches steady state around t = 2 s. The armature current follows the current reference very well, with fast response time and small ripples. Notice that the current ripple frequency is 5 kHz.

At t = 2.1 s, the load torque passes from 814 N.m to 100 N.m. The motor speed recovers fast and is back at 400 rpm at t = 2.75 s. The current reference lowers to about 40 A to generate a smaller electromagnetic torque, the load torque being reduced. As observed before, the armature current follows its reference perfectly.

At t = 2.75 s, the speed reference jumps down to 100 rpm. In order for the motor to decelerate following the negative speed ramp, the armature current reverses down to -160 A to generate a braking electromagnetic torque (dynamic braking mode). This causes the DC bus voltage to increase. The braking chopper limits the voltage value.

At t = 3.4 s, the motor speed reaches 100 rpm and the current reverses back to 40 A.

At t = 4 s, the speed stabilizes around its reference.

Notes

1) The power system has been discretized with a 1 us time step. The speed and current controllers use a 100 us and 20 us sampling time respectively in order to simulate a microcontroller control device.

2) In order to reduce the number of points stored in the scope memory, a decimation factor of 25 is used. Some transitions may thus not appear on the scope. To view detailed simulation results, reduce the decimation factor to 1.

3) A simplified version of the model using an average-value converter can be used by selecting 'Average' in the 'Model detail level' menu of the graphical user-interface. The time step can then be increased up to the smallest control system sample time value. This can be done by typing 'Ts = 20e-6' in the workspace in the case of this example. See also dc6_example_simplified model.

DC7 - Four-Quadrant Chopper 200 HP DC Drive

This example shows the DC7 four-quadrant chopper DC drive during speed regulation.

C.Semaille, Louis-A. Dessaint (Ecole de technologie superieure, Montreal)
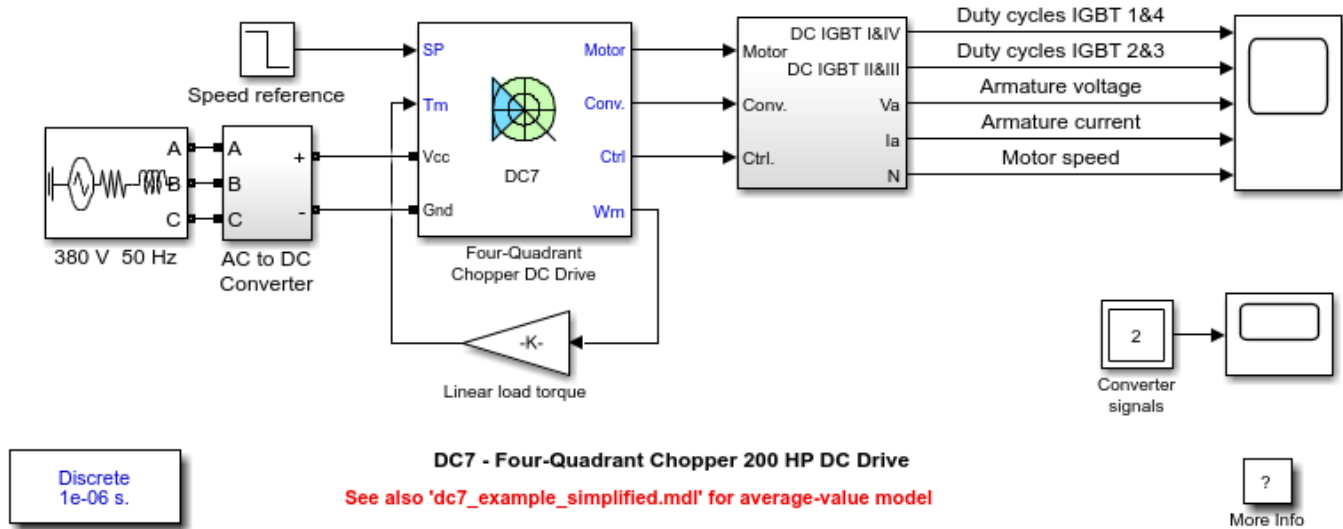
Description

The 200 HP DC motor is separately excited with a constant 150 V DC field voltage source. The armature voltage is provided by an IGBT converter controlled by two PI regulators. The converter is fed by a 515 V DC bus obtained by rectification of a 380 V AC 50 Hz voltage source. In order to limit the DC bus voltage during dynamic braking mode, a braking chopper has been added between the diode rectifier and the DC7 block.

The first regulator is a speed regulator, followed by a current regulator. The speed regulator outputs the armature current reference (in p.u.) used by the current controller in order to obtain the electromagnetic torque needed to reach the desired speed. The speed reference change rate follows acceleration and deceleration ramps in order to avoid sudden reference changes that could cause armature over-current and destabilize the system. The current regulator controls the armature current by computing the appropriate duty ratios of the 5 kHz pulses of the four IGBT devices (Pulse Width Modulation). For proper system behaviour, the instantaneous pulse values of IGBT devices 1 and 4 are opposite to those of IGBT devices 2 and 3. This generates the average armature voltage needed to obtain the desired armature current. In order to limit the amplitude of the current oscillations, a smoothing inductance is placed in series with the armature circuit.

Simulation

Before starting the simulation, set the initial bus voltage to 515 V via the GUI block ('Initial States Setting' button and 'Cbus' variable).

Start the simulation. You can observe the motor armature voltage and current, the four IGBT pulses and the motor speed on the scope. The current and speed references are also shown.

The motor is coupled to a linear load, which means that the mechanical torque of the load is proportional to the speed.

The speed reference is set at 500 rpm at t = 0 s. Observe that the motor speed follows the reference ramp accurately (+400 rpm/s) and reaches steady state around t = 1.3 s.

The armature current follows the current reference very well, with fast response time and small ripples. Notice that the current ripple frequency is 5 kHz.

At t = 2 s, speed reference drops to -1184 rpm. The current reference decreases to reduce the electromagnetic torque and causes the motor to decelerate with the help of the load torque.

At t = 2.2 s, the current reverses in order to produce a braking electromagnetic torque (dynamic braking mode). This causes the DC bus voltage to increase.

At t = 3.25 s, the motor reaches 0 rpm and the load torque reverses and becomes negative. The negative current now produces an accelerating electromagnetic torque to allow the motor to follow the negative speed ramp (-400 rpm/s). At t = 6.3 s, the speed reaches -1184 rpm and stabilizes around its reference.

Notes

1) The power system has been discretized with a 1 us time step. The speed and current controllers use a 100 us and 20 us sampling time respectively in order to simulate a microcontroller control device.

2) In order to reduce the number of points stored in the scope memory, a decimation factor of 25 is used. Some transitions may thus not appear on the scope. To view detailed simulation results, reduce the decimation factor to 1.

3) A simplified version of the model using an average-value converter can be used by selecting 'Average' in the 'Model detail level' menu of the graphical user-interface. The time step can then be increased up to the smallest control system sample time value. This can be done by typing 'Ts = 20e-6' in the workspace in the case of this example. See also dc7_example_simplified model.

Mechanical Shaft

This example shows the mechanical shaft model.

C.Semaille, Louis-A. Dessaint (Ecole de technologie superieure, Montreal)

Mechanical Shaft

Description

The model outputs the transmitted torque through the shaft regarding the speed difference between the driving side and the loaded side of the shaft.

The shaft has a stiffness of 17190 N.m and an internal damping factor of 600 N.m.s. This shaft is designed to have 0.1 degree of angular deflection for a 30 N.m load torque.

The shaft is driven by a variable speed source and is connected to a load. The load has an inertia of 0.35 kg.m2 and a viscous friction term of 0.006 N.m.s

Simulation

Start the simulation. You can observe the driving and the load speeds, the speed difference, the angular deflection and the transmitted torque values on the scope.

At t = 0 s, the driving speed starts climbing to 1750 rpm with a 500 rpm/s acceleration ramp. The angular deflection jumps to about 0.06 degree and the shaft transmits about 18.5 Nm to the load in order to accelerate it. At t = 0.2 s, the driving and load speeds tend to equalize. During the acceleration phase, the angular deflection increases slowly in order to transmit a higher torque to compensate the viscous friction increase.

At t = 3.5 s, the driving speed settles at 1750 rpm. This reduces the angular deflection and also the transmitted torque which settles around 1.1 Nm to compensate the viscous friction of the load.

At t = 5 s, the driving speed lowers towards 0 rpm with a -500 rpm/s deceleration ramp. The angular deflection becomes negative and thus the transmitted torque in order to decelerate the load. During

the deceleration phase, the angular deflection increases in order to transmit a higher deceleration torque to compensate the reduction of viscous friction.

At t = 8.5 s, the driving speed stabilizes at 0 rpm. This causes the angular deflection to reduce to 0 degree, the transmitted torque becomes nul and the load stops.

Notes

1) The shaft has been discretized with a 10 us time step.

2) In order to reduce the number of points stored in the scope memory, a decimation factor of 10 is used.

Speed Reducer

This example shows the speed reducer model connected between a high-speed shaft and a low-speed shaft.

C.Semaille, Louis-A. Dessaint (Ecole de technologie superieure, Montreal)

Description

The speed reducer is driven by a variable speed source and is connected to a load. The load has an inertia of 30 kg.m2 and a viscous friction term of 0.5 N.m.s.

The reduction device has a reduction ratio of 10 and it's inertia with respect to the high-speed side is 0.0005 kg.m2. The reduction ratio being quite low, the efficiency is high and worth 0.95.

The high-speed shaft has a stiffness of 17190 N.m and an internal damping factor of 600 N.m.s. This shaft is designed to have 0.1 degree of angular deflection for a 30 N.m load torque. The low-speed shaft, having a higher torque to transmit, has a stiffness of 171900 N.m and an internal damping factor of 6000 N.m.s. This shaft is designed to have 0.1 degree of angular deflection for a 300 N.m load torque.

Simulation

Start the simulation. You can observe the driving (high-speed) and load speeds (low-speed), the torque transmitted by the high-speed shaft and the torque transmitted by the low-speed shaft on the scope.

At t = 0 s, the driving speed starts climbing to 1750 rpm with a 500 rpm/s acceleration ramp. This causes the transmitted torque of the high-speed shaft to jump to about 18 N.m. Because of the reduction device, the torque transmitted to the load by the low-speed shaft is a lot bigger and is worth about 170 N.m.

During the accelerating phase, both torques keep increasing in order to compensate the viscous friction of the load. Notice that the load accelerates with a ramp of +50 rpm/s due to the reduction ratio of the speed reducer.

At t = 3.5 s, the driving speed settles at 1750 rpm. Since no more accelerating torque is needed, the input and output torques decrease and stabilize respectively to 0.965 N.m and 9.16 N.m at t = 4 s. The load speed is now equal to 175 rpm.

Notes

1) The speed reducer has been discretized with a 1 us time step.

Mechanical Coupling of Two Motor Drives I

This example shows mechanical coupling of two motor drives.
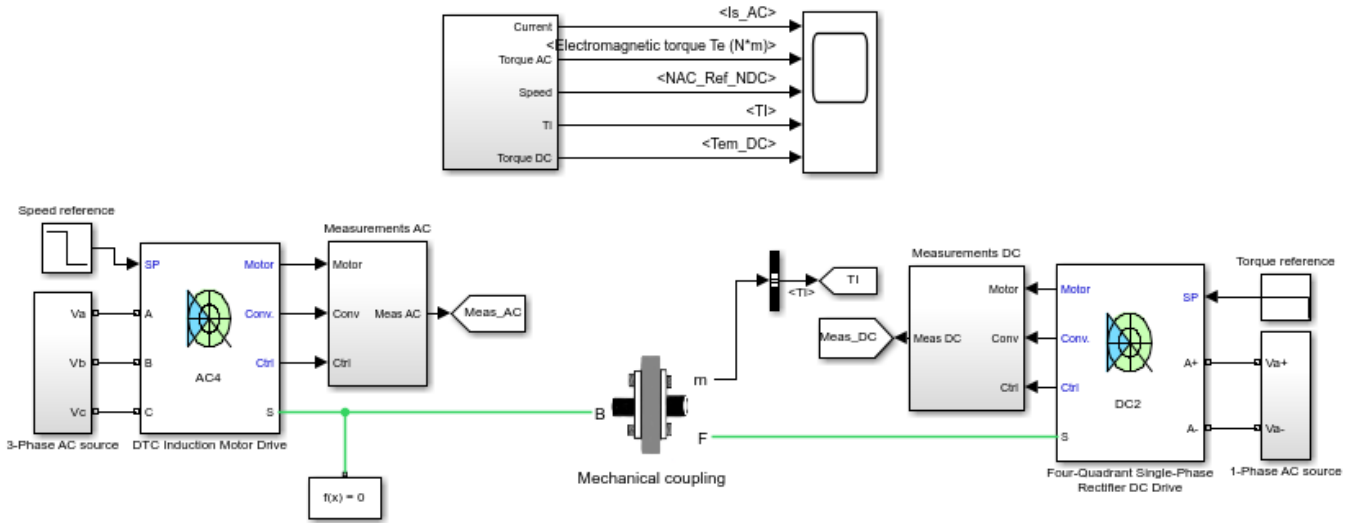
Louis-A. Dessaint , H. Fortin-Blanchette, C. Semaille (Ecole de technologie superieure, Montreal)

Mechanical Coupling of Two Motor Drives II

This example shows mechanical coupling of two motor drives.
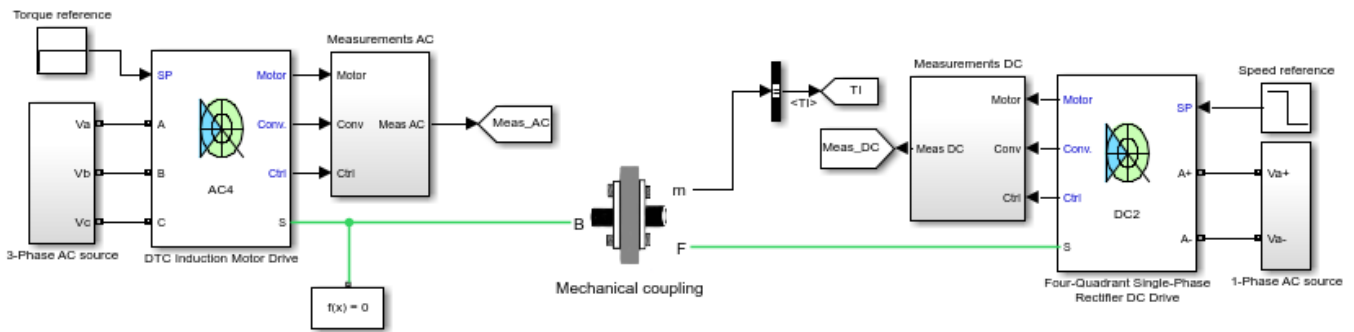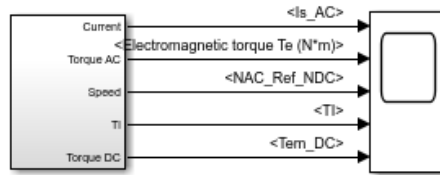
Louis-A. Dessaint , H. Fortin-Blanchette, C. Semaille (Ecole de technologie superieure, Montreal)

Winding Machine

This example shows a model of a winding machine.

Winding Machine

Louis-A. Dessaint , H. Fortin-Blanchette, C. Semaille (Ecole de technologie superieure, Montreal)
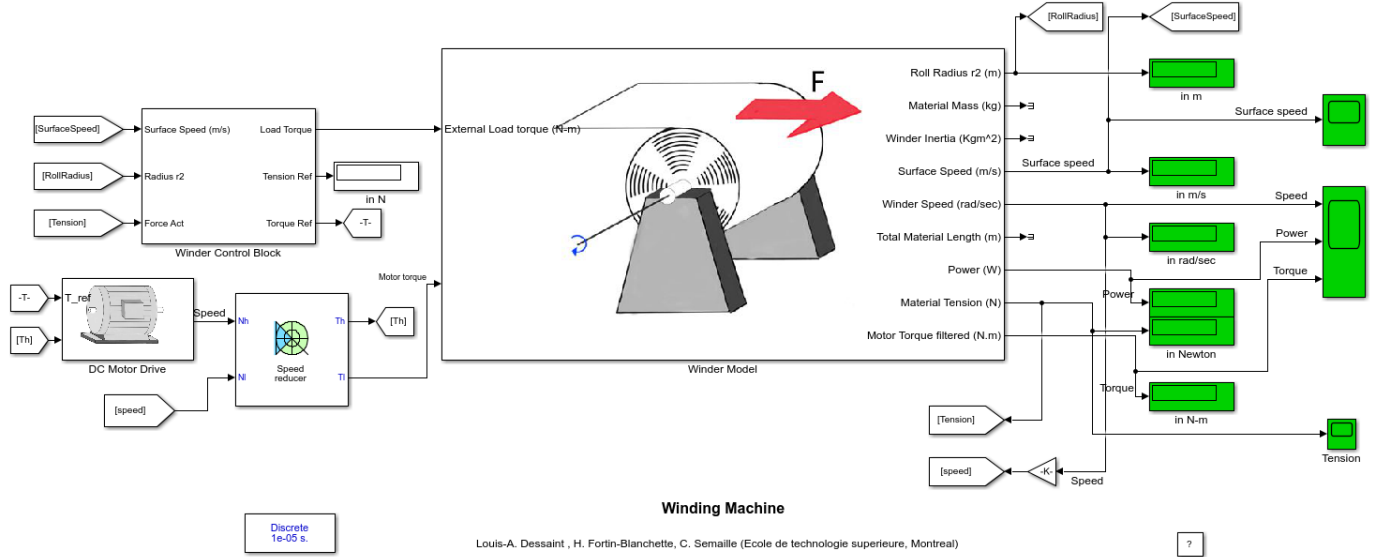
Robot Axis Control Using Brushless DC Motor Drives

This example shows robot axis control with brushless DC motor drive.

H. Le-Huy Laval University
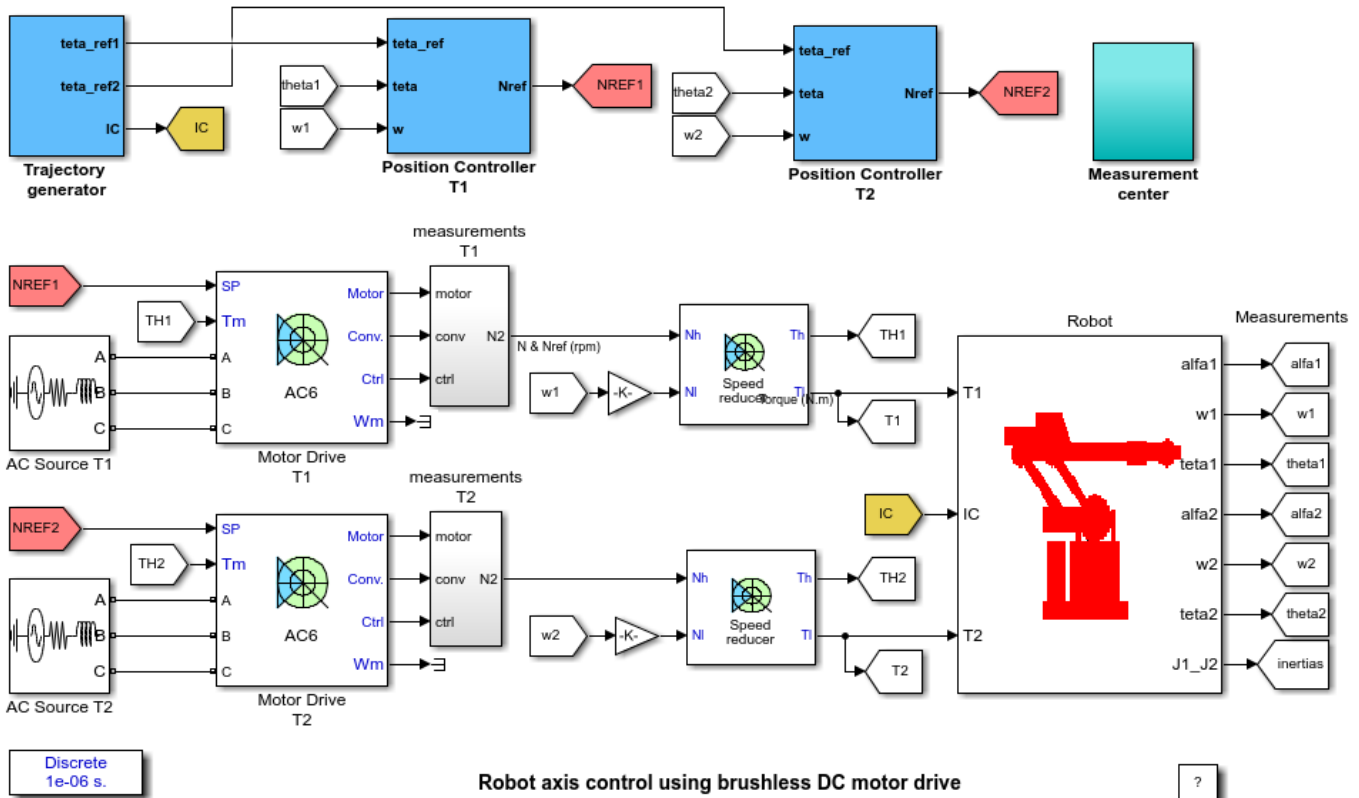
Robot axis control using brushless DC motor drive

The 'Ts' parameter used in this model
is set to 1e-6 by the Model Properties Callbacks

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Aircraft Electrical Power Generation and Distribution

This example shows an aircraft electrical power generation and distribution system. The AC power frequency is variable and depends of the engine speed

Olivier Tremblay, Louis-A. Dessaint (Ecole de technologie superieure, Montreal)

Aircraft Electrical Power Generation & Distribution System
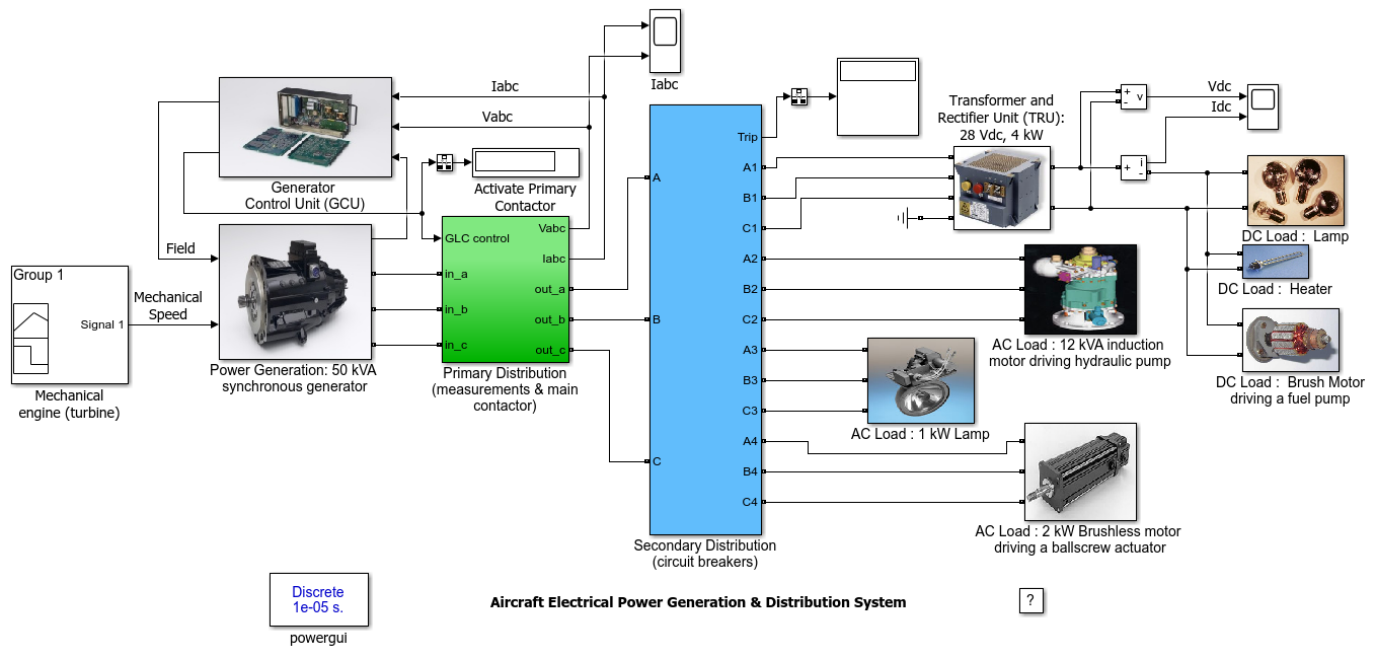
Description

The system is composed of six main sections.

The first section represents the generator mechanical drive and is modeled by a simple signal builder, which provides the mechanical speed of the engine shaft.

The second section represents the power AC generator. It is composed of a modified version of the simplified synchronous machine. The mechanical input of the modified machine of 50 kW is the engine speed. The Generator Control Unit regulates the voltage of the generator to 200 volts line to line.

The third section represents the Primary Distribution system. It is composed of three current and voltage sensors. There is also a 3-phase contactor controlled by the Generator Control Unit. Finally, a parasitic resistive load is required to avoid numerical oscillations.

The fourth section represents the secondary Power Distribution system. It is represented by 4 circuit breakers with adjustable current trip.

The fifth section represents the AC loads. There is a 4 kW Transformer And Rectifier Unit (which supplies 28 Vdc), a 12 kW induction machine (motor driving a pump), a 1 kW resistive load (lamps) and a 3 hp simplified (using an average value inverter) brushless DC drive (motor driving a ballscrew actuator).

Finally, the last section represents the DC loads. There are two resistive loads (heater and lamp) and a 300 W DC brush motor (motor driving a fuel pump).

Simulation

Start the simulation. You can observe the AC and the DC measurements at the top level of the schematic. There are also scopes inside the AC Power Generation, the Induction motor, the Brushless DC motor and the DC brush motor.

At time t = 0 s, the engine accelerates from 0 rpm to 12000 rpm in 0.4 second.

At time t = 0.3 s, the speed reaches the threshold of 9000 rpm. The Generator Control Unit activates the primary contactor, which enables the AC power on the aircraft. All the resistive loads are now online. The DC bus voltage increases to 28 Vdc. The induction machine and the DC brush motor accelerate to nominal speed (the two mechanical loads are proportional to the speed of the motors).

At time t = 1.4 s, the Brushless DC motor starts accelerating to the set point of 500 rpm. Observe that the speed follows precisely the acceleration ramp.

At time t = 1.9 s, the speed set point of the Brushless drive is changed to -500 rpm. You can see that the main current decreases, because the motor acts as a generator. The current flows from the motor to the breaking chopper inside the drive. Open the drive scope to observe the increasing of the DC bus voltage to the breaking chopper activation voltage (290 Vdc).

At time t = 2 s, the engine speed decelerates from 12 000 rpm to 10 000 rpm in 1 second. Observe the speed of the induction machine, which decelerates according to the reduction of the generator frequency.

At time t = 3 s, the engine speed accelerates from 10 000 rpm to 18 000 rpm in 1.5 second.

At time t = 3.5 s, there is a manual breaker trip on the Transformer And Rectifier Unit. This cause a reduction of the main current. Observe the voltage and the current on the DC monitoring scope.

At time t = 4.5 s, the engine speed decelerates from 18 000 rpm to 0 rpm in 1.5 second.

At time t = 5.26 s, the speed reaches the threshold of 8900 rpm. So the GCU de-activates the primary contactor.

Finally, note how well the AC voltage is regulated during the whole simulation period.

Thyristor-Based HVDC Transmission System (Detailed Model)

This example shows the steady-state and transient performance of a 12-pulse, 1000 MW (500 kV-2kA) 50/60 Hz HVDC transmission system.

Silvano Casoria (Hydro-Quebec)
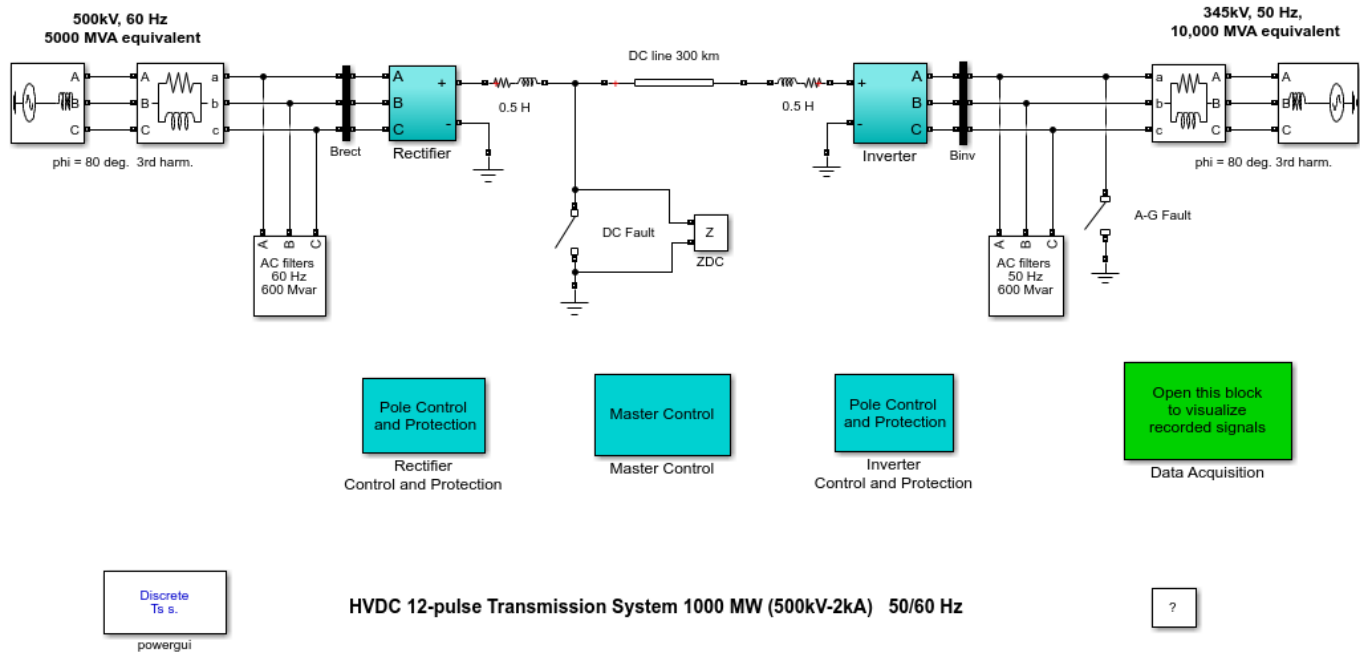
Description

A 1000 MW (500 kV, 2kA) DC interconnection is used to transmit power from a 500 kV, 5000 MVA, 60 Hz network to a 345 kV, 10 000 MVA, 50 Hz network.

The rectifier and the inverter are 12-pulse converters using two 6-pulse thyristor bridges connected in series. The rectifier and the inverter are interconnected through a 300 km distributed parameter line and two 0.5 H smoothing reactors. The transformer tap changers are not simulated and fixed taps are assumed. Open the two transformer blocks in the Rectifier and Inverter subsystems to see the factors applied on the primary voltage: 0.90 on rectifier side and 0.96 on inverter side. Reactive power required by the converters is provided by a set of capacitor banks plus 11th, 13th and high pass filters for a total of 600 Mvar on each side. Two circuit breakers are used to apply faults on the inverter AC side and rectifier DC side.

DC Protection functions are implemented in each converter. At the rectifier the DC fault protection will detect and force the delay angle into the inverter region so to extinguish the fault current. At the inverter the commutation failure prevention control will detect AC faults and reduce the maximum delay angle limit in order to decrease the risk of commutation failure. The Low AC voltage detection blocks will lock the DC fault protection when a drop in the AC voltage is detected. The Master Control block initiates the starting and stopping of the converters as well as the ramping up and down of the current references. The power system and the control system are both discretized for a sample time Ts=50 us. Notice that the "Model initialization" function of the model automatically sets Ts = 50e-6 in your MATLAB® workspace. A description of the control systems is provided in the HVDC Transmission System Case Study of the User's Manual.

Simulation

The system is programmed to start and reach a steady state. Then steps are applied on the reference current of the rectifier and on the inverter reference voltage in order to observe the dynamic response of the regulators. Finally, a stop sequence is initiated to bring the DC power down before blocking the converters. Start the simulation. Open the RECTIFIER and INVERTER scopes (in the Data Acquisition subsystem) and observe the DC line voltage on trace 1 (1pu = 500 kV) and the DC line current (reference and measured values) on trace 2 (1pu = 2kA).

Start-up and Stop

In the Master Control, the converters are deblocked and started by ramping the rectifier and inverter reference current. At t = 0.02 s (i.e. when the converters at deblocked), the reference current is ramped to reach the minimum value of 0.1 pu in 0.3 s (0.33 pu/s). At the end of this first ramp (t = 0.32 s) the DC line is charged at its nominal voltage and DC voltage reaches steady-state. At t= 0.4 s, the reference current is ramped from 0.1 pu to 1 pu (2kA) in 0.18 s (5 pu/s). At the end of this starting sequence (t=0.58 s) , the DC current reaches steady state. The RECTIFIER then controls the current and the INVERTER controls the voltage. In steady-state, the alpha firing angles (trace 3) are 16.5 degrees and 143 degrees respectively on the RECTIFIER and INVERTER sides. The extinction angle gamma (minimum value) is measured at the INVERTER and shown in trace 4. In steady-state, the minimum value is between 22 and 24 degrees. The control mode of operation (an integer between 0 to 6) is shown in trace 4 (0= blocked; 1=Current control; 2=Voltage control; 3=Alpha minimum limitation; 4=Alpha maximum limitation; 5=Forced or constant alpha; 6=Gamma control). At t = 1.4 s the Stop sequence is initiated by ramping down the current to 0.1 pu. At t = 1.6 s a Forced-alpha at the Rectifier extinguishes the current and at the Inverter the Forced-alpha brings down the DC voltage. At t = 1.7 s the pulses are blocked in both converters.

Step response of current and voltage regulators

Verify in the Master Control that the "Enable Ref. Current Step" switch is in the upper position. This switch is used to apply a step on the reference voltage. Also verify that the reference voltage step is enabled in the Inverter Control. At t=0.7 s, a -0.2 pu step is first applied on the reference current (decrease from 1 pu to 0.8 pu) and at t=0.8 s, the reference current is reset to its 1 pu original value.The current stabilizes in approximately 0.1 seconds. Steps are also applied on the reference voltage of the inverter (-0.1 pu / +0.1 pu at t=1.0 s / 1.1s).

DC line fault at the rectifier

Deactivate the steps applied on the current reference and on the voltage reference in the Master Control and in the inverter control respectively by setting the switches in lower position. In the DC Fault block, change to 1 the 100 multiplication factor in the Switching times so that a fault is now applied at t = 0.7 s. Reduce the Simulation stop time from 2 to 1.4 s. The DC Fault protection (DCPROT) in the rectifier is activated by default. Open the FAULT scope to observe the DC fault current. Restart the simulation.

At fault application the DC current quickly increases to 2.3 pu and the DC voltage falls to zero at the rectifier. This DC voltages drop is seen by the Voltage Dependent Current Order Limiter (VDCOL) which reduces the reference current to 0.3 pu at the rectifier. A DC current still continues to circulate in the fault. Then, at t = 0.77 s, the rectifier alpha firing angle is forced to 166 degrees by the DC protection because a DC voltage drop is detected (VdL< 0.5 pu for more than 70 ms). The rectifier now operates in inverter mode. The DC line voltage becomes negative and the energy stored in the line is returned to the AC network, causing rapid extinction of the fault current at its next zero-crossing. Then, alpha is released at t = 0.87 s and the normal DC voltage and current recover in approximately 0.4 s.

AC line-to-ground fault at the inverter

In the DC Fault block, change the multiplication factor of 1 in the Switching times to 100, so that the DC fault is now eliminated. In the A-G Fault block, change to 1 the 100 multiplication factor in the Switching times so that a 6 cycles line-to-ground fault is now applied at t = 0.7 s. The Low AC voltage detection (LACVD) subsystem in the rectifier and inverter protections and the Commutation Failure Prevention Control (CFPREV) in the inverter protection are activated by default. Restart the simulation.

Notice the 120 Hz oscillations in the DC voltage and currents during the fault. When the fault is cleared at t = 0.8 s, the VDCOL operates and reduces the reference current to 0.3 pu. The system recovers in approximately 0.35 s after fault clearing. The LACVD detects the fault and locks the DC Fault protection that should not detect a DC fault even if the DC line voltage dips. Look at the CFPREV output (A_min_I) which decreases the maximum delay angle limit in order to increase the commutation margin during and after the fault. Now deactivate the CFPREV protection by deselecting the "ON State" in the CFPREV dialog box. Restart the simulation and observe the difference in recovery time of the DC transmission. Note that a commutation failure now occurs during the recovery. A commutation failure is the result of a failure of the incoming valve to take over the direct current before the commutation voltage reverses its polarity. The symptoms are a zero DC voltage across the affected bridge causing an increase of the DC current at a rate determined mainly by the DC circuit inductance.

Thyristor-Based HVDC Transmission System (Average Model)

This example shows the steady-state and transient performance of a 12-pulse, 1000 MW (500 kV-2kA) 50/60 Hz HVDC transmission system.

Silvano Casoria (Hydro-Quebec)
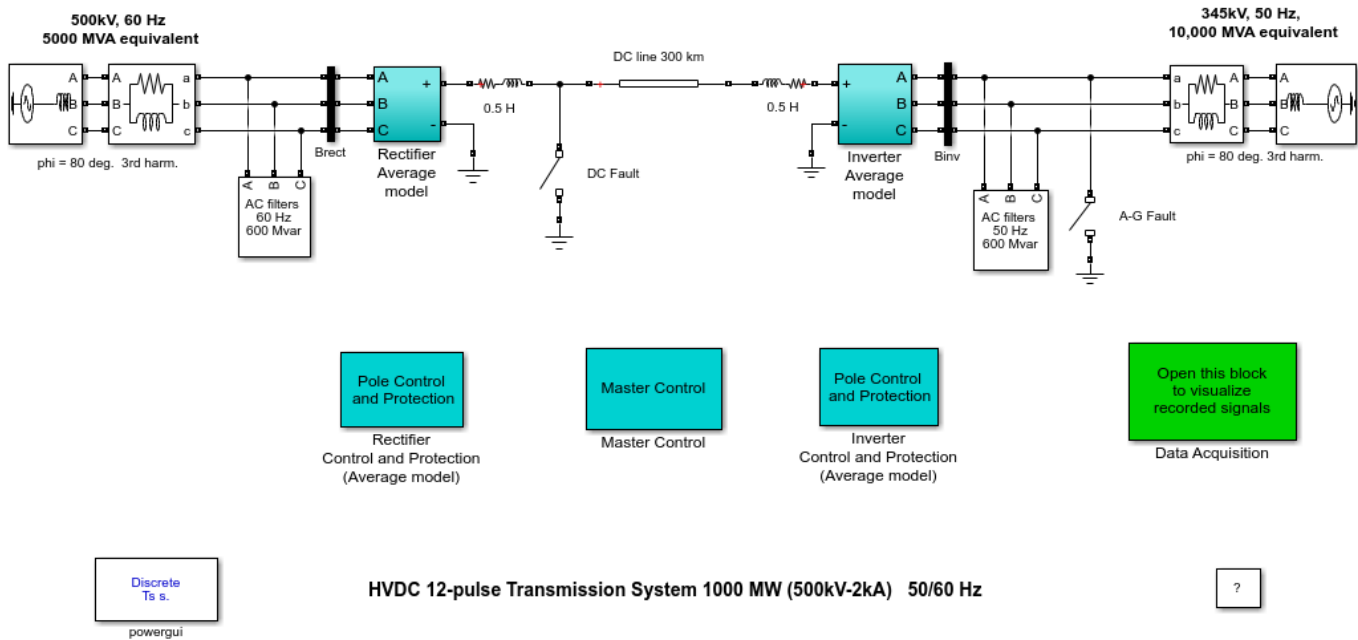
Detailed Versus Average Model

When modeling line-commutated converter based HVDC systems you can use two types of model, depending on the range of frequencies to be represented:

The detailed model such as the one presented in the power_hvdc12pulse model includes detailed representation of the converter unit with its power electronic thyristor bridge and converter transformer. This model is well suited for observing harmonics and control system dynamic and transient performance.

The average model such as the one presented here where the converter unit (bridge and transformer) is represented by an equivalent voltage source generating the bridge average DC voltage and AC sources generating the fundamental component of the currents flowing into the network. This model does not represent harmonics, but the dynamics resulting from control system and power system interaction is preserved. This model allows using a time step that is higher than the one of the control system or the network. Optimal performance however is obtained by using the same time step of the control system regulators.

Description

A 1000 MW (500 kV, 2kA) DC interconnection is used to transmit power from a 500 kV, 5000 MVA, 60 Hz network to a 345 kV, 10 000 MVA, 50 Hz network. The rectifier and the inverter are average models of 12-pulse converters representing two 6-pulse thyristor bridges connected in series. The rectifier and the inverter are interconnected through a 300 km distributed parameter line and two 0.5 H smoothing reactors.

The converter transformer linear representation is part of the average model block. The transformer tap changers are not simulated and fixed taps are assumed as model inputs. At the rectifier the tap ratio (Nprimary/Nsecondary) is 0.9 (pu) and at the inverter it is 0.96 (pu). Reactive power required by the converters is provided by a set of capacitor banks plus 11th, 13th and high pass filters for a total of 600 Mvar on each side. Note that since no harmonics are generated by the average converter model the Mvar could be provided entirely by capacitor banks.

Two circuit breakers are used to apply faults on the inverter AC side and rectifier DC side. Note that since thyristor valves are not present the 12-pulse Firing Control block is no longer necessary in the average model.

DC Protection functions are implemented in each converter. At the rectifier, the DC fault protection will detect and force the delay angle into the inverter region so to extinguish the fault current. At the inverter, the commutation failure prevention control will detect AC faults and reduce the maximum delay angle limit in order to decrease the risk of commutation failure. Note that the commutation failure phenomenon is not possible in the average model.

To assist the user in identifying conditions that may produce this phenomenon an indication from the model is provided (CF_alarm signal available from the Bus Selector block of the rectifier and inverter models). The Low AC Voltage Detection blocks will lock the DC fault protection when a drop in the AC voltage is detected. The Master Control block initiates the starting and stopping of the converters as well as the ramping up and down of the current references.

A description of the control systems is provided in the HVDC Transmission System Case Study of the User's Manual. The firing angle order (alpha_ord) output of the controller is an input of the average model.

The power system and the control system are both discretized for a sample time Ts=50 us. The "Model initialization" section of the model automatically sets Ts = 50e-6 in your MATLAB® workspace. It also sets the average model time step Ts_avg equal to Ts.

Simulation

The system is programmed to start and reach a steady state. Then steps are applied on the reference current of the rectifier and on the inverter reference voltage in order to observe the dynamic response of the regulators. Finally, a stop sequence is initiated to bring the DC power down before blocking the converters.

Start the simulation, open the RECTIFIER and INVERTER scopes (in the Data Acquisition subsystem) and observe the DC line voltage on trace 1 (1pu = 500 kV) and the DC line current (reference and measured values) on trace 2 (1pu = 2 kA).

Start-up and Stop

In the Master Control, the converters are unblocked and started by ramping the rectifier and inverter reference current.

At t = 0.02 s (i.e. when the converters at unblocked), the reference current is ramped to reach the minimum value of 0.1 pu in 0.3 s (0.33 pu/s). At the end of this first ramp (t = 0.32 s) the DC line is charged at its nominal voltage and DC voltage reaches steady-state.

At t= 0.4 s, the reference current is ramped from 0.1 pu to 1 pu (2kA) in 0.18 s (5 pu/s). At the end of this starting sequence (t=0.58 s), the DC current reaches steady state. The RECTIFIER then controls the current and the INVERTER controls the voltage.

In steady-state, the alpha firing angles (trace 3) are 17.7 degrees and 144.5 degrees respectively on the RECTIFIER and INVERTER sides. Note that in the detailed model these traces (16.5 degrees for the rectifier and 143 degrees for the inverter) are not the measured firing delay angles but the corresponding orders from the control regulators. In the detailed model, the firing angles are smaller because the regulators must advance the firing orders by two time steps in order to compensate for the delays introduced by interfacing of input AC voltages and output firing pulses of the 12-pulse Firing Control block. The extinction angle gamma value is an output of the average model. It is used at the INVERTER and shown in trace 5. In steady-state, its value is 23 degrees.

The control mode of operation (an integer between 0 to 6) is shown in trace 4 (0= blocked; 1=Current control; 2=Voltage control; 3=Alpha minimum limitation; 4=Alpha maximum limitation; 5=Forced or constant alpha; 6=Gamma control).

At t = 1.4 s the Stop sequence is initiated by ramping down the current to 0.1 pu.

At t = 1.6 s a Forced-alpha at the Rectifier extinguishes the current and at the Inverter the Forced-alpha brings down the DC voltage.

At t = 1.7 s the pulses are blocked in both converters.

Step response of current and voltage regulators

Verify in the Master Control that the "Enable Ref. Current Step" switch is in the upper position. This switch is used to apply a step on the reference voltage. Also verify that the Ref. Voltage Step is enabled in the Inverter Control. Start the simulation.

At t=0.7 s, a -0.2 pu step is first applied on the reference current (decrease from 1 pu to 0.8 pu) and at t=0.8 s, the reference current is reset to its 1 pu original value. The current stabilizes in approximately 0.1 seconds. Steps are also applied on the reference voltage of the inverter (-0.1 pu / +0.1 pu at t=1.0 s / 1.1s).

DC line fault at the rectifier

Deactivate the steps applied on the current reference and on the voltage reference in the Master Control and in the inverter control respectively by setting the switches in lower position.

The DC Fault protection (DCPROT) in the rectifier is activated by default. In the DC Fault block, change to 1 the 100 multiplication factor in the Switching times so that a fault is now applied at t = 0.7 s.

Reduce the Simulation stop time from 2 to 1.4 s. Open the FAULT scope to observe the DC fault current. Restart the simulation.

At fault application the DC current quickly increases to 2.63 pu and the DC voltage falls to zero at the rectifier. This DC voltages drop is seen by the Voltage Dependent Current Order Limiter (VDCOL) which reduces the reference current to 0.3 pu at the rectifier. A DC current still continues to circulate in the fault.

At t = 0.77 s, the rectifier alpha firing angle is forced to 166 degrees by the DC protection because a DC voltage drop is detected (VdL< 0.5 pu for more than 70 ms). The rectifier now operates in inverter mode. The DC line voltage becomes negative and the energy stored in the line is returned to the AC network, causing rapid extinction of the fault current at its next zero-crossing.

Alpha is released at t = 0.82 s and the normal DC voltage and current recover in approximately 0.4 s.

AC line-to-ground fault at the inverter

In the DC Fault block, change the multiplication factor of 1 in the Switching times to 100, so that the DC fault is now disabled. In the A-G Fault block, change to 1 the 100 multiplication factor in the Switching times so that a 6 cycles line-to-ground fault is now applied at t = 0.7 s.

The Low AC voltage detection (LACVD) subsystem in the rectifier and inverter protections and the Commutation Failure Prevention Control (CFPREV) in the inverter protection are activated by default.

Restart the simulation.

Note that the 120 Hz oscillations in the DC voltage and currents which are normally observed during single-phase fault with a detailed model do not exist in the average model. Indeed, only positive-sequence fundamental components of AC quantities are significant in the average model. The VDCOL would not operate during the fault. The system recovers in approximately 0.2 s after fault clearing (see the measured DC power Pd).

Abnormal inverter operation resulting from a commutation failure malfunction (CF) due to AC faults are not correctly represented by the average model equations. To assist the user in identifying such a condition an alarm signal (CF_alarm) is set whenever the onset of a CF is predicted.

Look at the CF_alarm signal at the inverter, triggered at t = 0.73 s. Open the CF_alarm block (inside the HVDC_CONV_AVG block of the inverter model) to examine the logic.

Look at the A_min_I signal of the PROTECTION INVERTER scope. This signal monitors the Commutation Failure Prevention (CFPREV) output of the Inverter Protection block. The A_min signal is used to decrease the delay angle limit in order to increase the commutation margin during and after the fault.

Finally, deactivate the CFPREV protection by deselecting the "ON State" in the CFPREV dialog box. Restart the simulation and observe the difference in recovery time of the DC transmission.

VSC-Based HVDC Transmission System (Detailed Model)

This example shows a VSC-based HVDC transmission link 200 MVA (+/- 100kV).

Silvano Casoria (Hydro-Quebec)
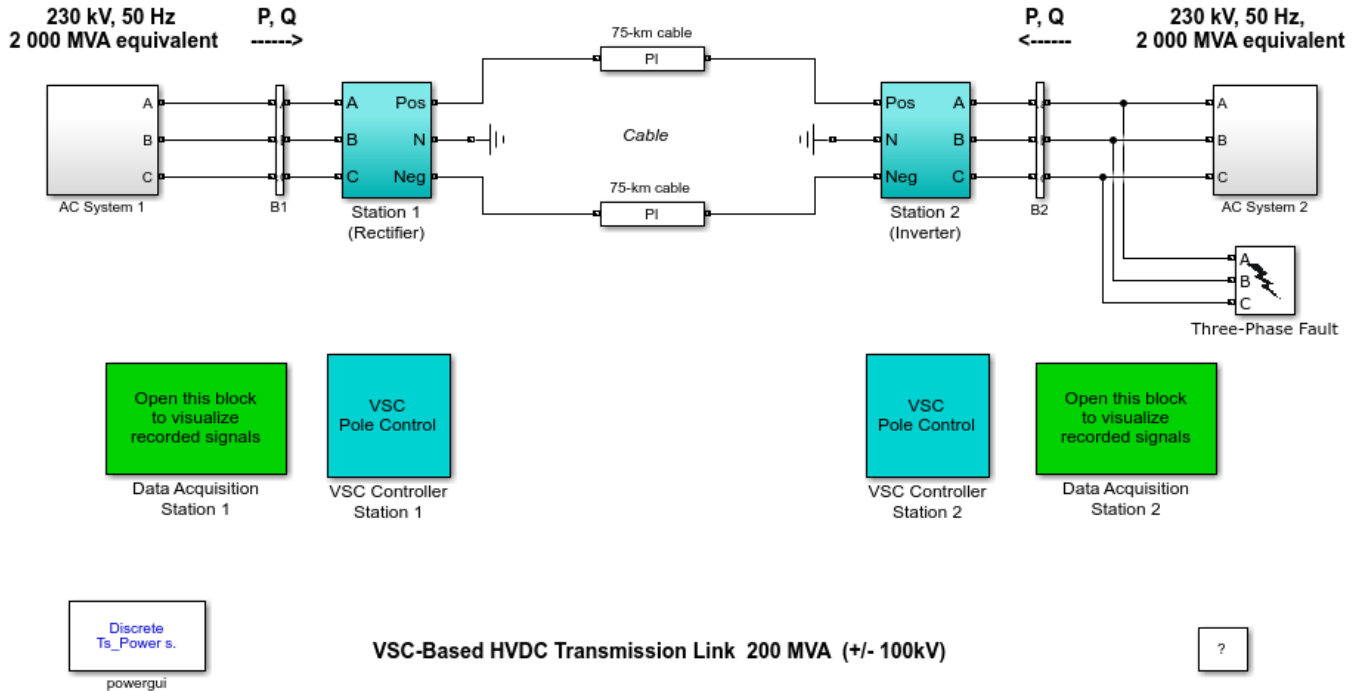
Description

A 200 MVA (+/- 100 kV DC) forced-commutated Voltage-Sourced Converter (VSC) interconnection is used to transmit power from a 230 kV, 2000 MVA, 50 Hz system to another identical AC system. The rectifier and the inverter are three-level Neutral Point Clamped (NPC) VSC converters using close IGBT/Diodes. The Sinusoidal Pulse Width Modulation (SPWM) switching uses a single-phase triangular carrier wave with a frequency of 27 times fundamental frequency (1350 Hz). Along with the converters, the station includes on the AC side: the step down Yg-D transformer, the AC filters, the converter reactor; and on the DC side: the capacitors, the DC filters. The transformer tap changers and saturation characteristics are not simulated. The 40 Mvar shunt AC filters are 27th and 54th high-pass tuned around the two dominating harmonics. The 0.15 p.u. converter reactor with the 0.15 p.u. transformer leakage reactance permits the VSC output voltage to shift in phase and amplitude with respect to the AC system Point of Common Coupling (PCC) (bus B1 for station 1 and B2 for station 2) and allows control of converter active and reactive power output. The reservoir DC capacitors are connected to the VSC terminals. They have an influence on the system dynamics and the voltage ripple on the DC side. The high-frequency blocking filters are tuned to the 3rd harmonic, i.e. the main harmonic present in the positive and negative pole voltages. The rectifier and the inverter are interconnected through a 75 km cable (i.e. 2 pi sections) and two 8 mH smoothing reactors. A circuit breaker is used to apply a three-phase to ground fault on the inverter AC side. A Three-Phase Programmable Voltage Source block is used in station 1 system to apply voltage sags.

The discrete control system generates the three sinusoidal modulating signals that are the reference value of the bridge phase voltages. The amplitude and phase of the modulating signals can be

calculated to control either: the reactive and real AC power flow at the PCC, or the reactive power flow at the PCC and the pole to pole DC voltage. It would also be possible to control the AC voltage amplitude at the PCC, but this option is not included in our model. A description of the control system is provided in the "VSC-Based HVDC Link" case study of the User's Manual. The power system and the control system are both discretized for a sample time Ts_Power=7.406e-6 s and Ts_control=74.06e-6 s respectively. They are multiples of the carrier period. Notice that the "Model initialization" function of the model automatically sets these two sample times in your MATLAB® workspace.

Simulation

Two simulations will permit to examine the system response to:

1) Steps on the regulators references, and

2) Minor and severe perturbations on the AC sides.

Steady-state - Step response of power (P & Q) and DC voltage regulators

The system is programmed to start and reach a steady state. Steps are then applied sequentially on: the reference active and reactive power of the rectifier; the reference DC voltage of the inverter. The dynamic response of the regulators is observed. Start the simulation. Open the BUS B1 STATION_1 and DC_SIDE_STATION_2 scopes (in the respective Data Acquisition subsystems). Examine in station 1: the active power on trace 2 (1 p.u. = 200 MW) and the reactive power (reference and measured values) on trace 3 (1 p.u. =200 Mvar); in station 2: the DC voltage (reference and measured values) on trace 2 (1 p.u. = 200 kV).

At t = 1.5 s, a -0.1 p.u. step is first applied to the reference active power (decrease from 1 p.u. to 0.9 pu). The power stabilizes in approximately 0.3 seconds. Steps are also applied to the reference reactive power of the rectifier (from 0 to -0.1 p.u.) at t = 2.0 s and on the reference DC voltage of the inverter (decrease from 1 p.u. to 0.95 p.u.) at t = 2.5 s. Note the regulators dynamics and how they are more or less mutually affected. The control design attempts to decouple the active and reactive power responses.

AC side perturbations

Deactivate the steps applied on the three references by changing the multiplication factors to 100 in the Step times. In the "Three-Phase Programmable Voltage Source" inside AC system 1 subsystem, change the Time variation setting to "Amplitude". Check that the source is now programmed for a step of -0.1 p.u on voltage magnitude at t = 1.5 s, for a duration of 7 cycles. In the "Three-Phase Fault" block change to 1 the multiplication factor in the Transition times. A 6 cycles three-phase fault will be applied at t = 2.1 s in station 2 PCC (Bus B2). Restart the simulation.

After the AC voltage sag in station 1, the active and reactive power deviation from the pre-disturbance is less than 0.09 p.u. and 0.2 p.u. respectively. The recovery time is less than 0.3 s and steady state is reached again. A second perturbation follows. During the severe three-phase fault at station 2, the transmitted DC power is almost halted and the DC voltage tends to increase (1.2 p.u.) since the DC side capacitance is being excessively charged. A special function (DC Voltage Control Override) in the Active Power Control (in station 1) attempts to limit the DC voltage within a fixed range (see the controller mask). The system recovers well after the fault within 0.5 s. You can observe overshoot in the active power (1.33 p.u. in station 1) and damped oscillations (around 10 Hz) in the reactive power.

Impact of the DC voltage balance control

Open the VOLTAGE_BALANCE_CONTROL_STATION_2 scope. Finally, open the control dialog box in Station 2 and verify that the DC Voltage Balance box is activated. The DC voltage balance control objective is to minimize the voltage unbalance (Udc_0_mean signal = sum of the positive and negative pole voltages). A way of producing an unbalance is to use unequal capacitance values in the positive and negative poles (for example, Cp divided by 2). Observe the Udc_0_mean signal first with the DC balance control activated and then deactivated. Note that this function response is relatively slow.

STATCOM (Phasor Model)

This example shows a Static Synchronous Compensator (STATCOM) used for midpoint voltage regulation on a 500-kV transmission line.

Pierre Giroux and Gibert Sybille (Hydro-Quebec)
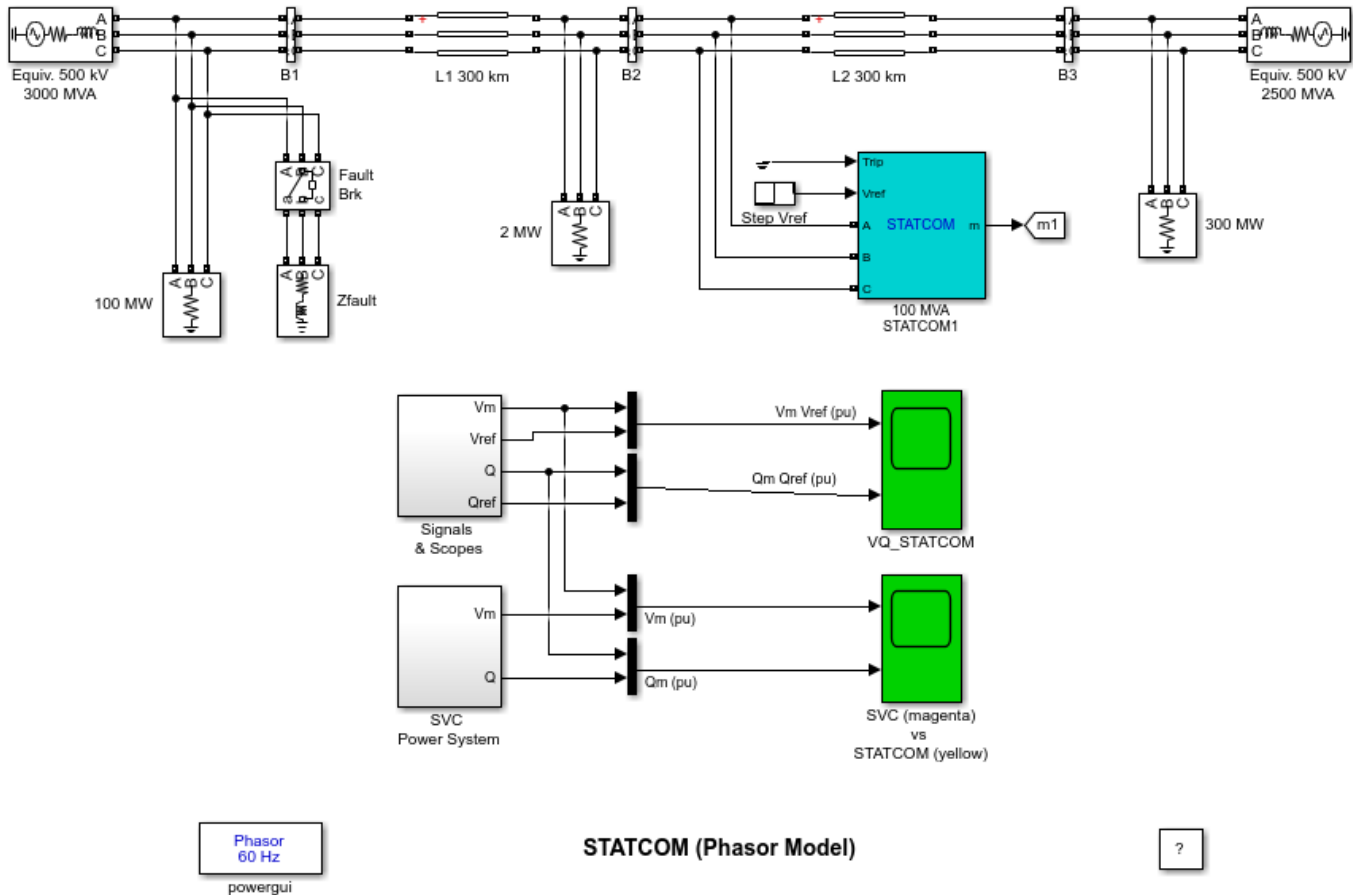
Description

The Static Synchronous Compensator (STATCOM) is one of the key FACTS devices. Based on a voltage-sourced converter, the STATCOM regulates system voltage by absorbing or generating reactive power. Contrary to a thyristor-based Static Var Compensator (SVC), STATCOM output current (inductive or capacitive) can be controlled independent of the AC system voltage.

The power grid consists of two 500-kV equivalents (respectively 3000 MVA and 2500 MVA) connected by a 600-km transmission line. When the STATCOM is not in operation, the "natural" power flow on the transmission line is 930 MW from bus B1 to B3. The STATCOM is located at the midpoint of the line (bus B2) and has a rating of +/- 100MVA. This STATCOM is a phasor model of a typical three-level PWM STATCOM. If you open the STATCOM dialog box and select "Display Power data", you will see that our model represents a STATCOM having a DC link nominal voltage of 40 kV with an equivalent capacitance of 375 uF. On the AC side, its total equivalent impedance is 0.22 pu on 100 MVA. This impedance represents the transformer leakage reactance and the phase reactor of the IGBT bridge of an actual PWM STATCOM.

Simulation

1. STATCOM Dynamic Response

We will now verify the dynamic response of our model. Open the STATCOM dialog box and select "Display Control parameters". Verify that the "Mode of operation" is set to "Voltage regulation" and that "External control of reference voltage Vref" is selected. Also, the "droop" parameter should be set to 0.03 and the "Vac Regulator Gains" to 5 (proportional gain Kp) and 1000 (integral gain Ki). Close the STATCOM dialog block and open the "Step Vref" block (the red timer block connected to the "Vref" input of the STATCOM). This block should be programmed to modify the reference voltage Vref as follows: Initially Vref is set to 1 pu; at t=0.2 s, Vref is decreased to 0.97 pu; then at t=0.4 s, Vref is increased to 1.03; and finally at 0.6 s, Vref is set back to 1 pu. Also, make sure that the fault breaker at bus B1will not operate during the simulation (the parameters "Switching of phase A, B and C" should not be selected).

Run the simulation and look at the "VQ_STATCOM" scope. The first graph displays the Vref signal (magenta trace) along with the measured positive-sequence voltage Vm at the STATCOM bus (yellow trace). The second graph displays the reactive power Qm (yellow trace) absorbed (positive value) or generated (negative value) by the STATCOM. The signal Qref (magenta trace) is not relevant to our simulation because the STATCOM is in "Voltage regulation" and not in "Var Control".

Looking at the Qm signal we can determine that the closed-loop time constant of the system is about 20 ms. This time constant depends primarily on the power system strength at bus B2 and on the programmed Vac Regulator gains of the STATCOM. To see the impact of the regulator gains, multiply the two gains of the Vac Regulator Gains by two and rerun the simulation. You should observe a much faster response with a small overshoot.

Looking at the Vm and Vref signals, you can see that the STATCOM does not operate as a perfect voltage regulator (Vm does not follow exactly the reference voltage Vref). This is due to the regulator droop (regulating slope) of 0.03 pu. For a given maximum capacitive/inductive range, this droop is used to extend the linear operating range of the STATCOM and also to ensure automatic load sharing with other voltage compensators (if any). Set the droop parameter to 0 and the voltage regulator gains back to 5 (Kp) and 1000 (Ki). If you then run a simulation, you will see that the measured voltage Vm now follows perfectly the reference voltage Vref.

2. STATCOM compared to a SVC under fault condition

We will now compare our STATCOM model with a SVC model having the same rating (+/- 100 MVA). If you double-click on the "SVC Power System" (the magenta block), you will see a SVC connected to a power grid similar to the power grid on which our STATCOM is connected. A remote fault will be simulated on both systems using a fault breaker in series with a fault impedance. The value of the fault impedance has been programmed to produce a 30% voltage sag at bus B2. Before running the simulation, you will first disable the "Step Vref" block by multiplying the time vector by 100. You will then program the fault breaker by selecting the parameters "Switching of phase A, B and C" and verify that the breaker is programmed (look at the "Transition times" parameter) to operate at t=0.2 s for a duration of 10 cycles. Check also that the fault breaker inside the "SVC Power System" has the same parameters. Finally, set the STATCOM droop back to its original value (0.03 pu).

Run the simulation and look at the "SVC vs STATCOM" scope. The first graph displays the measured voltage Vm on both systems (magenta trace for the SVC). The second graph displays the measured reactive power Qm generated by the SVC (magenta trace) and the STATCOM (yellow trace). During the 10-cycle fault, a key difference between the SVC and the STATCOM can be observed. The reactive power generated by the SVC is -0.48 pu and the reactive power generated by the STATCOM is -0.71 pu. We can then see that the maximum capacitive power generated by a SVC is proportional to the

square of the system voltage (constant susceptance) while the maximum capacitive power generated by a STATCOM decreases linearly with voltage decrease (constant current). This ability to provide more capacitive power during a fault is one important advantage of the STATCOM over the SVC. In addition, the STATCOM will normally exhibits a faster response than the SVC because with the voltage-sourced converter, the STATCOM has no delay associated with the thyristor firing (in the order of 4 ms for a SVC).

STATCOM (Detailed Model)

This example shows the operation of a +100 Mvar/-100 Mvar 48-pulse GTO STATCOM.

P. Giroux ; G. Sybille (Hydro-Quebec)
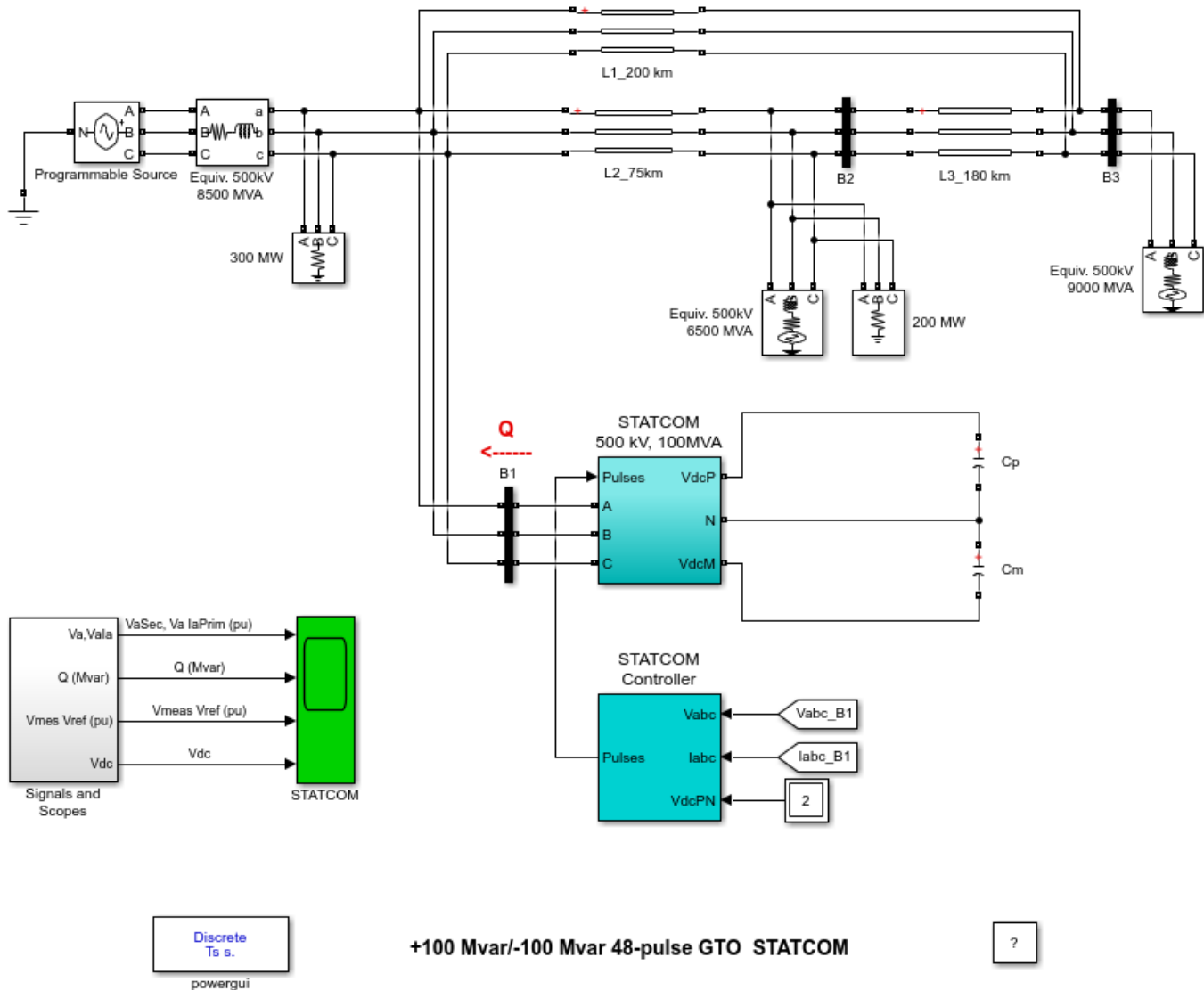
Description

A 100-Mvar STATCOM regulates voltage on a three-bus 500-kV system. The 48-pulse STATCOM uses a Voltage-Sourced Converter (VSC) built of four 12-pulse three-level GTO inverters. Look inside the STATCOM block to see how the VSC inverter is built. The four sets of three-phase voltages obtained at the output of the four three-level inverters are applied to the secondary windings of four phase-shifting transformers (-15 deg., -7.5 deg., 7.5 deg., +7.5 deg. phase shifts). The fundamental components of voltages obtained on the 500 kV side of the transformers are added in phase by the serial connection of primary windings. Please refer to the "power_48pulsegtoconverter" example to get details on the operation of the VSC.

During steady-state operation the STATCOM control system keeps the fundamental component of the VSC voltage in phase with the system voltage. If the voltage generated by the VSC is higher (or lower) than the system voltage, the STATCOM generates (or absorbs) reactive power. The amount of reactive power depends on the VSC voltage magnitude and on the transformer leakage reactances. The fundamental component of VSC voltage is controlled by varying the DC bus voltage. In order to vary the DC voltage, and therefore the reactive power, the VSC voltage angle (alpha) which is normally kept close to zero is temporarily phase shifted. This VSC voltage lag or lead produces a temporary flow of active power which results in an increase or decrease of capacitor voltages.

One of the three voltage sources used in the 500 kV system equivalents can be varied in order to observe the STATCOM dynamic response to changes in system voltage. Open the "Programmable Voltage Source" menu and look at the sequence of voltage steps which are programmed.

Simulation

Dynamic response of the STATCOM

Run the simulation and observe waveforms on the STATCOM scope block. The STATCOM is in voltage control mode and its reference voltage is set to Vref=1.0 pu. The voltage droop of the regulator is 0.03 pu/100 VA.Therefore when the STATCOM operating point changes from fully capacitive (+100 Mvar) to fully inductive (-100 Mvar) the STATCOM voltage varies between 1-0.03=0.97 pu and 1+0.03=1.03 pu.

Initially the programmable voltage source is set at 1.0491 pu, resulting in a 1.0 pu voltage at SVC terminals when the STATCOM is out of service. As the reference voltage Vref is set to 1.0 pu, the STATCOM is initially floating (zero current). The DC voltage is 19.3 kV. At t=0.1s, voltage is suddenly decreased by 4.5 % (0.955 pu of nominal voltage). The SVC reacts by generating reactive power (Q= +70 Mvar) in order to keep voltage at 0.979 pu. The 95% settling time is approximately 47 ms. At this point the DC voltage has increased to 20.4 kV. Then, at t=0.2 s the source voltage is increased to1.045 pu of its nominal value. The SVC reacts by changing its operating point from capacitive to inductive in order to keep voltage at 1.021 pu. At this point the STATCOM absorbs 72 Mvar and the DC voltage has been lowered to 18.2 kV. Observe on the first trace showing the STATCOM primary voltage and current that the current is changing from capacitive to inductive in approximately one cycle. Finally, at t=0.3 s the source voltage in set back to its nominal value and the STATCOM operating point comes back to zero Mvar.

If you look inside the "Signals and Scopes" subsystem you will have access to other control signals. Notice the transient changes on alpha angle when the DC voltage is increased or decreased in order to vary reactive power. The steady state value of alpha (0.5 degrees) is the phase shift required to maintain a small active power flow compensating transformer and converter losses.

How To Regenerate Initial Conditions

The initial states required to start this example in steady state have been saved in the "power_statcom_gto48p.mat" file. When you open this example, the InitFcn callback (in the Model Properties/Callbacks) automatically loads into your workspace the contents of this .mat file ("xInitial" variable).

If you modify this model, or change parameter values of power components, the initial conditions stored in the "xInitial" variable will no longer be valid and Simulink® will issue an error message. To regenerate the initial conditions for your modified model, follow the steps listed below:

1. In the Simulation/Configuration Parameters/Data Import/Export Parameters menu, uncheck the "Initial state" parameter and check the "Final states" parameter.

2. In the Programmable Voltage Source menu, disable the source voltage steps by setting the "Time variation of " parameter to "none".

3. Make sure that the Simulation Stop Time is 0.4 second. Note that in order to generate initial conditions coherent with the 60 Hz voltage source phase angles, the Stop Time must an integer number of 60 Hz cycles.

4. Start simulation. When simulation is completed, verify that steady state has been reached by looking at waveforms displayed on the scope. The final states which have been saved in the "xFinal" structure with time can be used as initial states for future simulations. Executing the next two commands copies these final conditions in "xInitial" and saves this variable in a new file (myModel_init.mat).

```
>> xInitial=xFinal;
```

```
>> save myModel_init xInitial
```

5. In the File/Model Properties/Callbacks/InitFcn window, change the line "xInitial = init_power_statcom_gto48p" to "load myModel_init.mat". Next time you open this model, the variable xInitial saved in the myModel_init.mat file will be loaded in your workspace.

6. In the Simulation/Configuration Parameters menu, check "Initial state".

7. Start simulation and verify that your model starts in steady-state.

8. In the Programmable Voltage Source menu, set the "Time variation of" parameter back to "Amplitude".

9. Save your model.

D-STATCOM (Detailed Model)

This example shows a detailed model of a distribution STATCOM.

Pierre Giroux ; Gilbert Sybille (Hydro-Quebec, IREQ)

Detailed Versus Average Model

When modeling VSC-based energy conversion systems in Specialized Power Systems, you can use two types of models, depending on the range of frequencies to be represented : the detailed model and the average model.

The detailed model (discrete) such as the one presented in this example. The detailed model includes detailed representation of power electronic IGBT converters. In order to achieve an acceptable accuracy with the 1680 Hz switching frequency used in this example, the model must be discretized at a relatively small time step (5 microseconds). This model is well suited for observing harmonics and control system dynamic performance over relatively short periods of times (typically hundreds of milliseconds to one second).

The average model (discrete) such as the one presented in the power_dstatcom_avg model in the FACTS library of examples. In this type of model the IGBT Voltage-Sourced Converters (VSC) are represented by equivalent voltage sources generating the AC voltage averaged over one cycle of the switching frequency. This model does not represent harmonics, but the dynamics resulting from control system and power system interaction is preserved. This model allows using much larger time steps (typically 50 microseconds), thus allowing simulations of several seconds.

Alternatively, a third type of model can be used for simulating on larger time frames: the **phasor model**. This type of model is not available for the D-STATCOM , but it is available for the STATCOM, a similar device, in the power_statcom.mdl model.

Description

A Distribution Static Synchronous Compensator (D-STATCOM) is used to regulate voltage on a 25-kV distribution network. Two feeders (21 km and 2 km) transmit power to loads connected at buses B2 and B3. A shunt capacitor is used for power factor correction at bus B2. The 600-V load connected to bus B3 through a 25kV/600V transformer represents a plant absorbing continuously changing currents, similar to an arc furnace, thus producing voltage flicker. The variable load current magnitude is modulated at a frequency of 5 Hz so that its apparent power varies approximately between 1 MVA and 5.2 MVA, while keeping a 0.9 lagging power factor. This load variation will allow you to observe the ability of the D-STATCOM to mitigate voltage flicker.

The D-STATCOM regulates bus B3 voltage by absorbing or generating reactive power. This reactive power transfer is done through the leakage reactance of the coupling transformer by generating a secondary voltage in phase with the primary voltage (network side). This voltage is provided by a voltage-sourced PWM inverter. When the secondary voltage is lower than the bus voltage, the D-STATCOM acts like an inductance absorbing reactive power. When the secondary voltage is higher than the bus voltage, the D-STATCOM acts like a capacitor generating reactive power.

The D-STATCOM consists of the following components:

- **a 25kV/1.25kV coupling transformer** which ensures coupling between the PWM inverter and the network.
- **a voltage-sourced PWM inverter** consisting of two IGBT bridges. This twin inverter configuration produces less harmonics than a single bridge, resulting in smaller filters and improved dynamic response. In this case, the inverter modulation frequency is 28*60=1.68 kHz so that the first harmonics will be around 3.36 kHz.
- **LC damped filters** connected at the inverter output. Resistances connected in series with capacitors provide a quality factor of 40 at 60 Hz.
- **a 10000-microfarad capacitor** acting as a DC voltage source for the inverter
- **a voltage regulator** that controls voltage at bus B3
- **a PWM pulse generator** using a modulation frequency of 1.68 kHz
- **anti-aliasing filters** used for voltage and current acquisition.

The D-STATCOM controller consists of several functional blocks:

- **a Phase Locked Loop (PLL)**. The PLL is synchronized to the fundamental of the transformer primary voltages.
- **two measurement systems**. Vmeas and Imeas blocks compute the d-axis and q-axis components of the voltages and currents by executing an abc-dq transformation in the synchronous reference determined by sin(wt) and cos(wt) provided by the PLL.
- **an inner current regulation loop**. This loop consists of two proportional-integral (PI) controllers that control the d-axis and q-axis currents. The controllers outputs are the Vd and Vq voltages that the PWM inverter has to generate. The Vd and Vq voltages are converted into phase voltages Va, Vb, Vc which are used to synthesize the PWM voltages. The Iq reference comes from the outer voltage regulation loop (in automatic mode) or from a reference imposed by Qref (in manual mode). The Id reference comes from the DC-link voltage regulator.
- **an outer voltage regulation loop**. In automatic mode (regulated voltage), a PI controller maintains the primary voltage equal to the reference value defined in the control system dialog box.
- **a DC voltage controller** which keeps the DC link voltage constant to its nominal value (Vdc=2.4 kV).

The electrical circuit is discretized using a sample time Ts=5 microseconds. The controller uses a larger sample time (32*Ts= 160 microseconds).

Simulation

1. D-STATCOM dynamic response

During this test, the variable load will be kept constant and you will observe the dynamic response of a D-STATCOM to step changes in source voltage. Check that the modulation of the Variable Load is not in service (Modulation Timing [Ton Toff]= [0.15 1]*100 > Simulation Stop time). The Programmable Voltage Source block is used to modulate the internal voltage of the 25-kV equivalent. The voltage is first programmed at 1.077 pu in order to keep the D-STATCOM initially floating (B3 voltage=1 pu and reference voltage Vref=1 pu). Three steps are programmed at 0.2 s, 0.3 s, and 0.4 s to successively increase the source voltage by 6%, decrease it by 6% and bring it back to its initial value (1.077 pu).

Start the simulation. Observe on Scope1 the phase A voltage and current waveforms of the D-STATCOM as well as controller signals on Scope2. After a transient lasting approximately 0.15 sec., the steady state is reached. Initially, the source voltage is such that the D-STATCOM is inactive. It does not absorb nor provide reactive power to the network. At t = 0.2 s, the source voltage is increased by 6%. The D-STATCOM compensates for this voltage increase by absorbing reactive power from the network (Q=+2.7 Mvar on trace 2 of Scope2). At t = 0.3 s, the source voltage is decreased by 6% from the value corresponding to Q = 0. The D-STATCOM must generate reactive power to maintain a 1 pu voltage (Q changes from +2.7 MVAR to -2.8 MVAR). Note that when the D-STATCOM changes from inductive to capacitive operation, the modulation index of the PWM inverter is increased from 0.56 to 0.9 (trace 4 of Scope2) which corresponds to a proportional increase in inverter voltage. Reversing of reactive power is very fast, about one cycle, as observed on D-STATCOM current (magenta signal on trace 1 of Scope1).

2. Mitigation of voltage flicker

During this test, voltage of the Programmable Voltage Source will be kept constant and you will enable modulation of the Variable Load so that you can observe how the D-STATCOM can mitigate voltage flicker. In the Programmable Voltage Source block menu, change the "Time Variation of" parameter to "None". In the Variable Load block menu, set the Modulation Timing parameter to [Ton Toff]= [0.15 1] (remove the 100 multiplication factor). Finally, in the D-STATCOM Controller, change the "Mode of operation" parameter to "Q regulation" and make sure that the reactive power reference value Qref (2nd line of parameters) is set to zero. In this mode, the D-STATCOM is floating and performs no voltage correction.

Run the simulation and observe on Scope3 variations of P and Q at bus B3 (1st trace) as well as voltages at buses B1 and B3 (trace 2). Without D-STATCOM, B3 voltage varies between 0.96 pu and 1.04 pu (+/- 4% variation). Now, in the D-STATCOM Controller, change the "Mode of operation" parameter back to "Voltage regulation" and restart simulation. Observe on Scope 3 that voltage fluctuation at bus B3 is now reduced to +/- 0.7 %. The D-STATCOM compensates voltage by injecting a reactive current modulated at 5 Hz (trace 3 of Scope3) and varying between 0.6 pu capacitive when voltage is low and 0.6 pu inductive when voltage is high.

D-STATCOM (Average Model)

This example shows an average model of a distribution STATCOM.
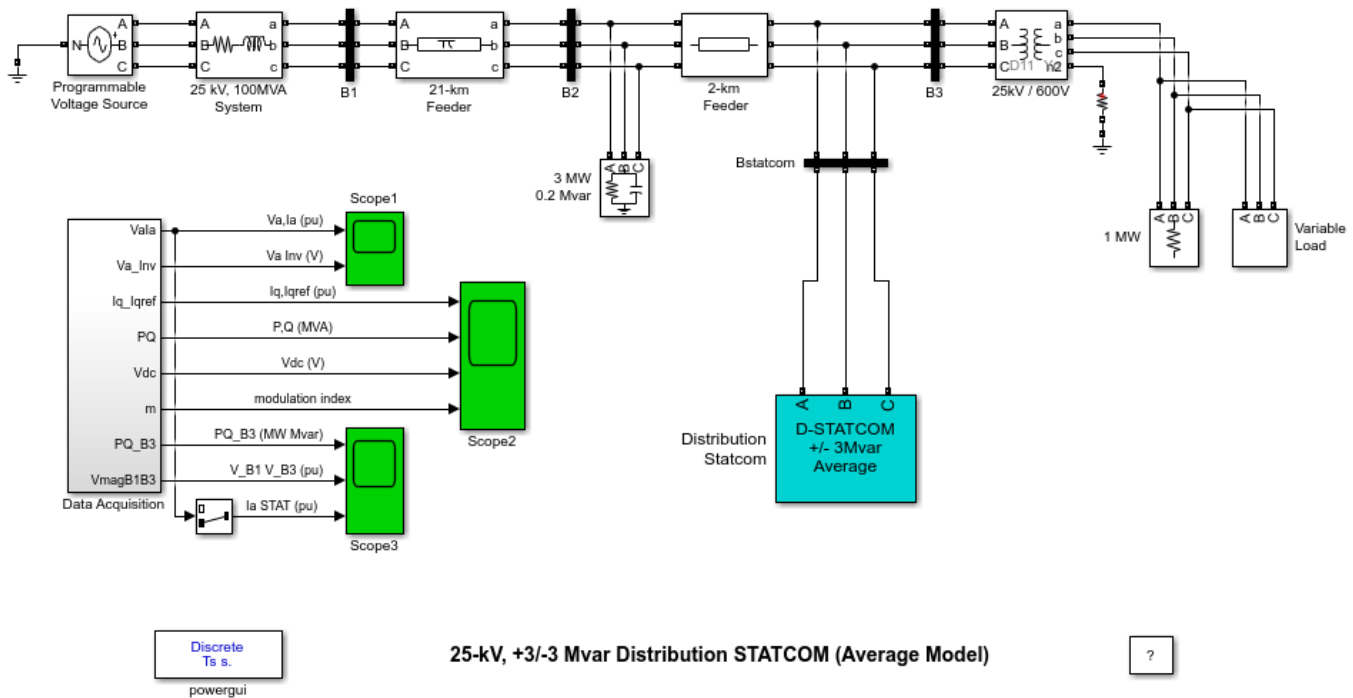
Pierre Giroux ; Gilbert Sybille (Hydro-Quebec, IREQ)

Detailed Versus Average Model

When modeling VSC-based energy conversion systems in Specialized Power Systems, you can use two types of models, depending on the range of frequencies to be represented : the detailed model and the average model.

The detailed model such as the one presented in the power_dstatcom_pwm model in the FACTS library of examples. The detailed model includes detailed representation of power electronic IGBT converters. In order to achieve an acceptable accuracy with the 1680 Hz switching frequency used in this example, the model must be discretized at a relatively small time step (5 microseconds). This model is well suited for observing harmonics and control system dynamic performance over relatively short periods of times (typically hundreds of milliseconds to one second).

The average model such as the one presented in this example. In this type of model the IGBT Voltage-Sourced Converters (VSC) are represented by equivalent voltage sources generating the AC voltage averaged over one cycle of the switching frequency. This model does not represent harmonics, but the dynamics resulting from control system and power system interaction is preserved. This model allows using much larger time steps (typically 40-50 microseconds), thus allowing simulations of several seconds.

Alternatively, a third type of model can be used for simulating on larger time frames: the **phasor model**. This type of model is not available for the D-STATCOM , but it is available for the STATCOM, a similar device, in the power_statcom model.

Description

A Distribution Static Synchronous Compensator (D-STATCOM) is used to regulate voltage on a 25-kV distribution network. Two feeders (21 km and 2 km) transmit power to loads connected at buses B2 and B3. A shunt capacitor is used for power factor correction at bus B2. The 600-V load connected to bus B3 through a 25kV/600V transformer represents a plant absorbing continuously changing currents, similar to an arc furnace, thus producing voltage flicker. The variable load current magnitude is modulated at a frequency of 5 Hz so that its apparent power varies approximately between 1 MVA and 5.2 MVA, while keeping a 0.9 lagging power factor. This load variation will allow you to observe the ability of the D-STATCOM to mitigate voltage flicker.

The D-STATCOM regulates bus B3 voltage by absorbing or generating reactive power. This reactive power transfer is done through the leakage reactance of the coupling transformer by generating a secondary voltage in phase with the primary voltage (network side). This voltage is provided by a voltage-sourced PWM inverter. When the secondary voltage is lower than the bus voltage, the D-STATCOM acts like an inductance absorbing reactive power. When the secondary voltage is higher than the bus voltage, the D-STATCOM acts like a capacitor generating reactive power.

The D-STATCOM consists of the following components:

- **a 25kV/1.25kV coupling transformer** which ensures coupling between the PWM inverter and the network.
- **a voltage-sourced PWM inverter**. In this example, the PWM inverter is replaced on the AC side with three equivalent voltage sources averaged over one cycle of the switching frequency (1.68 kHz). Harmonics generated by the inverter are therefore not visible with this average model. On the DC side, the inverter is modeled by a current source charging the DC capacitor. The DC current Idc is computed so that the instantaneous power at the AC inputs of the inverter remains equal the instantaneous power at the DC output (Va*Ia + Vb*Ib + Vc*Ic = Vdc*Idc).
- **LC damped filters connected** at the inverter output. Resistances connected in series with capacitors provide a quality factor of 40 at 60 Hz.
- **a 10000-microfarad capacitor** acting as a DC voltage source for the inverter
- **a voltage regulator** that controls voltage at bus B3
- **anti-aliasing filters** used for voltage and current acquisition.

The D-STATCOM controller consists of several functional blocks:

- **a Phase Locked Loop (PLL)**. The PLL is synchronized to the fundamental of the transformer primary voltages.
- **two measurement systems**. Vmeas and Imeas blocks compute the d-axis and q-axis components of the voltages and currents by executing an abc-dq transformation in the synchronous reference determined by sin(wt) and cos(wt) provided by the PLL.
- **an inner current regulation loop**. This loop consists of two proportional-integral (PI) controllers that control the d-axis and q-axis currents. The controllers outputs are the Vd and Vq voltages that the PWM inverter has to generate. The Vd and Vq voltages are converted into phase voltages Va, Vb, Vc which are used to synthesize the PWM voltages. The Iq reference comes from the outer voltage regulation loop (in automatic mode) or from a reference imposed by Qref (in manual mode). The Id reference comes from the DC-link voltage regulator.
- **an outer voltage regulation loop**. In automatic mode (regulated voltage), a PI controller maintains the primary voltage equal to the reference value defined in the control system dialog box.

- **a DC voltage controller** which keeps the DC link voltage constant to its nominal value (Vdc=2.4 kV).

The electrical circuit is discretized using a sample time Ts=40 microseconds. The controller uses a larger sample time (4*Ts=160 microseconds).

Simulation

1. D-STATCOM dynamic response

During this test, the variable load will be kept constant and you will observe the dynamic response of a D-STATCOM to step changes in source voltage. Check that the modulation of the Variable Load is not in service (Modulation Timing [Ton Toff]= [0.15 1]*100 > Simulation Stop time). The Programmable Voltage Source block is used to modulate the internal voltage of the 25-kV equivalent. The voltage is first programmed at 1.077 pu in order to keep the D-STATCOM initially floating (B3 voltage=1 pu and reference voltage Vref=1 pu). Three steps are programmed at 0.2 s, 0.3 s, and 0.4 s to successively increase the source voltage by 6%, decrease it by 6% and bring it back to its initial value (1.077 pu).

Start the simulation. Observe on Scope1 the phase A voltage and current waveforms of the D-STATCOM as well as controller signals on Scope2. After a transient lasting approximately 0.15 sec., the steady state is reached. Initially, the source voltage is such that the D-STATCOM is inactive. It does not absorb nor provide reactive power to the network. At t = 0.2 s, the source voltage is increased by 6%. The D-STATCOM compensates for this voltage increase by absorbing reactive power from the network (Q=+2.7 Mvar on trace 2 of Scope2). At t = 0.3 s, the source voltage is decreased by 6% from the value corresponding to Q = 0. The D-STATCOM must generate reactive power to maintain a 1 pu voltage (Q changes from +2.7 MVAR to -2.8 MVAR). Note that when the D-STATCOM changes from inductive to capacitive operation, the modulation index of the PWM inverter is increased from 0.56 to 0.9 (trace 4 of Scope2) which corresponds to a proportional increase in inverter voltage. Reversing of reactive power is very fast, about one cycle, as observed on D-STATCOM current (magenta signal on trace 1 of Scope1).

2. Mitigation of voltage flicker

During this test, voltage of the Programmable Voltage Source will be kept constant and you will enable modulation of the Variable Load so that you can observe how the D-STATCOM can mitigate voltage flicker. In the Programmable Voltage Source block menu, change the "Time Variation of" parameter to "None". In the Variable Load block menu, set the Modulation Timing parameter to [Ton Toff]= [0.15 1] (remove the 100 multiplication factor). Finally, in the D-STATCOM Controller, change the "Mode of operation" parameter to "Q regulation? and make sure that the reactive power reference value Qref (2nd line of parameters) is set to zero. In this mode, the D-STATCOM is floating and performs no voltage correction.

Run the simulation and observe on Scope3 variations of P and Q at bus B3 (1st trace) as well as voltages at buses B1 and B3 (trace 2). Without D-STATCOM, B3 voltage varies between 0.96 pu and 1.04 pu (+/- 4% variation). Now, in the D-STATCOM Controller, change the "Mode of operation" parameter back to "Voltage regulation" and restart simulation. Observe on Scope 3 that voltage fluctuation at bus B3 is now reduced to +/- 0.7 %. The D-STATCOM compensates voltage by injecting a reactive current modulated at 5 Hz (trace 3 of Scope3) and varying between 0.6 pu capacitive when voltage is low and 0.6 pu inductive when voltage is high.

SSSC (Phasor Model)

This example shows a Static Synchronous Series Compensator (SSSC) used for power oscillation damping.

Pierre Giroux and Gibert Sybille (Hydro-Quebec)
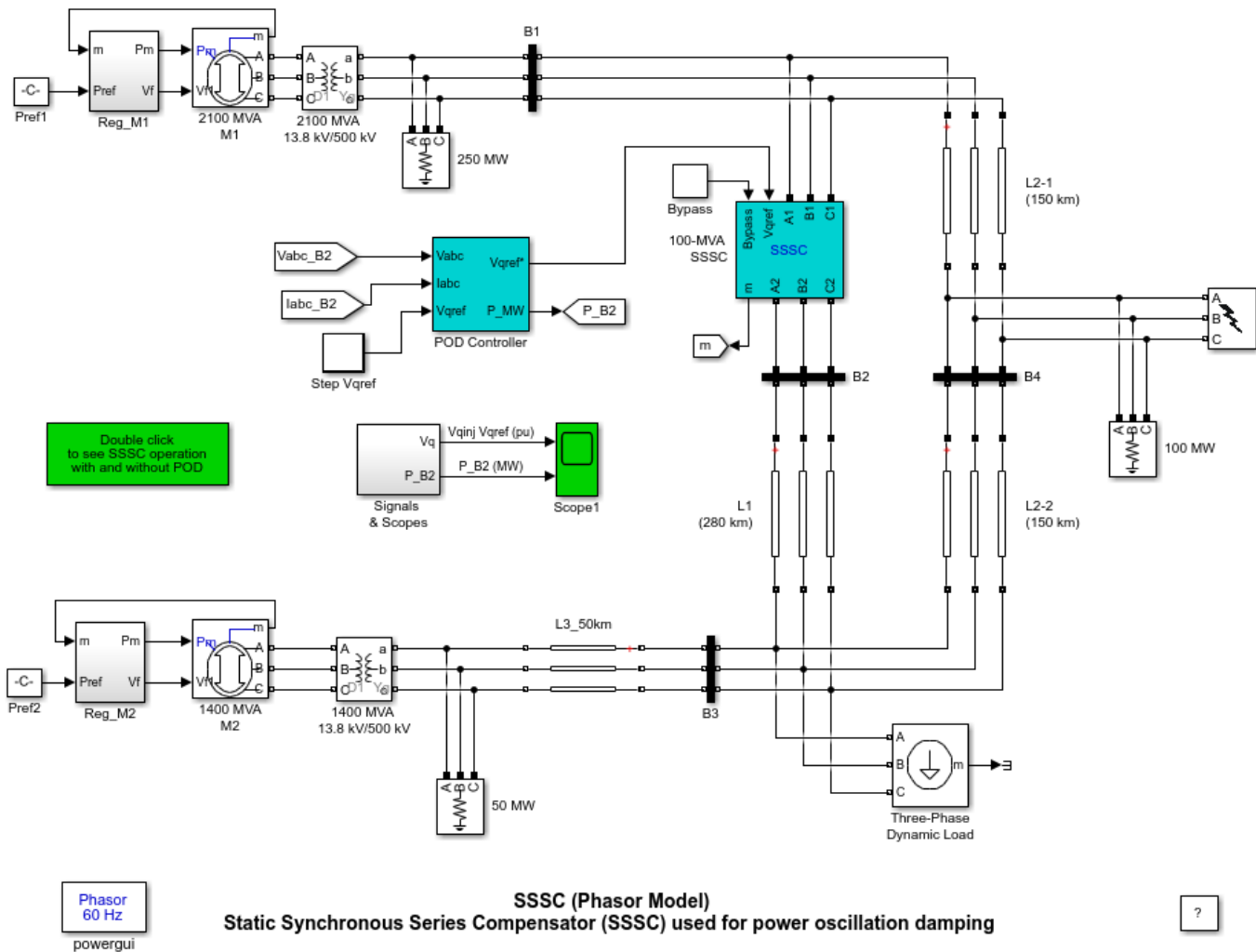
Description

The Static Synchronous Series Compensator (SSSC), one of the key FACTS devices, consists of a voltage-sourced converter and a transformer connected in series with a transmission line. The SSSC injects a voltage of variable magnitude in quadrature with the line current, thereby emulating an inductive or capacitive reactance. This emulated variable reactance in series with the line can then influence the transmitted electric power. The SSSC is used to damp power oscillation on a power grid following a three-phase fault.

The power grid consists of two power generation substations and one major load center at bus B3. The first power generation substation (M1) has a rating of 2100 MVA, representing 6 machines of 350 MVA and the other one (M2) has a rating of 1400 MVA, representing 4 machines of 350 MVA. The

load center of approximately 2200 MW is modeled using a dynamic load model where the active & reactive power absorbed by the load is a function of the system voltage.The generation substation M1 is connected to this load by two transmission lines L1 and L2. L1 is 280-km long and L2 is split in two segments of 150 km in order to simulate a three-phase fault (using a fault breaker) at the midpoint of the line. The generation substation M2 is also connected to the load by a 50-km line (L3). When the SSSC is bypassed, the power flow towards this major load is as follows: 664 MW flow on L1 (measured at bus B2), 563 MW flow on L2 (measured at B4) and 990 MW flow on L3 (measured at B3).

The SSSC, located at bus B1, is in series with line L1. It has a rating of 100MVA and is capable of injecting up to 10% of the nominal system voltage. This SSSC is a phasor model of a typical three-level PWM SSSC. If you open the SSSC dialog box and select "Display Power data", you will see that our model represents a SSSC having a DC link nominal voltage of 40 kV with an equivalent capacitance of 375 uF. On the AC side, its total equivalent impedance is 0.16 pu on 100 MVA. This impedance represents the transformer leakage reactance and the phase reactor of the IGBT bridge of an actual PWM SSSC.The SSSC injected voltage reference is normally set by a POD (Power Oscillation Damping) controller whose output is connected to the Vqref input of the SSSC. The POD controller consists of an active power measurement system, a general gain, a low-pass filter, a washout high-pass filter, a lead compensator, and an output limiter. The inputs to the POD controller are the bus voltage at B2 and the current flowing in L1. Look under mask to see how the controller is built.

Simulation

1. SSSC Dynamic Response

We will first verify the dynamic response of our model. Open the "Step Vqref" block (the red timer block connected to the "Vqref" input of the POD Controller).This block should be programmed to modify the reference voltage Vqref as follows: Initially Vqref is set to 0 pu; at t=2 s, Vqref is set to -0.08 pu (SSSC inductive); then at t=6 s, Vqref is set to 0.08 pu (SSSC capacitive). Double-click on the POD Controller block and set the POD status parameter to "off". This will disable the POD controller. Also, make sure that the fault breaker will not operate during the simulation (the parameters "Switching of phase A, B and C" should not be selected).

Run the simulation and look at Scope1. The first graph displays the Vqref signal (magenta trace) along with the measured injected voltage by the SSSC. The second graph displays the active power flow (P_B2) on line L1, measured at bus B2. We can see that the SSSC regulator follows very well the reference signal Vqref. Depending on the injected voltage, the power flow on line varies from 575 to 750 MW. In a real system the reference signal Vqref would typically be changed much more gradually in order to avoid the oscillation we see on the transmitted power (P_B2 signal). Double-click on the SSSC block and select "Display Control parameters". Modify the "Maximum rate of change for Vqref (pu/s)" parameter from 3 to 0.05. Rerun the simulation. The power oscillation on the active power should now be very small.

2. SSSC damping power oscillation

We will now compare the operation of our SSSC with and without POD control. Open the "Step Vqref" block and multiply by 1000 the time vector in order to disable the Vqref variations. Double-click on the fault breaker and select the parameters "Switching of phase A, B and C" to simulate a three-phase fault. The transition times should be set as follows: [20/60 30/60]+1; this means that the fault will be applied at 1.33 s and will last for 10 cycles. Run a simulation and observe the power oscillation on the L1 line (second graph on Scope1) following the three-phase fault.

Now, you will run a second simulation with the POD controller in operation. Double-click on the POD Controller block and set the POD status parameter to "on". Start the simulation. Looking again at the second graph on Scope1(P_B2 signal), we can see that the SSSC with a POD controller is a very effective tool to damp power oscillation. To see a figure showing a comparison of the SSSC operation with and without POD control, double-click on the blue block on the bottom right of the model.

SVC (Phasor Model)

This example shows steady-state and dynamic performance of the static var compensator model.

G. Sybille (Hydro-Quebec)

SVC (Phasor Model)
Phasor Simulation of a Static Var Compensator
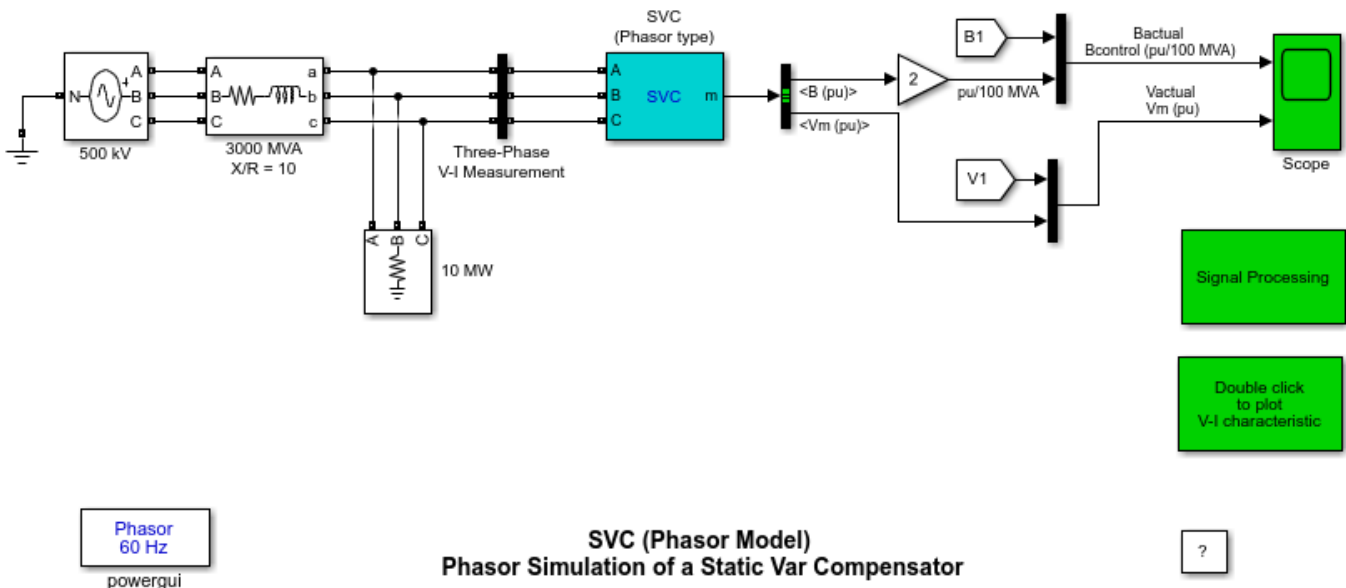
Description

A static var compensator (SVC) is used to regulate voltage on a 500 kV, 3000 MVA system. When system voltage is low the SVC generates reactive power (SVC capacitive). When system voltage is high it absorbs reactive power (SVC inductive). The SVC is rated +200 Mvar capacitive and 100 Mvar inductive. The Static Var Compensator block is a a phasor model representing the SVC static and dynamic characteristics at the system fundamental frequency.

To see the SVC control parameters, open the SVC dialog box and select "Display Control parameters". The SVC is set in voltage regulation mode with a reference voltage Vref=1.0 pu. The voltage droop is 0.03 pu/ 200MVA, so that the voltage varies from 0.97 pu to 1.015 pu when the SVC current goes from fully capacitive to fully inductive. Double click now on the blue block to display the SVC V-I characteristic.

The actual SVC positive-sequence voltage (V1) and susceptance (B1) are measured inside the 'Signal Processing' subsystem, using the complex voltages Vabc and complex currents Iabc returned by the Three-Phase V-I Measurement block.

1. Dynamic Response of the SVC

The Three-Phase Programmable Voltage Source is used to vary the system voltage and observe the SVC performance. Initially the source is generating nominal voltage. Then, voltage is successively decreased (0.97 pu at t = 0.1 s), increased (1.03 pu at t = 0.4 s) and finally returned to nominal voltage (1 pu at t = 0.7 s).

Start the simulation and observe the SVC dynamic response to voltage steps on the Scope. Trace 1 shows the actual positive-sequence susceptance B1 and control signal output B of the voltage

regulator. Trace 2 shows the actual system positive-sequence voltage V1 and output Vm of the SVC measurement system.

The SVC response speed depends on the voltage regulator integral gain Ki (Proportional gain Kp is set to zero), system strength (reactance Xn) and droop (reactance Xs). If the voltage measurement time constant and average time delay Td due to valve firing are neglected, the system can be approximated by a first order system having a closed loop time constant :

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Tc= 1/(Ki*(Xn+Xs))
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With given system parameters (Ki = 300; Xn = 0.0667 pu/200 MVA; Xs = 0.03 pu/200 MVA), Tc = 0.0345 s. If you increase the regulator gain or decrease the system strength, the measurement time constant and the valve firing delay Td will no longer be negligible and you will observe an oscillatory response and eventually instability.

2. Measurement of Steady-State V-I Characteristic

In order to measure the SVC steady-state V-I characteristic, you will now program a slow variation of the source voltage. Open the Programmable Voltage Source menu and change the "Type of Variation" parameter to "Modulation". The modulation parameters are set to apply a sinusoidal variation of the positive-sequence voltage between 0.75 and 1.25 pu in 20 seconds. Change the stop time to 20 s and restart simulation. When simulation is completed, double click the blue block. The theoretical V-I characteristic is displayed (in red) together with the measured characteristic (in blue).

SVC (Detailed Model)

This example shows the operation of a +300 Mvar/-100 Mvar Static Var Compensator (SVC).

Pierre Giroux and Gibert Sybille (Hydro-Quebec)

Description

A 300-Mvar Static Var Compensator (SVC) regulates voltage on a 6000-MVA 735-kV system. The SVC consists of a 735kV/16-kV 333-MVA coupling transformer, one 109-Mvar thyristor-controlled reactor bank (TCR) and three 94-Mvar thyristor-switched capacitor banks (TSC1 TSC2 TSC3) connected on the secondary side of the transformer. Switching the TSCs in and out allows a discrete variation of the secondary reactive power from zero to 282 Mvar capacitive (at 16 kV) by steps of 94 Mvar, whereas phase control of the TCR allows a continuous variation from zero to 109 Mvar inductive. Taking into account the leakage reactance of the transformer (15%), the SVC equivalent susceptance seen from the primary side can be varied continuously from -1.04 pu/100 MVA (fully inductive) to +3.23 pu/100 Mvar (fully capacitive). The SVC controller monitors the primary voltage and sends appropriate pulses to the 24 thyristors (6 thyristors per three-phase bank) in order to obtain the susceptance required by the voltage regulator.

Use Look under Mask to see how the TCR and TSC subsystems are built. Each three-phase bank is connected in delta so that, during normal balanced operation, the zero-sequence tripplen harmonics (3rd, 9th...) remain trapped inside the delta, thus reducing harmonic injection into the power system. The power system is represented by an inductive equivalent (6000 MVA short circuit level) and a 200-

MW load. The internal voltage of the equivalent can be varied by means of programmable source in order to observe the SVC dynamic response to changes in system voltage. Open the voltage source menu and look at the sequence of voltage steps which are programmed.

Simulation

Dynamic response of the SVC

Run the simulation and observe waveforms on the SVC scope block. The SVC is in voltage control mode and its reference voltage is set to Vref=1.0 pu. The voltage droop of the regulator is 0.01 pu/100 VA (0.03 pu/300MVA). Therefore when the SVC operating point changes from fully capacitive (+300 Mvar) to fully inductive (-100 Mvar) the SVC voltage varies between 1-0.03=0.97 pu and 1+0.01=1.01 pu.

Initially the source voltage is set at 1.004 pu, resulting in a 1.0 pu voltage at SVC terminals when the SVC is out of service . As the reference voltage Vref is set to 1.0 pu, the SVC is initially floating (zero current). This operating point is obtained with TSC1 in service and TCR almost at full conduction (alpha=96 degrees). At t=0.1s voltage is suddenly increased to 1.025 pu. The SVC reacts by absorbing reactive power (Q=-95 Mvar) in order to bring the voltage back to 1.01 pu. The 95% settling time is approximately 135 ms. At this point all TSCs are out of service and the TCR is almost at full conduction (alpha = 94 degrees) . At t=0.4 s the source voltage is suddenly lowered to 0.93 pu. The SVC reacts by generating 256 Mvar of reactive power, thus increasing the voltage to 0.974 pu. At this point the three TSCs are in service and the TCR absorbs approximately 40% of its nominal reactive power (alpha =120 degrees). Observe on the last trace of the scope how the TSCs are sequentially switched on and off. Each time a TSC is switched on the TCR alpha angle changes suddenly from 180 degrees (no conduction) to 90 degrees (full conduction). Finally, at t=0.7 s the voltage is increased to 1.0 pu and the SVC reactive power is reduced to zero.

Misfiring of TSC1

Each time a TSC is switched off a voltage remains trapped across the TSC capacitors. If you look at the 'TSC1 Misfiring' scope inside the "Signals and Scope" subsystem you can observe the TSC1 voltage (first trace) and the TSC1 current (second trace) for branch AB. The voltage across the positive thyristor (thyristor conducting the positive current) is shown on the 3rd trace and the pulses sent to this thyristor are shown on the 4th trace. Notice that the positive thyristor is fired at maximum negative TSC voltage, when the valve voltage is minimum. If by mistake the firing pulse is not sent at the right time, very large overcurrents can be observed in the TSC valves.

Look inside the SVC Controller block how a misfiring can be simulated on TSC1. A Timer block and a OR block are used to add pulses to the normal pulses coming from the Firing Unit. Open the Timer block menu and remove the 100 multiplication factor. The timer is now programmed to send a misfiring pulse lasting one sample time at time t= 0.121 s. Restart simulation. Observe that the misfiring pulse is sent when the valve voltage is maximum positive immediately after the TSC has blocked. This thyristor misfiring produces a large thyristor overcurrent (18 kA or 6.5 times the nominal peak current). Also, immediately after the thyristor has blocked, the thyristor voltage reaches 85 kV (3.8 times the nominal peak voltage). In order to prevent such overcurrents and overvoltages, thyristor valves are normally protected by metal oxide arresters (not simulated here).

TCSC (Phasor Model)

This example shows the Thyristor Controlled Series Capacitor (TCSC) phasor test system.

Dragan Jovcic (University of Aberdeen, Scotland, UK)
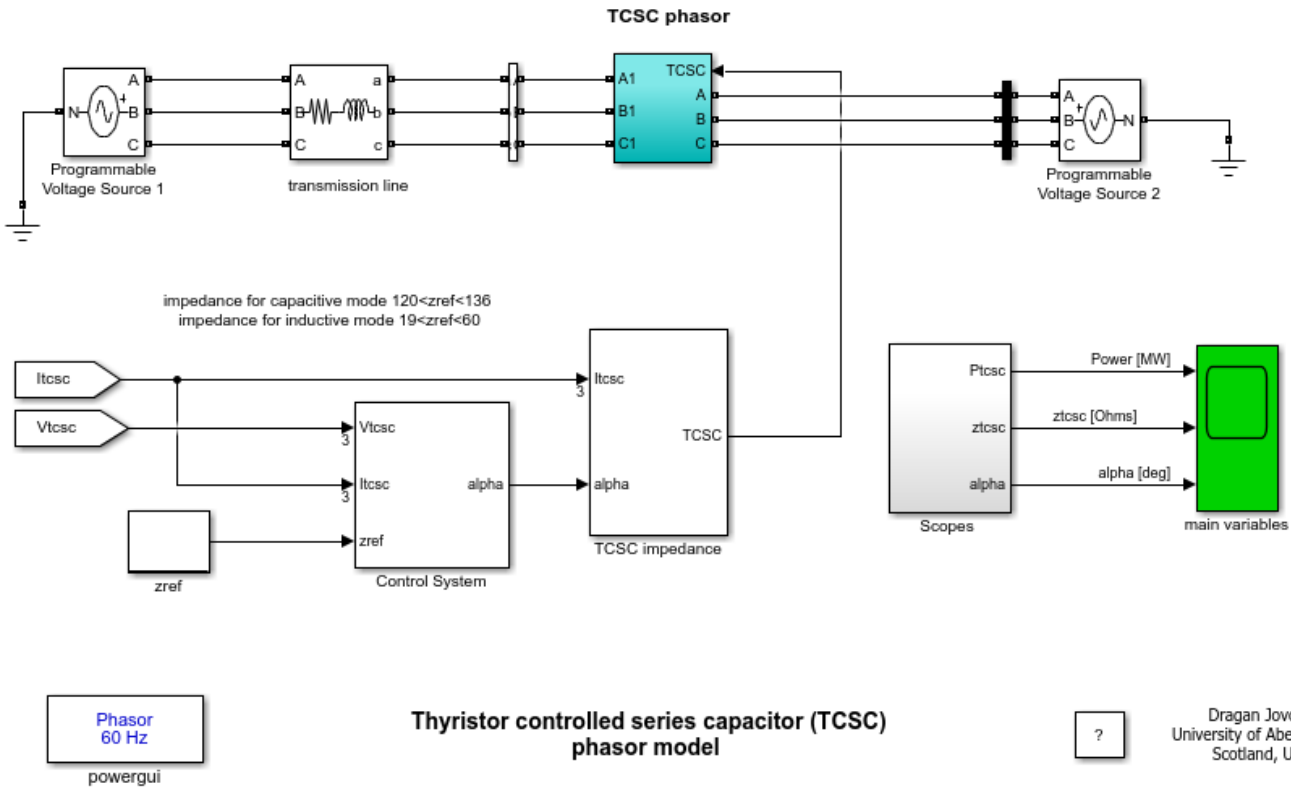
Phasor TCSC Model

This phasor tests system is similar to the TCSC thyristor-based tests system in the library. The phasor model however uses the equivalent impedances at the fundamental frequency, neglecting all transients, and therefore it is not as accurate as the thyristor model. Nevertheless, the phasor model is much simpler and the speed of simulation is increased. By comparing the responses with the detailed model we can observe very good matching of all variables in steady-state. Some small discrepancies are caused by the thyristor resistance and other TCSC losses which are not included in the phasor model.

Description

A TCSC is placed on a 500kV, long transmission line, to improve power transfer. Without the TCSC the power transfer is around 110MW, as seen during the first 0.5s of the simulation when the TCSC is bypassed. The TCSC is modeled as a voltage source using equivalent impedance at fundamental frequency in each phase. The nominal compensation is 75%, i.e. assuming only the capacitors (firing angle of 90deg). The natural oscillatory frequency of the TCSC is 163Hz, which is 2.7 times the fundamental frequency. The test system is described in [1].

The TCSC can operate in capacitive or inductive mode, although the latter is rarely used in practice. Since the resonance for this TCSC is around 58deg firing angle, the operation is prohibited in firing

angle range 49deg - 69deg. Note that the resonance for the overall system (when the line impedance is included) is around 67deg. The capacitive mode is achieved with firing angles 69-90deg. The impedance is lowest at 90deg, and therefore power transfer increases as the firing angle is reduced. In capacitive mode the range for impedance values is approximately 120-136 Ohm. This range corresponds to approximately 490-830MW power transfer range (100%-110% compensation). Comparing with the power transfer of 110 MW with an uncompensated line, TCSC enables significant improvement in power transfer level.

To change the operating mode (inductive/capacitive/manual) use the toggle switch in the control block dialog. The inductive mode corresponds to the firing angles 0-49deg, and the lowest impedance is at 0deg. In the inductive operating mode, the range of impedances is 19-60 Ohm, which corresponds to 100-85 MW range of power transfer level. The inductive mode reduces power transfer over the line. A constant firing angle can also be applied and the same limits will apply as above.

TCSC Control

When TCSC operates in the constant impedance mode it uses voltage and current feedback for calculating the TCSC impedance. The reference impedance indirectly determines the power level, although an automatic power control mode could also be introduced.

A separate PI controller is used in each operating mode. The capacitive mode also employs a phase lead compensator. Each controller further includes an adaptive control loop to improve performance over a wide operating range. The controller gain scheduling compensates for the gain changes in the system, caused by the variations in the impedance.

The TCSC is simulated as a controllable voltage source in each phase. The voltage magnitude is the product of equivalent complex impedance and the line current. The expression for the TCSC impedance is given in [1].

Simulation

Run the simulation and observe waveforms on the main variables scope block. The TCSC is in the capacitive impedance control mode and the reference impedance is set to 128 Ohm. For the first 0.5s, the TCSC is bypassed (assuming a circuit breaker), and the power transfer is 110 MW. At 0.5s TCSC begins to regulate the impedance to 128 Ohm and this increases power transfer to 610MW. Note that the TCSC starts with alpha at 90deg to enable lowest switching disturbance on the line.

Dynamic Response

At 2.5s a 5% change in the reference impedance is applied. The response indicates that TCSC enables tracking of the reference impedance and the settling time is around 500ms. At 3.3s a 4% reduction in the source voltage is applied, followed by the return to 1p.u. at 3.8s. It is seen that the TCSC controller compensates for these disturbances and the TCSC impedance stays constant. The TCSC response time is 200ms-300ms. Note that the shape of transient response is inaccurate with phasor models and the thyristor based model should be used for studying transients.

Reference

[1] D.Jovcic, G.N.Pillai "Analytical Modelling of TCSC Dynamics" IEEE® Transactions on Power Delivery, vol 20, Issue 2, April 2005, pp. 1097-1104

TCSC (Detailed Model)

This example shows the Thyristor Controlled Series Capacitor (TCSC) test system.

Dragan Jovcic (University of Aberdeen, Scotland, UK)
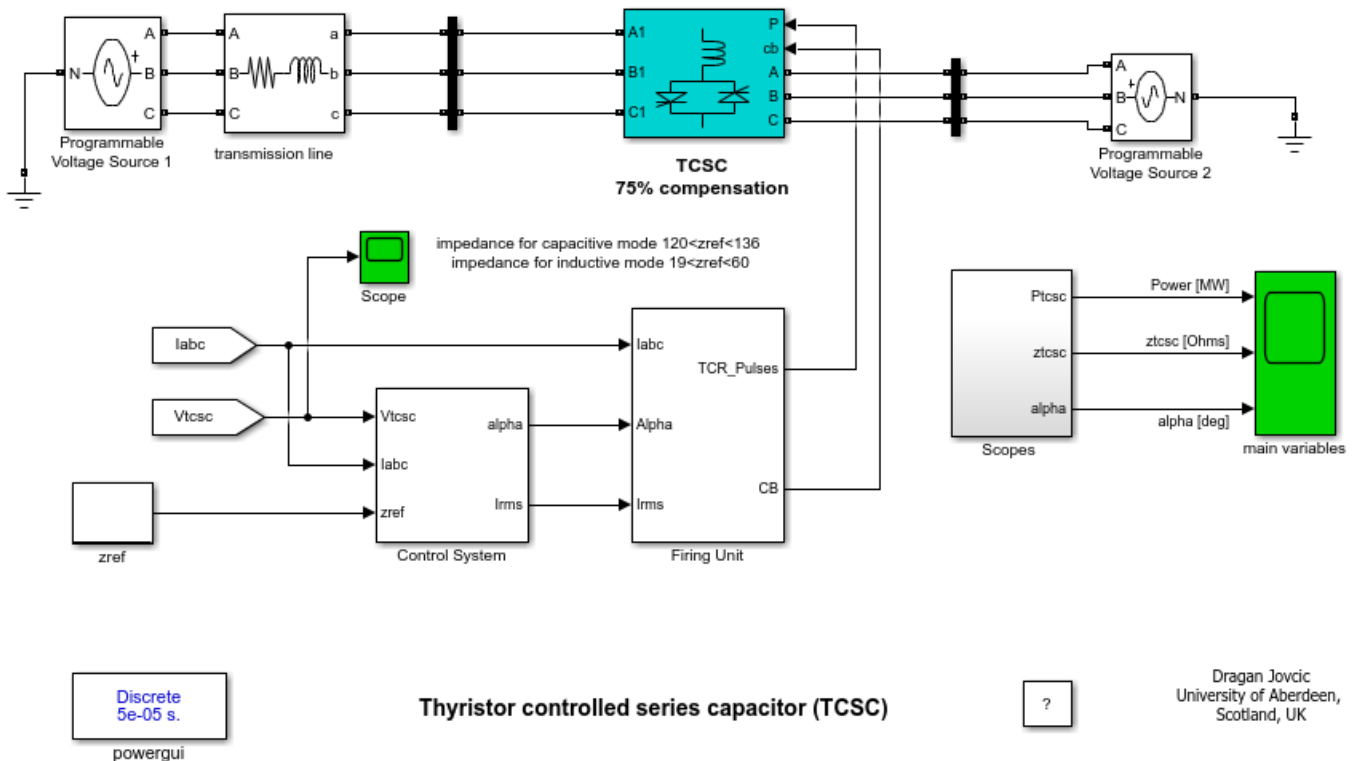
Description

A TCSC is placed on a 500kV, long transmission line, to improve power transfer. Without the TCSC the power transfer is around 110MW, as seen during the first 0.5s of the simulation when the TCSC is bypassed. The TCSC consists of a fixed capacitor and a parallel Thyristor Controlled Reactor (TCR) in each phase. The nominal compensation is 75%, i.e. using only the capacitors (firing angle of 90deg). The natural oscillatory frequency of the TCSC is 163Hz, which is 2.7 times the fundamental frequency. The test system is described in [1].

The TCSC can operate in capacitive or inductive mode, although the latter is rarely used in practice. Since the resonance for this TCSC is around 58deg firing angle, the operation is prohibited in firing angle range 49deg - 69deg. Note that the resonance for the overall system (when the line impedance is included) is around 67deg. The capacitive mode is achieved with firing angles 69-90deg. The impedance is lowest at 90deg, and therefore power transfer increases as the firing angle is reduced. In capacitive mode the range for impedance values is approximately 120-136 Ohm. This range corresponds to approximately 490-830MW power transfer range (100%-110% compensation). Comparing with the power transfer of 110 MW with an uncompensated line, TCSC enables significant improvement in power transfer level.

To change the operating mode (inductive/capacitive/manual) use the toggle switch in the control block dialog. The inductive mode corresponds to the firing angles 0-49deg, and the lowest impedance

is at 0deg. In the inductive operating mode, the range of impedances is 19-60 Ohm, which corresponds to 100-85 MW range of power transfer level. The inductive mode reduces power transfer over the line. A constant firing angle can also be applied and the same limits will apply as above.

TCSC Control

When TCSC operates in the constant impedance mode it uses voltage and current feedback for calculating the TCSC impedance. The reference impedance indirectly determines the power level, although an automatic power control mode could also be introduced.

A separate PI controller is used in each operating mode. The capacitive mode also employs a phase lead compensator. Each controller further includes an adaptive control loop to improve performance over a wide operating range. The controller gain scheduling compensates for the gain changes in the system, caused by the variations in the impedance.

The firing circuit uses three single-phase PLL units for synchronisation with the line current. Line current is used for synchronisation, rather than line voltage, since the TCSC voltage can vary widely during the operation.

Simulation

Run the simulation and observe waveforms on the main variables scope block. The TCSC is in the capacitive impedance control mode and the reference impedance is set to 128 Ohm. For the first 0.5s, the TCSC is bypassed using the circuit breaker, and the power transfer is 110 MW. At 0.5s TCSC begins to regulate the impedance to 128 Ohm and this increases power transfer to 610MW. Note that the TCSC starts with alpha at 90deg to enable lowest switching disturbance on the line.

Dynamic Response

At 2.5s a 5% change in the reference impedance is applied. The response indicates that TCSC enables tracking of the reference impedance and the settling time is around 500ms. At 3.3s a 4% reduction in the source voltage is applied, followed by the return to 1p.u. at 3.8s. It is seen that the TCSC controller compensates for these disturbances and the TCSC impedance stays constant. The TCSC response time is 200ms-300ms.

Note: Using the Simulink® Accelerator™ can speed up the execution of this model by approximately 9 times.

Reference

[1] D.Jovcic, G.N.Pillai "Analytical Modelling of TCSC Dynamics" IEEE® Transactions on Power Delivery, vol 20, Issue 2, April 2005, pp. 1097-1104

SVC and PSS (Phasor Model)

This example shows the use of the phasor solution for transient stability analysis of multi-machine systems. It analyzes transient stability of a two-machine transmission system with Power System Stabilizers (PSS) and Static Var Compensator (SVC).

Gilbert Sybille (Hydro-Quebec)
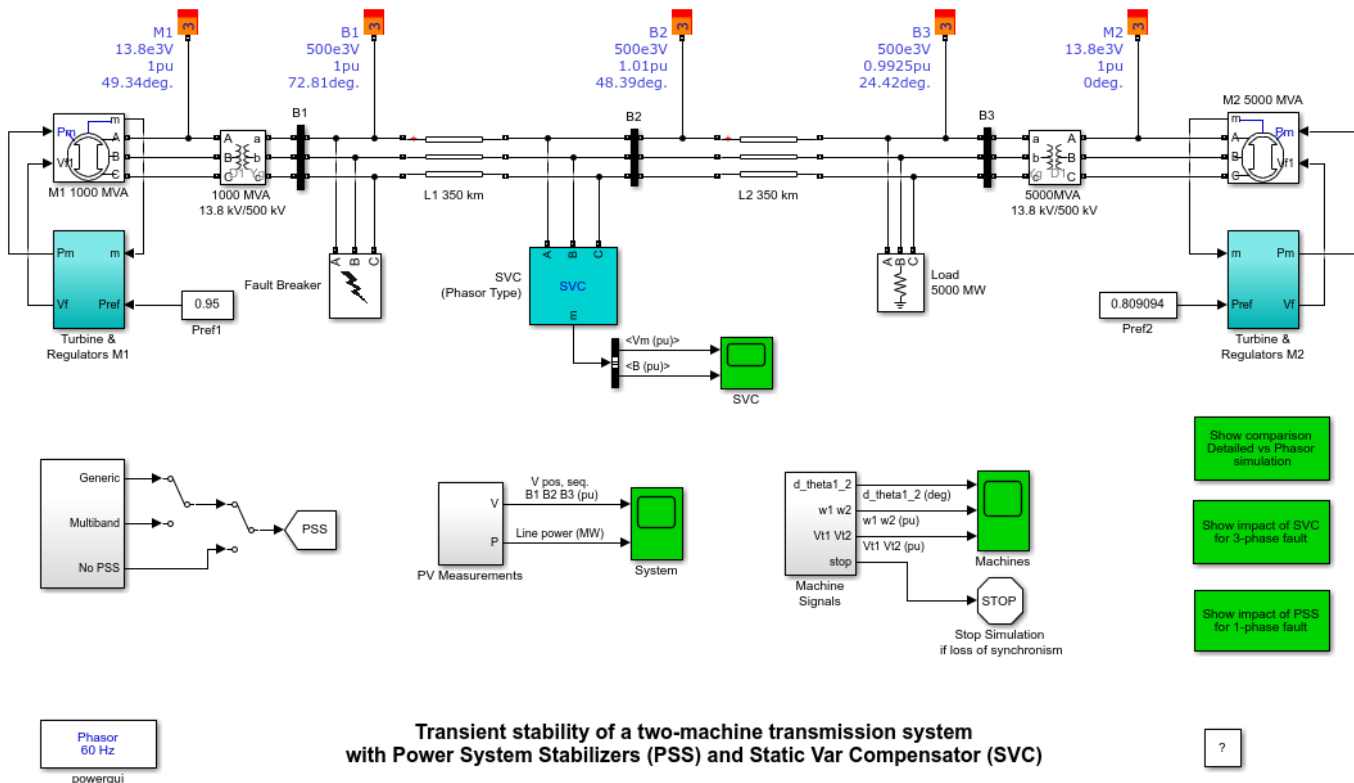
Description

A 1000 MW hydraulic generation plant (machine M1) is connected to a load center through a long 500 kV, 700 km transmission line. The load center is modeled by a 5000 MW resistive load . The load is fed by the remote 1000 MW plant and a local generation of 5000 MW (machine M2). The system has been initialized so that the line carries 950 MW which is close to its surge impedance loading (SIL = 977 MW). In order to maintain system stability after faults, the transmission line is shunt compensated at its center by a 200-Mvar Static Var Compensator (SVC). Notice that this SVC model is a phasor model valid only for transient stability solution. The SVC does not have a Power Oscillation Damping (POD) unit. The two machines are equipped with a Hydraulic Turbine and Governor (HTG), Excitation system and Power System Stabilizer (PSS). These blocks are located in the two 'Turbine and Regulator' subsystems.Two types of stabilizers can be selected : a generic model using the acceleration power (Pa= difference between mechanical power Pm and output electrical power Peo) and a Multi-band stabilizer using the speed deviation (dw). The stabilizer type can be selected by specifying a value (0=No PSS 1=Pa PSS or 2= dw MB PSS) in the PSS constant block.

In this example we apply faults on the 500 kV system and observe the impact of the PSS and SVC on system stability.

Simulation

Note: Before starting the example, open the Powergui block and notice that 'Phasor simulation' has been checked. The phasor solution is much faster than the 'standard' detailed solution. In this solution method, the network differential equations are replaced by a set of algebraic equations at a fixed frequency, thus reducing dramatically the simulation time. This allows transient stability studies of multi-machine systems, as illustrated below.

1. Initialization

Note that the system has already been initialized to start in steady-state. If you are familiar with the Load Flow procedure you can skip this item and proceed to step 2.

Open the mask of the M1 1000 MVA and M2 5000 MVA Synchronous Machine blocks:

In the Load Flow tab of machine M1, the 'Generator type' parameter is set to 'PV', indicating that the load flow will be performed with the machine controlling its active power and its terminal voltage. The 'Active power generation' parameter is set to 950e6 W and the terminal voltage is defined by the Load Flow Bus block labeled M1 and connected to the machine terminals.

The 'Generator type' parameter of machine M2 is set to 'swing', indicating that the machine will be used as a swing bus for balancing the power.

In the Powergui menu, select 'Load Flow'. A new window appears. A summary of the load flow settings is displayed in a table. Press the 'Compute' button to solve the load flow. The table now display the actual machine active and reactive powers.

Press the 'Apply' button to apply the load flow solution to the model.

Look in the hydraulic turbine and governor (HTG) and Excitation system contained in the two Regulator subsystems to note that the initial mechanical power and field voltage have been automatically initialized by the Load Flow. The reference mechanical powers and reference voltages for the two machines have also been updated in the two constant blocks connected at the HTG and excitation system inputs: Pref1=0.95 pu (950 MW), Vref1=1pu; Pref2=0.8091 pu (4046 MW), Vref2=1 pu.

2. Single-phase fault - Impact of PSS - No SVC

Open the SVC dialog box and notice that the SVC is set to operate in 'Var control (fixed susceptance)' mode with Bref = 0. Setting Bref to zero is equivalent to putting the SVC out of service. Verify also that the two PSS (Pa type) are in service (value=1 in the PSS constant block) Start the simulation and observe signals on the 'Machines' scope. For this type of fault the system is stable without SVC. After fault clearing, the 0.8 Hz oscillation is quickly damped. This oscillation mode is typical of inter-area oscillations in a large power system. First trace on the 'Machine' scope shows the rotor angle difference d_theta1_2 between the two machines. Power transfer is maximum when this angle reaches 90 degrees. This signal is a good indication of system stability. If d_theta1_2 exceeds 90 degrees for a too long period of time, the machines will loose synchronism and the system goes unstable. Second trace shows the machine speeds. Notice that machine 1 speed increases during the fault because during that period its electrical power is lower than its mechanical power. By simulating over a long period of time (50 seconds) you will also notice that the machine speeds oscillate together at a low frequency (0.025 Hz) after fault clearing. The two PSS (Pa type) succeed to damp the 0.8 Hz mode but they are not efficient for damping the 0.025 Hz mode. If you select instead the Multi-Band PSS (value=2 in the PSS constant block) you will notice that this stabilizer type succeeds to damp both the 0.8 Hz mode and the 0.025 Hz mode.

You will now repeat the test with the two PSS out of service (value=0 in the PSS constant block). Restart simulation. Notice that the system is unstable without PSS. You can compare results with and without PSS by double clicking on the 2nd blue block on the right side. You can also compare the results obtained with the two solution methods 'Detailed' and 'Phasor' by double-clicking on the first blue block on the right side.

Note: This system is naturally unstable without PSS, even for small disturbances. For example, if you remove the fault (by deselecting phase A in the Fault Breaker) and apply a Pref step of 0.05 pu on machine 1, you will see the instability slowly building up after a few seconds.

3. Three-phase fault - Impact of SVC - two PSS in service

You will now apply a 3-phase fault and observe the impact of the SVC for stabilizing the network during a severe contingency. Put the two PSS (Pa type) in service (value=1 in the PSS constant block. Reprogram the 'Fault Breaker' block in order to apply a 3-phase-to-ground fault. Verify that the SVC is in fixed susceptance mode with Bref = 0. Start the simulation. By looking at the d_theta1_2 signal, you should observe that the two machines quickly fall out of synchronism after fault clearing. In order not to pursue unnecessary simulation, the Simulink® 'Stop' block is used to stop the simulation when the angle difference reaches 3*360degrees.

Now open the SVC block menu and change the SVC mode of operation to 'Voltage regulation'. The SVC will now try to support the voltage by injecting reactive power on the line when the voltage is lower than the reference voltage (1.009 pu). The chosen SVC reference voltage corresponds to the bus voltage with the SVC out of service. In steady state the SVC will therefore be 'floating' and waiting for voltage compensation when voltage departs from its reference set point.

Restart simulation and observe that the system is now stable with a 3-phase fault. You can compare results with and without SVC by double clicking on the 3rd blue block on the right side.

Reference

[1] D.Jovcic, G.N.Pillai "Analytical Modeling of TCSC Dynamics" IEEE® Transactions on Power Delivery, vol 20, Issue 2, April 2005, pp. 1097-1104

UPFC (Phasor Model)

This example shows an Unified Power Flow Controller (UPFC) used to relieve power congestion on a 500/230 kV grid.

Gibert Sybille and Pierre Giroux (Hydro-Quebec)
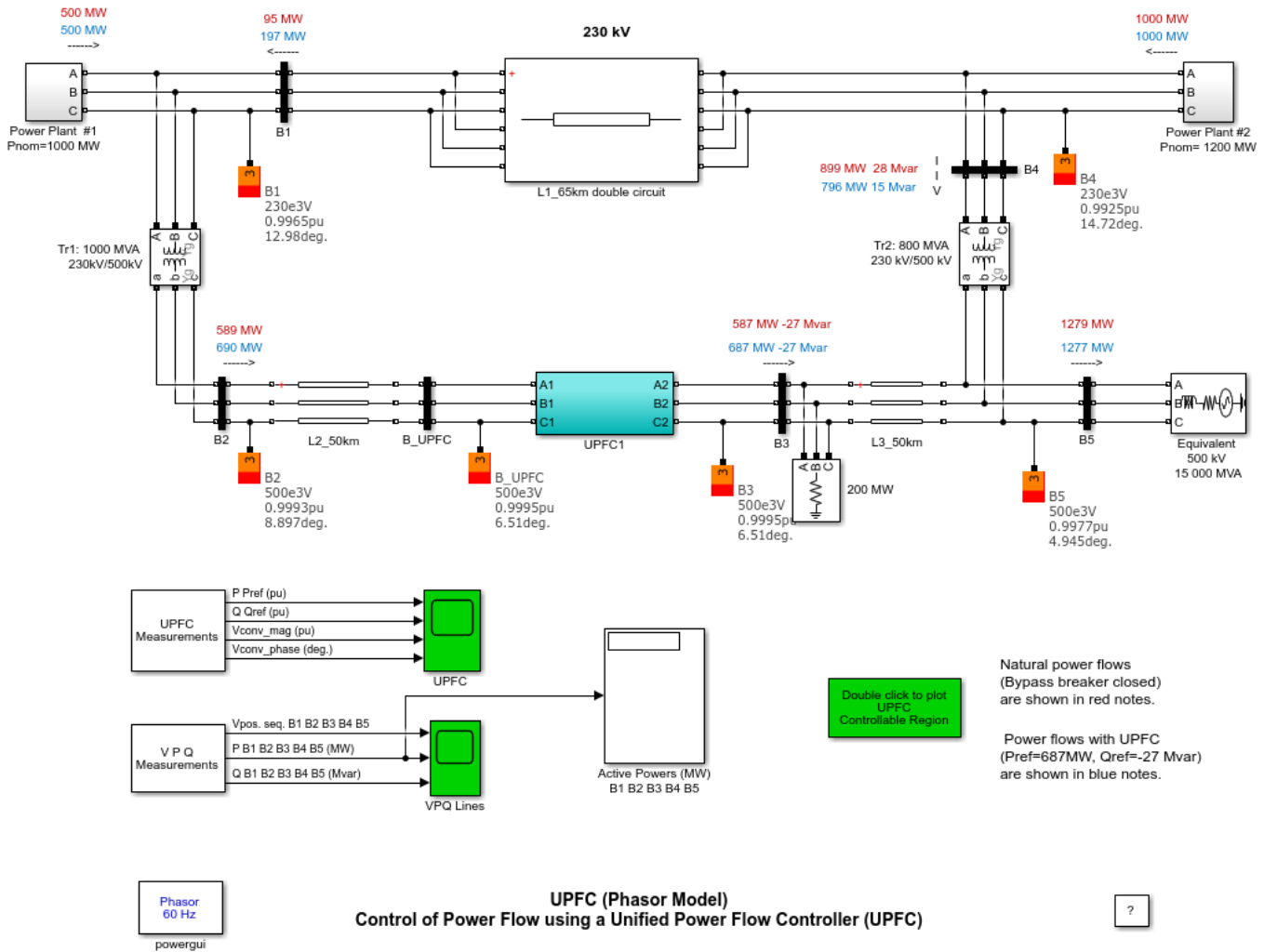
Description

A UPFC is used to control the power flow in a 500 kV /230 kV transmission system. The system, connected in a loop configuration, consists essentially of five buses (B1 to B5) interconnected through transmission lines (L1, L2, L3) and two 500 kV/230 kV transformer banks Tr1 and Tr2. Two power plants located on the 230-kV system generate a total of 1500 MW which is transmitted to a 500-kV 15000-MVA equivalent and to a 200-MW load connected at bus B3. The plant models include a speed regulator, an excitation system as well as a power system stabilizer (PSS). In normal operation, most of the 1200-MW generation capacity of power plant #2 is exported to the 500-kV equivalent through three 400-MVA transformers connected between buses B4 and B5. We are considering a contingency case where only two transformers out of three are available (Tr2= 2*400 MVA = 800 MVA).

Using the load flow option of the powergui block, the model has been initialized with plants #1 and #2 generating respectively 500 MW and 1000 MW and the UPFC out of service (Bypass breaker closed). The resulting power flow obtained at buses B1 to B5 is indicated by red numbers on the circuit diagram. The load flow shows that most of the power generated by plant #2 is transmitted through the 800-MVA transformer bank (899 MW out of 1000 MW), the rest (101 MW), circulating in the loop. Transformer Tr2 is therefore overloaded by 99 MVA. The example illustrates how the UPFC can relieve this power congestion.

The UPFC located at the right end of line L2 is used to control the active and reactive powers at the 500-kV bus B3, as well as the voltage at bus B_UPFC. It consists of a phasor model of two 100-MVA, IGBT-based, converters (one connected in shunt and one connected in series and both interconnected through a DC bus on the DC side and to the AC power system, through coupling reactors and transformers). Parameters of the UPFC power components are given in the dialog box. The series converter can inject a maximum of 10% of nominal line-to-ground voltage (28.87 kV) in series with line L2. The blue numbers on the diagram show the power flow with the UPFC in service and controlling the B3 active and reactive powers respectively at 687 MW and -27 Mvar.

Simulation

The UPFC reference active and reactive powers are set in the blocks labeled "Pref(pu)" and "Qref(pu)". Initially the Bypass breaker is closed and the resulting natural power flow at bus B3 is 587 MW and -27 Mvar. The Pref block is programmed with an initial active power of 5.87 pu corresponding to the natural flow. Then, at t=10s, Pref is increased by 1 pu (100 MW), from 5.87 pu to 6.87 pu, while Qref is kept constant at -0.27 pu.

Run the simulation and look on the UPFC Scope how P and Q measured at bus B3 follow the reference values. At t=5 s, when the Bypass breaker is opened the natural power is diverted from the Bypass breaker to the UPFC series branch without noticeable transient. At t=10 s, the power increases at a rate of 1 pu/s. It takes one second for the power to increase to 687 MW. This 100 MW increase of active power at bus B3 is achieved by injecting a series voltage of 0.089 pu with an angle of 94 degrees. This results in an approximate 100 MW decrease in the active power flowing through Tr2 (from 899 MW to 796 MW), which now carries an acceptable load. See the variations of active powers at buses B1 to B5 on the VPQ Lines Scope.

UPFC (Detailed Model)

This example shows a detailed model of a 48-Pulse, GTO-based unified power flow controller (500 kV, 100 MVA).

Pierre Giroux ; Gilbert Sybille (Hydro-Quebec, IREQ)
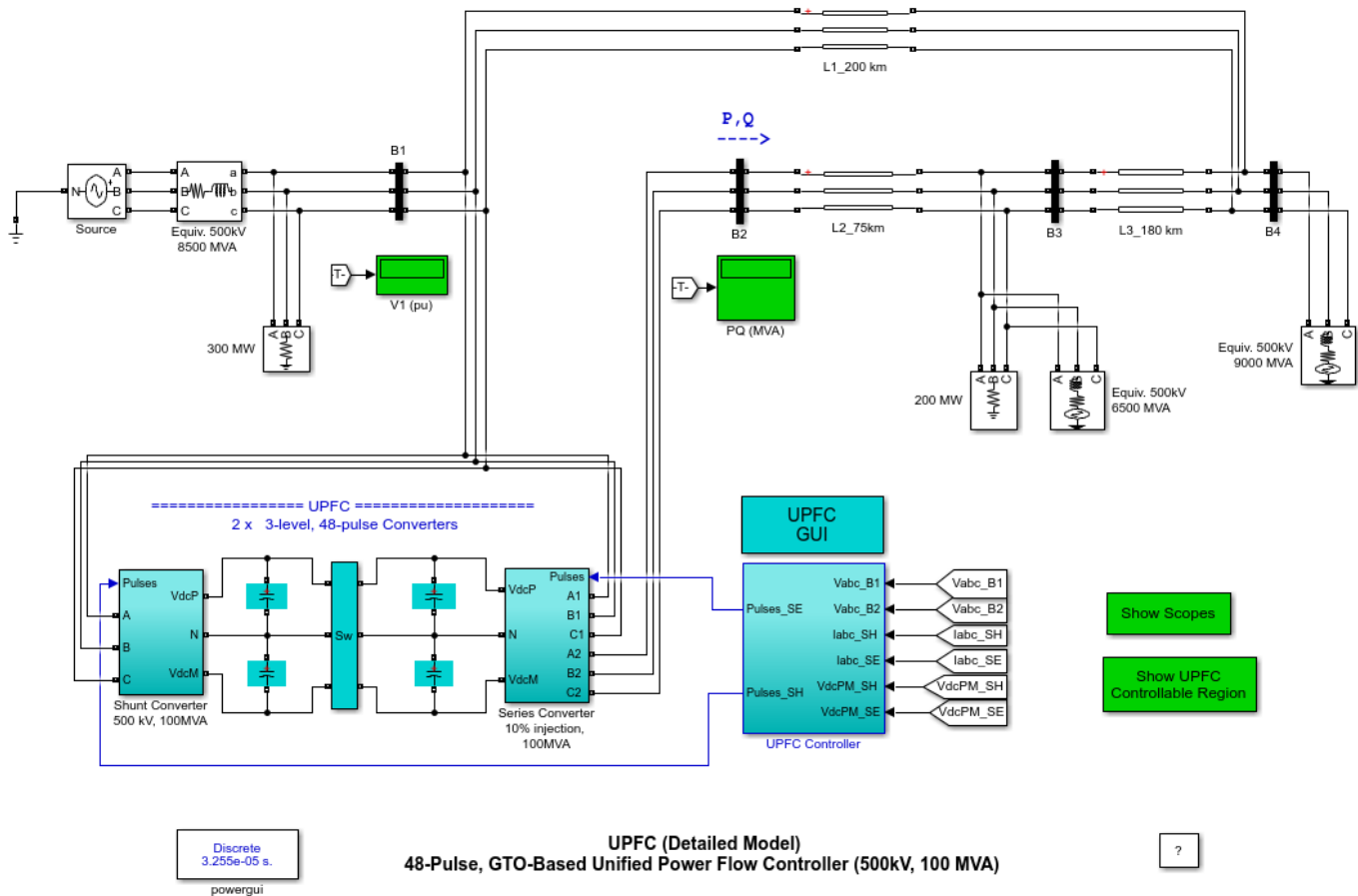
Description

A Unified Power Flow Controller (UPFC) is used to control the power flow in a 500 kV transmission system. The UPFC located at the left end of the 75-km line L2, between the 500 kV buses B1 and B2, is used to control the active and reactive powers flowing through bus B2 while controlling voltage at bus B1. It consists of two 100-MVA, three-level, 48-pulse GTO-based converters, one connected in shunt at bus B1 and one connected in series between buses B1 and B2. The shunt and series converters can exchange power through a DC bus. The series converter can inject a maximum of 10% of nominal line-to-ground voltage (28.87 kV) in series with line L2.

This pair of converters can be operated in three modes:

• **Unified Power Flow Controller (UPFC)** mode, when the shunt and series converters are interconnected through the DC bus. When the disconnect switches between the DC buses of the shunt and series converter are opened, two additional modes are available:

- Shunt converter operating as a **Static Synchronous Compensator (STATCOM)** controlling voltage at bus B1
- Series converter operating as a **Static Synchronous Series Capacitor (SSSC)** controlling injected voltage, while keeping injected voltage in quadrature with current.

The mode of operation as well as the reference voltage and reference power values can be changed by means of the "UPFC GUI" block.

The principle of operation of the harmonic neutralized converters is explained in another example entitled "Three-phase 48-pulse GTO converter". This power_48pulsegtoconverter model is accessible in the Power Electronics Models library of examples. When the two converters are operated in UPFC mode, the shunt converter operates as a STATCOM. It controls the bus B1 voltage by controlling the absorbed or generated reactive power while also allowing active power transfer to the series converter through the DC bus. The reactive power variation is obtained by varying the DC bus voltage. The four three-level shunt converters operate at a constant conduction angle (Sigma= 180-7.5 = 172.5 degrees), thus generating a quasi-sinusoidal 48-step voltage waveform. The first significant harmonics are the 47th and the 49th.

When operating in UPFC mode, the magnitude of the series injected voltage is varied by varying the Sigma conduction angle, therefore generating higher harmonic contents than the shunt converter. As illustrated in this example, when the series converter operates in SSSC mode it generates a "true" 48-pulse waveform.

The natural power flow through bus B2 when zero voltage is generated by the series converter (zero voltage on converter side of the four converter transformers) is P=+870 MW and Q=-70 Mvar. In UPFC mode, both the magnitude and phase angle and the series injected voltage can be varied, thus allowing control of P and Q. The UPFC controllable region is obtained by keeping the injected voltage to its maximum value (0.1 pu) and varying its phase angle from zero to 360 degrees. To see the resulting P-Q trajectory, double click the "Show UPFC Controllable Region". Any point located inside the PQ elliptic region can be obtained in UPFC mode.

Simulation

1. Power control in UPFC mode

Open the UPFC GUI block menu. The GUI allows you to choose the operation mode (UPFC, STATCOM or SSSC) as well as the Pref/Qref reference powers and/or Vref reference voltage settings . Also, in order to observe the dynamic response of the control system, the GUI allows you to specify a step change of any reference value at a specific time.

Make sure that the operation mode is set to "UPFC (Power Flow Control)". The reference active and reactive powers are specified in the last two lines of the GUI menu. Initially, Pref= +8.7 pu/100MVA (+870 MW) and Qref=-0.6 pu/100MVA (-60 Mvar). At t=0.25 sec Pref is changed to +10 pu (+1000MW). Then, at t=0.5 sec, Qref is changed to +0.7 pu (+70 Mvar). The reference voltage of the shunt converter (specified in the 2nd line of the GUI) will be kept constant at Vref=1 pu during the whole simulation (Step Time=0.3*100> Simulation stop time (0.8 sec). When the UPFC is in power control mode, the changes in STATCOM reference reactive power and in SSSC injected voltage (specified respectively in 1st and 3rd line of the GUI) as are not used.

Run the simulation for 0.8 sec. Open the "Show Scopes" subsystem. Observe on traces 1 and 2 of the UPFC scope the variations of P and Q. After a transient period lasting approximately 0.15 sec, the steady state is reached (P=+8.7 pu; Q=-0.6 pu). Then P and Q are ramped to the new settings (P= +10 pu Q=+0.7 pu). Observe on traces 3 and 4 the resulting changes in P Q on the three

transmission lines. The performance of the shunt and series converters can be observed respectively on the STATCOM and SSSC scopes. If you zoom on the first trace of the STATCOM scope, you can observe the 48-step voltage waveform Vs generated on the secondary side of the shunt converter transformers (yellow trace) superimposed with the primary voltage Vp (magenta) and the primary current Ip (cyan). The dc bus voltage (trace 2) varies in the 19kV-21kV range. If you zoom on the first trace of the SSSC scope, you can observe the injected voltage waveforms Vinj measured between buses B1 and B2.

2. Var control in STATCOM mode

In the GUI block menu, change the operation mode to "STATCOM (Var Control)". Make sure that the STATCOM references values (1st line of parameters, [T1 T2 Q1 Q2]) are set to [0.3 0.5 +0.8 -0.8]. In this mode, the STATCOM is operated as a variable source of reactive power. Initially, Q is set to zero, then at T1=0.3 sec Q is increased to +0.8 pu (STATCOM absorbing reactive power) and at T2=0.5 sec, Q is reversed to -0.8 pu (STATCOM generating reactive power).

Run the simulation and observe on the STATCOM scope the dynamic response of the STATCOM. Zoom on the first trace around t=0.5 sec when Q is changed from +0.8 pu to -0.8 pu. When Q=+0.8 pu, the current flowing into the STATCOM (cyan trace) is lagging voltage (magenta trace), indicating that STATCOM is absorbing reactive power. When Qref is changed from +0.8 to -0.8, the current phase shift with respect to voltage changes from 90 degrees lagging to 90 degrees leading within one cycle. This control of reactive power is obtained by varying the magnitude of the secondary voltage Vs generated by the shunt converter while keeping it in phase with the bus B1 voltage Vp. This change of Vs magnitude is performed by controlling the dc bus voltage. When Q is changing from +0.8 pu to -0.8 pu, Vdc (trace 3) increases from 17.5 kV to 21 kV.

3. Series voltage injection in SSSC mode

In the GUI block menu change the operation mode to "SSSC (Voltage injection)". Make sure that the SSSC references values (3rd line of parameters) [Vinj_Initial Vinj_Final StepTime]) are set to [0.0 0.08 0.3]. The initial voltage is set to 0 pu, then at t=0.3 sec it will be ramped to 0.8 pu.

Run the simulation and observe on the SSSC scope the impact of injected voltage on P and Q flowing in the 3 transmission lines. Contrary to the UPFC mode, in SSCC mode the series inverter operates with a constant conduction angle (Sigma= 172.5 degrees). The magnitude of the injected voltage is controlled by varying the dc voltage which is proportional to Vinj (3rd trace). Also, observe the waveforms of injected voltages (1st trace) and currents flowing through the SSSC (2nd trace). Voltages and currents stay in quadrature so that the SSSC operates as a variable inductance or capacitance.

OLTC Phase Shifting Transformer (Phasor Model)

This example shows the operation of two models of delta-hexagonal Phase Shifting Transformer (PST) using On Load Tap Changers (OLTC).

Gilbert Sybille (Hydro-Quebec)

Phasor Simulation of Delta-Hexagonal Phase Shifting Transformers
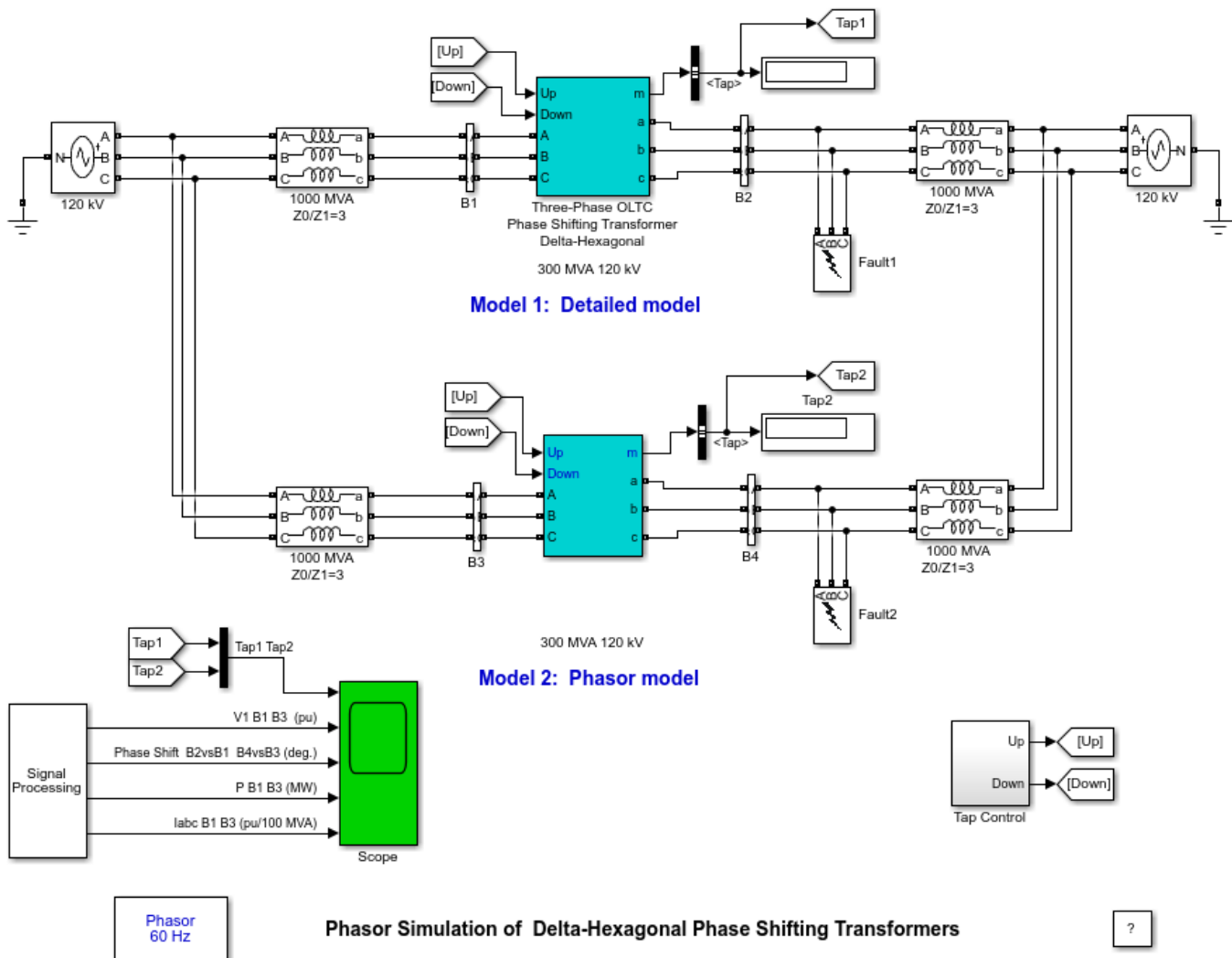
Description

Two 120 kV 1000 MVA networks are interconnected through a phase shifting transformer (PST). The phase shift can be varied on load by means of On Load Tap Changers (OLTC).

Simulation

Open loop control of power transfer

In order to observe impact of phase shift on power transfer, the phase shift is increased from zero to 32.2 degrees lagging (tap +5), then phase shift is reduced to zero and increased again up to 32.2

degrees leading. This is performed by sending 5 pulses to the "Up" input, and then, 10 pulses to the "Down" input ". As the tap selection is a relatively slow mechanical process (3 sec per tap as specified in the "Tap selection time" parameter of the block menus), the simulation Stop time is set to 50 s.

Start simulation and observe PST operation on the Scope. Results obtained with the two models are superimposed on five traces.

- Trace 1 shows the tap position.
- Trace 2 shows a superposition of positive-sequence voltages measured at bus B1 (yellow) and bus B3 (magenta).
- Trace 3 shows the phase shifts of positive-sequence voltages measured at output terminals (abc) with respect to input terminals (ABC).
- Trace 4 compares the active power measured at bus B1 (yellow) and bus B3 (magenta).
- Trace 5 compares the phase currents at bus B1 (yellow) and bus B3 (magenta).

When simulation starts the OLTCs are at position 0 (zero phase shift). As the two networks are symmetrical with both internal angles set at 0 degree, there is no current flowing. Then, phase shift is increased and bus B2 (or B4) is lagging bus B1 (or B3). As B2 is lagging the internal voltage of the source located on right side, power flows from right to left. Power measured from left to right is therefore negative for positive tap positions. The maximum power is obtained at tap +5, or -5 when the phase shift is respectively -32.2 degrees and +32.2 degrees. The active power can be computed from P= V1.V2*sin(psi)/ (X1 +X2 + Xpst) , where: V1=V2=internal voltages= 1.0 pu ; X1= X2 = network reactances = 1pu /1000 MVA Xpst= PST leakage reactance at tap 5. The PST leakage reactance varies with tap position (from zero at tap zero to 0.15 pu at maximum tap (10)). The positive-sequence impedance of the phasor model is available as a signal at its measurement output "m". The reactance obtained at tap 5 is Xpst=0.1067 pu/ 300 MVA. The total reactance expressed in pu/100 MVA is X= 0.1 +0.1 + 0.1067/3 =0.2356 pu/100 MVA. The expected active power at tap 5 is P= 1*sin(32.2deg)/0.2356 = 2.26 pu/100 MVA or 226 MW, which corresponds well with the measured value on trace 4 (224 MW). Because of the voltage developed across the PST leakage reactance, the phase shift measured between PST input and output voltages (trace 3) is lower than the expected value. For example, 27.2 degrees is obtained at tap 5, instead of the 32.2 degrees theoretical value computed at no load. The phase shift variation depends on load current.

Initializing the phasor model

For the phasor model to start initialized at t=0, the current sources used in the model must be initialized with current values corresponding to steady state. Suppose that you want to start with the initial tap position 5. First, in the two block menus, "set Initial tap" parameter to 5. Then disconnect the signals connected to the "Up" and "Down" inputs of the two models, so that the taps stay at position 5. If you start simulation you will notice a transient in the phasor model signals at t=0 because the model is not initialized. Use the "Steady-State Voltages and Currents" option of the powergui to obtain the initial current flowing in the detailed model at bus B4. The phase A output current identified "B2/Ia" is 1129.4 A rms, 169 degrees. This current converted to per unit based on PST rating is 1129/1443 = 0.7824 pu/ 300MVA. Specify [0.7824 169] in the "Initial pos. seq. output current" parameter. If you now restart simulation, you should observe no transient at t=0.

Operation under unbalanced conditions

The phasor model is valid for unbalanced conditions. If you check "Phase A Fault" in the two fault breakers, a single phase fault will be applied at t=5s. The currents measured at buses B1 and B3 should be identical. (For example at tap position +5: Ia=3.48 pu, Ib=2.25 pu Ic=2.10 pu).

Simulation with phasor model only

In order to appreciate the gain in simulation speed provided by the phasor model, delete the detailed PST model and replace it with a duplicate of the phasor model. Reconnect control signals to the "Up" and "Down" inputs. Restart simulation. The model runs approximately 5 times faster, mainly because the OLTC switches of the detailed model are not simulated.

OLTC Regulating Transformer (Phasor Model)

This example shows the operation of two models of on load tap changer (OLTC) regulating transformer.

Gilbert Sybille (Hydro-Quebec)
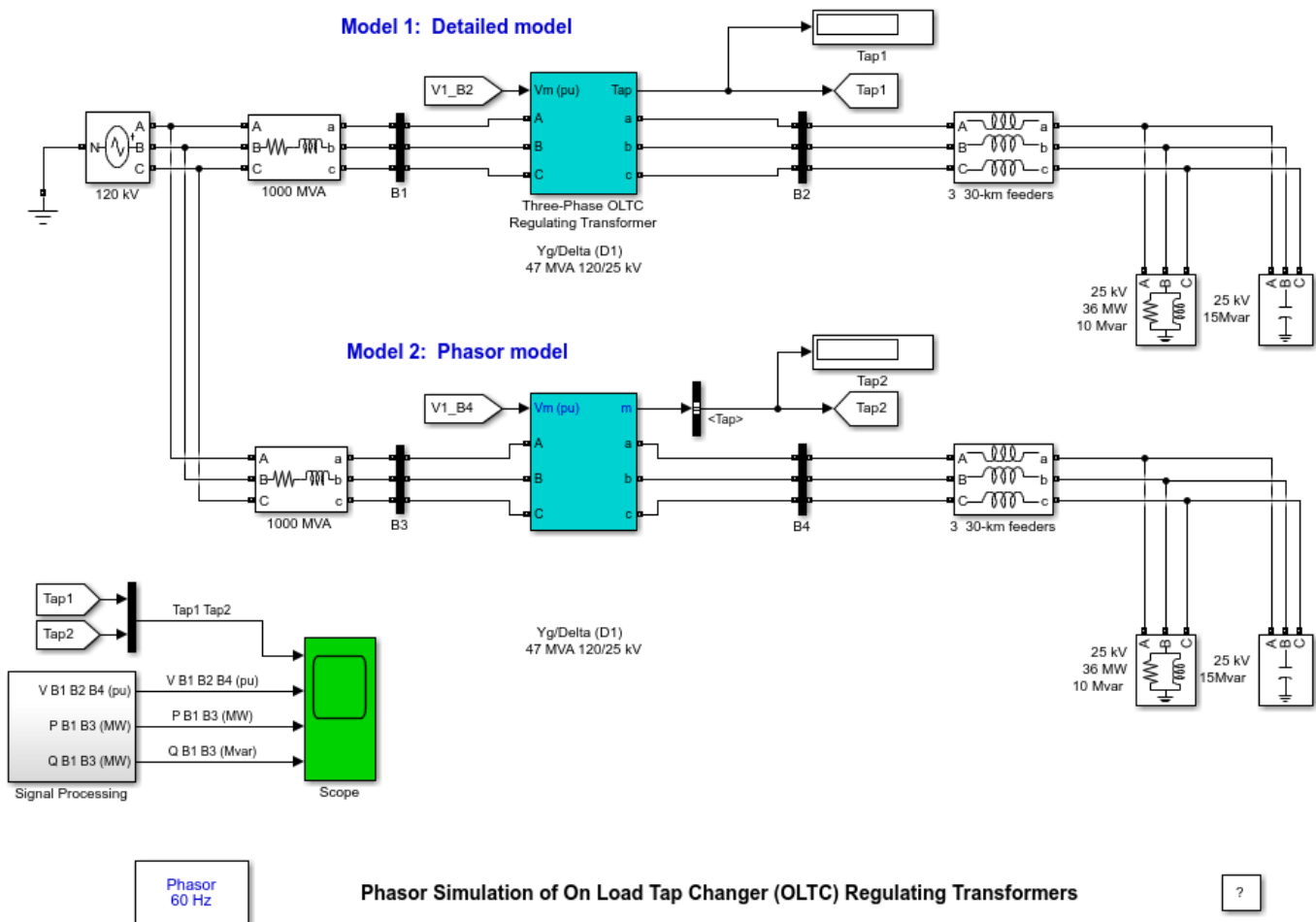
Description

A 25 kV distribution network consisting of three 30-km distribution feeders connected in parallel supplies power to a 36 MW /10 Mvar load (0.964 PF lagging) from a 120 kV, 1000 MVA system and a 120kV/25 kV OLTC regulating transformer. Reactive power compensation is provided at load bus by a 15 Mvar capacitor bank. The same circuit is duplicated in order to compare the performance of two different models of OLTC transformers:

- Model 1 is a detailed model where all OLTC switches and transformer characteristics are represented. This model can be used with either continuous or discrete 'simulation type' modes of Powergui to get detailed wave shapes or with the phasor simulation method to observe variations of phasor voltages and currents. To simulate Model 1 in continuous or discrete you need to delete Model 2 from the example.

- Model 2 is a simplified phasor model where the transformer and OLTC are simulated by current sources. This model can be used only with the phasor 'simulation type' mode of Powergui. It is much faster to execute and it should be the preferred model for transient stability studies, when several such devices are used in the same system.

Both OLTC transformer models implement a three-phase regulating transformer rated 47 MVA, 120 kV/25 kV, Wye/ Delta, with the OLTC connected on the high voltage side (120 kV). The OLTC transformers are used to regulate system voltage at 25 kV buses B2 and B4.

Voltage regulation is performed by varying the transformer turn ratio. This is obtained by connecting on each phase, a tapped winding (regulation winding) in series with each 120/sqrt(3) kV winding. Nine (9) OLTC switches allow selection of 8 different taps (tap positions 1 to 8, plus tap 0 which provides nominal 120kV/25 kV ratio). A reversing switch included in the OLTC allows reversing connections of the regulation winding so that it is connected either additive (positive tap positions) or subtractive (negative tap positions). For a fixed 25 kV secondary voltage, each tap provides a voltage correction of +/-0.01875 pu or +/-1.875% of nominal 120 kV voltage. Therefore, a total of 17 tap positions, including tap 0, allow a voltage variation from 0.85 pu (102 kV) to 1.15 pu (138 kV) by steps of 0.01875 pu (2.25 kV).

The positive-sequence voltages measured at buses B2 and B4 are provided as inputs to the voltage regulators (input 'Vmeas' of the transformer blocks). Open the two transformer block menus and look at their parameters. The voltage regulators are in service ('Voltage regulator' parameter = 'on'). The reference voltage is set to 1.04 pu. In order to start simulation with 25-KV voltages close to 1.04 pu at buses B2 and B4, the initial tap positions are set at -4, so that the transformers are boosting the voltage by a factor 1/(1-4*0.01875)=1.081.

The detailed model is built with a fixed number of taps (8). Note that the phasor model provides more flexibility as it allows selection of primary and secondary winding connections (wye or Delta) as well as changing the number of taps and using the OLTC either on primary or secondary side.

Look under mask to see how the transformer models are built. The detailed model is built from three Multi-Winding Transformer blocks and three OLTC subsystems which contain switches performing tap selection and reversal of the regulation winding. The tap transition is performed by temporarily short-circuiting two adjacent transformer taps through resistors (5 ohm resistances and 60 ms transition time as specified in the block menu). The phasor model is built with current sources emulating the transformer impedance which depends on winding resistances, leakage reactances and tap position. Both models use a voltage regulator that generates pulses at the 'Up' or 'Down' outputs and orders a tap change either in the positive or negative direction. The voltage regulation depends on the specified dead band (DB = two times the voltage step or 0.0375 pu). This means that the maximum voltage error at buses B2 and B4 should be 0.01875 pu. As long as the maximum tap number is not reached (-8 or +8), voltage should stay in the range : (Vref-DB/2< V<1.04+DB/2) = (1.021< V< 1.059).

Simulation

As tap selection is a relatively slow mechanical process (4 sec per tap as specified in the 'Tap selection time' parameter of the block menus), the simulation Stop time is set to 2 minutes (120 s). The Three-Phase Programmable Voltage Source is used to vary the 120 kV system voltage in order to observe the OLTC performance. Initially, the source is generating its nominal voltage. Then, voltage is successively decreased (0.95 pu at t = 10 s) and increased (1.10 pu at t = 50 s).

Start the simulation and observe OLTC operation on the Scope.

- Trace 1 shows the tap position.
- Trace 2 shows a superposition of positive-sequence voltages at 120 kV bus B1 (yellow), at 25 kV bus B2 (magenta) and bus B4 (cyan).
- Traces 3 and 4 show the active and reactive powers measured on 120 kV side (buses B1 and B3).

When simulation starts the OLTCs are at position -4 and the resulting voltage at bus B2 and B4 is 1.038 pu. At t=10 s, the source internal voltage is suddenly lowered to 0.95 pu , so that the 25 kV voltages drop to 0.986 pu, outside of the permitted voltage range (1.021< V<1.059). The voltage regulator then orders further voltage boosting and the OLTC stabilizes at tap=-6 (V=1.025 pu) At t=50 s, the source internal voltage is suddenly increased to 1.10 pu , so that the 25 kV voltages now reach to 1.19 pu. The voltage regulator then starts to decrease voltage by moving taps in the upward direction and the OLTCs stabilize at tap=+1 (V=1.043 pu).

Simulation with phasor model only

In order to appreciate the gain in simulation speed provided by the phasor model, delete the detailed transformer model and replace it with a duplicate of the phasor model. Restart simulation. The model runs approximately 2.5 times faster, mainly because the OLTC switches of the detailed model are not simulated.

Note: The voltage glitches observed with the phasor model when the source voltage is stepped down (t= 10s) and up (t=50 s) can be ignored. They are caused by the first order transfer functions (one cycle time constant) which are used inside the model to break algebraic loops.

Simplified Model of a Small Scale Micro-Grid

This example shows the behavior of a simplified model of a small-scale micro grid during 24 hours on a typical day. The model uses Phasor solution provided by Specialized Power Systems in order to accelerate simulation speed.

Hiroumi Mita (MathWorks)
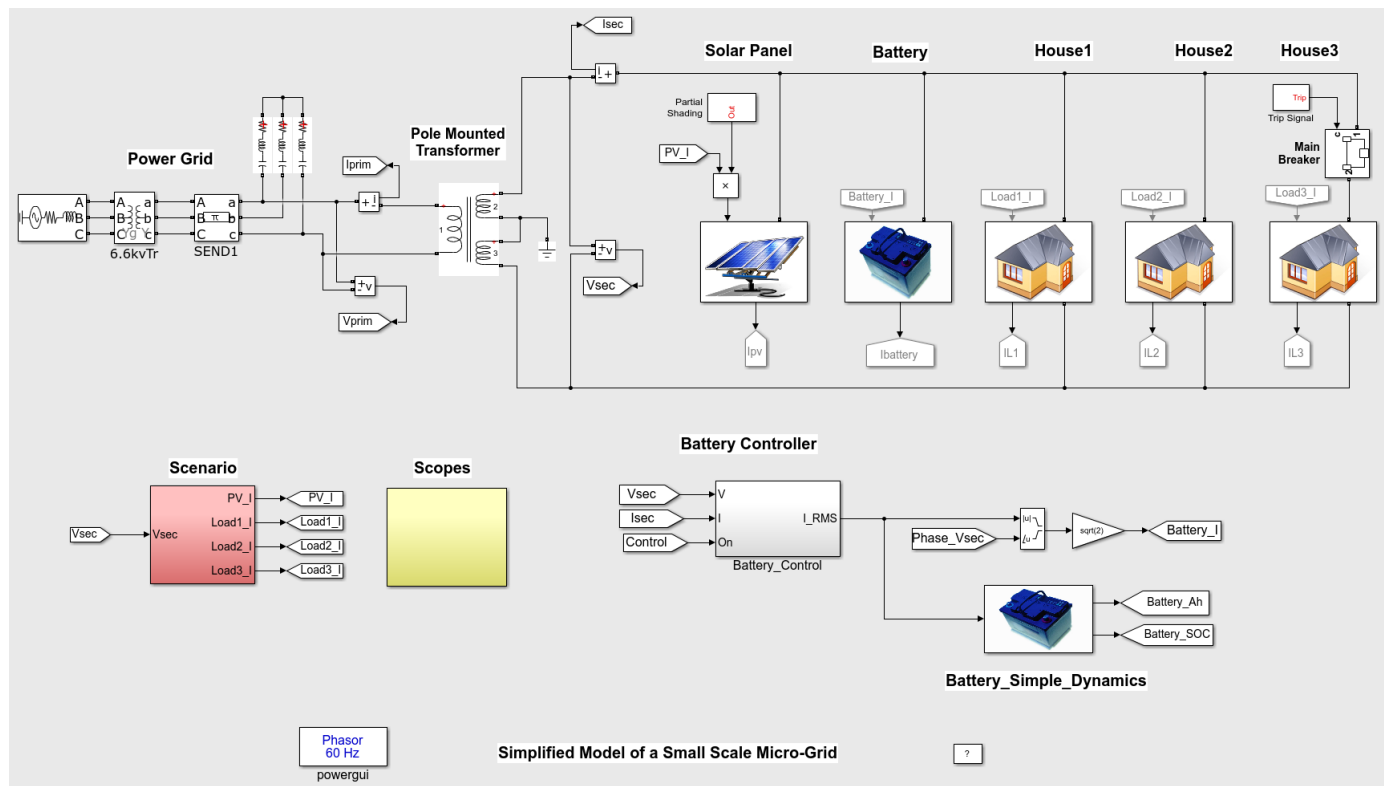
Description

The micro-grid is a single-phase AC network. Energy sources are an electricity network, a solar power generation system and a storage battery.

The storage battery is controlled by a battery controller. It absorbs surplus power when there is excess energy in the micro-network, and provides additional power if there is a power shortage in the micro-network. Three ordinary houses consume energy (maximum of 2.5 kW) as electric charges.

The micro-array is connected to the power network via a transformer mounted on a post which lowers the voltage of 6.6 kV to 200 V.

The solar power generation and storage battery are DC power sources that are converted to single-phase AC. The control strategy assumes that the microarray does not depend entirely on the power supplied by the power grid, and the power supplied by the solar power generation and storage are sufficient at all times.

Simulation

From 20h to 4h, the solar power generation is 0 W. It reaches the peak amount (5 kW) from 14h to 15h.

As a typical load change in ordinary houses, the amount of electric power load reaches peak consumption at 9h (6,500 W), 19h, and 22h (7,500 W).

From 0h to 12h and from 18h to 24h, battery control is performed by battery controller. The battery control performs tracking control of the current so that active power which flows into system power from the secondary side of the pole transformer is set to 0. Then, the active power of secondary side of the pole mounted transformer is always around zero.

The storage battery supplies the insufficient current when the power of the micro-grid is insufficient and absorbs surplus current from the micro-grid when its power is surpasses the electric load.

From 12h to 18h, battery control is not performed. SOC (State Of Charge) of the storage battery is fixed to a constant and does not change since charge or discharge of the storage

battery are not performed by the battery controller. When there is a power shortage in the micro-grid, the system power supplies insufficient power. When there is a surplus power in the micro-grid, surplus power is returned to the system power.

At 8h, electricity load No. 3 of an ordinary house is set to OFF for 10 sec by the breaker. A spike is observed in the active power on the secondary side of the pole transformer and the electric power of the storage battery.

Single-Phase, 240 Vrms, 3500 W Transformerless Grid-Connected PV Array

This example shows the operation of a typical transformerless photovoltaic (PV) residential system connected to the electrical utility grid.
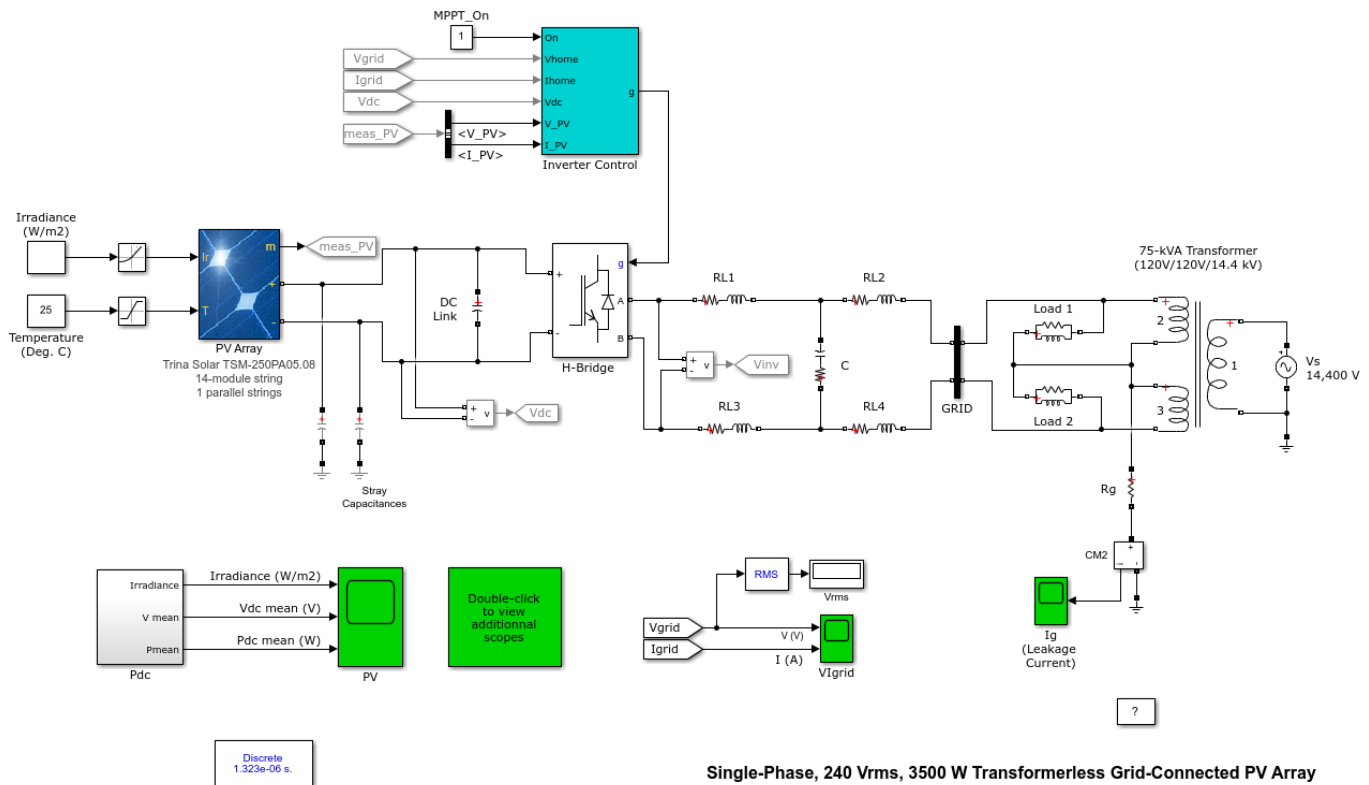
Single-Phase, 240 Vrms, 3500 W Transformerless Grid-Connected PV Array

PV Array

The SPS PV array model implements a PV array built of series- and parallel-connected PV modules. It allows modeling a variety of preset PV modules available from NREL System Advisor Model (Jan. 2014) as well as a user-defined PV module. The PV array block has two inputs that allow you to supply varying sun irradiance (input Ir in W/m^2) and temperature (input T in deg. C) data.

In our example, the PV array consists of one string of 14 Trina Solar TSM-250 modules connected in series. At 25 deg. C and with a solar irradiance of 1000 W/m2, the string can produce 3500 W.

Two small capacitors, connected on the + and - terminals of the PV array, are used to model the parasitic capacitance between the PV modules and the ground.

One-phase DC/AC Converter

The inverter is modeled using a PWM-controlled single-phase full-bridge IGBT module (H-bridge). The topology of the grid-side filter is the classical LCL configuration with the inductors split equally between the line and the neutral branches.

Inverter Control

The control system contains five major Simulink®-based subsystems:

- MPPT Controller: The Maximum Power Point Tracking (MPPT) controller is based on the 'Perturb and Observe' technique. This MPPT system automatically varies the VDC reference signal of the inverter VDC regulator in order to obtain a DC voltage which will extract maximum power from the PV string.
- VDC Regulator: Determine the required Id (active current) reference for the current regulator.
- Current Regulator: Based on the current references Id and Iq (reactive current), the regulator determines the required reference voltages for the inverter. In our example, the Iq reference is set to zero.
- PLL & Measurements: Required for synchronization and voltage/current measurements.
- PWM Generator: Use the PWM bipolar modulation method to generate firing signals to the IGBTs. In our example, the PWM carrier frequency is set to 3780 Hz (63*60).

Load & Utility Grid

The grid is modeled using a typical pole-mounted transformer and an ideal AC source of 14.4 kVrms. The transformer 240V secondary winding is center-tapped and the central neutral wire is grounded via a small resistance Rg. The residential load (10 kW / 4 kvar @ 240 Vrms) is equally distributed between the two "hot" (120 V) terminals.

Simulation

Run the simulation and observe the resulting signals on the various scopes.

The initial input irradiance to the PV array model is 250 W/m2 and the operating temperature is 25 deg. C. When steady-state is reached (around t=0.25 sec.), we get a PV voltage (Vdc_mean) of 424.5 V and the power extracted (Pdc_mean) from the array is 856 W. At t=0.4 sec, sun irradiance is rapidly ramped up from 250 W/m^2 to 750 W/m^2. Due to the MPPT operation, the control system increases the VDC reference to 434.2 V in order to extract maximum power from the PV string (2624 W). These values correspond well to the expected values. To confirm that, use the Plot button of the PV Array menu to plot the I-V and P-V characteristics of the PV string based on the manufacturer specifications.

If you look at the leakage current (Ig scope), you will notice that there are no current flowing through the stray capacitance of the PV modules. This is due to PWM method used and the filter topology. Now, if you select the PWM unipolar modulation method (using the Inverter control menu) and repeat the simulation, you will see a significant leakage current in the system.

250-kW Grid-Connected PV Array

This example shows a detailed model of a 250-kW PV array connected to a 25-kV grid via a three-phase converter.
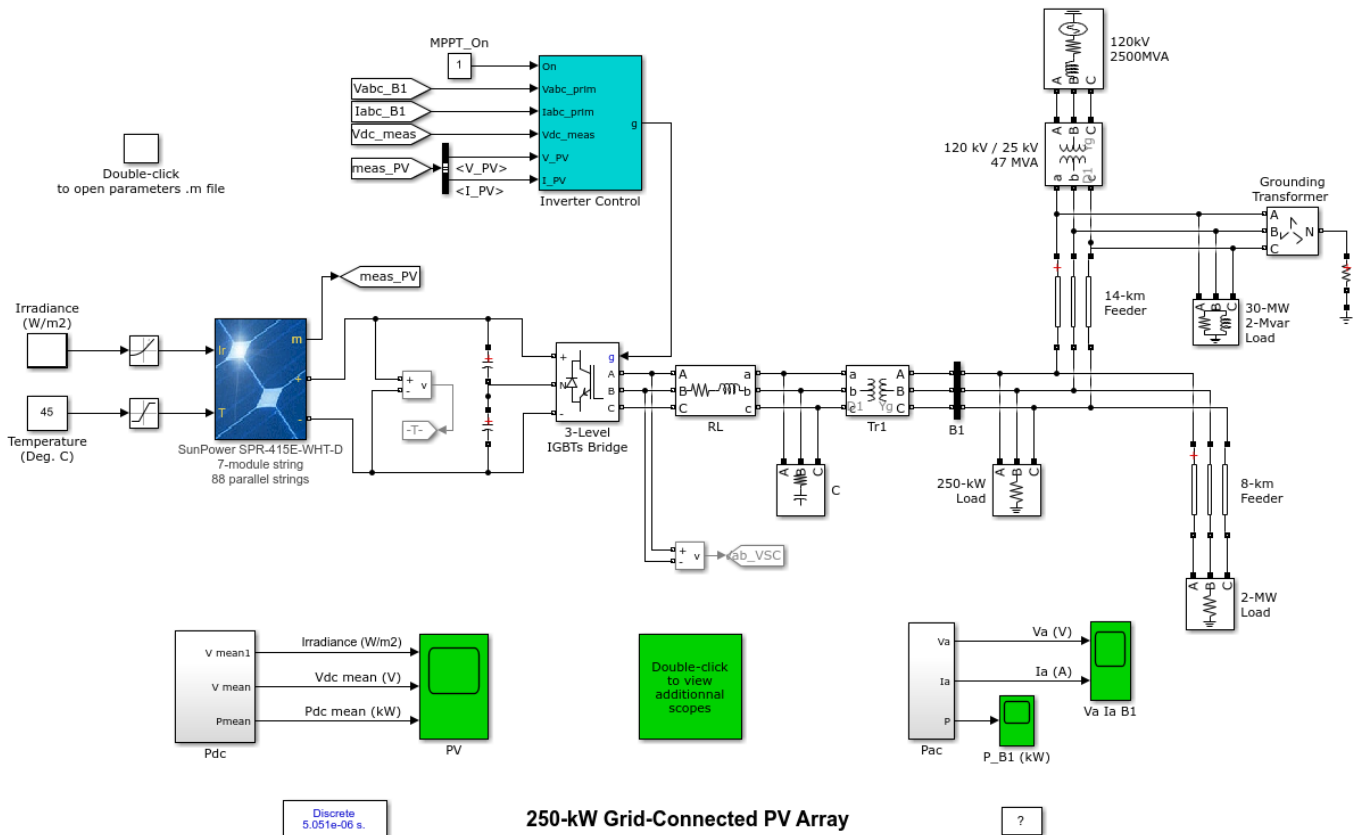
PV Array

The PV array consists of 86 parallel strings. Each string has 7 SunPower SPR-415E modules connected in series. Note that the model menu allows you to plot the I-V and P-V characteristics of the selected module or of the whole array.

Three-phase DC/AC Converter

The converter is modeled using a 3-level IGBT bridge PWM-controlled. The inverter choke RL and a small harmonics filter C are used to filter the harmonics generated by the IGBT bridge. A 250-kVA 250V/25kV three-phase transformer is used to connect the inverter to the utility distribution system.

Inverter Control

The control system contains five major Simulink®-based subsystems:

• MPPT Controller: The Maximum Power Point Tracking (MPPT) controller is based on the 'Perturb and Observe' technique. This MPPT system automatically varies the VDC reference signal of the inverter VDC regulator in order to obtain a DC voltage which will extract maximum power from the PV array.

- VDC Regulator: Determine the required Id (active current) reference for the current regulator.
- Current Regulator: Based on the current references Id and Iq (reactive current), the regulator determines the required reference voltages for the inverter. In our example, the Iq reference is set to zero.
- PLL & Measurements: Required for synchronization and voltage/current measurements.
- PWM Generator: Generate firing signals to the IGBTs based on the required reference voltages. In our example, the carrier frequency is set to 1980 Hz (33*60).

Utility Grid

The grid is modeled as a typical North American distribution grid. It included two 25-kV feeders, loads, grounding transformer and an equivalent 120-kV transmission system.

Simulation

Run the simulation and observe the resulting signals on the various scopes.

The initial input irradiance to the PV array model is 1000 W/m2 and the operating temperature is 45 deg. C. When steady-state is reached (around t=0.15 sec.), we get a PV voltage (Vdc_mean) of 481 V and the power extracted (Pdc_mean) from the array is 236 kW. These values correspond very well to the expected values from the PV module manufacturer specifications.

At t=0.3 sec, sun irradiance is rapidly ramped down from 1000 W/m^2 to 200 W/m^2. Due to the MPPT operation, the control system reduces the VDC reference to 464 V in order to extract maximum power from the PV array (46 kW).

400-kW Grid-Connected PV Farm (Average Model)

This example shows an average model of a small PV farm (400 kW) connected to a 25-kV grid using two-stage converter.
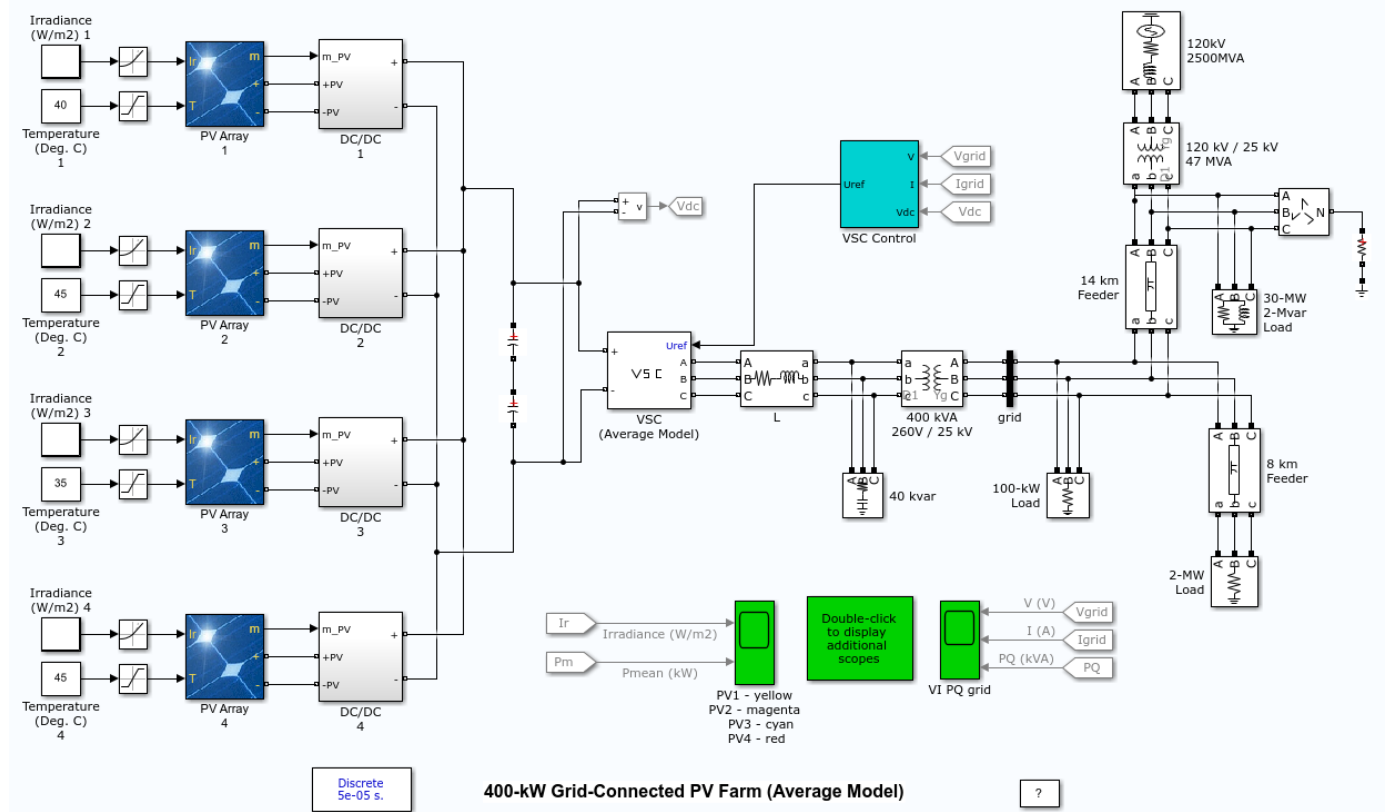
Description

The PV farm consists of four PV arrays delivering each a maximum of 100 kW at 1000 W/m2 sun irradiance. A single PV array block consist of 64 parallel strings where each string has 5 SunPower SPR-315E modules connected in series.

Each PV array is connected to a DC/DC converter (average model). The outputs of the boost converters are connected to a common DC bus of 500 V. Each boost is controlled by individual Maximum Power Point Trackers (MPPT). The MPPTs use the "Perturb and Observe" technique to vary the voltage across the terminals of the PV array in order get the maximum possible power.

A three-phase Voltage Source Converter (VSC) converts the 500 V DC to 260 V AC and keeps unity power factor. A 400-kVA 260V/25kV three-phase coupling transformer is used to connect the converter to the grid. The grid model consists of typical 25-kV distribution feeders and 120-kV equivalent transmission system.

In the average model the boost and VSC converters are represented by equivalent voltage sources generating the AC voltage averaged over one cycle of the switching frequency. Such a model does not represent harmonics, but the dynamics resulting from control system and power system interaction is preserved. This model allows using much larger time steps (50 us), resulting in a much faster simulation.

Note that in the average model the four PV-array models contain an algebraic loop. Algebraic loops are required to get an iterative and accurate solution of the PV models when large sample times are used. These algebraic loops are easily solved by Simulink®.

Simulation

Start the simulation and see the resulting signals on the various scopes. This three-second simulation allows us to observe the operation of each individual PV Array system under varying irradiances.

Partial Shading of a PV Module

This example shows partial shading of a 250-W PV module consisting of 60 cells connected in series.
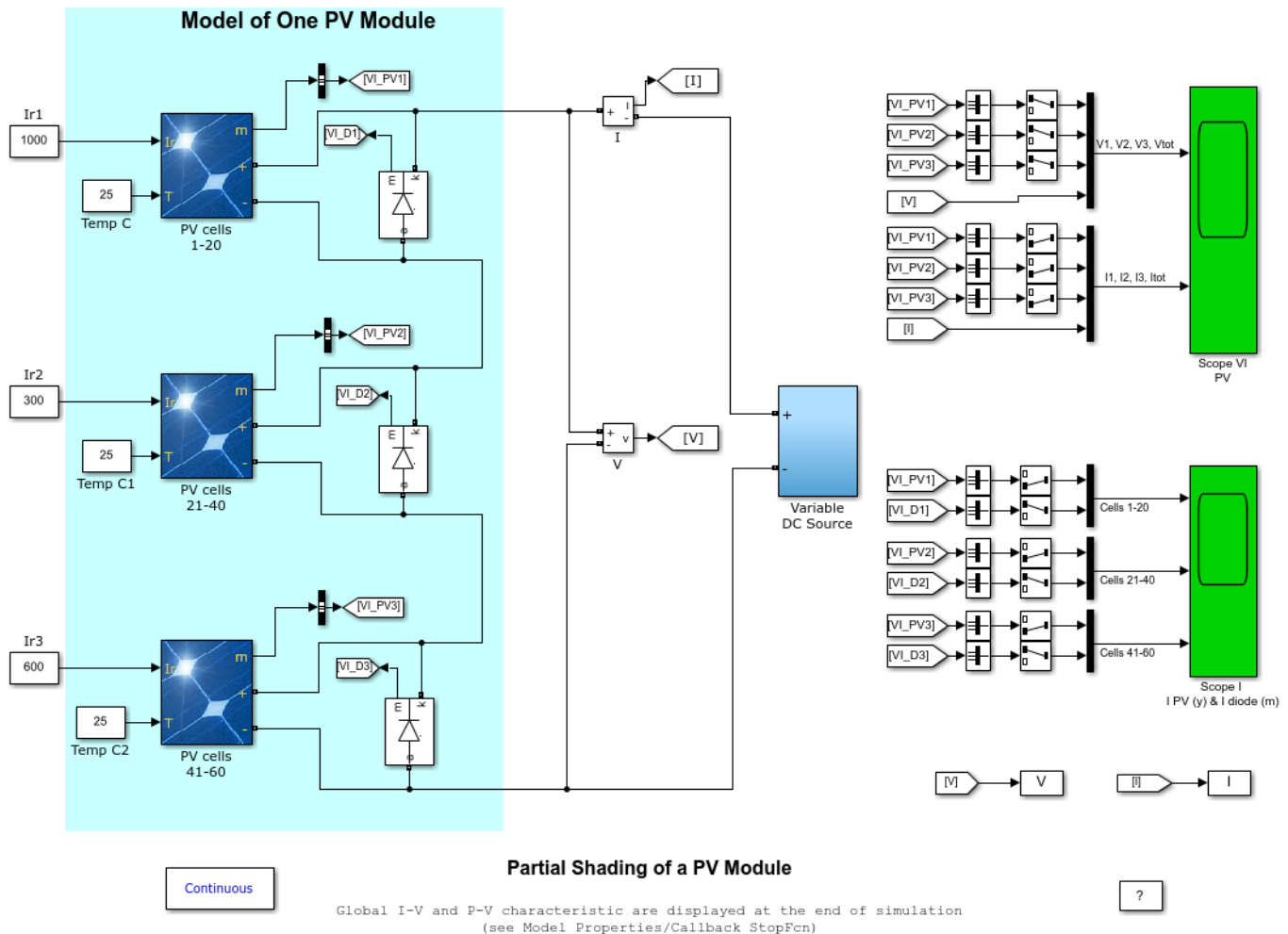
Description

The PV module is connected to a variable DC voltage source for measuring its I-V and P-V characteristics. It is modeled as three strings of 20 series-connected cells in parallel with bypass diodes that allow current flow when cells are shaded or damaged. Standard irradiance of 1000 W/m2 is applied on the first string of 20 cells while partial shading is applied on strings 2 (cells 21-40) and string 3 (cells 41-60), resulting in respective irradiances of 300 W/m2 and 600 W/m2.

Simulation

Simulate the model. The global I-V and P-V characteristics are plotted at the end of simulation. Note that the P-V curve exhibits three maxima. When this PV module is connected to a voltage-sourced converter, this may be challenging for the Maximum Power Point Tracking (MPPT) algorithm to converge on the highest peak. The Global Maximum Power Point (GMPP) (Pm= 104 W) indicated by a

red circle on the figure is 34% lower than the expected maximum power (250/3*(1 +0.3 +0.6) = 158 W).

24-hour Simulation of a Vehicle-to-Grid (V2G) System

This example shows a vehicle-to-grid system used to regulate the frequency on a microgrid when events occur during a full day. The phasor mode of Specialized Power Systems allows a fast simulation of a 24 hour scenario.

24-hour Simulation of a Vehicle-to-Grid (V2G) System
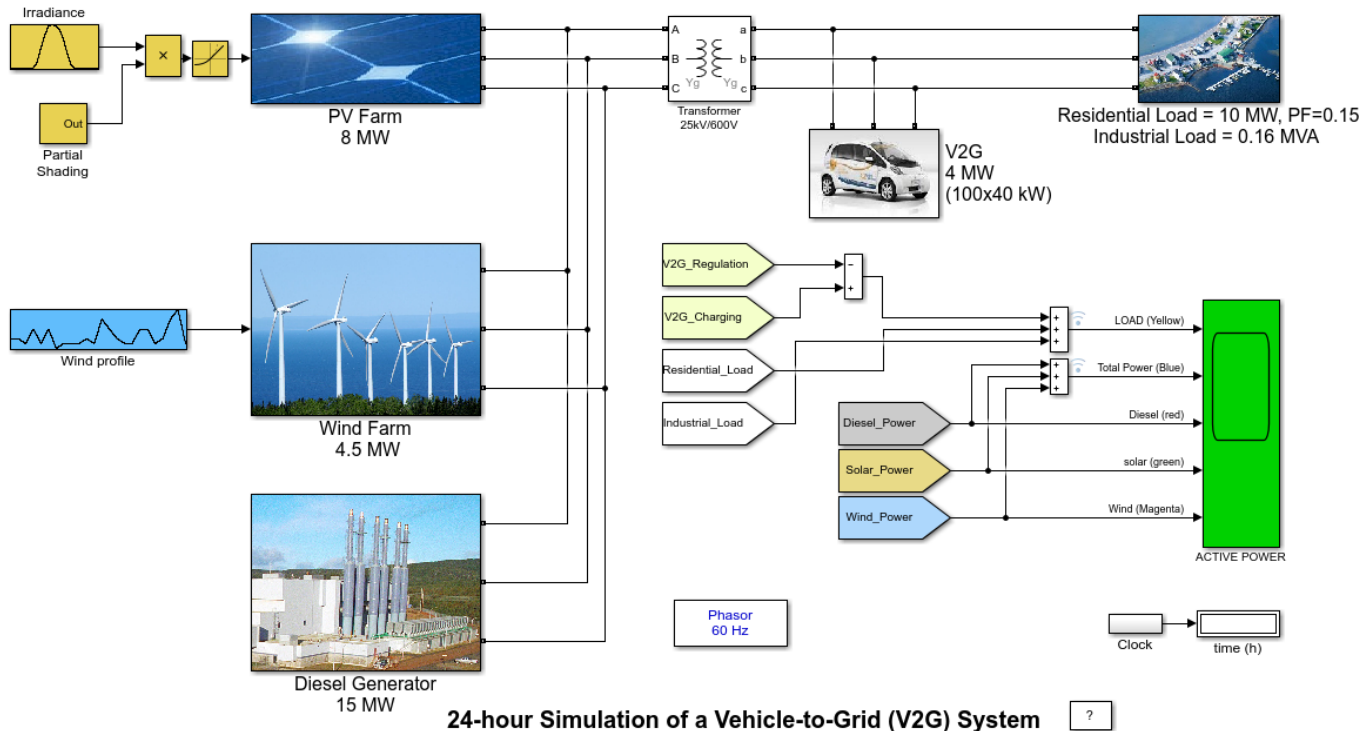
Description

The microgrid is divided into four important parts: A diesel generator, acting as the base power generator; A PV farm combined with a wind farm, to produce renewable energy; a V2G system installed next to the last part of the system which is the load of the grid. The size of the microgrid represents approximately a community of a thousand households during a low consumption day in spring or fall. There are 100 electric vehicles in the base model which means that there is a 1:10 ratio between the cars and the households. This is a possible scenario in a foreseeable future.

Diesel Generator

The diesel generator balances the power consumed and the power produced. We can determine the frequency deviation of the grid by looking at the rotor speed of its synchronous machine.

Renewable Energy

There are two sources of renewable energy in this microgrid. First, a PV farm produces energy proportional to three factors: the size of the area covered by the PV farm, the efficiency of the solar panels and the irradiance data. Second, a simplified model of a wind farm produces electrical power following a linear relationship with the wind. When the wind reaches a nominal value, the wind farm produces the nominal power. The wind farm trips from the grid when the wind speed exceeds the maximum wind value, until the wind gets back to its nominal value.

Vehicle-to-Grid

The V2G has two functions: Controls the charge of the batteries connected to it and uses the available power to regulate the grid when an event occurs during the day. The block implements five different car-user profiles:

• Profile #1: People going to work with a possibility to charge their car at work
• Profile #2: People going to work with a possibility to charge their car at work but with a longer ride
• Profile #3: People going to work with no possibility to charge their car at work
• Profile #4: People staying at home
• Profile #5: People working on a night shift

Load

The load is composed of residential load and an asynchronous machine that is used to represents the impact of an industrial inductive load (like a ventilation system) on the microgrid. The residential load follows a consumption profile with a given power factor. The asynchronous machine is controlled by a square relation between the rotor speed and the mechanical torque.

Scenario

The simulation lasts 24 hours. The solar intensity follows a normal distribution where the highest intensity is reached at midday. The wind varies greatly during the day and has multiple peaks and lows. The residential load follows a typical pattern similar to a normal household consumption. The consumption is low during the day and increases to a peak during the evening, and slowly decreases during the night. Three events will affect the grid frequency during the day:

• The kick-off of the asynchronous machine early at the third hour
• A partial shading at noon affecting the production of solar power
• A wind farm trip at 22h when the wind exceeds the maximum wind power allowed

Simulation

Run the example model and observe the power profile (generation and consumption) during the entire day. You can see the contribution of each generation system and the impact of the Vehicle-to-Grid system on peak regulation.

Detailed Model of a 100-kW Grid-Connected PV Array

This example shows a detailed model of a 100-kW array connected to a 25-kV grid via a DC-DC boost converter and a three-phase three-level VSC.

Pierre Giroux, Gilbert Sybille (Hydro-Quebec, IREQ) Carlos Osorio, Shripad Chandrachood (The MathWorks)
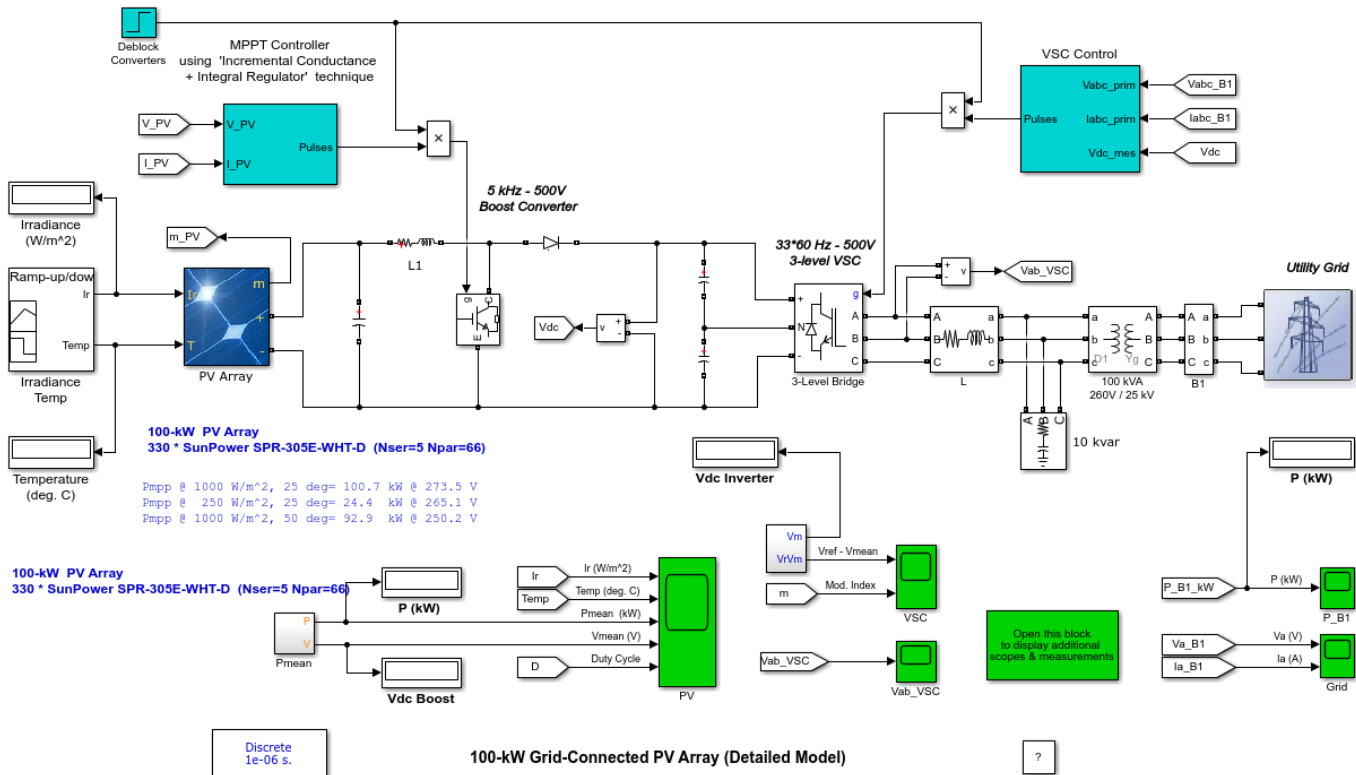
Description

A 100-kW PV array is connected to a 25-kV grid via a DC-DC boost converter and a three-phase three-level Voltage Source Converter (VSC). Maximum Power Point Tracking (MPPT) is implemented in the boost converter by means of a Simulink® model using the 'Incremental Conductance + Integral Regulator' technique.

Another example (see power_PVarray_grid_avg model) uses average models for the DC_DC and VSC converters. In this average model the MPPT controller is based on the 'Perturb and Observe' technique.

The detailed model contains the following components:

• **PV array** delivering a maximum of 100 kW at 1000 W/m^2 sun irradiance.

• **5-kHz DC-DC boost converter** increasing voltage from PV natural voltage (273 V DC at maximum power) to 500 V DC. Switching duty cycle is optimized by a MPPT controller that uses the 'Incremental Conductance + Integral Regulator' technique. This MPPT system automatically varies the duty cycle in order to generate the required voltage to extract maximum power.

- **1980-Hz 3-level 3-phase VSC**. The VSC converts the 500 V DC link voltage to 260 V AC and keeps unity power factor. The VSC control system uses two control loops: an external control loop which regulates DC link voltage to +/- 250 V and an internal control loop which regulates Id and Iq grid currents (active and reactive current components). Id current reference is the output of the DC voltage external controller. Iq current reference is set to zero in order to maintain unity power factor. Vd and Vq voltage outputs of the current controller are converted to three modulating signals Uabc_ref used by the PWM Generator. The control system uses a sample time of 100 microseconds for voltage and current controllers as well as for the PLL synchronization unit. Pulse generators of Boost and VSC converters use a fast sample time of 1 microsecond in order to get an appropriate resolution of PWM waveforms.
- **10-kvar capacitor bank** filtering harmonics produced by VSC.
- **100-kVA 260V/25kV three-phase coupling transformer**.
- **Utility grid** (25-kV distribution feeder + 120 kV equivalent transmission system).

The 100-kW PV array uses 330 SunPower modules (SPR-305E-WHT-D). The array consists of 66 strings of 5 series-connected modules connected in parallel (66*5*305.2 W= 100.7 kW).

The 'Module' parameter of the PV Array block allows you to choose among various array types of the NREL System Advisor Model (https://sam.nrel.gov/).

The manufacturer specifications for one module are:

- Number of series-connected cells : 96
- Open-circuit voltage: Voc= 64.2 V
- Short-circuit current: Isc = 5.96 A
- Voltage and current at maximum power : Vmp =54.7 V, Imp= 5.58 A

The PV array block menu allows you to plot the I-V and P-V characteristics for one module and for the whole array.

The PV array block has two inputs that allow you varying sun irradiance (input 1 in W/m^2) and temperature (input 2 in deg. C). The irradiance and temperature profiles are defined by a Signal Builder block which is connected to the PV array inputs.

Simulation

Run the model and observe the following sequence of events on Scopes.

Simulation starts with standard test conditions (25 deg. C, 1000 W/m^2).

From t=0 sec to t= 0.05 sec, pulses to Boost and VSC converters are blocked. PV voltage corresponds to open-circuit voltage (Nser*Voc=5*64.2=321 V, see Vmean trace on PV scope). The three-level bridge operates as a diode rectifier and DC link capacitors are charged above 500 V (see Vmean trace on VSC scope).

At t=0.05 sec, Boost and VSC converters are de-blocked. DC link voltage is regulated at Vdc=500V. Duty cycle of boost converter is fixed (D= 0.5 as shown on PV scope).

Steady state is reached at t=0.25 sec. Resulting PV voltage is therefore V_PV = (1-D)*Vdc= (1-0.5)*500=250 V (see Vmean trace on PV scope). The PV array output power is 96 kW (see Pmean trace on PV scope) whereas specified maximum power with a 1000 W/m^2 irradiance is 100.7 kW. Observe on Scope Grid that phase A voltage and current at 25 kV bus are in phase (unity power

factor). At t=0.4 sec MPPT is enabled. The MPPT regulator starts regulating PV voltage by varying duty cycle in order to extract maximum power. Maximum power (100.4 kW) is obtained when duty cycle is D=0.454.

At t=0.6 sec, PV array mean voltage =274 V as expected from PV module specifications (Nser*Vmp=5*54.7= 273.5 V).

From t=0.6 sec to t=1.1 sec, sun irradiance is ramped down from 1000 W/m^2 to 250 W/m^2. MPPT continues tracking maximum power.

At t=1.2 sec when irradiance has decreased to 250 W/m^2, duty cycle is D=0.461. Corresponding PV voltage and power are Vmean= 268 V and Pmean=24.3 kW. Note that the MMPT continues tracking maximum power during this fast irradiance change.

From t=1.2 sec to t=2.5 sec sun irradiance is restored back to 1000 W/m^2 and then temperature is increased to 50 deg. C. in order to observe impact of temperature increase. Note that when temperature increases from 25 deg. C to 50 deg. C, the array output power decreases from 100.7 kW to 93 kW.

References

For details on various MPPT techniques, refer to the following paper:

Moacyr A. G. de Brito, Leonardo P. Sampaio, Luigi G. Jr., Guilherme A. e Melo, Carlos A. Canesin "Comparative Analysis of MPPT Techniques for PV Applications", 2011 International Conference on Clean Electrical Power (ICCEP).

The module characteristics were extracted from NREL System Advisor Model (https://sam.nrel.gov/).

Average Model of a 100-kW Grid-Connected PV Array

This example shows an average model of a 100-kW array connected to a 25-kV grid via a DC-DC boost converter and a three-phase three-level VSC.

Pierre Giroux, Gilbert Sybille (Hydro-Quebec, IREQ) Carlos Osorio, Shripad Chandrachood (The MathWorks)

100-kW Grid-Connected PV Array (Average Model)

Description

A 100-kW PV array is connected to a 25-kV grid via a DC-DC boost converter and a three-phase three-level Voltage Source Converter (VSC). Maximum Power Point Tracking (MPPT) is implemented in the boost converter by means of a Simulink® model using the 'Perturb & Observe' technique.

Another example (see power_PVarray_grid_det model) uses detailed models for the DC_DC and VSC converters. In this detailed model the MPPT controller is based on the 'Incremental Conductance + Integral Regulator' technique.

The average model contains the following components.

- **PV array** delivering a maximum of 100 kW at 1000 W/m^2 sun irradiance.
- **DC-DC boost converter** (orange blocks)
- **3-level 3-phase VSC** (blue blocks).
- **100-kVA 260V/25kV three-phase coupling transformer**.
- **Utility grid**

Please refer to the power_PVarray_grid_det model for a complete description of the PV array, converters and connection to the grid.

The main difference between the detailed model and this average model is in the way that DC-DC boost converter and three-phase VSC are modeled. In this average model the boost and VSC converters are represented by equivalent voltage sources generating the AC voltage averaged over one cycle of the switching frequency. Such a model does not represent harmonics, but the dynamics resulting from control system and power system interaction is preserved. This model allows using much larger time steps than the detailed model (50 microseconds vs 1 microsecond), resulting in a much faster simulation. Note that in the average model the PV-array model contains an algebraic loop. This algebraic loop is required to get an iterative and accurate solution of the PV model when large sample times are used. This algebraic loop is easily solved by Simulink.

The 'Perturb and Observe' MPPT algorithm is implemented in the MPPT Control MATLAB® Function block.

The 100-kW PV array consists of 66 strings of 5 series-connected 305.2-W modules connected in parallel (66*5*305.2 W= 100.7 kW). Manufacturer specifications for one module are:

- Number of series-connected cells : 96
- Open-circuit voltage: Voc= 64.2 V
- Short-circuit current: Isc = 5.96 A
- Voltage and current at maximum power : Vmp =54.7 V, Imp= 5.58 A

The PV array block has two inputs that allow you varying sun irradiance (input 1 in W/m^2) and temperature (input 2 in deg. C). The irradiance and temperature profiles are defined by a Signal Builder block which is connected to the PV array inputs.

Simulation

Run the model and observe the following sequence of events on Scopes.

Simulation starts with standard test conditions (25 deg. C, 1000 W/m^2).

From t=0 sec to t= 0.3 sec, duty cycle of boost converter is fixed (D= 0.5 as shown on PV scope). Resulting PV voltage is therefore V= (1-D)*Vdc= (1-0.5)*500=250 V (see V_PV trace on PV scope). The PV array output power is 96 kW (see Pmean trace) whereas specified maximum power with a 1000 W/m^2 irradiance is 100.7 kW. Observe on Grid scope that phase A voltage and current at 25 kV bus are in phase (unity power factor).

At t=0.3 sec MPPT is enabled. The MPPT regulator starts regulating PV voltage by varying duty cycle in order to extract maximum power. Maximum power (100.7 kW) is obtained when duty cycle is D=0.453.

From t=0.3 sec to t=0.5 sec, the PV array operates at standard test conditions (25 deg. C, 1000 W/m^2). Duty cycle D varies between 0.450 and 0.459. PV voltage = 273.5 V (Nser*Vmp=5*54.7= 273.5 V) and mean power = 100.7 kW as expected from PV module specifications .

From t=0.5 sec to t=1.0 sec, sun irradiance is ramped down from 1000 W/m^2 to 250 W/m^2. It can be seen that this type of MPPT controller tracks maximum power only while irradiance stays constant.

From t=1.0 sec to t=1.5 sec when irradiance stays constant and equal to 250 W/m^2, duty cycle D varies between 0.466 and 0.474. Corresponding PV voltage and power are V_PV= 265 V and Pmean=24.4 kW.

From t=1.5 sec to t=6.0 sec sun irradiance is restored back to 1000 W/m^2 and then temperature is varied between 50 deg. C. and 0 deg. C. in order to observe impact of temperature. Note that maximum PV output power (107.5 kW) is obtained at minimum temperature (0 deg. C).

References

For details on various MPPT techniques, refer to the following paper:

Moacyr A. G. de Brito, Leonardo P. Sampaio, Luigi G. Jr., Guilherme A. e Melo, Carlos A. Canesin "Comparative Analysis of MPPT Techniques for PV Applications", 2011 International Conference on Clean Electrical Power (ICCEP).

The module characteristics were extracted from NREL System Advisor Model (https://sam.nrel.gov/).

Wind Farm (IG)

This example shows phasor simulation of a 9-MW wind farm using Induction Generators (IG) driven by variable-pitch wind turbines.

Richard Gagnon (Hydro-Quebec)

This example uses an initial state vector to start the simulation from steady-state. When you make changes to the model (add, delete, rename blocks, etc), the initial state vector needs to be regenerated or disabled, otherwise Simulink signals an error when the simulation is started.
To disable the use of initial state vector for this model, go to the Data Import/Export section of the Model Configuration Parameters tool and uncheck the **Initial state** option under the 'Load from workspace' section.
To regenerate the initial state vector after a change to the model, follow the instructions given in the initialization file for this model.
Type open('init_power_wind_ig') at the matlab prompt to view this file.

Description

A wind farm consisting of six 1.5-MW wind turbines is connected to a 25-kV distribution system exports power to a 120-kV grid through a 25-km 25-kV feeder. The 9-MW wind farm is simulated by three pairs of 1.5 MW wind-turbines. Wind turbines use squirrel-cage induction generators (IG). The stator winding is connected directly to the 60 Hz grid and the rotor is driven by a variable-pitch wind turbine. The pitch angle is controlled in order to limit the generator output power at its nominal value

for winds exceeding the nominal speed (9 m/s). In order to generate power the IG speed must be slightly above the synchronous speed. Speed varies approximately between 1 pu at no load and 1.005 pu at full load. Each wind turbine has a protection system monitoring voltage, current and machine speed.

Reactive power absorbed by the IGs is partly compensated by capacitor banks connected at each wind turbine low voltage bus (400 kvar for each pair of 1.5 MW turbine). The rest of reactive power required to maintain the 25-kV voltage at bus B25 close to 1 pu is provided by a 3-Mvar STATCOM with a 3% droop setting.

Open the "Wind Farm" block and look at "Wind Turbine 1". Open the turbine menu and look at the two sets of parameters specified for the turbine and the generator. Each wind turbine block represents two 1.5 MW turbines. Open the turbine menu, select "Turbine data" and check "Display wind-turbine power characteristics". The turbine mechanical power as function of turbine speed is displayed for wind speeds ranging from 4 m/s to 10 m/s. The nominal wind speed yielding the nominal mechanical power (1pu=3 MW) is 9 m/s. The wind turbine model and the statcom model (from the FACTS library) are phasor models that allow transient stability type studies with long simulation times. In this example, the system is observed during 20 s.

The wind speed applied to each turbine is controlled by the "Wind 1" to "Wind 3" blocks . Initially, wind speed is set at 8 m/s, then starting at t=2s for "Wind turbine 1", wind speed is rammed to 11 m/s in 3 seconds. The same gust of wind is applied to Turbine 2 and Turbine 3, respectively with 2 seconds and 4 seconds delays. Then, at t=15 s a temporary fault is applied at the low voltage terminals (575 V) of "Wind Turbine 2".

Simulation

Turbine response to a change in wind speed

Start simulation and observe the signals on the "Wind Turbines" scope monitoring active and reactive power, generator speed, wind speed and pitch angle for each turbine. For each pair of turbine the generated active power starts increasing smoothly (together with the wind speed) to reach its rated value of 3 MW in approximately 8s. Over that time frame the turbine speed will have increased from 1.0028 pu to 1.0047 pu. Initially, the pitch angle of the turbine blades is zero degree. When the output power exceed 3 MW, the pitch angle is increased from 0 deg to 8 deg in order to bring output power back to its nominal value. Observe that the absorbed reactive power increases as the generated active power increases. At nominal power, each pair of wind turbine absorbs 1.47 Mvar. For a 11m/s wind speed, the total exported power measured at the B25 bus is 9 MW and the statcom maintains voltage at 0.984 pu by generating 1.62 Mvar (see "B25 Bus" and "Statcom" scopes).

Operation of protection system

At t=15 s, a phase to phase fault is applied at wind turbine 2 terminals, causing the turbine to trip at t=15.11 s. If you look inside the "Wind Turbine Protections" block you will see that the trip has been initiated by the AC Undervoltage protection. After turbine 2 has tripped, turbines 1 and 3 continue to generate 3 MW each.

Impact of STATCOM

You will now observe the impact of the "STATCOM". First, open the "Three-Phase Fault" block menu and disable the phase to phase fault. Then put the "STATCOM" out of service by double clicking the "Manual Switch" block connected to the "Trip" input of the "STATCOM". Restart simulation. Observe on " B25 Bus" scope that because of the lack of reactive power support, the voltage at bus "B25" now drops to 0.91pu. This low voltage condition results in an overload of the IG of "Wind Turbine 1".

"Wind Turbine 1" is tripped at t=13.43 s. If you look inside the "Wind Turbine Protections" block you will see that the trip has been initiated by the AC Overcurrent protection.

Regenerate Initial Conditions

This example is set-up with all states initialized so that the simulation starts in steady-state. The initial conditions have been saved in the "power_wind_ig.mat" file. When you open this model, the InitFcn callback (in the Model Properties/Callbacks) automatically loads into your workspace the contents of this .mat file ("xInitial" variable).

If you modify this model, or change parameter values of power components, the initial conditions stored in the "xInitial" variable will no longer be valid and Simulink® will issue an error message. To regenerate the initial conditions for your modified model, follow the steps listed below:

1. In the Configuration Parameters pane, uncheck the "Initial state" parameter.

2. Open the "Wind Farm" subsystem and in the Timer blocks labeled "Wind1" and "Wind2", Wind3" temporarily disable the changes of wind speed by multiplying the "Time(s)" vector by 100.

3. In the "Wind Farm" subsystem, double click the "Three-Phase Fault" block and disable the AB to ground fault (deselect "Phase A Fault" and "Phase B Fault").

4. Start simulation. When Simulation is completed, verify that steady state has been reached by looking at waveforms displayed on the scopes. The final states which have been saved in the "xFinal" array can be used as initial states for future simulations. Executing the next two commands copies these final conditions in "xInitial" and saves this variable in a new file (myModel_init.mat).

```
>> xInitial=xFinal;
```

```
>> save myModel_init xInitial
```

5. In the InitFcn window of Model Properties pane, replace the first line of initialization commands with "load myModel_init". Next time you open this model, the variable xInitial saved in the myModel_init.mat file will be loaded in your workspace.

6. In the Configuration Parameters pane, check "Initial state".

7. Start simulation and verify that your model starts in steady-state.

8. Open the "Wind Farm" subsystem and in the Timer blocks labeled "Wind1", "Wind2" and Wind3" re-enable the changes of wind speed respectively a t=2 s , t=4 s and t=6 s (remove the 100 multiplication factors).

9. In the "Wind Farm" subsystem, re-enable the AB to ground fault in the "Three-Phase Fault" block (check "Phase A Fault" and "Phase B Fault")

10. Save your model.

Wind-Turbine Asynchronous Generator in Isolated Network

This example shows a wind turbine asynchronous generator in an isolated network.

R. Reid, B. Saulnier, R. Gagnon; Hydro-Quebec (IREQ)
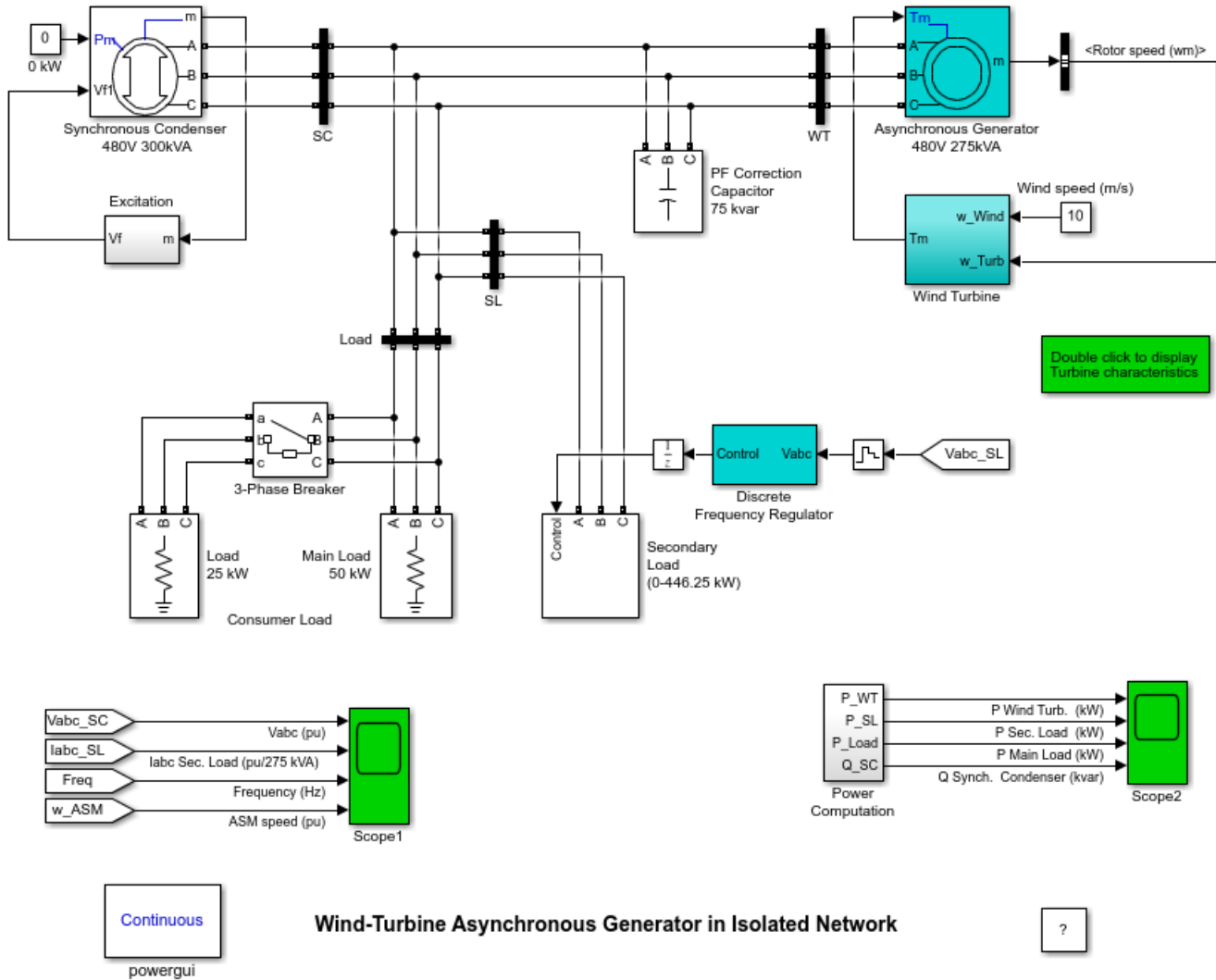
This example uses an initial state vector to start the simulation from steady-state. When you make changes to the model (add, delete, rename blocks, etc), the initial state vector needs to be regenerated or disabled, otherwise Simulink signals an error when the simulation is started.
To disable the use of initial state vector for this model, go to the Data Import/Export section of the Model Configuration Parameters tool and uncheck the **Initial state** option under the 'Load from workspace' section.
To regenerate the initial state vector after a change to the model, follow the instructions given in the initialization file for this model.
Type open('init_power_windgen') at the matlab prompt to view this file.

Description

A generic model of the High-Penetration, No Storage, Wind-Diesel (HPNSWD) system is presented in this example [1]. This technology was developed by Hydro-Quebec to reduce the cost of supplying electricity in remote northern communities [2]. The optimal wind penetration (installed wind capacity/peak electrical demand) for this system depends on the site delivery cost of fuel and

available wind resource. The first commercial application of HPNSWD technology was commissioned in 1999 by Northern Power Systems (Vermont, USA) on St. Paul Island, Alaska [3]. The HPNSWD system presented in this example uses a 480 V, 300 kVA synchronous machine, a wind turbine driving a 480 V, 275 kVA induction generator, a 50 kW customer load and a variable secondary load (0 to 446.25 kW).

At low wind speeds both the induction generator and the diesel-driven synchronous generator are required to feed the load. When the wind power exceeds the load demand, it is possible to shut down the diesel generator. In this all-wind mode, the synchronous machine is used as a synchronous condenser and its excitation system controls the grid voltage at its nominal value. A secondary load bank is used to regulate the system frequency by absorbing the wind power exceeding consumer demand.

The Wind Turbine block uses a 2-D Lookup Table to compute the turbine torque output (Tm) as a function of wind speed (w_Wind) and turbine speed (w_Turb). When you opened this example, the Pm (w_Wind, w_Turb) characteristics was automatically loaded in your workspace (psbwindgen_char array). To display the turbine characteristics, double click on the block located below the Wind Turbine block.

The Secondary Load block consists of eight sets of three-phase resistors connected in series with GTO thyristor switches. The nominal power of each set follows a binary progression so that the load can be varied from 0 to 446.25 kW by steps of 1.75kW. GTOs are simulated by ideal switches.

The frequency is controlled by the Discrete Frequency Regulator block. This controller uses a standard three-phase Phase Locked Loop (PLL) system to measure the system frequency. The measured frequency is compared to the reference frequency (60 Hz) to obtain the frequency error. This error is integrated to obtain the phase error. The phase error is then used by a Proportional-Differential (PD) controller to produce an output signal representing the required secondary load power. This signal is converted to an 8-bit digital signal controlling switching of the eight three-phase secondary loads. In order to minimize voltage disturbances, switching is performed at zero crossing of voltage.

Simulation

For the example, the wind speed (10m/s) is such that the wind turbine produces enough power to supply the load. The diesel generator (not simulated) is stopped and the synchronous machine operates as a synchronous condenser with its mechanical power input (Pm) set at zero. The example illustrates the dynamic performance of the frequency regulation system when an additional 25 kW customer load is switched on.

Start simulation and observe voltages, currents, powers, asynchronous machine speed and system frequency on the two scopes. Initial conditions (xInitial vector) have been automatically loaded in your workspace so that simulation starts in steady state.

As the asynchronous machine operates in generator mode, its speed is slightly above the synchronous speed (1.011 pu). According to turbine characteristics, for a 10 m/s wind speed, the turbine output power is 0.75 pu (206 kW). Because of the asynchronous machine losses, the wind turbine produces 200 kW. As the main load is 50 kW, the secondary load absorbs 150 kW to maintain a constant 60 Hz frequency. At t=0.2 s, the additional load of 25 kW is switched on. The frequency momentarily drops to 59.85 Hz and the frequency regulator reacts to reduce the power absorbed by the secondary load in order to bring the frequency back to 60 Hz. Voltage stays at 1 pu and no flicker is observed.

Regenerate Initial Conditions

This example is set-up with all states initialized so that the simulation starts in steady-state. The initial conditions have been saved in the "power_windgen.mat" file. When you open this model, the InitFcn callback (in the Model Properties/Callbacks) automatically loads into your workspace the contents of this .mat file ("xInitial" variable).

If you modify this model, or change parameter values of power components, the initial conditions stored in the "xInitial" variable will no longer be valid and Simulink® will issue an error message. To regenerate the initial conditions for your modified model, follow the steps listed below:

1. In the Configuration Parameters pane, uncheck the "Initial state" parameter.

2. Double click the 3-Phase Breaker block and disable breaker switching (deselect "Switching of phase X" parameters for phases A, B and C").

3. Change the Simulation Stop Time to 20 s. Note that in order to generate initial conditions coherent with the 60 Hz frequency, the Stop Time must an integer number of 60 Hz cycles.

4. Start simulation. When Simulation is completed, verify that steady state has been reached by looking at waveforms displayed on the scopes. The final states which have been saved in the "xFinal" array can be used as initial states for future simulations. Executing the next two commands copies these final conditions in "xInitial" and saves this variable in a new file (myModel_init.mat).

```
>> xInitial=xFinal;
```

```
>> save myModel_init xInitial
```

5. In the InitFcn window of Model Properties pane, replace the first line of initialization commands with "load myModel_init". Next time you open this model, the variable xInitial saved in the myModel_init.mat file will be loaded in your workspace.

6. In the Configuration Parameters pane, check "Initial state".

7. Start simulation and verify that your model starts in steady-state.

8. Double click the 3-Phase Breaker block and re-enable breaker switching (check "Switching of phase X" parameters for phases A, B and C").

9. Change the Simulation Stop Time back to 5 s.

10. Save your model.

References

[1] R. Gagnon, B. Saulnier, G. Sybille, P. Giroux; "Modeling of a Generic High-Penetration No-Storage Wind-Diesel System Using MATLAB®/Power System Blockset" 2002 Global Windpower Conference, April 2002, Paris, France

[2] B. Saulnier, A.O. Barry, B. Dube, R. Reid; "Design and Development of a Regulation and Control System for the High-Penetration No-Storage Wind/Diesel Scheme" European Community Wind Energy Conference 88, 6-10 june 1988, Herning, Denmark

[3] L. Mott (NPS), B. Saulnier (IREQ) " Commercial Wind-Diesel Project, St. Paul Island, Alaska" 14th Prime Power Diesel Inter-Utility Conference, May 28-June 2, Winnipeg, Manitoba, Canada

Wind Farm (DFIG Phasor Model)

This example shows phasor simulation of a 9 MW wind farm using Doubly-Fed Induction Generator (DFIG) driven by a wind turbine.

Richard Gagnon, Bernard Saulnier, Alain Forcione (Hydro-Quebec)

Note: This example uses a generic model of a DFIG wind turbine. The model is useful for education and academic works.

Wind Farm (DFIG Phasor Model)

This example uses an initial state vector to start the simulation from steady-state. When you make changes to the model (add, delete, rename blocks, etc), the initial state vector needs to be regenerated or disabled, otherwise Simulink signals an error when the simulation is started.
To disable the use of initial state vector for this model, go to the Data Import/Export section of the Model Configuration Parameters tool and uncheck the
Initial state option under the 'Load from workspace' section.
To regenerate the initial state vector after a change to the model, follow the instructions given in the initialization file for this model.
Type open('init_power_wind_dfig') at the matlab prompt to view this file.

Description

A 9-MW wind farm consisting of six 1.5 MW wind turbines connected to a 25-kV distribution system exports power to a 120-kV grid through a 30-km, 25-kV feeder. A 2300V, 2-MVA plant consisting of a motor load (1.68 MW induction motor at 0.93 PF) and of a 200-kW resistive load is connected on the same feeder at bus B25. Both the wind turbine and the motor load have a protection system monitoring voltage, current and machine speed. The DC link voltage of the DFIG is also monitored.

Wind turbines use a doubly-fed induction generator (DFIG) consisting of a wound rotor induction generator and an AC/DC/AC IGBT-based PWM converter. The stator winding is connected directly to the 60 Hz grid while the rotor is fed at variable frequency through the AC/DC/AC converter. The DFIG technology allows extracting maximum energy from the wind for low wind speeds by optimizing the turbine speed, while minimizing mechanical stresses on the turbine during gusts of wind. The optimum turbine speed producing maximum mechanical energy for a given wind speed is proportional to the wind speed. For wind speeds lower than 10 m/s the rotor is running at subsynchronous speed . At high wind speed it is running at hypersynchronous speed. Open the turbine menu, select "Turbine data" and check "Display wind-turbine power characteristics". The

turbine mechanical power as function of turbine speed is displayed for wind speeds ranging from 5 m/s to 16.2 m/s. The DFIG is controlled in order to follow the red curve. Turbine speed optimization is obtained between point B and point C on this curve. Another advantage of the DFIG technology is the ability for power electronic converters to generate or absorb reactive power, thus eliminating the need for installing capacitor banks as in the case of squirrel-cage induction generators.

The wind-turbine model is a phasor model that allows transient stability type studies with long simulation times. In this example, the system is observed during 50 s.

Open the wind turbine block menu and look at the four sets of parameters specified for the turbine, the generator and the converters (grid-side and rotor-side). The 6-wind-turbine farm is simulated by a single wind-turbine block by multiplying the following three parameters by six, as follows:

1. The nominal wind turbine mechanical output: 6*1.5e6 watts, specified in the Turbine data menu

2. The generator rated power: 6*1.5/0.9 MVA (6*1.5 MW at 0.9 PF), specified in the Generator data menu

3. The nominal DC bus capacitor: 6*10000 microfarads, specified in the Converters data menu

Also, notice in the Control parameters menu that the "Mode of operation" is set to " Voltage regulation". The terminal voltage will be controlled to a value imposed by the reference voltage (Vref = 1 pu) and the voltage droop (Xs = 0.02 pu).

Simulation

1. Turbine response to a change in wind speed

Open the "Wind Speed" step block specifying the wind speed. Initially, wind speed is set at 8 m/s, then at t = 5s, wind speed increases suddenly at 14 m/s. Start simulation and observe the signals on the "Wind Turbine" scope monitoring the wind turbine voltage, current, generated active and reactive powers, DC bus voltage and turbine speed. At t = 5 s, the generated active power starts increasing smoothly (together with the turbine speed) to reach its rated value of 9 MW in approximately 15 s. Over that time frame the turbine speed will have increased from 0.8 pu to 1.21 pu. Initially, the pitch angle of the turbine blades is zero degree and the turbine operating point follows the red curve of the turbine power characteristics up to point D. Then the pitch angle is increased from 0 deg to 0.76 deg in order to limit the mechanical power. Observe also the voltage and the generated reactive power. The reactive power is controlled to maintain a 1 pu voltage. At nominal power, the wind turbine absorbs 0.68 Mvar (generated Q = -0.68 Mvar) to control voltage at 1pu. If you change the mode of operation to "Var regulation" with the "Generated reactive power Qref " set to zero, you will observe that voltage increases to 1.021 pu when the wind turbine generates its nominal power at unity power factor.

2. Simulation of a voltage sag on the 120-kV system

You will now observe the impact of a voltage sag resulting from a remote fault on the 120-kV system. First, in the wind speed step block, disable the wind speed step by changing the Final value from 14 to 8 m/s. Then open the 120-kV voltage source menu. In the parameter "Time variation of", select " Amplitude". A 0.15 pu voltage drop lasting 0.5 s is programmed to occur at t = 5 s. Make sure that the control mode is still in Var regulation with Qref = 0. Start simulation and open the "Grid" scope. Observe the plant voltage and current as well as the motor speed. Note that the wind farm produces 1.87 MW. At t = 5 s, the voltage falls below 0.9 pu and at t = 5.22 s, the protection system trips the plant because an undervoltage lasting more than 0.2 s has been detected (look at the protection settings and status in the "Plant" subsystem). The plant current falls to zero and motor speed

decreases gradually, while the wind farm continues generating at a power level of 1.87 MW. After the plant has tripped, 1.25 MW of power (P_B25 measured at bus B25) is exported to the grid.

Now, change the wind turbine control mode to "Voltage regulation" and repeat the test. You will notice that the plant does not trip anymore. This is because the voltage support provided by the 5 Mvar reactive power generated by the wind-turbines during the voltage sag keeps the plant voltage above the 0.9 pu protection threshold. The plant voltage during the voltage sag is now 0.93 pu.

3. Simulation of a fault on the 25-kV system

Finally, you will now observe impact of a single phase-to-ground fault occurring on the 25-kV line at B25 bus. First disable the 120-kV voltage step. Now open the "Fault" block menu and select "Phase A Fault". Check that the fault is programmed to apply a 9-cycle single-phase to ground fault at t = 5 s.

You should observe that when the wind turbine is in "Voltage regulation" mode, the positive-sequence voltage at wind-turbine terminals (V1_B575) drops to 0.8 pu during the fault, which is above the undervoltage protection threshold (0.75 pu for a t > 0.1 s). The wind farm therefore stays in service. However, if the "Var regulation" mode is used with Qref = 0, the voltage drops under 0.7 pu and the undervoltage protection trips the wind farm. We can now observe that the turbine speed increases. At t= 40 s the pitch angle starts to increase in order to limit the speed.

Regenerate Initial Conditions

This example is set-up with all states initialized so that the simulation starts in steady-state. The initial conditions have been saved in the "power_wind_dfig.mat" file. When you open this model, the InitFcn callback (in the Model Properties/Callbacks) automatically loads into your workspace the contents of this .mat file ("xInitial" variable).

If you modify this model, or change parameter values of power components, the initial conditions stored in the "xInitial" variable will no longer be valid and Simulink® will issue an error message. To regenerate the initial conditions for your modified model, follow the steps listed below:

1. In the Configuration Parameters pane, uncheck the "Initial state" parameter.

2. Double click the Step block labeled "Wind Speed (m/s)" and temporarily disable the change of wind speed by multiplying the Step time by 100.

3. Double click the Breaker block and make sure that no fault is applied (Phase A, B and C checkboxes not selected).

4. Double click the 120 kV voltage source block and make sure that the "Time variation of" parameter is set to "None".

5. Start simulation. When Simulation is completed, verify that steady state has been reached by looking at waveforms displayed on the scopes. The final states which have been saved in the "xFinal" array can be used as initial states for future simulations. Executing the next two commands copies these final conditions in "xInitial" and saves this variable in a new file (myModel_init.mat).

```
>> xInitial=xFinal;
```

```
>> save myModel_init xInitial
```

6. In the InitFcn window of Model Properties pane, replace the line "xInitial = init_power_wind_dfig;" with "load myModel_init.mat". Next time you open this model, the variable xInitial saved in the myModel_init.mat file will be loaded in your workspace.

7. In the Configuration Parameters pane, check "Initial state".

8. Start simulation and verify that your model starts in steady-state.

9. Double click the Step block labeled "Wind Speed (m/s)" and re-enable the change of wind speed at t=5 s (remove the 100 multiplication factor).

10. Save your model.

Wind Farm - DFIG Detailed Model

This example shows a 9 MW wind farm using a detailed model of a Doubly-Fed Induction Generator (DFIG) driven by a wind turbine.

Richard Gagnon (Hydro-Quebec)

Wind Farm - DFIG Detailed Model

This example uses an initial state vector to start the simulation from steady-state. When you make changes to the model (add, delete, rename blocks, etc), the initial state vector needs to be regenerated or disabled, otherwise Simulink signals an error when the simulation is started.
To disable the use of initial state vector for this model, go to the Data Import/Export section of the Model Configuration Parameters tool and uncheck the **Initial state** option under the 'Load from workspace' section.
To regenerate the initial state vector after a change to the model, follow the instructions given in the initialization file for this model.
Type open('init_power_wind_dfig_det') at the MATLAB prompt to view this file.

1. Simulation Methods of the DFIG

Depending on the range of frequencies to be represented, three simulation methods are currently available in Specialized Power Systems to model VSC based energy conversion systems connected on power grids.

The detailed model (discrete) such as the one presented in this example. The detailed model includes detailed representation of power electronic IGBT converters. In order to achieve an acceptable accuracy with the 1620 Hz and 2700 Hz switching frequencies used in this example, the model must be discretized at a relatively small time step (5 microseconds). This model is well suited for observing harmonics and control system dynamic performance over relatively short periods of times (typically hundreds of milliseconds to one second).

The average model (discrete) such as the one presented in the power_wind_dfig_avg model in the Renewable Energy examples library. In this type of model the IGBT Voltage-sourced converters (VSC) are represented by equivalent voltage sources generating the AC voltage averaged over one cycle of the switching frequency. This model does not represent harmonics, but the dynamics resulting from

control system and power system interaction is preserved. This model allows using much larger time steps (typically 50 microseconds), thus allowing simulations of several seconds.

The phasor model (continuous) such as the one presented in the "power_wind_dfig" model in the Renewable Energy examples library. This model is better adapted to simulate the low frequency electromechanical oscillations over long periods of time (tens of seconds to minutes). In the phasor simulation method, the sinusoidal voltages and currents are replaced by phasor quantities (complex numbers) at the system nominal frequency (50 Hz or 60 Hz).This is the same technique which is used in transient stability software.

2. Description

A 9 MW wind farm consisting of six 1.5 MW wind turbines connected to a 25 kV distribution system exports power to a 120 kV grid through a 30 km, 25 kV feeder.

Wind turbines using a doubly-fed induction generator (DFIG) consist of a wound rotor induction generator and an AC/DC/AC IGBT-based PWM converter. The stator winding is connected directly to the 60 Hz grid while the rotor is fed at variable frequency through the AC/DC/AC converter. The DFIG technology allows extracting maximum energy from the wind for low wind speeds by optimizing the turbine speed, while minimizing mechanical stresses on the turbine during gusts of wind.

In this example the wind speed is maintained constant at 15 m/s. The control system uses a torque controller in order to maintain the speed at 1.2 pu. The reactive power produced by the wind turbine is regulated at 0 Mvar.

Look under the mask of "DFIG Wind Turbine" block to see how the model is built. The sample time used to discretize the model (Ts= 50 microseconds) is specified in the Initialization function of the Model Properties.

Open the "DFIG Wind Turbine" block menu to see the data of the generator, the converter, the turbine, the drive train and the control systems. In the Display menu select "Turbine data for 1 wind turbine", check "Display wind turbine power characteristics" and then click Apply. The turbine Cp curves are displayed in Figure 1. The turbine power, the tip speed ratio lambda and the Cp values are displayed in Figure 2 as function of wind speed. For a wind speed of 15 m/s, the turbine output power is 1 pu of its rated power, the pitch angle is 8.7 deg and the generator speed is 1.2 pu.

3. Simulation

In this example you will observe the steady-state operation of the DFIG and its dynamic response to voltage sag resulting from a remote fault on the 120-kV system. Open the "120 kV" block modeling the voltage source and see how a six-cycle 0.5 pu voltage drop is programmed at t=0.03 s

Start simulation. Observe voltage and current waveforms on the Scope block. At simulation start the "xInitial" variable containing the initial state variables is automatically loaded (from the "power_wind_dfig_det.mat" file specified in the Model Properties) so that the simulation starts in steady state.

Initially the DFIG wind farm produces 9 MW. The corresponding turbine speed is 1.2 pu of generator synchronous speed. The DC voltage is regulated at 1150 V and reactive power is kept at 0 Mvar. At t=0.03 s the positive-sequence voltage suddenly drops to 0.5 p.u. causing an oscillation on the DC bus voltage and on the DFIG output power. During the voltage sag the control system tries to regulate DC voltage and reactive power at their set points (1150 V, 0 Mvar). The system recovers in approximately 4 cycles.

4. Regenerate Initial Conditions

This example is set-up with all states initialized so that the simulation starts in steady-state. Otherwise, due to the long time constants of the electromechanical part of the wind turbine model and to its relatively slow regulators you would have to wait for tens of seconds before reaching steady-state. The initial conditions have been saved in the "power_wind_dfig_det.mat" file. When you start simulation, the InitFcn callback (in the Model Properties/Callbacks) automatically loads into your workspace the contents of this .mat file ("xInitial" variable specified in the "Initial state" parameter in the Configuration Parameters pane).

If you modify this model, or change parameter values of power components, the initial conditions stored in the "xInitial" variable will no longer be valid and Simulink® will issue an error message. To regenerate the initial conditions for your modified model, follow the steps listed below:

1. In the Configuration Parameters pane, uncheck the "Initial state" parameter.

2. In the 120 kV Three-phase Voltage Source menu, disable the source voltage step by setting the "Time variation of " parameter to "none".

3. In order to shorten the time required to reach steady-state, temporarily decrease the inertia of the turbine-generator group. Open the DFIG Wind Turbine menu and in the Drive train data and Generator data, divide the H inertia constants by 10.

4. Change the Simulation Stop Time to 5 seconds. Note that in order to generate initial conditions coherent with the 60 Hz voltage source phase angles, the Stop Time must be an integer number of 60 Hz cycles.

5. Change the Simulation Mode from "Normal" to "Accelerator".

6. Start simulation. When Simulation is completed, verify that steady state has been reached by looking at waveforms displayed on the Scope block. The final states which have been saved in the "xFinal" structure with time can be used as initial states for future simulations. Executing the next two commands copies these final conditions in "xInitial" and saves this variable in a new file (myModel_init.mat).

```
>> xInitial=xFinal;
```

```
>> save myModel_init xInitial
```

7. In the InitFcn window of Model Properties pane, replace the first line of initialization commands with "load myModel_init". Next time you start a simulation with this model, the variable xInitial saved in the myModel_init.mat file will be loaded in your workspace.

8. In the Configuration Parameters pane, check "Initial state".

9. In the Wind Turbine Generator and Drive train data, reset the inertia constants H back to their original values.

10. Start simulation and verify that your model starts in steady-state.

11. In the 120 kV Three-phase voltage source menu, set the "Time variation of" parameter back to "Amplitude".

12. Change the Simulation Stop Time and Simulation Mode back to their original values (0.2 seconds, Normal).

13. Save your model.

Wind Farm - DFIG Average Model

This example shows a 9 MW wind farm using an average model of a Doubly-Fed Induction Generator (DFIG) driven by a wind turbine.

Richard Gagnon (Hydro-Quebec)

9 MW Wind Farm
(6 x 1.5 MW)

Wind Farm - DFIG Average Model

This example uses an initial state vector to start the simulation from steady-state. When you make changes to the model (add, delete, rename blocks, etc), the initial state vector needs to be regenerated or disabled, otherwise Simulink signals an error when the simulation is started.
To disable the use of initial state vector for this model, go to the Data Import/Export section of the Model Configuration Parameters tool and uncheck the **Initial state** option under the 'Load from workspace' section.
To regenerate the initial state vector after a change to the model, follow the instructions given in the initialization file for this model.
Type open('init_power_wind_dfig_avg') at the MATLAB prompt to view this file.

1. Simulation Methods of the DFIG

Depending on the range of frequencies to be represented, three simulation methods are currently available in Specialized Power Systems to model VSC based energy conversion systems connected on power grids.

The detailed model (discrete) such as the one presented in the power_wind_dfig_det model in the Renewable Energy examples library. The detailed model includes detailed representation of power electronic IGBT converters. In order to achieve an acceptable accuracy with the 1620 Hz and 2700 Hz switching frequencies used in this example, the model must be discretized at a relatively small time step (5 microseconds). This model is well suited for observing harmonics and control system dynamic performance over relatively short periods of times (typically hundreds of milliseconds to one second).

The average model (discrete) such as the one presented in this example. In this type of model the IGBT Voltage-sourced converters (VSC) are represented by equivalent voltage sources generating the AC voltage averaged over one cycle of the switching frequency. This model does not represent

harmonics, but the dynamics resulting from control system and power system interaction is preserved. This model allows using much larger time steps (typically 50 microseconds), thus allowing simulations of several seconds.

The phasor model (continuous) such as the one presented in the "power_wind_dfig" model in the Renewable Energy examples library. This model is better adapted to simulate the low frequency electromechanical oscillations over long periods of time (tens of seconds to minutes). In the phasor simulation method, the sinusoidal voltages and currents are replaced by phasor quantities (complex numbers) at the system nominal frequency (50 Hz or 60 Hz). This is the same technique which is used in transient stability software.

2. Description

A 9 MW wind farm consisting of six 1.5 MW wind turbines connected to a 25 kV distribution system exports power to a 120 kV grid through a 30 km, 25 kV feeder.

Wind turbines using a doubly-fed induction generator (DFIG) consist of a wound rotor induction generator and an AC/DC/AC IGBT-based PWM converter modeled by voltage sources. The stator winding is connected directly to the 60 Hz grid while the rotor is fed at variable frequency through the AC/DC/AC converter. The DFIG technology allows extracting maximum energy from the wind for low wind speeds by optimizing the turbine speed, while minimizing mechanical stresses on the turbine during gusts of wind.

In this example the wind speed is maintained constant at 15 m/s. The control system uses a torque controller in order to maintain the speed at 1.2 pu. The reactive power produced by the wind turbine is regulated at 0 Mvar.

Look under the mask of "DFIG Wind Turbine" block to see how the model is built. The sample time used to discretize the model (Ts= 50 microseconds) is specified in the Initialization function of the Model Properties.

Open the "DFIG Wind Turbine" block menu to see the data of the generator, the converter, the turbine, the drive train and the control systems. In the Display menu select "Turbine data for 1 wind turbine" check "Display wind turbine power characteristics" and then click Apply. The turbine Cp curves are displayed in Figure 1. The turbine power, the tip speed ratio lambda and the Cp values are displayed in Figure 2 as function of wind speed. For a wind speed of 15 m/s, the turbine output power is 1 pu of its rated power, the pitch angle is 8.7 deg and the generator speed is 1.2 pu.

3. Simulation

In this example you will observe the steady-state operation of the DFIG and its dynamic response to voltage sag resulting from a remote fault on the 120-kV system. Open the "120 kV" block modeling the voltage source and see how a six-cycle 0.5 pu voltage drop is programmed at t=0.03 s

Start simulation. Observe voltage and current waveforms on the Scope block. At simulation start the "xInitial" variable containing the initial state variables is automatically loaded (from the "power_wind_dfig_avg.mat" file specified in the Model Properties) so that the simulation starts in steady state.

Initially the DFIG wind farm produces 9 MW. The corresponding turbine speed is 1.2 pu of generator synchronous speed. The DC voltage is regulated at 1150 V and reactive power is kept at 0 Mvar. At t=0.03 s the positive-sequence voltage suddenly drops to 0.5 p.u. causing an oscillation on the DC bus voltage and on the DFIG output power. During the voltage sag the control system tries to regulate DC voltage and reactive power at their set points (1150 V, 0 Mvar). The system recovers in approximately 4 cycles.

Double click the blue block entitled "Show Detailed and Average Simulation Results". A figure opens showing comparison of the phase A voltage at DFIG terminals, DC link voltage, active and reactive powers and speed for the detailed model and the average model. Notice that the two models are in good agreement. The average model represents correctly the low frequency control and power system oscillations produced by the voltage sag, but voltage waveforms do not show the high frequency harmonics produced by the PWM switching of the two converters.

4. Regenerate Initial Conditions

This example is set-up with all states initialized so that the simulation starts in steady-state. Otherwise, due to the long time constants of the electromechanical part of the wind turbine model and to its relatively slow regulators you would have to wait for tens of seconds before reaching steady-state. The initial conditions have been saved in the "power_wind_dfig_avg.mat" file. When you start simulation, the InitFcn callback (in the Model Properties/Callbacks) automatically loads into your workspace the contents of this .mat file ("xInitial" variable specified in the "Initial state" parameter in the Configuration Parameters pane).

If you modify this model, or change parameter values of power components, the initial conditions stored in the "xInitial" variable will no longer be valid and Simulink® will issue an error message. To regenerate the initial conditions for your modified model, follow the steps listed below:

1. In the Configuration Parameters pane, uncheck the "Initial state" parameter.

2. In the 120 kV Three-phase Voltage Source menu, disable the source voltage step by setting the "Time variation of " parameter to "none".

3. In order to shorten the time required to reach steady-state, temporarily decrease the inertia of the turbine-generator group. Open the DFIG Wind Turbine menu and in the Drive train data and Generator data, divide the H inertia constants by 10.

4. Change the Simulation Stop Time to 5 seconds. Note that in order to generate initial conditions coherent with the 60 Hz voltage source phase angles, the Stop Time must be an integer number of 60 Hz cycles.

5. Change the Simulation Mode from "Normal" to "Accelerator".

6. Start simulation. When Simulation is completed, verify that steady state has been reached by looking at waveforms displayed on the Scope block. The final states which have been saved in the "xFinal" structure with time can be used as initial states for future simulations. Executing the next two commands copies these final conditions in "xInitial" and saves this variable in a new file (myModel_init.mat).

```
>> xInitial=xFinal;
```

```
>> save myModel_init xInitial
```

7. In the InitFcn window of Model Properties pane, replace the first line of initialization commands with "load myModel_init". Next time you start a simulation with this model, the variable xInitial saved in the myModel_init.mat file will be loaded in your workspace.

8. In the Configuration Parameters pane, check "Initial state".

9. In the Wind Turbine Generator and Drive train data, reset the inertia constants H back to their original values.

10. Start simulation and verify that your model starts in steady-state.

11. In the 120 kV Three-phase voltage source menu, set the "Time variation of" parameter back to "Amplitude".

12. Change the Simulation Stop Time and Simulation Mode back to their original values (0.2 seconds, Normal).

13. Save your model.

Wind Farm - Synchronous Generator and Full Scale Converter (Type 4) Detailed Model

This example shows a 10 MW wind farm using a detailed model of a Type 4 wind turbine.
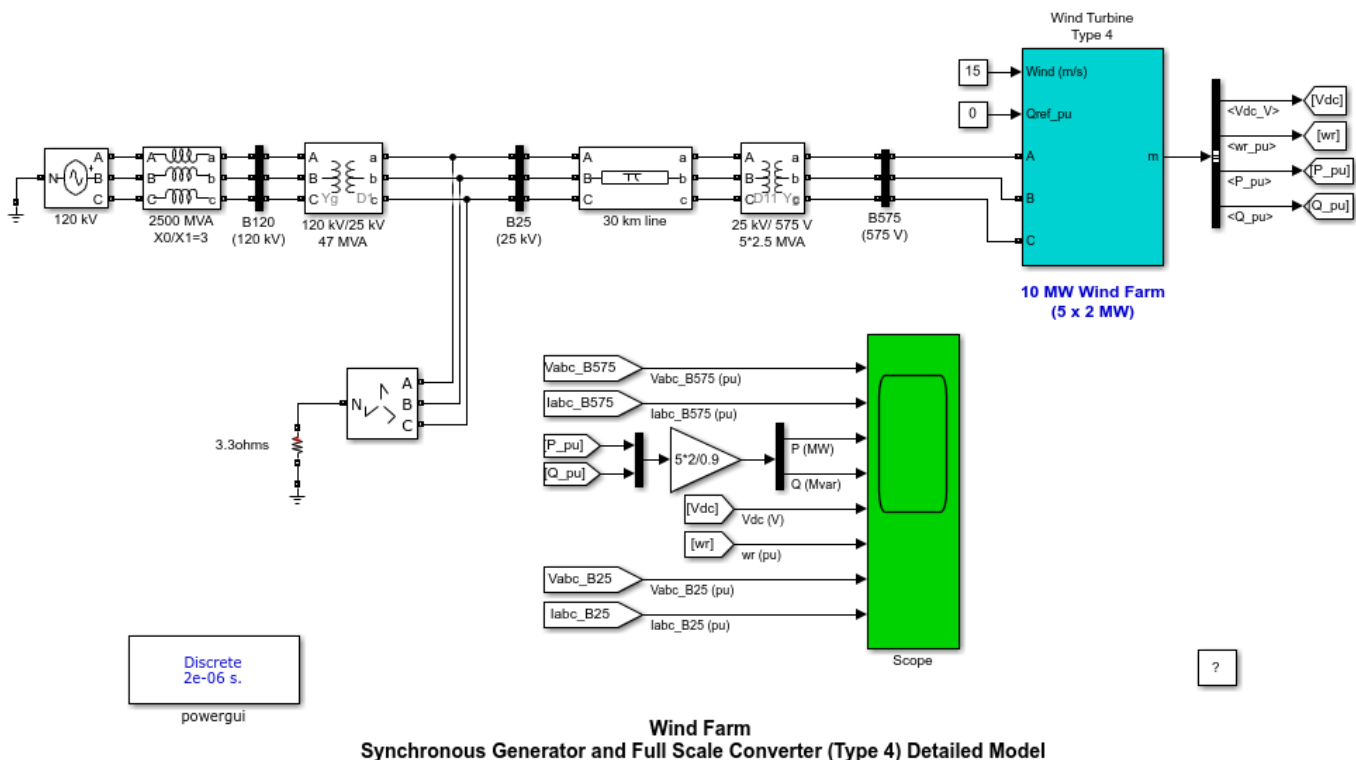
Richard Gagnon and Jacques Brochu (Hydro-Quebec)

Wind Farm
Synchronous Generator and Full Scale Converter (Type 4) Detailed Model

This example uses an initial state vector to start the simulation from steady-state. When you make changes to the model (add, delete, rename blocks, etc), the initial state vector needs to be regenerated or disabled, otherwise Simulink signals an error when the simulation is started.
To disable the use of initial state vector for this model, go to the Data Import/Export section of the Model Configuration Parameters tool and uncheck the *Initial state* option under the 'Load from workspace' section.
To regenerate the initial state vector after a change to the model, follow the instructions given in the initialization file for this model.
Type open('init_power_wind_type_4_det') at the MATLAB prompt to view this file.

1. Simulation Methods of the Type 4 Wind Turbine

Depending on the range of frequencies to be represented, three simulation methods are currently available in Specialized Power Systems to model VSC based energy conversion systems connected on power grids.

The detailed model (discrete) such as the one presented in this example. The detailed model includes detailed representation of power electronic IGBT converters. In order to achieve an acceptable accuracy with the 2000 Hz and 3000 Hz switching frequencies used in this example, the model must be discretized at a relatively small time step (2 microseconds). This model is well suited for observing harmonics and control system dynamic performance over relatively short periods of times (typically hundreds of milliseconds to one second).

The average model (discrete) such as the one presented in the power_wind_type_4_avg model in the Renewable Energy examples library. In this type of model the IGBT Voltage-sourced converters

(VSC) are represented by equivalent voltage sources generating the AC voltage averaged over one cycle of the switching frequency. A similar method is used for DC-DC converter. The average model does not represent harmonics, but the dynamics resulting from control system and power system interaction is preserved. This model allows using much larger time steps (typically 50 microseconds), thus allowing simulations of several seconds.

The phasor model (continuous) such as the one presented in the "power_wind_dfig" model in the Renewable Energy example library. This model is better adapted to simulate the low frequency electromechanical oscillations over long periods of time (tens of seconds to minutes). In the phasor simulation method, the sinusoidal voltages and currents are replaced by phasor quantities (complex numbers) at the system nominal frequency (50 Hz or 60 Hz).This is the same technique which is used in transient stability software.

2. Description

A 10 MW wind farm consisting of five 2 MW wind turbines connected to a 25 kV distribution system exports power to a 120 kV grid through a 30 km, 25 kV feeder.

The Type 4 wind turbine presented in this example consists of a synchronous generator connected to a diode rectifier, a DC-DC IGBT-based PWM boost converter and a DC/AC IGBT-based PWM converter. The Type 4 technology allows extracting maximum energy from the wind for low wind speeds by optimizing the turbine speed, while minimizing mechanical stresses on the turbine during gusts of wind.

In this example the wind speed is maintained constant at 15 m/s. The control system of the DC-DC converter is used to maintain the speed at 1 pu. The reactive power produced by the wind turbine is regulated at 0 Mvar.

Right-click on the "Wind Turbine Type 4" block and select "Look Under Mask" to see how the model is built. The sample time used to discretize the model (Ts= 2 microseconds) is specified in the Initialization function of the Model Properties.

Open the "Wind Turbine Type 4" block menu to see the data of the generator, the converter, the turbine, the drive train and the control systems. In the Display menu select "Turbine data for 1 wind turbine", check "Display wind turbine power characteristics" and then click Apply. The turbine Cp curves are displayed in Figure 1. The turbine power, the tip speed ratio lambda and the Cp values are displayed in Figure 2 as function of wind speed. For a wind speed of 15 m/s, the turbine output power is 1 pu of its rated power, the pitch angle is 8.8 deg and the generator speed is 1 pu.

3. Simulation

In this example you will observe the steady-state operation of the Type 4 wind turbine and its dynamic response to voltage sag resulting from a remote fault on the 120-kV system. Open the "120 kV" block modeling the voltage source and see how a six-cycle 0.25 pu voltage drop is programmed at t=0.03 s

Start simulation. Observe voltage and current waveforms on the Scope block. At simulation start the "xInitial" variable containing the initial state variables is automatically loaded (from the "power_wind_type_4_det.mat" file specified in the Model Properties) so that the simulation starts in steady state.

Initially the Type 4 wind farm produces 10 MW. The corresponding turbine speed is 1 pu of generator synchronous speed. The DC voltage is regulated at 1100 V and reactive power is kept at 0 Mvar. At t=0.03 s the positive-sequence voltage suddenly drops to 0.75 p.u. causing an increase on the DC bus

voltage and a drop on the Type 4 wind turbine output power. During the voltage sag the control systems try to regulate DC voltage and reactive power at their set points (1100 V, 0 Mvar). The system recovers after fault elimination.

4. Regenerate Initial Conditions

This example is set-up with all states initialized so that the simulation starts in steady-state. Otherwise, due to the long time constants of the electromechanical part of the wind turbine model and to its relatively slow regulators you would have to wait for tens of seconds before reaching steady-state. The initial conditions have been saved in the "power_wind_type_4_det.mat" file. When you start simulation, the InitFcn callback (in the Model Properties/Callbacks) automatically loads into your workspace the contents of this .mat file ("xInitial" variable specified in the "Initial state" parameter in the Simulation/Configuration Parameters menu).

If you modify this model, or change parameter values of power components, the initial conditions stored in the "xInitial" variable will no longer be valid and Simulink® will issue an error message. To regenerate the initial conditions for your modified model, follow the steps listed below:

1. In the Configuration Parameters pane, uncheck the "Initial state" parameter.

2. In the 120 kV Three-phase Voltage Source menu, disable the source voltage step by setting the "Time variation of " parameter to "none".

3. In order to shorten the time required to reach steady-state, temporarily decrease the inertia of the turbine-generator group. Open the Wind Turbine Type 4 menu and in the Drive train data and Generator data, divide the H inertia constants by 10.

4. Change the Simulation Stop Time to 5 seconds. Note that in order to generate initial conditions coherent with the 60 Hz voltage source phase angles, the Stop Time must be an integer number of 60 Hz cycles.

5. Change the Simulation Mode from "Normal" to "Accelerator".

6. Start simulation. When Simulation is completed, verify that steady state has been reached by looking at waveforms displayed on the Scope block. The final states which have been saved in the "xFinal" structure with time can be used as initial states for future simulations. Executing the next two commands copies these final conditions in "xInitial" and saves this variable in a new file (myModel_init.mat).

```
>> xInitial=xFinal;
```

```
>> save myModel_init xInitial
```

7. In the InitFcn window of Model Properties pane, replace the first line of initialization commands with "load myModel_init". Next time you start a simulation with this model, the variable xInitial saved in the myModel_init.mat file will be loaded in your workspace.

8. In the Configuration Parameters pane, check "Initial state".

9. In the Wind Turbine Generator and Drive train data, reset the inertia constants H back to their original values.

10. Start simulation and verify that your model starts in steady-state.

11. In the 120 kV Three-phase voltage source menu, set the "Time variation of" parameter back to "Amplitude".

12. Change the Simulation Stop Time and Simulation Mode back to their original values (0.2 seconds, Normal).

13. Save your model.

Wind Farm - Synchronous Generator and Full Scale Converter (Type 4) Average Model

This example shows a 10 MW wind farm using an average model of a Type 4 wind turbine.
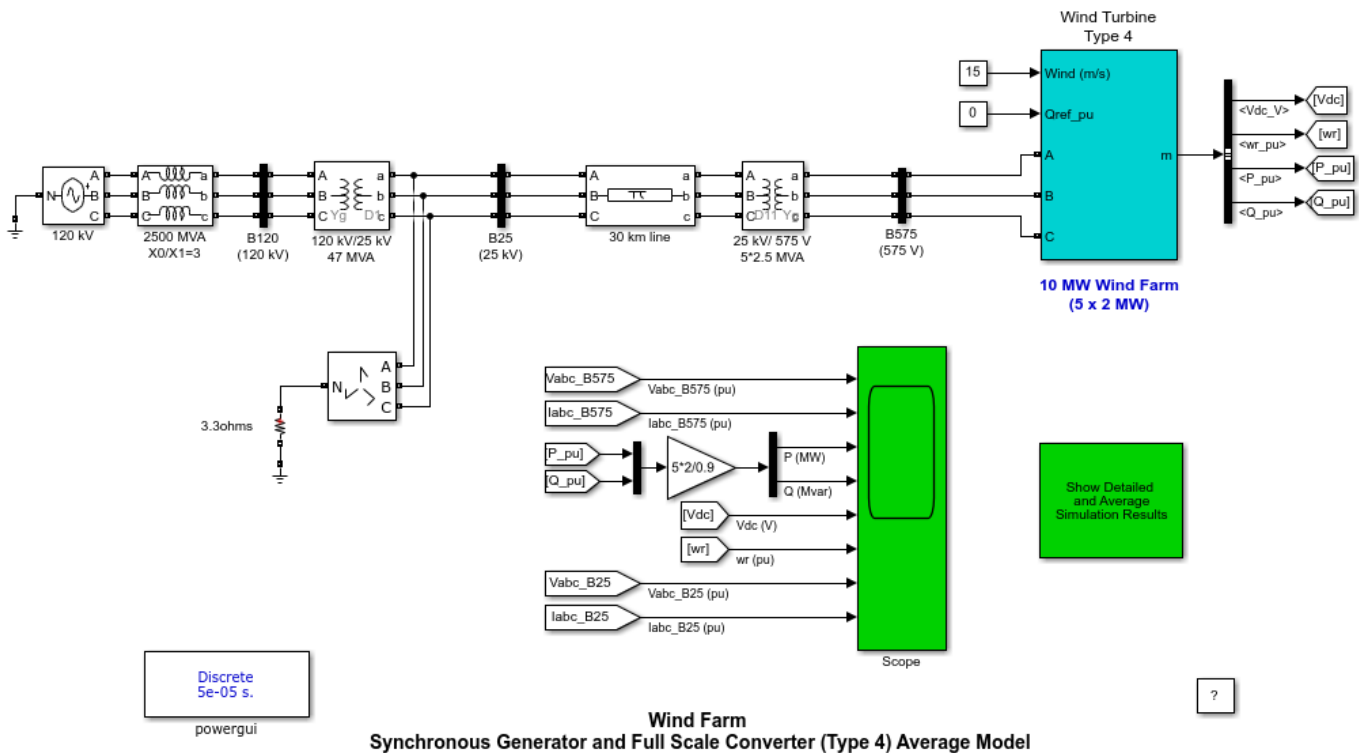
Richard Gagnon and Jacques Brochu (Hydro-Quebec)

Wind Farm
Synchronous Generator and Full Scale Converter (Type 4) Average Model

This example uses an initial state vector to start the simulation from steady-state. When you make changes to the model (add, delete, rename blocks, etc), the initial state vector needs to be regenerated or disabled, otherwise Simulink signals an error when the simulation is started.
To disable the use of initial state vector for this model, go to the Data Import/Export section of the Model Configuration Parameters tool and uncheck the
Initial state option under the 'Load from workspace' section.
To regenerate the initial state vector after a change to the model, follow the instructions given in the initialization file for this model.
Type open('init_power_wind_type_4_avg') at the MATLAB prompt to view this file.

1. Simulation Methods of the Type 4 Wind Turbine

Depending on the range of frequencies to be represented, three simulation methods are currently available in Specialized Power Systems to model VSC based energy conversion systems connected on power grids.

The detailed model (discrete) such as the one presented in the power_wind_type_4_det model. The detailed model includes detailed representation of power electronic IGBT converters. In order to achieve an acceptable accuracy with the 2000 Hz and 3000 Hz switching frequencies used in this example, the model must be discretized at a relatively small time step (2 microseconds). This model is well suited for observing harmonics and control system dynamic performance over relatively short periods of times (typically hundreds of milliseconds to one second).

The average model (discrete) such as the one presented in this example. In this type of model the IGBT Voltage-sourced converters (VSC) are represented by equivalent voltage sources generating the

AC voltage averaged over one cycle of the switching frequency. A similar method is used for DC-DC converter. The average model does not represent harmonics, but the dynamics resulting from control system and power system interaction is preserved. This model allows using much larger time steps (typically 50 microseconds), thus allowing simulations of several seconds.

The phasor model (continuous) such as the one presented in the "power_wind_dfig" model in the Renewable Energy examples library. This model is better adapted to simulate the low frequency electromechanical oscillations over long periods of time (tens of seconds to minutes). In the phasor simulation method, the sinusoidal voltages and currents are replaced by phasor quantities (complex numbers) at the system nominal frequency (50 Hz or 60 Hz).This is the same technique which is used in transient stability software.

2. Description

A 10 MW wind farm consisting of five 2 MW wind turbines connected to a 25 kV distribution system exports power to a 120 kV grid through a 30 km, 25 kV feeder.

The Type 4 wind turbine presented in this example consists of a synchronous generator connected to a diode rectifier, a DC-DC IGBT-based PWM boost converter and a DC/AC IGBT-based PWM converter modeled by voltage sources. The Type 4 technology allows extracting maximum energy from the wind for low wind speeds by optimizing the turbine speed, while minimizing mechanical stresses on the turbine during gusts of wind.

In this example the wind speed is maintained constant at 15 m/s. The control system of the DC-DC converter is used to maintain the speed at 1 pu. The reactive power produced by the wind turbine is regulated at 0 Mvar.

Right-click on the "Wind Turbine Type 4" block and select "Look Under Mask" to see how the model is built. The sample time used to discretize the model (Ts= 50 microseconds) is specified in the Initialization function of the Model Properties.

Open the "Wind Turbine Type 4" block menu to see the data of the generator, the converter, the turbine, the drive train and the control systems. In the Display menu select "Turbine data for 1 wind turbine", check "Display wind turbine power characteristics" and then click Apply. The turbine Cp curves are displayed in Figure 1. The turbine power, the tip speed ratio lambda and the Cp values are displayed in Figure 2 as function of wind speed. For a wind speed of 15 m/s, the turbine output power is 1 pu of its rated power, the pitch angle is 8.9 deg and the generator speed is 1 pu.

3. Simulation

In this example you will observe the steady-state operation of the Type 4 wind turbine and its dynamic response to voltage sag resulting from a remote fault on the 120-kV system. Open the "120 kV" block modeling the voltage source and see how a six-cycle 0.25 pu voltage drop is programmed at t=0.03 s

Start simulation. Observe voltage and current waveforms on the Scope block. At simulation start the "xInitial" variable containing the initial state variables is automatically loaded (from the "power_wind_type_4_avg.mat" file specified in the Model Properties) so that the simulation starts in steady state.

Initially the Type 4 wind farm produces 10 MW. The corresponding turbine speed is 1 pu of generator synchronous speed. The DC voltage is regulated at 1100 V and reactive power is kept at 0 Mvar. At t=0.03 s the positive-sequence voltage suddenly drops to 0.75 p.u. causing an increase on the DC bus voltage and a drop on the Type 4 wind turbine output power. During the voltage sag the control

systems try to regulate DC voltage and reactive power at their set points (1100 V, 0 Mvar). The system recovers after fault elimination.

4. Regenerate Initial Conditions

This example is set-up with all states initialized so that the simulation starts in steady-state. Otherwise, due to the long time constants of the electromechanical part of the wind turbine model and to its relatively slow regulators you would have to wait for tens of seconds before reaching steady-state. The initial conditions have been saved in the "power_wind_type_4_avg.mat" file. When you start simulation, the InitFcn callback (in the Model Properties/Callbacks) automatically loads into your workspace the contents of this .mat file ("xInitial" variable specified in the "Initial state" parameter in the Simulation/Configuration Parameters menu).

If you modify this model, or change parameter values of power components, the initial conditions stored in the "xInitial" variable will no longer be valid and Simulink® will issue an error message. To regenerate the initial conditions for your modified model, follow the steps listed below:

1. In the Configuration Parameters pane, uncheck the "Initial state" parameter.

2. In the 120 kV Three-phase Voltage Source menu, disable the source voltage step by setting the "Time variation of " parameter to "none".

3. In order to shorten the time required to reach steady-state, temporarily decrease the inertia of the turbine-generator group. Open the Wind Turbine Type 4 menu and in the Drive train data and Generator data, divide the H inertia constants by 10.

4. Change the Simulation Stop Time to 5 seconds. Note that in order to generate initial conditions coherent with the 60 Hz voltage source phase angles, the Stop Time must be an integer number of 60 Hz cycles.

5. Change the Simulation Mode from "Normal" to "Accelerator".

6. Start simulation. When Simulation is completed, verify that steady state has been reached by looking at waveforms displayed on the Scope block. The final states which have been saved in the "xFinal" structure with time can be used as initial states for future simulations. Executing the next two commands copies these final conditions in "xInitial" and saves this variable in a new file (myModel_init.mat).

```
>> xInitial=xFinal;
```

```
>> save myModel_init xInitial
```

7. In the InitFcn window of Model Properties pane, replace the first line of initialization commands with "load myModel_init". Next time you start a simulation with this model, the variable xInitial saved in the myModel_init.mat file will be loaded in your workspace.

8. In the Configuration Parameters pane, check "Initial state".

9. In the Wind Turbine Generator and Drive train data, reset the inertia constants H back to their original values.

10. Start simulation and verify that your model starts in steady-state.

11. In the 120 kV Three-phase voltage source menu, set the "Time variation of" parameter back to "Amplitude".

12. Change the Simulation Stop Time and Simulation Mode back to their original values (0.2 seconds, Normal).

13. Save your model.

Solid-Oxide Fuel Cell Connected to Three-Phase Electrical Power System

This example shows a model of a solid oxide fuel cell (SOFC) which can be utilized in Specialized Power Systems.
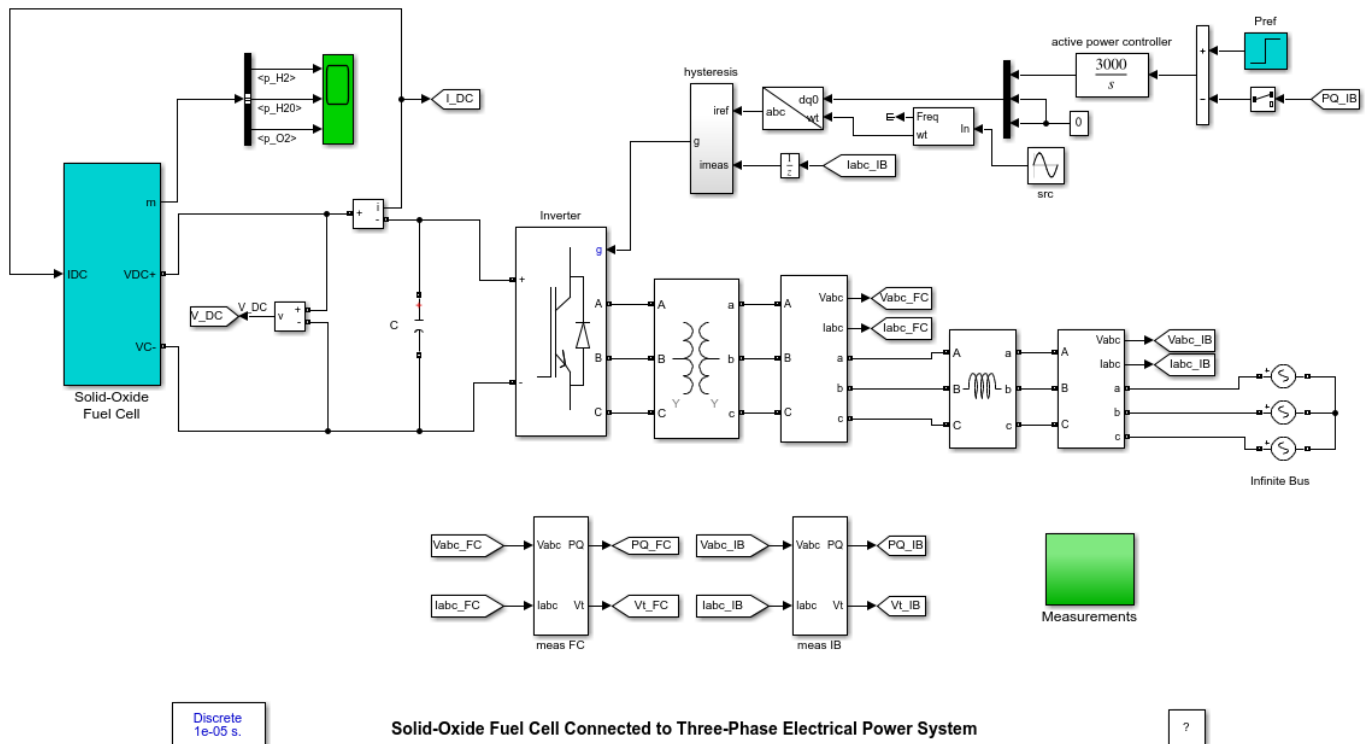
Description

The system consists of a SOFC which is connected to a three-phase infinite bus through an IGBT inverter. The inverter uses hysteresis switching and controls active power by manipulation of direct-axis current while holding reactive power at 0VAr.

The measurement blocks are rated at 50kW. Therefore, an active power reference of 1pu = 50kW.

Simulation

Start the simulation.

At t = 0s, an active power reference (Pref) of 0.3pu is commanded. Observe that the reference is captured within 0.2s.

At t = 0.4s, Pref = 1pu is commanded. Again, the reference is captured within 0.2s.

Observation of the H2, H20 and O2 pressures shows that the fuel cell does not reach a new equilibrium for the simulation of duration 1 second. Extended simulation periods are required to observe the dynamics of the chemical reaction.

Notes

This model assumes the following:

1 The fuel cell gases are ideal
2 Only one pressure is defined in the interior of the electrodes
3 The fuel cell temperature is invariant
4 Nernst's equation applies.

The model is taken from:

Y. Zhu and K. Tomsovic, 'Development of models for analyzing the load-following performance of microturbines and fuel cells', Electric Power Systems Research, 62, 2002, 1-11.